

Backing up Slicing: Verifying the interprocedural two-phase Horwitz-Reps-Binkley Slicer

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Abstract

Slicing is a widely-used technique with applications in e.g. compiler technology and software security. Thus verification of algorithms in these areas is often based on the correctness of slicing, which should ideally be proven independent of concrete programming languages and with the help of well-known verifying techniques such as proof assistants.

After verifying static intraprocedural and dynamic slicing [3], we focus now on the sophisticated interprocedural two-phase Horwitz-Reps-Binkley slicer [1], including summary edges which were added in [2].

Again, abstracting from concrete syntax we base our work on a graph representation of the program fulfilling certain structural and well-formedness properties. The framework is instantiated with a simple While language with procedures, showing its validity.

0.1 Auxiliary lemmas

theory *AuxLemmas* **imports** *Main* **begin**

Lemma concerning maps and @

lemma *map-append-append-maps*:

assumes *map:map f xs = ys@zs*

obtains *xs' xs''* **where** *map f xs' = ys* **and** *map f xs'' = zs* **and** *xs=xs'@xs''*

by (*metis append-eq-conv-conj append-take-drop-id assms drop-map take-map that*)

Lemma concerning splitting of lists

lemma *path-split-general*:

assumes *all:∀ zs. xs ≠ ys@zs*

obtains *j zs* **where** *xs = (take j ys)@zs* **and** *j < length ys*

and *∀ k > j. ∀ zs'. xs ≠ (take k ys)@zs'*

proof(*atomize-elim*)

from *⟨∀ zs. xs ≠ ys@zs⟩*

show *∃ j zs. xs = take j ys @ zs ∧ j < length ys ∧*

(∀ k > j. ∀ zs'. xs ≠ take k ys @ zs')

```

proof(induct ys arbitrary:xs)
  case Nil thus ?case by auto
next
  case (Cons y' ys')
  note  $IH = \langle \bigwedge xs. \forall zs. xs \neq ys' @ zs \implies \exists j zs. xs = take\ j\ ys' @ zs \wedge j < length\ ys' \wedge (\forall k. j < k \implies (\forall zs'. xs \neq take\ k\ ys' @ zs')) \rangle$ 
  show ?case
  proof(cases xs)
    case Nil thus ?thesis by simp
  next
    case (Cons x' xs')
    with  $\langle \forall zs. xs \neq (y' \# ys') @ zs \rangle$  have  $x' \neq y' \vee (\forall zs. xs' \neq ys' @ zs)$ 
      by simp
    show ?thesis
    proof(cases x' = y')
      case True
      with  $\langle x' \neq y' \vee (\forall zs. xs' \neq ys' @ zs) \rangle$  have  $\forall zs. xs' \neq ys' @ zs$  by simp
      from  $IH[OF\ this]$  have  $\exists j zs. xs' = take\ j\ ys' @ zs \wedge j < length\ ys' \wedge (\forall k. j < k \implies (\forall zs'. xs' \neq take\ k\ ys' @ zs'))$  .
      then obtain  $j\ zs$  where  $xs' = take\ j\ ys' @ zs$ 
        and  $j < length\ ys'$ 
        and  $all\ sub: \forall k. j < k \implies (\forall zs'. xs' \neq take\ k\ ys' @ zs')$ 
        by blast
      from  $\langle xs' = take\ j\ ys' @ zs \rangle$  True
        have  $(x' \# xs') = take\ (Suc\ j)\ (y' \# ys') @ zs$ 
        by simp
      from  $all\ sub\ True$  have  $all\ imp: \forall k. j < k \implies (\forall zs'. (x' \# xs') \neq take\ (Suc\ k)\ (y' \# ys') @ zs')$ 
        by auto
      { fix  $l$  assume  $(Suc\ j) < l$ 
        then obtain  $k$  where  $[simp]: l = Suc\ k$  by (cases l) auto
        with  $\langle (Suc\ j) < l \rangle$  have  $j < k$  by simp
        with  $all\ imp$ 
        have  $\forall zs'. (x' \# xs') \neq take\ (Suc\ k)\ (y' \# ys') @ zs'$ 
          by simp
        hence  $\forall zs'. (x' \# xs') \neq take\ l\ (y' \# ys') @ zs'$ 
          by simp }
      with  $\langle (x' \# xs') = take\ (Suc\ j)\ (y' \# ys') @ zs \rangle$   $\langle j < length\ ys' \rangle$  Cons
      show ?thesis by (metis Suc-length-conv less-Suc-eq-0-disj)
    next
      case False
      with Cons have  $\forall i\ zs'. i > 0 \implies xs \neq take\ i\ (y' \# ys') @ zs'$ 
        by auto(case-tac i,auto)
      moreover
      have  $\exists zs. xs = take\ 0\ (y' \# ys') @ zs$  by simp
      ultimately show ?thesis by (rule-tac x=0 in exI,auto)
    qed
  qed

```

qed
qed

end

Chapter 1

The Framework

As slicing is a program analysis that can be completely based on the information given in the CFG, we want to provide a framework which allows us to formalize and prove properties of slicing regardless of the actual programming language. So the starting point for the formalization is the definition of an abstract CFG, i.e. without considering features specific for certain languages. By doing so we ensure that our framework is as generic as possible since all proofs hold for every language whose CFG conforms to this abstract CFG.

Static Slicing analyses a CFG prior to execution. Whereas dynamic slicing can provide better results for certain inputs (i.e. trace and initial state), static slicing is more conservative but provides results independent of inputs.

Correctness for static slicing is defined using a weak simulation between nodes and states when traversing the original and the sliced graph. The weak simulation property demands that if a (node, state) tuples (n_1, s_1) simulates (n_2, s_2) and making an observable move in the original graph leads from (n_1, s_1) to (n'_1, s'_1) , this tuple simulates a tuple (n_2, s_2) which is the result of making an observable move in the sliced graph beginning in (n'_2, s'_2) .

1.1 Basic Definitions

theory *BasicDefs* **imports** *AuxLemmas* **begin**

```
fun fun-upds :: ('a  $\Rightarrow$  'b)  $\Rightarrow$  'a list  $\Rightarrow$  'b list  $\Rightarrow$  ('a  $\Rightarrow$  'b)
where fun-upds f [] ys = f
      | fun-upds f xs [] = f
      | fun-upds f (x#xs) (y#ys) = (fun-upds f xs ys)(x := y)
```

notation *fun-upds* ('(- /[:=]/ -'))

lemma *fun-upds-nth*:

```
[[i < length xs; length xs = length ys; distinct xs]]
 $\implies$  f(xs [:=] ys)(xs!i) = (ys!i)
```

proof(*induct xs arbitrary:ys i*)

case Nil thus ?*case* **by simp**

next

```

case (Cons x' xs')
note IH = ⟨ $\bigwedge ys\ i. \llbracket i < \text{length } xs'; \text{length } xs' = \text{length } ys; \text{distinct } xs \rrbracket$ 
   $\implies f(xs'[:=]ys)(xs!i) = ys!i$ 
from ⟨distinct (x'#xs')⟩ have distinct xs' and x'  $\notin$  set xs' by simp-all
from ⟨length (x'#xs') = length ys⟩ obtain y' ys' where [simp]:ys = y'#ys'
  and length xs' = length ys'
  by(cases ys) auto
show ?case
proof(cases i)
  case 0 thus ?thesis by simp
next
  case (Suc j)
  with ⟨i < length (x'#xs')⟩ have j < length xs' by simp
  from IH[OF this ⟨length xs' = length ys'⟩ ⟨distinct xs'⟩]
  have f(xs'[:=]ys')(xs!j) = ys!j .
  with ⟨x'  $\notin$  set xs'⟩ ⟨j < length xs'⟩
  have f((x'#xs')[:=]ys)((x'#xs')!(Suc j)) = ys!(Suc j) by fastforce
  with Suc show ?thesis by simp
qed
qed

```

lemma fun-upds-eq:

```

assumes V  $\in$  set xs and length xs = length ys and distinct xs
shows f(xs[:=]ys) V = f'(xs[:=]ys) V
proof –
from ⟨V  $\in$  set xs⟩ obtain i where i < length xs and xs!i = V
  by(fastforce simp:in-set-conv-nth)
with ⟨length xs = length ys⟩ ⟨distinct xs⟩
have f(xs[:=]ys)(xs!i) = (ys!i) by -(rule fun-upds-nth)
moreover
from ⟨i < length xs⟩ ⟨xs!i = V⟩ ⟨length xs = length ys⟩ ⟨distinct xs⟩
have f'(xs[:=]ys)(xs!i) = (ys!i) by -(rule fun-upds-nth)
ultimately show ?thesis using ⟨xs!i = V⟩ by simp
qed

```

lemma fun-upds-notin:x \notin set xs \implies f(xs[:=]ys) x = f x
by(induct xs arbitrary:ys,auto,case-tac ys,auto)

1.1.1 distinct-fst

definition distinct-fst :: ('a \times 'b) list \Rightarrow bool **where**
 distinct-fst \equiv distinct \circ map fst

lemma distinct-fst-Nil [simp]:
 distinct-fst []
by(simp add:distinct-fst-def)

lemma *distinct-fst-Cons* [*simp*]:
 $distinct\text{-fst } ((k,x)\#kxs) = (distinct\text{-fst } kxs \wedge (\forall y. (k,y) \notin set\ kxs))$
by(*auto simp:distinct-fst-def image-def*)

lemma *distinct-fst-isin-same-fst*:
 $\llbracket (x,y) \in set\ xs; (x,y') \in set\ xs; distinct\text{-fst } xs \rrbracket$
 $\implies y = y'$
by(*induct xs,auto simp:distinct-fst-def image-def*)

1.1.2 Edge kinds

Every procedure has a unique name, e.g. in object oriented languages *pname* refers to class + procedure.

A state is a call stack of tuples, which consists of:

1. data information, i.e. a mapping from the local variables in the call frame to their values, and
2. control flow information, e.g. which node called the current procedure.

Update and predicate edges check and manipulate only the data information of the top call stack element. Call and return edges however may use the data and control flow information present in the top stack element to state if this edge is traversable. The call edge additionally has a list of functions to determine what values the parameters have in a certain call frame and control flow information for the return. The return edge is concerned with passing the values of the return parameter values to the underlying stack frame. See the funtions *transfer* and *pred* in locale *CFG*.

datatype (*'var','val','ret','pname*) *edge-kind* =
 $UpdateEdge\ ('var \rightarrow 'val) \Rightarrow ('var \rightarrow 'val) \quad (\uparrow-)$
 $| PredicateEdge\ ('var \rightarrow 'val) \Rightarrow bool \quad ('(-)\surd)$
 $| CallEdge\ ('var \rightarrow 'val) \times 'ret \Rightarrow bool\ 'ret\ 'pname$
 $\quad ((('var \rightarrow 'val) \rightarrow 'val)\ list) \quad (-:\leftrightarrow- 70)$
 $| ReturnEdge\ ('var \rightarrow 'val) \times 'ret \Rightarrow bool\ 'pname$
 $\quad ('var \rightarrow 'val) \Rightarrow ('var \rightarrow 'val) \Rightarrow ('var \rightarrow 'val) \quad (-\leftrightarrow- 70)$

definition *intra-kind* :: (*'var','val','ret','pname*) *edge-kind* $\Rightarrow bool$
where *intra-kind* *et* $\equiv (\exists f. et = \uparrow f) \vee (\exists Q. et = (Q)\surd)$

lemma *edge-kind-cases* [*case-names Intra Call Return*]:
 $\llbracket intra\text{-kind } et \implies P; \bigwedge Q\ r\ p\ fs. et = Q:r\hookrightarrow\ pfs \implies P;$
 $\bigwedge Q\ p\ f. et = Q\hookleftarrow\ pf \implies P \rrbracket \implies P$
by(*cases et,auto simp:intra-kind-def*)

end

1.2 CFG

theory *CFG* imports *BasicDefs* begin

1.2.1 The abstract CFG

Locale fixes and assumptions

```
locale CFG =
  fixes sourcenode :: 'edge  $\Rightarrow$  'node
  fixes targetnode :: 'edge  $\Rightarrow$  'node
  fixes kind :: 'edge  $\Rightarrow$  ('var,'val,'ret,'pname) edge-kind
  fixes valid-edge :: 'edge  $\Rightarrow$  bool
  fixes Entry::'node ('('Entry-'))
  fixes get-proc::'node  $\Rightarrow$  'pname
  fixes get-return-edges::'edge  $\Rightarrow$  'edge set
  fixes procs::('pname  $\times$  'var list  $\times$  'var list) list
  fixes Main::'pname
  assumes Entry-target [dest]:  $\llbracket \text{valid-edge } a; \text{targetnode } a = (-\text{Entry-}) \rrbracket \Longrightarrow \text{False}$ 
  and get-proc-Entry:get-proc (-Entry-) = Main
  and Entry-no-call-source:
     $\llbracket \text{valid-edge } a; \text{kind } a = Q:r \hookrightarrow pfs; \text{sourcenode } a = (-\text{Entry-}) \rrbracket \Longrightarrow \text{False}$ 
  and edge-det:
     $\llbracket \text{valid-edge } a; \text{valid-edge } a'; \text{sourcenode } a = \text{sourcenode } a';$ 
     $\text{targetnode } a = \text{targetnode } a' \rrbracket \Longrightarrow a = a'$ 
  and Main-no-call-target: $\llbracket \text{valid-edge } a; \text{kind } a = Q:r \hookrightarrow \text{Main}f \rrbracket \Longrightarrow \text{False}$ 
  and Main-no-return-source: $\llbracket \text{valid-edge } a; \text{kind } a = Q' \hookleftarrow \text{Main}f \rrbracket \Longrightarrow \text{False}$ 
  and callee-in-procs:
     $\llbracket \text{valid-edge } a; \text{kind } a = Q:r \hookrightarrow pfs \rrbracket \Longrightarrow \exists \text{ins outs. } (p,\text{ins},\text{outs}) \in \text{set } \text{procs}$ 
  and get-proc-intra: $\llbracket \text{valid-edge } a; \text{intra-kind}(\text{kind } a) \rrbracket$ 
     $\Longrightarrow \text{get-proc}(\text{sourcenode } a) = \text{get-proc}(\text{targetnode } a)$ 
  and get-proc-call:
     $\llbracket \text{valid-edge } a; \text{kind } a = Q:r \hookrightarrow pfs \rrbracket \Longrightarrow \text{get-proc}(\text{targetnode } a) = p$ 
  and get-proc-return:
     $\llbracket \text{valid-edge } a; \text{kind } a = Q' \hookleftarrow pf \rrbracket \Longrightarrow \text{get-proc}(\text{sourcenode } a) = p$ 
  and call-edges-only: $\llbracket \text{valid-edge } a; \text{kind } a = Q:r \hookrightarrow pfs \rrbracket$ 
     $\Longrightarrow \forall a'. \text{valid-edge } a' \wedge \text{targetnode } a' = \text{targetnode } a \longrightarrow$ 
     $(\exists Qx \text{ rx fsx. } \text{kind } a' = Qx:\text{rx} \hookrightarrow pfsx)$ 
  and return-edges-only: $\llbracket \text{valid-edge } a; \text{kind } a = Q' \hookleftarrow pf \rrbracket$ 
     $\Longrightarrow \forall a'. \text{valid-edge } a' \wedge \text{sourcenode } a' = \text{sourcenode } a \longrightarrow$ 
     $(\exists Qx \text{ fx. } \text{kind } a' = Qx \hookleftarrow pfx)$ 
  and get-return-edge-call:
     $\llbracket \text{valid-edge } a; \text{kind } a = Q:r \hookrightarrow pfs \rrbracket \Longrightarrow \text{get-return-edges } a \neq \{\}$ 
  and get-return-edges-valid:
     $\llbracket \text{valid-edge } a; a' \in \text{get-return-edges } a \rrbracket \Longrightarrow \text{valid-edge } a'$ 
  and only-call-get-return-edges:
```

$\llbracket \text{valid-edge } a; a' \in \text{get-return-edges } a \rrbracket \implies \exists Q \ r \ p \ fs. \text{kind } a = Q:r \hookrightarrow_p fs$
and *call-return-edges*:
 $\llbracket \text{valid-edge } a; \text{kind } a = Q:r \hookrightarrow_p fs; a' \in \text{get-return-edges } a \rrbracket$
 $\implies \exists Q' \ f'. \text{kind } a' = Q' \hookrightarrow_p f'$
and *return-needs-call*: $\llbracket \text{valid-edge } a; \text{kind } a = Q' \hookrightarrow_p f' \rrbracket$
 $\implies \exists !a'. \text{valid-edge } a' \wedge (\exists Q \ r \ fs. \text{kind } a' = Q:r \hookrightarrow_p fs) \wedge a \in \text{get-return-edges } a'$
and *intra-proc-additional-edge*:
 $\llbracket \text{valid-edge } a; a' \in \text{get-return-edges } a \rrbracket$
 $\implies \exists a''. \text{valid-edge } a'' \wedge \text{sourcenode } a'' = \text{targetnode } a \wedge$
 $\text{targetnode } a'' = \text{sourcenode } a' \wedge \text{kind } a'' = (\lambda cf. \text{False})_{\checkmark}$
and *call-return-node-edge*:
 $\llbracket \text{valid-edge } a; a' \in \text{get-return-edges } a \rrbracket$
 $\implies \exists a''. \text{valid-edge } a'' \wedge \text{sourcenode } a'' = \text{sourcenode } a \wedge$
 $\text{targetnode } a'' = \text{targetnode } a' \wedge \text{kind } a'' = (\lambda cf. \text{False})_{\checkmark}$
and *call-only-one-intra-edge*:
 $\llbracket \text{valid-edge } a; \text{kind } a = Q:r \hookrightarrow_p fs \rrbracket$
 $\implies \exists !a'. \text{valid-edge } a' \wedge \text{sourcenode } a' = \text{sourcenode } a \wedge \text{intra-kind}(\text{kind } a')$
and *return-only-one-intra-edge*:
 $\llbracket \text{valid-edge } a; \text{kind } a = Q' \hookrightarrow_p f' \rrbracket$
 $\implies \exists !a'. \text{valid-edge } a' \wedge \text{targetnode } a' = \text{targetnode } a \wedge \text{intra-kind}(\text{kind } a')$
and *same-proc-call-unique-target*:
 $\llbracket \text{valid-edge } a; \text{valid-edge } a'; \text{kind } a = Q_1:r_1 \hookrightarrow_p fs_1; \text{kind } a' = Q_2:r_2 \hookrightarrow_p fs_2 \rrbracket$
 $\implies \text{targetnode } a = \text{targetnode } a'$
and *unique-callers:distinct-fst-procs*
and *distinct-formal-ins*: $(p, \text{ins}, \text{outs}) \in \text{set procs} \implies \text{distinct ins}$
and *distinct-formal-outs*: $(p, \text{ins}, \text{outs}) \in \text{set procs} \implies \text{distinct outs}$

begin

lemma *get-proc-get-return-edge*:

assumes *valid-edge a and a' ∈ get-return-edges a*
shows *get-proc (sourcenode a) = get-proc (targetnode a')*

proof –

from *assms obtain ax where valid-edge ax and sourcenode a = sourcenode ax*
and *targetnode a' = targetnode ax and intra-kind(kind ax)*
by *(auto dest:call-return-node-edge simp:intra-kind-def)*
thus *?thesis by(fastforce intro:get-proc-intra)*

qed

lemma *call-intra-edge-False*:

assumes *valid-edge a and kind a = Q:r ↦_p fs and valid-edge a'*
and *sourcenode a = sourcenode a' and intra-kind(kind a')*
shows *kind a' = (λ cf. False)_✓*

proof –

from *(valid-edge a) (kind a = Q:r ↦_p fs) obtain ax where ax ∈ get-return-edges a*


```

  by(fastforce dest:get-return-edge-call)
with ⟨valid-edge a⟩ obtain a'' where valid-edge a''
  and sourcenode a'' = sourcenode a and kind a'' = (λcf. False)✓
  by(fastforce dest:call-return-node-edge)
from ⟨kind a'' = (λcf. False)✓⟩ have intra-kind(kind a'')
  by(simp add:intra-kind-def)
with assms ⟨valid-edge a''⟩ ⟨sourcenode a'' = sourcenode a⟩
  ⟨kind a'' = (λcf. False)✓⟩
show ?thesis by(fastforce dest:call-only-one-intra-edge)
qed

```

lemma formal-in-THE:

```

[[valid-edge a; kind a = Q:r↔pfs; (p,ins,outs) ∈ set procs]]
  ⇒ (THE ins. ∃ outs. (p,ins,outs) ∈ set procs) = ins
by(fastforce dest:distinct-fst-isin-same-fst intro:unique-callers)

```

lemma formal-out-THE:

```

[[valid-edge a; kind a = Q↔pf; (p,ins,outs) ∈ set procs]]
  ⇒ (THE outs. ∃ ins. (p,ins,outs) ∈ set procs) = outs
by(fastforce dest:distinct-fst-isin-same-fst intro:unique-callers)

```

Transfer and predicate functions

```

fun params :: (('var → 'val) → 'val) list ⇒ ('var → 'val) ⇒ 'val option list
where params [] cf = []
  | params (f#fs) cf = (f cf)#params fs cf

```

lemma params-nth:

```

i < length fs ⇒ (params fs cf)!i = (fs!i) cf
by(induct fs arbitrary:i,auto,case-tac i,auto)

```

```

lemma [simp]:length (params fs cf) = length fs
  by(induct fs) auto

```

```

fun transfer :: ('var,'val,'ret,'pname) edge-kind ⇒ (('var → 'val) × 'ret) list ⇒
  (('var → 'val) × 'ret) list
where transfer (↑f) (cf#cfs) = (f (fst cf),snd cf)#cfs
  | transfer (Q)✓ (cf#cfs) = (cf#cfs)
  | transfer (Q:r↔pfs) (cf#cfs) =
    (let ins = THE ins. ∃ outs. (p,ins,outs) ∈ set procs in
     empty(ins [:=] params fs (fst cf)),r)#cf#cfs)
  | transfer (Q↔pf) (cf#cfs) = (case cfs of [] ⇒ []
    | cf'#cfs' ⇒ (f (fst cf) (fst cf'),snd cf')#cfs')
  | transfer et [] = []

```

```

fun transfers :: ('var,'val,'ret,'pname) edge-kind list  $\Rightarrow$  (('var  $\rightarrow$  'val)  $\times$  'ret) list
 $\Rightarrow$ 
      (('var  $\rightarrow$  'val)  $\times$  'ret) list
where transfers [] s = s
      | transfers (et#ets) s = transfers ets (transfer et s)

```

```

fun pred :: ('var,'val,'ret,'pname) edge-kind  $\Rightarrow$  (('var  $\rightarrow$  'val)  $\times$  'ret) list  $\Rightarrow$  bool
where pred ( $\uparrow$ f) (cf#cfs) = True
      | pred (Q) $\surd$  (cf#cfs) = Q (fst cf)
      | pred (Q:r $\leftrightarrow$ pf) (cf#cfs) = Q (fst cf,r)
      | pred (Q $\leftrightarrow$ pf) (cf#cfs) = (Q cf  $\wedge$  cfs  $\neq$  [])
      | pred et [] = False

```

```

fun preds :: ('var,'val,'ret,'pname) edge-kind list  $\Rightarrow$  (('var  $\rightarrow$  'val)  $\times$  'ret) list  $\Rightarrow$ 
bool
where preds [] s = True
      | preds (et#ets) s = (pred et s  $\wedge$  preds ets (transfer et s))

```

```

lemma transfers-split:
  (transfers (ets@ets') s) = (transfers ets' (transfers ets s))
by(induct ets arbitrary:s) auto

```

```

lemma preds-split:
  (preds (ets@ets') s) = (preds ets s  $\wedge$  preds ets' (transfers ets s))
by(induct ets arbitrary:s) auto

```

```

abbreviation state-val :: (('var  $\rightarrow$  'val)  $\times$  'ret) list  $\Rightarrow$  'var  $\rightarrow$  'val
where state-val s V  $\equiv$  (fst (hd s)) V

```

valid-node

```

definition valid-node :: 'node  $\Rightarrow$  bool
where valid-node n  $\equiv$ 
  ( $\exists$  a. valid-edge a  $\wedge$  (n = sourcenode a  $\vee$  n = targetnode a))

```

```

lemma [simp]: valid-edge a  $\Longrightarrow$  valid-node (sourcenode a)
by(fastforce simp:valid-node-def)

```

```

lemma [simp]: valid-edge a  $\Longrightarrow$  valid-node (targetnode a)
by(fastforce simp:valid-node-def)

```

1.2.2 CFG paths

```

inductive path :: 'node  $\Rightarrow$  'edge list  $\Rightarrow$  'node  $\Rightarrow$  bool
  (-  $\dashrightarrow^*$  - [51,0,0] 80)
where
  empty-path:valid-node n  $\Longrightarrow$  n -[] $\rightarrow^*$  n

```

| *Cons-path*:
 $\llbracket n'' - as \rightarrow^* n'; \text{valid-edge } a; \text{sourcenode } a = n; \text{targetnode } a = n' \rrbracket$
 $\implies n - a \# as \rightarrow^* n'$

lemma *path-valid-node*:
assumes $n - as \rightarrow^* n'$ **shows** *valid-node* n **and** *valid-node* n'
using $\langle n - as \rightarrow^* n' \rangle$
by (*induct rule: path.induct, auto*)

lemma *empty-path-nodes* $[dest]: n - [] \rightarrow^* n' \implies n = n'$
by (*fastforce elim: path.cases*)

lemma *path-valid-edges*: $n - as \rightarrow^* n' \implies \forall a \in \text{set } as. \text{valid-edge } a$
by (*induct rule: path.induct*) *auto*

lemma *path-edge*: *valid-edge* $a \implies \text{sourcenode } a - [a] \rightarrow^* \text{targetnode } a$
by (*fastforce intro: Cons-path empty-path*)

lemma *path-Append*: $\llbracket n - as \rightarrow^* n''; n'' - as' \rightarrow^* n' \rrbracket$
 $\implies n - as @ as' \rightarrow^* n'$
by (*induct rule: path.induct, auto intro: Cons-path*)

lemma *path-split*:
assumes $n - as @ a \# as' \rightarrow^* n'$
shows $n - as \rightarrow^* \text{sourcenode } a$ **and** *valid-edge* a **and** $\text{targetnode } a - as' \rightarrow^* n'$
using $\langle n - as @ a \# as' \rightarrow^* n' \rangle$
proof (*induct as arbitrary: n*)
case Nil case 1
thus *?case* **by** (*fastforce elim: path.cases intro: empty-path*)
next
case Nil case 2
thus *?case* **by** (*fastforce elim: path.cases intro: path-edge*)
next
case Nil case 3
thus *?case* **by** (*fastforce elim: path.cases*)
next
case (Cons ax asx)
note *IH1* = $\langle \bigwedge n. n - asx @ a \# as' \rightarrow^* n' \implies n - asx \rightarrow^* \text{sourcenode } a \rangle$
note *IH2* = $\langle \bigwedge n. n - asx @ a \# as' \rightarrow^* n' \implies \text{valid-edge } a \rangle$
note *IH3* = $\langle \bigwedge n. n - asx @ a \# as' \rightarrow^* n' \implies \text{targetnode } a - as' \rightarrow^* n' \rangle$
{ case 1
hence *sourcenode* $ax = n$ **and** *targetnode* $ax - asx @ a \# as' \rightarrow^* n'$ **and** *valid-edge*
 ax
by (*auto elim: path.cases*)

```

from IH1[OF ⟨ targetnode ax -asx@a#as'→* n'⟩]
have targetnode ax -asx→* sourcenode a .
with ⟨sourcenode ax = n⟩ ⟨valid-edge ax⟩ show ?case by(fastforce intro:Cons-path)
next
  case 2 hence targetnode ax -asx@a#as'→* n' by(auto elim:path.cases)
  from IH2[OF this] show ?case .
next
  case 3 hence targetnode ax -asx@a#as'→* n' by(auto elim:path.cases)
  from IH3[OF this] show ?case .
}
qed

```

lemma *path-split-Cons*:

```

assumes n -as→* n' and as ≠ []
obtains a' as' where as = a'#as' and n = sourcenode a'
and valid-edge a' and targetnode a' -as'→* n'
proof(atomize-elim)
from ⟨as ≠ []⟩ obtain a' as' where as = a'#as' by(cases as) auto
with ⟨n -as→* n'⟩ have n -[]@a'#as'→* n' by simp
hence n -[]→* sourcenode a' and valid-edge a' and targetnode a' -as'→* n'
  by(rule path-split)+
from ⟨n -[]→* sourcenode a'⟩ have n = sourcenode a' by fast
with ⟨as = a'#as'⟩ ⟨valid-edge a'⟩ ⟨targetnode a' -as'→* n'⟩
show ∃ a' as'. as = a'#as' ∧ n = sourcenode a' ∧ valid-edge a' ∧
  targetnode a' -as'→* n'
  by fastforce
qed

```

lemma *path-split-snoc*:

```

assumes n -as→* n' and as ≠ []
obtains a' as' where as = as'@[a'] and n -as'→* sourcenode a'
and valid-edge a' and n' = targetnode a'
proof(atomize-elim)
from ⟨as ≠ []⟩ obtain a' as' where as = as'@[a'] by(cases as rule:rev-cases)
auto
with ⟨n -as→* n'⟩ have n -as'@a'#[]→* n' by simp
hence n -as'→* sourcenode a' and valid-edge a' and targetnode a' -[]→* n'
  by(rule path-split)+
from ⟨targetnode a' -[]→* n'⟩ have n' = targetnode a' by fast
with ⟨as = as'@[a']⟩ ⟨valid-edge a'⟩ ⟨n -as'→* sourcenode a'⟩
show ∃ as' a'. as = as'@[a'] ∧ n -as'→* sourcenode a' ∧ valid-edge a' ∧
  n' = targetnode a'
  by fastforce
qed

```

lemma *path-split-second*:

assumes $n - as@a\#as' \rightarrow^* n'$ **shows** *sourcenode* $a - a\#as' \rightarrow^* n'$
proof –
from $\langle n - as@a\#as' \rightarrow^* n' \rangle$ **have** *valid-edge* a **and** *targetnode* $a - as' \rightarrow^* n'$
by(*auto intro:path-split*)
thus *?thesis* **by**(*fastforce intro:Cons-path*)
qed

lemma *path-Entry-Cons*:

assumes $(-Entry-) - as \rightarrow^* n'$ **and** $n' \neq (-Entry-)$
obtains n **where** *sourcenode* $a = (-Entry-)$ **and** *targetnode* $a = n$
and $n - tl\ as \rightarrow^* n'$ **and** *valid-edge* a **and** $a = hd\ as$
proof(*atomize-elim*)
from $\langle (-Entry-) - as \rightarrow^* n' \rangle$ $\langle n' \neq (-Entry-) \rangle$ **have** $as \neq []$
by(*cases as,auto elim:path.cases*)
with $\langle (-Entry-) - as \rightarrow^* n' \rangle$ **obtain** $a'\ as'$ **where** $as = a'\#as'$
and $(-Entry-) = \text{sourcenode } a'$ **and** *valid-edge* a' **and** *targetnode* $a' - as' \rightarrow^* n'$
by(*erule path-split-Cons*)
thus $\exists a\ n.$ *sourcenode* $a = (-Entry-)$ \wedge *targetnode* $a = n$ \wedge $n - tl\ as \rightarrow^* n' \wedge$
valid-edge $a \wedge a = hd\ as$
by *fastforce*
qed

lemma *path-det*:

$\llbracket n - as \rightarrow^* n'; n - as \rightarrow^* n'' \rrbracket \implies n' = n''$
proof(*induct as arbitrary:n*)
case *Nil* **thus** *?case* **by**(*auto elim:path.cases*)
next
case $(Cons\ a'\ as')$
note $IH = \langle \bigwedge n. \llbracket n - as' \rightarrow^* n'; n - as' \rightarrow^* n'' \rrbracket \implies n' = n'' \rangle$
from $\langle n - a'\#as' \rightarrow^* n' \rangle$ **have** *targetnode* $a' - as' \rightarrow^* n'$
by(*fastforce elim:path-split-Cons*)
from $\langle n - a'\#as' \rightarrow^* n'' \rangle$ **have** *targetnode* $a' - as' \rightarrow^* n''$
by(*fastforce elim:path-split-Cons*)
from $IH[OF\ \langle \text{targetnode } a' - as' \rightarrow^* n' \rangle\ \text{this}]$ **show** *?thesis* .
qed

definition

sourcenodes $:: 'edge\ list \Rightarrow 'node\ list$
where *sourcenodes* $xs \equiv map\ \text{sourcenode } xs$

definition

kinds $:: 'edge\ list \Rightarrow ('var, 'val, 'ret, 'pname)\ edge\text{-}kind\ list$
where *kinds* $xs \equiv map\ kind\ xs$

definition

targetnodes $:: 'edge\ list \Rightarrow 'node\ list$

where $\text{targetnodes } xs \equiv \text{map } \text{targetnode } xs$

lemma *path-sourcenode*:

$\llbracket n - as \rightarrow^* n'; as \neq [] \rrbracket \implies \text{hd } (\text{sourcenodes } as) = n$
by(*fastforce elim:path-split-Cons simp:sourcenodes-def*)

lemma *path-targetnode*:

$\llbracket n - as \rightarrow^* n'; as \neq [] \rrbracket \implies \text{last } (\text{targetnodes } as) = n'$
by(*fastforce elim:path-split-snoc simp:targetnodes-def*)

lemma *sourcenodes-is-n-Cons-butlast-targetnodes*:

$\llbracket n - as \rightarrow^* n'; as \neq [] \rrbracket \implies$
 $\text{sourcenodes } as = n \# (\text{butlast } (\text{targetnodes } as))$
proof(*induct as arbitrary:n*)
 case Nil thus ?case by simp
next
 case (Cons a' as')
 note $IH = \langle \bigwedge n. \llbracket n - as' \rightarrow^* n'; as' \neq [] \rrbracket$
 $\implies \text{sourcenodes } as' = n \# (\text{butlast } (\text{targetnodes } as')) \rangle$
 from $\langle n - a' \# as' \rightarrow^* n' \rangle$ **have** $n = \text{sourcenode } a' \text{ and } \text{targetnode } a' - as' \rightarrow^* n'$
 by(*auto elim:path-split-Cons*)
 show *?case*
 proof(*cases as' = []*)
 case True
 with $\langle \text{targetnode } a' - as' \rightarrow^* n' \rangle$ **have** $\text{targetnode } a' = n'$ **by** *fast*
 with $\langle n = \text{sourcenode } a' \rangle$ **show** *?thesis*
 by(*simp add:sourcenodes-def targetnodes-def*)
 next
 case False
 from $IH[OF \langle \text{targetnode } a' - as' \rightarrow^* n' \rangle \text{ this}]$
 have $\text{sourcenodes } as' = \text{targetnode } a' \# \text{butlast } (\text{targetnodes } as')$.
 with $\langle n = \text{sourcenode } a' \rangle$ **False** **show** *?thesis*
 by(*simp add:sourcenodes-def targetnodes-def*)
qed
qed

lemma *targetnodes-is-tl-sourcenodes-App-n'*:

$\llbracket n - as \rightarrow^* n'; as \neq [] \rrbracket \implies$
 $\text{targetnodes } as = (\text{tl } (\text{sourcenodes } as))@[n']$
proof(*induct as arbitrary:n' rule:rev-induct*)
 case Nil thus ?case by simp
next

```

case (snoc a' as')
note IH = ⟨ $\bigwedge n'. \llbracket n - as' \rightarrow^* n'; as' \neq [] \rrbracket$ 
   $\implies \text{targetnodes } as' = \text{tl } (\text{sourcenodes } as') @ [n']\rangle$ 
from ⟨ $n - as' @ [a'] \rightarrow^* n'$ ⟩ have  $n - as' \rightarrow^* \text{sourcenode } a'$  and  $n' = \text{targetnode } a'$ 
  by(auto elim:path-split-snoc)
show ?case
proof(cases as' = [])
  case True
    with ⟨ $n - as' \rightarrow^* \text{sourcenode } a'$ ⟩ have  $n = \text{sourcenode } a'$  by fast
    with True ⟨ $n' = \text{targetnode } a'$ ⟩ show ?thesis
    by(simp add:sourcenodes-def targetnodes-def)
  next
    case False
    from IH[OF ⟨ $n - as' \rightarrow^* \text{sourcenode } a'$ ⟩ this]
    have  $\text{targetnodes } as' = \text{tl } (\text{sourcenodes } as') @ [\text{sourcenode } a']$  .
    with ⟨ $n' = \text{targetnode } a'$ ⟩ False show ?thesis
    by(simp add:sourcenodes-def targetnodes-def)
qed
qed

```

Intraprocedural paths

definition *intra-path* :: 'node \Rightarrow 'edge list \Rightarrow 'node \Rightarrow bool
 (- $\dashrightarrow_{\iota}^*$ - [51,0,0] 80)
where $n - as \rightarrow_{\iota}^* n' \equiv n - as \rightarrow^* n' \wedge (\forall a \in \text{set } as. \text{intra-kind}(\text{kind } a))$

lemma *intra-path-get-procs*:

assumes $n - as \rightarrow_{\iota}^* n'$ **shows** $\text{get-proc } n = \text{get-proc } n'$

proof -

from ⟨ $n - as \rightarrow_{\iota}^* n'$ ⟩ **have** $n - as \rightarrow^* n'$ **and** $\forall a \in \text{set } as. \text{intra-kind}(\text{kind } a)$
by(simp-all add:intra-path-def)

thus ?thesis

proof(induct as arbitrary:n)

case Nil **thus** ?case **by** fastforce

next

case (Cons a' as')

note IH = ⟨ $\bigwedge n. \llbracket n - as' \rightarrow^* n'; \forall a \in \text{set } as'. \text{intra-kind } (\text{kind } a) \rrbracket$
 $\implies \text{get-proc } n = \text{get-proc } n'\rangle$

from $\langle \forall a \in \text{set } (a' \# as'). \text{intra-kind } (\text{kind } a) \rangle$

have $\text{intra-kind}(\text{kind } a')$ **and** $\forall a \in \text{set } as'. \text{intra-kind } (\text{kind } a)$ **by** simp-all

from $\langle n - a' \# as' \rightarrow^* n' \rangle$ **have** $\text{sourcenode } a' = n$ **and** $\text{valid-edge } a'$

and $\text{targetnode } a' - as' \rightarrow^* n'$ **by**(auto elim:path.cases)

from IH[OF ⟨ $\text{targetnode } a' - as' \rightarrow^* n'$ ⟩ $\langle \forall a \in \text{set } as'. \text{intra-kind } (\text{kind } a) \rangle$]

have $\text{get-proc } (\text{targetnode } a') = \text{get-proc } n'$.

from $\langle \text{valid-edge } a' \rangle \langle \text{intra-kind}(\text{kind } a') \rangle$

have $\text{get-proc } (\text{sourcenode } a') = \text{get-proc } (\text{targetnode } a')$

by(rule get-proc-intra)

with $\langle \text{sourcenode } a' = n \rangle \langle \text{get-proc } (\text{targetnode } a') = \text{get-proc } n' \rangle$

show ?case **by** simp

qed
qed

lemma *intra-path-Append*:
 $\llbracket n - as \rightarrow_i * n''; n'' - as' \rightarrow_i * n' \rrbracket \implies n - as @ as' \rightarrow_i * n'$
by(*fastforce intro:path-Append simp:intra-path-def*)

lemma *get-proc-get-return-edges*:
assumes *valid-edge a* **and** $a' \in \text{get-return-edges } a$
shows $\text{get-proc}(\text{targetnode } a) = \text{get-proc}(\text{sourcenode } a')$
proof –
from $\langle \text{valid-edge } a \rangle \langle a' \in \text{get-return-edges } a \rangle$
obtain a'' **where** $\text{valid-edge } a''$ **and** $\text{sourcenode } a'' = \text{targetnode } a$
and $\text{targetnode } a'' = \text{sourcenode } a'$ **and** $\text{kind } a'' = (\lambda cf. \text{False})_{\surd}$
by(*fastforce dest:intra-proc-additional-edge*)
from $\langle \text{valid-edge } a'' \rangle \langle \text{kind } a'' = (\lambda cf. \text{False})_{\surd} \rangle$
have $\text{get-proc}(\text{sourcenode } a'') = \text{get-proc}(\text{targetnode } a'')$
by(*fastforce intro:get-proc-intra simp:intra-kind-def*)
with $\langle \text{sourcenode } a'' = \text{targetnode } a \rangle \langle \text{targetnode } a'' = \text{sourcenode } a' \rangle$
show *?thesis* **by** *simp*
qed

Valid paths

declare *conj-cong*[*fundef-cong*]

fun *valid-path-aux* :: $'\text{edge list} \Rightarrow '\text{edge list} \Rightarrow \text{bool}$
where $\text{valid-path-aux } cs [] \longleftrightarrow \text{True}$
 $|\ \text{valid-path-aux } cs (a \# as) \longleftrightarrow$
 $(\text{case } (\text{kind } a) \text{ of } Q:r \leftrightarrow_p fs \Rightarrow \text{valid-path-aux } (a \# cs) as$
 $|\ Q \leftrightarrow_p f \Rightarrow \text{case } cs \text{ of } [] \Rightarrow \text{valid-path-aux } [] as$
 $|\ c' \# cs' \Rightarrow a \in \text{get-return-edges } c' \wedge$
 $\text{valid-path-aux } cs' as$
 $|\ - \Rightarrow \text{valid-path-aux } cs as)$

lemma *vpa-induct* [*consumes 1, case-names vpa-empty vpa-intra vpa-Call vpa-ReturnEmpty vpa-ReturnCons*]:

assumes *major: valid-path-aux xs ys*
and *rules: $\bigwedge cs. P cs []$*
 $\bigwedge cs a as. \llbracket \text{intra-kind}(\text{kind } a); \text{valid-path-aux } cs as; P cs as \rrbracket \implies P cs (a \# as)$
 $\bigwedge cs a as Q r p fs. \llbracket \text{kind } a = Q:r \leftrightarrow_p fs; \text{valid-path-aux } (a \# cs) as; P (a \# cs) as \rrbracket$
 $\implies P cs (a \# as)$
 $\bigwedge cs a as Q p f. \llbracket \text{kind } a = Q \leftrightarrow_p f; cs = []; \text{valid-path-aux } [] as; P [] as \rrbracket$
 $\implies P cs (a \# as)$
 $\bigwedge cs a as Q p f c' cs'. \llbracket \text{kind } a = Q \leftrightarrow_p f; cs = c' \# cs'; \text{valid-path-aux } cs' as;$

$a \in \text{get-return-edges } c'; P \text{ cs}' \text{ as}]$
 $\implies P \text{ cs } (a\#as)$
shows $P \text{ xs } \text{ ys}$
using *major*
apply(*induct ys arbitrary: xs*)
by(*auto intro:rules split:edge-kind.split-asm list.split-asm simp:intra-kind-def*)

lemma *valid-path-aux-intra-path*:
 $\forall a \in \text{set as. intra-kind}(\text{kind } a) \implies \text{valid-path-aux } \text{cs } \text{as}$
by(*induct as,auto simp:intra-kind-def*)

lemma *valid-path-aux-callstack-prefix*:
 $\text{valid-path-aux } (\text{cs}@cs') \text{ as} \implies \text{valid-path-aux } \text{cs } \text{as}$
proof(*induct cs@cs' as arbitrary:cs cs' rule:vpa-induct*)
case *vpa-empty thus ?case by simp*
next
case (*vpa-intra a as*)
hence *valid-path-aux cs as by simp*
with (*intra-kind (kind a)*) **show** *?case by (cases kind a,auto simp:intra-kind-def)*
next
case (*vpa-Call a as Q r p fs cs'' cs'*)
note $IH = \langle \bigwedge \text{xs ys. } a\#cs''@cs' = \text{xs}@ys \implies \text{valid-path-aux } \text{xs } \text{as} \rangle$
have $a\#cs''@cs' = (a\#cs'')@cs'$ **by** *simp*
from $IH[OF \text{ this}]$ **have** *valid-path-aux (a#cs'') as .*
with ($\text{kind } a = Q:r \hookrightarrow pfs$) **show** *?case by simp*
next
case (*vpa-ReturnEmpty a as Q p f cs'' cs'*)
hence *valid-path-aux cs'' as by simp*
with ($\text{kind } a = Q \hookrightarrow pf$) ($cs''@cs' = []$) **show** *?case by simp*
next
case (*vpa-ReturnCons a as Q p f c' cs' csx csx'*)
note $IH = \langle \bigwedge \text{xs ys. } cs' = \text{xs}@ys \implies \text{valid-path-aux } \text{xs } \text{as} \rangle$
from ($csx@csx' = c'\#cs'$)
have $csx = [] \wedge csx' = c'\#cs' \vee (\exists \text{zs. } csx = c'\#\text{zs} \wedge \text{zs}@csx' = cs')$
by(*simp add:append-eq-Cons-conv*)
thus *?case*
proof
assume $csx = [] \wedge csx' = c'\#cs'$
hence $csx = []$ **and** $csx' = c'\#cs'$ **by** *simp-all*
from ($csx' = c'\#cs'$) **have** $cs' = []@tl \text{ cs}'$ **by** *simp*
from $IH[OF \text{ this}]$ **have** *valid-path-aux [] as .*
with ($csx = []$) ($\text{kind } a = Q \hookrightarrow pf$) **show** *?thesis by simp*
next
assume $\exists \text{zs. } csx = c'\#\text{zs} \wedge \text{zs}@csx' = cs'$
then obtain *zs* **where** $csx = c'\#\text{zs}$ **and** $cs' = \text{zs}@csx'$ **by** *auto*
from $IH[OF \langle cs' = \text{zs}@csx' \rangle]$ **have** *valid-path-aux zs as .*
with ($csx = c'\#\text{zs}$) ($\text{kind } a = Q \hookrightarrow pf$) ($a \in \text{get-return-edges } c'$)

show *?thesis* **by** *simp*
qed
qed

fun *upd-cs* :: 'edge list \Rightarrow 'edge list \Rightarrow 'edge list
where *upd-cs* *cs* [] = *cs*
| *upd-cs* *cs* (*a*#*as*) =
(*case* (*kind* *a*) *of* *Q*:*r* \leftrightarrow *pfs* \Rightarrow *upd-cs* (*a*#*cs*) *as*
| *Q* \leftrightarrow *pf* \Rightarrow *case* *cs* *of* [] \Rightarrow *upd-cs* *cs* *as*
| *c*'#*cs*' \Rightarrow *upd-cs* *cs*' *as*
| - \Rightarrow *upd-cs* *cs* *as*)

lemma *upd-cs-empty* [*dest*]:
upd-cs *cs* [] = [] \Longrightarrow *cs* = []
by(*cases* *cs*) *auto*

lemma *upd-cs-intra-path*:
 $\forall a \in \text{set } as. \text{intra-kind}(\text{kind } a) \Longrightarrow \text{upd-cs } cs \text{ } as = cs$
by(*induct* *as*,*auto simp:intra-kind-def*)

lemma *upd-cs-Append*:
 $\llbracket \text{upd-cs } cs \text{ } as = cs'; \text{upd-cs } cs' \text{ } as' = cs'' \rrbracket \Longrightarrow \text{upd-cs } cs \text{ } (as@as') = cs''$
by(*induct* *as arbitrary:cs,auto split:edge-kind.split list.split*)

lemma *upd-cs-empty-split*:
assumes *upd-cs* *cs* *as* = [] **and** *cs* \neq [] **and** *as* \neq []
obtains *xs ys* **where** *as* = *xs*@*ys* **and** *xs* \neq [] **and** *upd-cs* *cs* *xs* = []
and $\forall xs' ys'. xs = xs'@ys' \wedge ys' \neq [] \longrightarrow \text{upd-cs } cs \text{ } xs' \neq []$
and *upd-cs* [] *ys* = []
proof(*atomize-elim*)
from $\langle \text{upd-cs } cs \text{ } as = [] \rangle \langle cs \neq [] \rangle \langle as \neq [] \rangle$
show $\exists xs \text{ } ys. as = xs@ys \wedge xs \neq [] \wedge \text{upd-cs } cs \text{ } xs = [] \wedge$
 $(\forall xs' \text{ } ys'. xs = xs'@ys' \wedge ys' \neq [] \longrightarrow \text{upd-cs } cs \text{ } xs' \neq []) \wedge$
 $\text{upd-cs } [] \text{ } ys = []$
proof(*induct* *as arbitrary:cs*)
case *Nil* **thus** *?case* **by** *simp*
next
case (*Cons* *a'* *as'*)
note *IH* = $\langle \bigwedge cs. \llbracket \text{upd-cs } cs \text{ } as' = []; cs \neq []; as' \neq [] \rrbracket$
 $\Longrightarrow \exists xs \text{ } ys. as' = xs@ys \wedge xs \neq [] \wedge \text{upd-cs } cs \text{ } xs = [] \wedge$
 $(\forall xs' \text{ } ys'. xs = xs'@ys' \wedge ys' \neq [] \longrightarrow \text{upd-cs } cs \text{ } xs' \neq []) \wedge$
 $\text{upd-cs } [] \text{ } ys = [] \rangle$
show *?case*
proof(*cases* *kind* *a'* *rule:edge-kind-cases*)

```

case Intra
with  $\langle \text{upd-cs } cs \ (a' \# as') = [] \rangle$  have  $\text{upd-cs } cs \ as' = []$ 
  by (fastforce simp:intra-kind-def)
with  $\langle cs \neq [] \rangle$  have  $as' \neq []$  by fastforce
from  $IH[OF \ \langle \text{upd-cs } cs \ as' = [] \ \langle cs \neq [] \rangle \ \text{this} ]$  obtain  $xs \ ys$  where  $as' =$ 
 $xs @ ys$ 
  and  $xs \neq []$  and  $\text{upd-cs } cs \ xs = []$  and  $\text{upd-cs } [] \ ys = []$ 
  and  $\forall xs' \ ys'. \ xs = xs' @ ys' \wedge \ ys' \neq [] \longrightarrow \text{upd-cs } cs \ xs' \neq []$  by blast
from  $\langle \text{upd-cs } cs \ xs = [] \rangle$  Intra have  $\text{upd-cs } cs \ (a' \# xs) = []$ 
  by (fastforce simp:intra-kind-def)
from  $\langle \forall xs' \ ys'. \ xs = xs' @ ys' \wedge \ ys' \neq [] \longrightarrow \text{upd-cs } cs \ xs' \neq [] \rangle$   $\langle xs \neq [] \rangle$  Intra
have  $\forall xs' \ ys'. \ a' \# xs = xs' @ ys' \wedge \ ys' \neq [] \longrightarrow \text{upd-cs } cs \ xs' \neq []$ 
  apply auto
  apply (case-tac xs') apply (auto simp:intra-kind-def)
  by (erule-tac x=[] in allE,fastforce)+
with  $\langle as' = xs @ ys \rangle$   $\langle \text{upd-cs } cs \ (a' \# xs) = [] \rangle$   $\langle \text{upd-cs } [] \ ys = [] \rangle$ 
show ?thesis apply (rule-tac x=a' \# xs in exI) by fastforce
next
case (Call Q p f)
with  $\langle \text{upd-cs } cs \ (a' \# as') = [] \rangle$  have  $\text{upd-cs } (a' \# cs) \ as' = []$  by simp
with  $\langle cs \neq [] \rangle$  have  $as' \neq []$  by fastforce
from  $IH[OF \ \langle \text{upd-cs } (a' \# cs) \ as' = [] \rangle - \ \text{this} ]$  obtain  $xs \ ys$  where  $as' =$ 
 $xs @ ys$ 
  and  $xs \neq []$  and  $\text{upd-cs } (a' \# cs) \ xs = []$  and  $\text{upd-cs } [] \ ys = []$ 
  and  $\forall xs' \ ys'. \ xs = xs' @ ys' \wedge \ ys' \neq [] \longrightarrow \text{upd-cs } (a' \# cs) \ xs' \neq []$  by blast
from  $\langle \text{upd-cs } (a' \# cs) \ xs = [] \rangle$  Call have  $\text{upd-cs } cs \ (a' \# xs) = []$  by simp
from  $\langle \forall xs' \ ys'. \ xs = xs' @ ys' \wedge \ ys' \neq [] \longrightarrow \text{upd-cs } (a' \# cs) \ xs' \neq [] \rangle$ 
 $\langle xs \neq [] \rangle$   $\langle cs \neq [] \rangle$  Call
have  $\forall xs' \ ys'. \ a' \# xs = xs' @ ys' \wedge \ ys' \neq [] \longrightarrow \text{upd-cs } cs \ xs' \neq []$ 
  by auto (case-tac xs',auto)
with  $\langle as' = xs @ ys \rangle$   $\langle \text{upd-cs } cs \ (a' \# xs) = [] \rangle$   $\langle \text{upd-cs } [] \ ys = [] \rangle$ 
show ?thesis apply (rule-tac x=a' \# xs in exI) by fastforce
next
case (Return Q p f)
with  $\langle \text{upd-cs } cs \ (a' \# as') = [] \rangle$   $\langle cs \neq [] \rangle$  obtain  $c' \ cs'$  where  $cs = c' \# cs'$ 
  and  $\text{upd-cs } cs' \ as' = []$  by (cases cs) auto
show ?thesis
proof (cases cs' = [])
  case True
  with  $\langle cs = c' \# cs' \rangle$   $\langle \text{upd-cs } cs' \ as' = [] \rangle$  Return show ?thesis
  apply (rule-tac x=[a'] in exI) apply clarsimp
  by (case-tac xs') auto
next
case False
with  $\langle \text{upd-cs } cs' \ as' = [] \rangle$  have  $as' \neq []$  by fastforce
from  $IH[OF \ \langle \text{upd-cs } cs' \ as' = [] \rangle \ \text{False} \ \text{this} ]$  obtain  $xs \ ys$  where  $as' =$ 
 $xs @ ys$ 
  and  $xs \neq []$  and  $\text{upd-cs } cs' \ xs = []$  and  $\text{upd-cs } [] \ ys = []$ 
  and  $\forall xs' \ ys'. \ xs = xs' @ ys' \wedge \ ys' \neq [] \longrightarrow \text{upd-cs } cs' \ xs' \neq []$  by blast

```

```

from ⟨upd-cs cs' xs = []⟩ ⟨cs = c'#cs'⟩ Return have upd-cs cs (a'#xs) = []
  by simp
from ⟨ $\forall xs' ys'. xs = xs'@ys' \wedge ys' \neq [] \longrightarrow upd-cs cs' xs' \neq []$ ⟩
  ⟨xs ≠ []⟩ ⟨cs = c'#cs'⟩ Return
have  $\forall xs' ys'. a'#xs = xs'@ys' \wedge ys' \neq [] \longrightarrow upd-cs cs xs' \neq []$ 
  by auto(case-tac xs',auto)
with ⟨as' = xs@ys⟩ ⟨upd-cs cs (a'#xs) = []⟩ ⟨upd-cs [] ys = []⟩
show ?thesis apply(rule-tac x=a'#xs in exI) by fastforce
qed
qed
qed
qed

```

lemma *upd-cs-snoc-Return-Cons*:

```

assumes kind a = Q↔pf
shows upd-cs cs as = c'#cs' ⟹ upd-cs cs (as@[a]) = cs'
proof(induct as arbitrary:cs)
  case Nil
    with ⟨kind a = Q↔pf⟩ have upd-cs cs [a] = cs' by simp
    thus ?case by simp
  next
    case (Cons a' as')
    note IH = (λcs. upd-cs cs as' = c'#cs' ⟹ upd-cs cs (as'@[a]) = cs')
    show ?case
    proof(cases kind a' rule:edge-kind-cases)
      case Intra
        with ⟨upd-cs cs (a'#as') = c'#cs'⟩
        have upd-cs cs as' = c'#cs' by(fastforce simp:intra-kind-def)
        from IH[OF this] have upd-cs cs (as'@[a]) = cs' .
        with Intra show ?thesis by(fastforce simp:intra-kind-def)
      next
        case Call
        with ⟨upd-cs cs (a'#as') = c'#cs'⟩
        have upd-cs (a'#cs) as' = c'#cs' by simp
        from IH[OF this] have upd-cs (a'#cs) (as'@[a]) = cs' .
        with Call show ?thesis by simp
      next
        case Return
        show ?thesis
        proof(cases cs)
          case Nil
            with ⟨upd-cs cs (a'#as') = c'#cs'⟩ Return
            have upd-cs cs as' = c'#cs' by simp
            from IH[OF this] have upd-cs cs (as'@[a]) = cs' .
            with Nil Return show ?thesis by simp
          next
            case (Cons cx csx)
            with ⟨upd-cs cs (a'#as') = c'#cs'⟩ Return

```

```

    have upd-cs csx as' = c'#cs' by simp
    from IH[OF this] have upd-cs csx (as'@[a]) = cs' .
    with Cons Return show ?thesis by simp
  qed
qed
qed

```

```

lemma upd-cs-snoc-Call:
  assumes kind a = Q:r↦pfs
  shows upd-cs cs (as@[a]) = a#(upd-cs cs as)
proof(induct as arbitrary:cs)
  case Nil
  with ⟨kind a = Q:r↦pfs⟩ show ?case by simp
next
  case (Cons a' as')
  note IH = ⟨∧cs. upd-cs cs (as'@[a]) = a#upd-cs cs as'⟩
  show ?case
  proof(cases kind a' rule:edge-kind-cases)
    case Intra
    with IH[of cs] show ?thesis by(fastforce simp:intra-kind-def)
  next
    case Call
    with IH[of a'#cs] show ?thesis by simp
  next
    case Return
    show ?thesis
  proof(cases cs)
    case Nil
    with IH[of []] Return show ?thesis by simp
  next
    case (Cons cx csx)
    with IH[of csx] Return show ?thesis by simp
  qed
qed
qed
qed

```

```

lemma valid-path-aux-split:
  assumes valid-path-aux cs (as@as')
  shows valid-path-aux cs as and valid-path-aux (upd-cs cs as) as'
  using ⟨valid-path-aux cs (as@as')⟩
proof(induct cs as@as' arbitrary:as as' rule:vpa-induct)
  case (vpa-intra cs a as as')
  note IH1 = ⟨∧xs ys. as = xs@ys ⟹ valid-path-aux cs xs⟩
  note IH2 = ⟨∧xs ys. as = xs@ys ⟹ valid-path-aux (upd-cs cs xs) ys⟩

```

```

{ case 1
  from vpa-intra
  have  $as'' = [] \wedge a\#as = as' \vee (\exists xs. a\#xs = as'' \wedge as = xs@as')$ 
    by(simp add:Cons-eq-append-conv)
  thus ?case
  proof
    assume  $as'' = [] \wedge a\#as = as'$ 
    thus ?thesis by simp
  next
    assume  $\exists xs. a\#xs = as'' \wedge as = xs@as'$ 
    then obtain  $xs$  where  $a\#xs = as''$  and  $as = xs@as'$  by auto
    from IH1[OF  $\langle as = xs@as' \rangle$ ] have valid-path-aux cs xs .
    with  $\langle a\#xs = as'' \rangle \langle \text{intra-kind } (kind\ a) \rangle$ 
    show ?thesis by(fastforce simp:intra-kind-def)
  qed
next
case 2
  from vpa-intra
  have  $as'' = [] \wedge a\#as = as' \vee (\exists xs. a\#xs = as'' \wedge as = xs@as')$ 
    by(simp add:Cons-eq-append-conv)
  thus ?case
  proof
    assume  $as'' = [] \wedge a\#as = as'$ 
    hence  $as = []@tl\ as'$  by(cases  $as'$ ) auto
    from IH2[OF this] have valid-path-aux (upd-cs cs []) (tl  $as'$ ) by simp
    with  $\langle as'' = [] \wedge a\#as = as' \rangle \langle \text{intra-kind } (kind\ a) \rangle$ 
    show ?thesis by(fastforce simp:intra-kind-def)
  next
    assume  $\exists xs. a\#xs = as'' \wedge as = xs@as'$ 
    then obtain  $xs$  where  $a\#xs = as''$  and  $as = xs@as'$  by auto
    from IH2[OF  $\langle as = xs@as' \rangle$ ] have valid-path-aux (upd-cs cs xs)  $as'$  .
    from  $\langle a\#xs = as'' \rangle \langle \text{intra-kind } (kind\ a) \rangle$ 
    have upd-cs cs xs = upd-cs cs  $as''$  by(fastforce simp:intra-kind-def)
    with  $\langle \text{valid-path-aux } (upd-cs\ cs\ xs)\ as' \rangle$ 
    show ?thesis by simp
  qed
}
next
case (vpa-Call cs a as Q r p fs  $as''$ )
note IH1 =  $\langle \wedge xs\ ys. as = xs@ys \implies \text{valid-path-aux } (a\#cs)\ xs \rangle$ 
note IH2 =  $\langle \wedge xs\ ys. as = xs@ys \implies \text{valid-path-aux } (upd-cs\ (a\#cs)\ xs)\ ys \rangle$ 
{ case 1
  from vpa-Call
  have  $as'' = [] \wedge a\#as = as' \vee (\exists xs. a\#xs = as'' \wedge as = xs@as')$ 
    by(simp add:Cons-eq-append-conv)
  thus ?case
  proof
    assume  $as'' = [] \wedge a\#as = as'$ 
    thus ?thesis by simp

```

```

next
  assume  $\exists xs. a\#xs = as'' \wedge as = xs@as'$ 
  then obtain  $xs$  where  $a\#xs = as''$  and  $as = xs@as'$  by auto
  from IH1[OF  $\langle as = xs@as' \rangle$ ] have valid-path-aux  $(a\#cs) xs$  .
  with  $\langle a\#xs = as'' \rangle$ [THEN sym]  $\langle kind a = Q:r\leftrightarrow pfs \rangle$ 
  show ?thesis by simp
qed
next
case 2
from vpa-Call
have  $as'' = [] \wedge a\#as = as' \vee (\exists xs. a\#xs = as'' \wedge as = xs@as')$ 
  by(simp add:Cons-eq-append-conv)
thus ?case
proof
  assume  $as'' = [] \wedge a\#as = as'$ 
  hence  $as = []@tl as'$  by(cases  $as'$ ) auto
  from IH2[OF this] have valid-path-aux  $(upd-cs (a\#cs) []) (tl as')$  .
  with  $\langle as'' = [] \wedge a\#as = as' \rangle$   $\langle kind a = Q:r\leftrightarrow pfs \rangle$ 
  show ?thesis by clarsimp
next
  assume  $\exists xs. a\#xs = as'' \wedge as = xs@as'$ 
  then obtain  $xs$  where  $a\#xs = as''$  and  $as = xs@as'$  by auto
  from IH2[OF  $\langle as = xs@as' \rangle$ ] have valid-path-aux  $(upd-cs (a\#cs) xs) as'$  .
  with  $\langle a\#xs = as'' \rangle$ [THEN sym]  $\langle kind a = Q:r\leftrightarrow pfs \rangle$ 
  show ?thesis by simp
qed
}
next
case (vpa-ReturnEmpty  $cs a as Q p f as''$ )
note IH1 =  $\langle \wedge xs ys. as = xs@ys \implies valid-path-aux [] xs \rangle$ 
note IH2 =  $\langle \wedge xs ys. as = xs@ys \implies valid-path-aux (upd-cs [] xs) ys \rangle$ 
{ case 1
  from vpa-ReturnEmpty
  have  $as'' = [] \wedge a\#as = as' \vee (\exists xs. a\#xs = as'' \wedge as = xs@as')$ 
    by(simp add:Cons-eq-append-conv)
  thus ?case
  proof
    assume  $as'' = [] \wedge a\#as = as'$ 
    thus ?thesis by simp
  next
    assume  $\exists xs. a\#xs = as'' \wedge as = xs@as'$ 
    then obtain  $xs$  where  $a\#xs = as''$  and  $as = xs@as'$  by auto
    from IH1[OF  $\langle as = xs@as' \rangle$ ] have valid-path-aux  $[] xs$  .
    with  $\langle a\#xs = as'' \rangle$ [THEN sym]  $\langle kind a = Q\leftrightarrow pf \rangle$   $\langle cs = [] \rangle$ 
    show ?thesis by simp
  qed
}
next
case 2
from vpa-ReturnEmpty

```

```

have as'' = [] ∧ a#as = as' ∨ (∃ xs. a#xs = as'' ∧ as = xs@as')
  by(simp add:Cons-eq-append-conv)
thus ?case
proof
  assume as'' = [] ∧ a#as = as'
  hence as = []@tl as' by(cases as') auto
  from IH2[OF this] have valid-path-aux [] (tl as') by simp
  with ⟨as'' = [] ∧ a#as = as'⟩ ⟨kind a = Q↔pf⟩ ⟨cs = []⟩
  show ?thesis by fastforce
next
  assume ∃ xs. a#xs = as'' ∧ as = xs@as'
  then obtain xs where a#xs = as'' and as = xs@as' by auto
  from IH2[OF ⟨as = xs@as'⟩] have valid-path-aux (upd-cs [] xs) as' .
  from ⟨a#xs = as''⟩[THEN sym] ⟨kind a = Q↔pf⟩ ⟨cs = []⟩
  have upd-cs [] xs = upd-cs cs as'' by simp
  with ⟨valid-path-aux (upd-cs [] xs) as'⟩ show ?thesis by simp
qed
}
next
case (vpa-ReturnCons cs a as Q p f c' cs' as'')
note IH1 = ⟨∧ xs ys. as = xs@ys ⇒ valid-path-aux cs' xs⟩
note IH2 = ⟨∧ xs ys. as = xs@ys ⇒ valid-path-aux (upd-cs cs' xs) ys⟩
{ case 1
  from vpa-ReturnCons
  have as'' = [] ∧ a#as = as' ∨ (∃ xs. a#xs = as'' ∧ as = xs@as')
    by(simp add:Cons-eq-append-conv)
  thus ?case
  proof
    assume as'' = [] ∧ a#as = as'
    thus ?thesis by simp
  next
    assume ∃ xs. a#xs = as'' ∧ as = xs@as'
    then obtain xs where a#xs = as'' and as = xs@as' by auto
    from IH1[OF ⟨as = xs@as'⟩] have valid-path-aux cs' xs .
    with ⟨a#xs = as''⟩[THEN sym] ⟨kind a = Q↔pf⟩ ⟨cs = c'#cs'⟩
      ⟨a ∈ get-return-edges c'⟩
    show ?thesis by simp
  qed
}
next
case 2
  from vpa-ReturnCons
  have as'' = [] ∧ a#as = as' ∨ (∃ xs. a#xs = as'' ∧ as = xs@as')
    by(simp add:Cons-eq-append-conv)
  thus ?case
  proof
    assume as'' = [] ∧ a#as = as'
    hence as = []@tl as' by(cases as') auto
    from IH2[OF this] have valid-path-aux (upd-cs cs' []) (tl as') .
    with ⟨as'' = [] ∧ a#as = as'⟩ ⟨kind a = Q↔pf⟩ ⟨cs = c'#cs'⟩

```



```

      ⟨a ∈ get-return-edges c'⟩
    show ?thesis by fastforce
  next
    assume ∃ xs. a#xs = as'' ∧ as = xs@as'
    then obtain xs where a#xs = as'' and as = xs@as' by auto
    from IH2[OF ⟨as = xs@as'⟩] have valid-path-aux (upd-cs cs' xs) as'.
    from ⟨a#xs = as''⟩[THEN sym] ⟨kind a = Q↔pf'⟩ ⟨cs = c'#cs'⟩
    have upd-cs cs' xs = upd-cs cs as'' by simp
    with ⟨valid-path-aux (upd-cs cs' xs) as'⟩ show ?thesis by simp
  qed
}
qed simp-all

```

lemma *valid-path-aux-Append*:

```

  [[valid-path-aux cs as; valid-path-aux (upd-cs cs as) as]]
  ⇒ valid-path-aux cs (as@as')
by(induct rule:vpa-induct,auto simp:intra-kind-def)

```

lemma *vpa-snoc-Call*:

```

  assumes kind a = Q:r↔pfs
  shows valid-path-aux cs as ⇒ valid-path-aux cs (as@[a])
proof(induct rule:vpa-induct)
  case (vpa-empty cs)
  from ⟨kind a = Q:r↔pfs⟩ have valid-path-aux cs [a] by simp
  thus ?case by simp
next
  case (vpa-intra cs a' as')
  from ⟨valid-path-aux cs (as'@[a])⟩ ⟨intra-kind (kind a')⟩
  have valid-path-aux cs (a'#(as'@[a]))
    by(fastforce simp:intra-kind-def)
  thus ?case by simp
next
  case (vpa-Call cs a' as' Q' r' p' fs')
  from ⟨valid-path-aux (a'#cs) (as'@[a])⟩ ⟨kind a' = Q':r'↔pf's'⟩
  have valid-path-aux cs (a'#(as'@[a])) by simp
  thus ?case by simp
next
  case (vpa-ReturnEmpty cs a' as' Q' p' f')
  from ⟨valid-path-aux [] (as'@[a])⟩ ⟨kind a' = Q'↔pf'⟩ ⟨cs = []⟩
  have valid-path-aux cs (a'#(as'@[a])) by simp
  thus ?case by simp
next
  case (vpa-ReturnCons cs a' as' Q' p' f' c' cs')
  from ⟨valid-path-aux cs' (as'@[a])⟩ ⟨kind a' = Q'↔pf'⟩ ⟨cs = c'#cs'⟩
  ⟨a' ∈ get-return-edges c'⟩
  have valid-path-aux cs (a'#(as'@[a])) by simp
  thus ?case by simp

```

qed

definition *valid-path* :: 'edge list \Rightarrow bool
where *valid-path* as \equiv *valid-path-aux* [] as

lemma *valid-path-aux-valid-path*:
valid-path-aux cs as \implies *valid-path* as
by(*fastforce* *intro:valid-path-aux-callstack-prefix simp:valid-path-def*)

lemma *valid-path-split*:
assumes *valid-path* (as@as') shows *valid-path* as and *valid-path* as'
using (valid-path (as@as'))
apply(*auto simp:valid-path-def*)
apply(*erule valid-path-aux-split*)
apply(*drule valid-path-aux-split(2)*)
by(*fastforce intro:valid-path-aux-callstack-prefix*)

definition *valid-path'* :: 'node \Rightarrow 'edge list \Rightarrow 'node \Rightarrow bool
(- $\longrightarrow_{\sqrt{*}}$ - [51,0,0] 80)
where *vp-def*: $n -as \rightarrow_{\sqrt{*}} n' \equiv n -as \rightarrow^* n' \wedge$ *valid-path* as

lemma *intra-path-vp*:
assumes $n -as \rightarrow_{\iota^*} n'$ shows $n -as \rightarrow_{\sqrt{*}} n'$
proof -
from (n -as $\rightarrow_{\iota^*} n'$) have $n -as \rightarrow^* n'$ and $\forall a \in$ set as. *intra-kind*(*kind* a)
by(*simp-all add:intra-path-def*)
from (forall a in set as. *intra-kind*(*kind* a)) have *valid-path-aux* [] as
by(*rule valid-path-aux-intra-path*)
thus ?thesis using (n -as $\rightarrow^* n'$) by(*simp add:vp-def valid-path-def*)
qed

lemma *vp-split-Cons*:
assumes $n -as \rightarrow_{\sqrt{*}} n'$ and as \neq []
obtains a' as' where as = a'#as' and n = *sourcenode* a'
and *valid-edge* a' and *targetnode* a' -as' $\rightarrow_{\sqrt{*}} n'$
proof(*atomize-elim*)
from (n -as $\rightarrow_{\sqrt{*}} n'$) (as \neq []) obtain a' as' where as = a'#as'
and n = *sourcenode* a' and *valid-edge* a' and *targetnode* a' -as' $\rightarrow^* n'$
by(*fastforce elim:path-split-Cons simp:vp-def*)
from (n -as $\rightarrow_{\sqrt{*}} n'$) have *valid-path* as by(*simp add:vp-def*)
from (as = a'#as') have as = [a']@as' by *simp*
with (valid-path as) have *valid-path* ([a']@as') by *simp*

hence *valid-path* as' **by** (*rule valid-path-split*)
with $\langle \text{targetnode } a' - as' \rightarrow^* n' \rangle$ **have** $\text{targetnode } a' - as' \rightarrow_{\sqrt{*}} n'$ **by** (*simp add:vp-def*)
with $\langle as = a' \# as' \rangle \langle n = \text{sourcenode } a' \rangle \langle \text{valid-edge } a' \rangle$
show $\exists a' as'. as = a' \# as' \wedge n = \text{sourcenode } a' \wedge \text{valid-edge } a' \wedge$
 $\text{targetnode } a' - as' \rightarrow_{\sqrt{*}} n'$ **by** *blast*

qed

lemma *vp-split-snoc*:

assumes $n - as \rightarrow_{\sqrt{*}} n'$ **and** $as \neq []$
obtains $a' as'$ **where** $as = as'@[a']$ **and** $n - as' \rightarrow_{\sqrt{*}} \text{sourcenode } a'$
and *valid-edge* a' **and** $n' = \text{targetnode } a'$

proof (*atomize-elim*)

from $\langle n - as \rightarrow_{\sqrt{*}} n' \rangle \langle as \neq [] \rangle$ **obtain** $a' as'$ **where** $as = as'@[a']$
and $n - as' \rightarrow^* \text{sourcenode } a'$ **and** *valid-edge* a' **and** $n' = \text{targetnode } a'$
by (*clarsimp simp:vp-def*) (*erule path-split-snoc,auto*)
from $\langle n - as \rightarrow_{\sqrt{*}} n' \rangle \langle as = as'@[a'] \rangle$ **have** *valid-path* $(as'@[a'])$ **by** (*simp add:vp-def*)
hence *valid-path* as' **by** (*rule valid-path-split*)
with $\langle n - as' \rightarrow^* \text{sourcenode } a' \rangle$ **have** $n - as' \rightarrow_{\sqrt{*}} \text{sourcenode } a'$ **by** (*simp add:vp-def*)
with $\langle as = as'@[a'] \rangle \langle \text{valid-edge } a' \rangle \langle n' = \text{targetnode } a' \rangle$
show $\exists a' a'. as = as'@[a'] \wedge n - as' \rightarrow_{\sqrt{*}} \text{sourcenode } a' \wedge \text{valid-edge } a' \wedge$
 $n' = \text{targetnode } a'$

by *blast*

qed

lemma *vp-split*:

assumes $n - as@a\#as' \rightarrow_{\sqrt{*}} n'$
shows $n - as \rightarrow_{\sqrt{*}} \text{sourcenode } a$ **and** *valid-edge* a **and** $\text{targetnode } a - as' \rightarrow_{\sqrt{*}} n'$

proof –

from $\langle n - as@a\#as' \rightarrow_{\sqrt{*}} n' \rangle$ **have** $n - as \rightarrow^* \text{sourcenode } a$ **and** *valid-edge* a
and $\text{targetnode } a - as' \rightarrow^* n'$
by (*auto intro:path-split simp:vp-def*)
from $\langle n - as@a\#as' \rightarrow_{\sqrt{*}} n' \rangle$ **have** *valid-path* $(as@a\#as')$ **by** (*simp add:vp-def*)
hence *valid-path* as **and** *valid-path* $(a\#as')$ **by** (*auto intro:valid-path-split*)
from $\langle \text{valid-path } (a\#as') \rangle$ **have** *valid-path* $([a]@as')$ **by** *simp*
hence *valid-path* as' **by** (*rule valid-path-split*)
with $\langle n - as \rightarrow^* \text{sourcenode } a \rangle \langle \text{valid-path } as \rangle \langle \text{valid-edge } a \rangle \langle \text{targetnode } a - as' \rightarrow^*$
 $n' \rangle$
show $n - as \rightarrow_{\sqrt{*}} \text{sourcenode } a$ *valid-edge* a $\text{targetnode } a - as' \rightarrow_{\sqrt{*}} n'$
by (*auto simp:vp-def*)

qed

lemma *vp-split-second*:

assumes $n - as@a\#as' \rightarrow_{\sqrt{*}} n'$ **shows** $\text{sourcenode } a - a\#as' \rightarrow_{\sqrt{*}} n'$

proof –

from $\langle n - as@a\#as' \rightarrow_{\sqrt{*}} n' \rangle$ **have** $\text{sourcenode } a - a\#as' \rightarrow^* n'$
by (*fastforce elim:path-split-second simp:vp-def*)
from $\langle n - as@a\#as' \rightarrow_{\sqrt{*}} n' \rangle$ **have** *valid-path* $(as@a\#as')$ **by** (*simp add:vp-def*)
hence *valid-path* $(a\#as')$ **by** (*rule valid-path-split*)
with $\langle \text{sourcenode } a - a\#as' \rightarrow^* n' \rangle$ **show** *?thesis* **by** (*simp add:vp-def*)

qed

```

function valid-path-rev-aux :: 'edge list  $\Rightarrow$  'edge list  $\Rightarrow$  bool
  where valid-path-rev-aux cs []  $\longleftrightarrow$  True
  | valid-path-rev-aux cs (as@[a])  $\longleftrightarrow$ 
    (case (kind a) of Q $\leftrightarrow$ pf  $\Rightarrow$  valid-path-rev-aux (a#cs) as
      | Q:r $\hookrightarrow$ pfs  $\Rightarrow$  case cs of []  $\Rightarrow$  valid-path-rev-aux [] as
        | c'#cs'  $\Rightarrow$  c'  $\in$  get-return-edges a  $\wedge$ 
          valid-path-rev-aux cs' as
      | -  $\Rightarrow$  valid-path-rev-aux cs as)
by auto(case-tac b rule:rev-cases,auto)
termination by lexicographic-order

```

```

lemma vpra-induct [consumes 1,case-names vpra-empty vpra-intra vpra-Return
  vpra-CallEmpty vpra-CallCons]:
  assumes major: valid-path-rev-aux xs ys
  and rules:  $\bigwedge$ cs. P cs []
     $\bigwedge$ cs a as. [[intra-kind(kind a); valid-path-rev-aux cs as; P cs as]
       $\Rightarrow$  P cs (as@[a])]
     $\bigwedge$ cs a as Q p f. [[kind a = Q $\leftrightarrow$ pf; valid-path-rev-aux (a#cs) as; P (a#cs) as]
       $\Rightarrow$  P cs (as@[a])]
     $\bigwedge$ cs a as Q r p fs. [[kind a = Q:r $\hookrightarrow$ pfs; cs = []; valid-path-rev-aux [] as;
      P [] as]  $\Rightarrow$  P cs (as@[a])]
     $\bigwedge$ cs a as Q r p fs c' cs'. [[kind a = Q:r $\hookrightarrow$ pfs; cs = c'#cs';
      valid-path-rev-aux cs' as; c'  $\in$  get-return-edges a; P cs' as]
       $\Rightarrow$  P cs (as@[a])]
  shows P xs ys
using major
apply(induct ys arbitrary:xs rule:rev-induct)
by(auto intro:rules split:edge-kind.split-asm list.split-asm simp:intra-kind-def)

```

```

lemma vpra-callstack-prefix:
  valid-path-rev-aux (cs@cs') as  $\Rightarrow$  valid-path-rev-aux cs as
proof(induct cs@cs' as arbitrary:cs cs' rule:vpra-induct)
  case vpra-empty thus ?case by simp
next
  case (vpra-intra a as)
  hence valid-path-rev-aux cs as by simp
  with <intra-kind (kind a)> show ?case by(fastforce simp:intra-kind-def)
next
  case (vpra-Return a as Q p f)
  note IH = < $\bigwedge$ ds ds'. a#cs@cs' = ds@ds'  $\Rightarrow$  valid-path-rev-aux ds as>

```

have $a\#cs@cs' = (a\#cs)@cs'$ **by** *simp*
from $IH[OF\ this]$ **have** *valid-path-rev-aux* $(a\#cs)$ **as** .
with $\langle kind\ a = Q \leftrightarrow_{pf} \rangle$ **show** *?case* **by** *simp*
next
case $(vpra-CallEmpty\ a\ as\ Q\ r\ p\ fs)$
hence *valid-path-rev-aux* cs **as** **by** *simp*
with $\langle kind\ a = Q:r \hookrightarrow_{pfs} \rangle$ $\langle cs@cs' = [] \rangle$ **show** *?case* **by** *simp*
next
case $(vpra-CallCons\ a\ as\ Q\ r\ p\ fs\ c'\ csx)$
note $IH = \langle \bigwedge cs\ cs'.\ csx = cs@cs' \implies valid-path-rev-aux\ cs\ as \rangle$
from $\langle cs@cs' = c'\#csx \rangle$
have $(cs = [] \wedge cs' = c'\#csx) \vee (\exists zs.\ cs = c'\#zs \wedge zs@cs' = csx)$
by $(simp\ add:append-eq-Cons-conv)$
thus *?case*
proof
assume $cs = [] \wedge cs' = c'\#csx$
hence $cs = []$ **and** $cs' = c'\#csx$ **by** *simp-all*
from $\langle cs' = c'\#csx \rangle$ **have** $csx = []@tl\ cs'$ **by** *simp*
from $IH[OF\ this]$ **have** *valid-path-rev-aux* $[]$ **as** .
with $\langle cs = [] \rangle$ $\langle kind\ a = Q:r \hookrightarrow_{pfs} \rangle$ **show** *?thesis* **by** *simp*
next
assume $\exists zs.\ cs = c'\#zs \wedge zs@cs' = csx$
then obtain zs **where** $cs = c'\#zs$ **and** $csx = zs@cs'$ **by** *auto*
from $IH[OF\ \langle csx = zs@cs' \rangle]$ **have** *valid-path-rev-aux* zs **as** .
with $\langle cs = c'\#zs \rangle$ $\langle kind\ a = Q:r \hookrightarrow_{pfs} \rangle$ $\langle c' \in get-return-edges\ a \rangle$ **show** *?thesis*
by *simp*
qed
qed

function *upd-rev-cs* $:: 'edge\ list \Rightarrow 'edge\ list \Rightarrow 'edge\ list$
where *upd-rev-cs* $cs\ [] = cs$
| *upd-rev-cs* $cs\ (as@[a]) =$
 $(case\ (kind\ a)\ of\ Q \leftrightarrow_{pf} \Rightarrow upd-rev-cs\ (a\#cs)\ as$
 $\quad | Q:r \hookrightarrow_{pfs} \Rightarrow case\ cs\ of\ [] \Rightarrow upd-rev-cs\ cs\ as$
 $\quad \quad | c'\#cs' \Rightarrow upd-rev-cs\ cs'\ as$
 $\quad | - \Rightarrow upd-rev-cs\ cs\ as)$
by *auto(case-tac\ b\ rule:rev-cases,auto)*
termination **by** *lexicographic-order*

lemma *upd-rev-cs-empty* $[dest]:$
 $upd-rev-cs\ cs\ [] = [] \implies cs = []$
by $(cases\ cs)\ auto$

lemma *valid-path-rev-aux-split*:
assumes *valid-path-rev-aux* $cs\ (as@as')$

shows $\text{valid-path-rev-aux } cs \ as' \ \mathbf{and} \ \text{valid-path-rev-aux } (\text{upd-rev-cs } cs \ as') \ as$
using $\langle \text{valid-path-rev-aux } cs \ (as@as') \rangle$
proof $(\text{induct } cs \ as@as' \ \text{arbitrary:} as \ as' \ \text{rule:} vpra\text{-induct})$
case $(vpra\text{-intra } cs \ a \ as \ as')$
note $IH1 = \langle \bigwedge xs \ ys. \ as = xs@ys \implies \text{valid-path-rev-aux } cs \ ys \rangle$
note $IH2 = \langle \bigwedge xs \ ys. \ as = xs@ys \implies \text{valid-path-rev-aux } (\text{upd-rev-cs } cs \ ys) \ xs \rangle$
{ case 1
from $vpra\text{-intra}$
have $as' = [] \wedge as@[a] = as'' \vee (\exists xs. \ as = as''@xs \wedge xs@[a] = as')$
by $(\text{cases } as' \ \text{rule:} rev\text{-cases}) \ \text{auto}$
thus $?case$
proof
assume $as' = [] \wedge as@[a] = as''$
thus $?thesis \ \text{by } simp$
next
assume $\exists xs. \ as = as''@xs \wedge xs@[a] = as'$
then obtain xs **where** $as = as''@xs$ **and** $xs@[a] = as'$ **by** $auto$
from $IH1[OF \ \langle as = as''@xs \rangle]$ **have** $\text{valid-path-rev-aux } cs \ xs$.
with $\langle xs@[a] = as' \rangle \langle \text{intra-kind } (kind \ a) \rangle$
show $?thesis \ \text{by}(\text{fastforce } simp:intra\text{-kind-def})$
qed
next
case 2
from $vpra\text{-intra}$
have $as' = [] \wedge as@[a] = as'' \vee (\exists xs. \ as = as''@xs \wedge xs@[a] = as')$
by $(\text{cases } as' \ \text{rule:} rev\text{-cases}) \ \text{auto}$
thus $?case$
proof
assume $as' = [] \wedge as@[a] = as''$
hence $as = \text{butlast } as''@[]$ **by** $(\text{cases } as) \ \text{auto}$
from $IH2[OF \ \text{this}]$ **have** $\text{valid-path-rev-aux } (\text{upd-rev-cs } cs \ []) \ (\text{butlast } as'')$.
with $\langle as' = [] \wedge as@[a] = as'' \rangle \langle \text{intra-kind } (kind \ a) \rangle$
show $?thesis \ \text{by}(\text{fastforce } simp:intra\text{-kind-def})$
next
assume $\exists xs. \ as = as''@xs \wedge xs@[a] = as'$
then obtain xs **where** $as = as''@xs$ **and** $xs@[a] = as'$ **by** $auto$
from $IH2[OF \ \langle as = as''@xs \rangle]$ **have** $\text{valid-path-rev-aux } (\text{upd-rev-cs } cs \ xs) \ as''$
from $\langle xs@[a] = as' \rangle \langle \text{intra-kind } (kind \ a) \rangle$
have $\text{upd-rev-cs } cs \ xs = \text{upd-rev-cs } cs \ as'$ **by** $(\text{fastforce } simp:intra\text{-kind-def})$
with $\langle \text{valid-path-rev-aux } (\text{upd-rev-cs } cs \ xs) \ as'' \rangle$
show $?thesis \ \text{by } simp$
qed
}
next
case $(vpra\text{-Return } cs \ a \ as \ Q \ p \ f \ as')$
note $IH1 = \langle \bigwedge xs \ ys. \ as = xs@ys \implies \text{valid-path-rev-aux } (a\#cs) \ ys \rangle$
note $IH2 = \langle \bigwedge xs \ ys. \ as = xs@ys \implies \text{valid-path-rev-aux } (\text{upd-rev-cs } (a\#cs) \ ys) \ xs \rangle$

```

{ case 1
  from vpra-Return
  have  $as' = [] \wedge as@[a] = as'' \vee (\exists xs. as = as''@xs \wedge xs@[a] = as')$ 
    by(cases as' rule:rev-cases) auto
  thus ?case
  proof
    assume  $as' = [] \wedge as@[a] = as''$ 
    thus ?thesis by simp
  next
    assume  $\exists xs. as = as''@xs \wedge xs@[a] = as'$ 
    then obtain xs where  $as = as''@xs$  and  $xs@[a] = as'$  by auto
    from IH1[OF  $\langle as = as''@xs \rangle$ ] have valid-path-rev-aux (a#cs) xs .
    with  $\langle xs@[a] = as' \rangle \langle kind\ a = Q \leftrightarrow pf \rangle$ 
    show ?thesis by fastforce
  qed
next
case 2
  from vpra-Return
  have  $as' = [] \wedge as@[a] = as'' \vee (\exists xs. as = as''@xs \wedge xs@[a] = as')$ 
    by(cases as' rule:rev-cases) auto
  thus ?case
  proof
    assume  $as' = [] \wedge as@[a] = as''$ 
    hence  $as = butlast\ as''@[]$  by(cases as) auto
    from IH2[OF this]
    have valid-path-rev-aux (upd-rev-cs (a#cs) []) (butlast as'') .
    with  $\langle as' = [] \wedge as@[a] = as'' \rangle \langle kind\ a = Q \leftrightarrow pf \rangle$ 
    show ?thesis by fastforce
  next
    assume  $\exists xs. as = as''@xs \wedge xs@[a] = as'$ 
    then obtain xs where  $as = as''@xs$  and  $xs@[a] = as'$  by auto
    from IH2[OF  $\langle as = as''@xs \rangle$ ]
    have valid-path-rev-aux (upd-rev-cs (a#cs) xs) as'' .
    from  $\langle xs@[a] = as' \rangle \langle kind\ a = Q \leftrightarrow pf \rangle$ 
    have upd-rev-cs (a#cs) xs = upd-rev-cs cs as' by fastforce
    with  $\langle valid-path-rev-aux (upd-rev-cs (a#cs) xs) as'' \rangle$ 
    show ?thesis by simp
  qed
}
next
case (vpra-CallEmpty cs a as Q r p fs as'')
  note IH1 =  $\langle \wedge xs\ ys. as = xs@ys \implies valid-path-rev-aux [] ys \rangle$ 
  note IH2 =  $\langle \wedge xs\ ys. as = xs@ys \implies valid-path-rev-aux (upd-rev-cs [] ys) xs \rangle$ 
  { case 1
    from vpra-CallEmpty
    have  $as' = [] \wedge as@[a] = as'' \vee (\exists xs. as = as''@xs \wedge xs@[a] = as')$ 
      by(cases as' rule:rev-cases) auto
    thus ?case
    proof

```

```

    assume  $as' = [] \wedge as@[a] = as''$ 
    thus ?thesis by simp
  next
    assume  $\exists xs. as = as''@xs \wedge xs@[a] = as'$ 
    then obtain  $xs$  where  $as = as''@xs$  and  $xs@[a] = as'$  by auto
    from IH1[OF  $\langle as = as''@xs \rangle$ ] have valid-path-rev-aux  $[] xs$  .
    with  $\langle xs@[a] = as' \rangle$   $\langle kind\ a = Q:r \hookrightarrow pfs \rangle$   $\langle cs = [] \rangle$ 
    show ?thesis by fastforce
  qed
next
case 2
from vpra-CallEmpty
have  $as' = [] \wedge as@[a] = as'' \vee (\exists xs. as = as''@xs \wedge xs@[a] = as')$ 
  by(cases  $as'$  rule:rev-cases) auto
thus ?case
proof
  assume  $as' = [] \wedge as@[a] = as''$ 
  hence  $as = butlast\ as''@[]$  by(cases  $as$ ) auto
  from IH2[OF this]
  have valid-path-rev-aux (upd-rev-cs  $[] []$ ) (butlast  $as''$ ) .
  with  $\langle as' = [] \wedge as@[a] = as'' \rangle$   $\langle kind\ a = Q:r \hookrightarrow pfs \rangle$   $\langle cs = [] \rangle$ 
  show ?thesis by fastforce
next
  assume  $\exists xs. as = as''@xs \wedge xs@[a] = as'$ 
  then obtain  $xs$  where  $as = as''@xs$  and  $xs@[a] = as'$  by auto
  from IH2[OF  $\langle as = as''@xs \rangle$ ]
  have valid-path-rev-aux (upd-rev-cs  $[] xs$ )  $as''$  .
  with  $\langle xs@[a] = as' \rangle$   $\langle kind\ a = Q:r \hookrightarrow pfs \rangle$   $\langle cs = [] \rangle$ 
  show ?thesis by fastforce
qed
}
next
case (vpra-CallCons  $cs\ a\ as\ Q\ r\ p\ fs\ c'\ cs'\ as''$ )
note IH1 =  $\langle \wedge xs\ ys. as = xs@ys \implies valid-path-rev-aux\ cs'\ ys \rangle$ 
note IH2 =  $\langle \wedge xs\ ys. as = xs@ys \implies valid-path-rev-aux\ (upd-rev-cs\ cs'\ ys)\ xs \rangle$ 
{ case 1
  from vpra-CallCons
  have  $as' = [] \wedge as@[a] = as'' \vee (\exists xs. as = as''@xs \wedge xs@[a] = as')$ 
    by(cases  $as'$  rule:rev-cases) auto
  thus ?case
  proof
    assume  $as' = [] \wedge as@[a] = as''$ 
    thus ?thesis by simp
  next
    assume  $\exists xs. as = as''@xs \wedge xs@[a] = as'$ 
    then obtain  $xs$  where  $as = as''@xs$  and  $xs@[a] = as'$  by auto
    from IH1[OF  $\langle as = as''@xs \rangle$ ] have valid-path-rev-aux  $cs'\ xs$  .
    with  $\langle xs@[a] = as' \rangle$   $\langle kind\ a = Q:r \hookrightarrow pfs \rangle$   $\langle cs = c' \# cs' \rangle$   $\langle c' \in get-return-edges$ 

```

a)


```

    show ?thesis by fastforce
  qed
next
case 2
from vpra-CallCons
have  $as' = [] \wedge as@[a] = as'' \vee (\exists xs. as = as''@xs \wedge xs@[a] = as')$ 
  by(cases as' rule:rev-cases) auto
thus ?case
proof
  assume  $as' = [] \wedge as@[a] = as''$ 
  hence  $as = butlast as''@[]$  by(cases as) auto
  from IH2[OF this]
  have valid-path-rev-aux (upd-rev-cs cs' []) (butlast as'') .
  with  $\langle as' = [] \wedge as@[a] = as'' \rangle \langle kind\ a = Q:r \leftrightarrow pfs \rangle \langle cs = c' \# cs' \rangle$ 
     $\langle c' \in get\ return\ edges\ a \rangle$  show ?thesis by fastforce
next
  assume  $\exists xs. as = as''@xs \wedge xs@[a] = as'$ 
  then obtain xs where  $as = as''@xs$  and  $xs@[a] = as'$  by auto
  from IH2[OF  $\langle as = as''@xs \rangle$ ]
  have valid-path-rev-aux (upd-rev-cs cs' xs) as'' .
  with  $\langle xs@[a] = as' \rangle \langle kind\ a = Q:r \leftrightarrow pfs \rangle \langle cs = c' \# cs' \rangle$ 
     $\langle c' \in get\ return\ edges\ a \rangle$ 
  show ?thesis by fastforce
qed
}
qed simp-all

lemma valid-path-rev-aux-Append:
   $\llbracket valid\ path\ rev\ aux\ cs\ as';\ valid\ path\ rev\ aux\ (upd\ rev\ cs\ cs\ as')\ as \rrbracket$ 
 $\implies valid\ path\ rev\ aux\ cs\ (as@as')$ 
by(induct rule:vpra-induct,
  auto simp:intra-kind-def simp del:append-assoc simp:append-assoc[THEN sym])

lemma vpra-Cons-intra:
  assumes intra-kind(kind a)
  shows valid-path-rev-aux cs as  $\implies valid\ path\ rev\ aux\ cs\ (a\#as)$ 
proof(induct rule:vpra-induct)
  case (vpra-empty cs)
  have valid-path-rev-aux cs [] by simp
  with  $\langle intra\ kind\ (kind\ a) \rangle$  have valid-path-rev-aux cs ([a])
    by(simp only:valid-path-rev-aux.simps intra-kind-def,fastforce)
  thus ?case by simp
qed(simp only:append-Cons[THEN sym] valid-path-rev-aux.simps intra-kind-def,fastforce)+

lemma vpra-Cons-Return:
  assumes kind a =  $Q \leftrightarrow pf$ 

```

```

shows valid-path-rev-aux cs as  $\implies$  valid-path-rev-aux cs (a#as)
proof(induct rule:vpra-induct)
  case (vpra-empty cs)
  from  $\langle \text{kind } a = Q \leftrightarrow_p f \rangle$  have valid-path-rev-aux cs ( $[] @ [a]$ )
    by(simp only:valid-path-rev-aux.simps,clarsimp)
  thus ?case by simp
next
  case (vpra-intra cs a' as')
  from  $\langle \text{valid-path-rev-aux cs (a\#as')} \rangle$   $\langle \text{intra-kind (kind a')} \rangle$ 
  have valid-path-rev-aux cs ( $(a\#as') @ [a']$ )
    by(simp only:valid-path-rev-aux.simps,fastforce simp:intra-kind-def)
  thus ?case by simp
next
  case (vpra-Return cs a' as' Q' p' f')
  from  $\langle \text{valid-path-rev-aux (a'\#cs) (a\#as')} \rangle$   $\langle \text{kind } a' = Q' \leftrightarrow_{p'} f' \rangle$ 
  have valid-path-rev-aux cs ( $(a\#as') @ [a']$ )
    by(simp only:valid-path-rev-aux.simps,clarsimp)
  thus ?case by simp
next
  case (vpra-CallEmpty cs a' as' Q' r' p' fs')
  from  $\langle \text{valid-path-rev-aux } [] \text{ (a\#as')} \rangle$   $\langle \text{kind } a' = Q':r' \leftrightarrow_{p'} fs' \rangle$   $\langle cs = [] \rangle$ 
  have valid-path-rev-aux cs ( $(a\#as') @ [a']$ )
    by(simp only:valid-path-rev-aux.simps,clarsimp)
  thus ?case by simp
next
  case (vpra-CallCons cs a' as' Q' r' p' fs' c' cs')
  from  $\langle \text{valid-path-rev-aux cs' (a\#as')} \rangle$   $\langle \text{kind } a' = Q':r' \leftrightarrow_{p'} fs' \rangle$   $\langle cs = c' \# cs' \rangle$ 
   $\langle c' \in \text{get-return-edges } a' \rangle$ 
  have valid-path-rev-aux cs ( $(a\#as') @ [a']$ )
    by(simp only:valid-path-rev-aux.simps,clarsimp)
  thus ?case by simp
qed

```

lemma *upd-rev-cs-Cons-intra*:

```

assumes intra-kind(kind a) shows upd-rev-cs cs (a#as) = upd-rev-cs cs as
proof(induct as arbitrary:cs rule:rev-induct)
  case Nil
  from  $\langle \text{intra-kind (kind a)} \rangle$ 
  have upd-rev-cs cs ( $[] @ [a]$ ) = upd-rev-cs cs  $[]$ 
    by(simp only:upd-rev-cs.simps,auto simp:intra-kind-def)
  thus ?case by simp
next
  case (snoc a' as')
  note  $IH = \langle \bigwedge cs. \text{upd-rev-cs cs (a\#as')} = \text{upd-rev-cs cs as'} \rangle$ 
  show ?case
  proof(cases kind a' rule:edge-kind-cases)
    case Intra
    from  $IH$  have upd-rev-cs cs (a\#as') = upd-rev-cs cs as' .

```

```

with Intra have upd-rev-cs cs ((a#as')@[a']) = upd-rev-cs cs (as'@[a'])
  by(fastforce simp:intra-kind-def)
thus ?thesis by simp
next
  case Return
  from IH have upd-rev-cs (a'#cs) (a#as') = upd-rev-cs (a'#cs) as' .
  with Return have upd-rev-cs cs ((a#as')@[a']) = upd-rev-cs cs (as'@[a'])
    by(auto simp:intra-kind-def)
  thus ?thesis by simp
next
  case Call
  show ?thesis
  proof(cases cs)
    case Nil
    from IH have upd-rev-cs [] (a#as') = upd-rev-cs [] as' .
    with Call Nil have upd-rev-cs cs ((a#as')@[a']) = upd-rev-cs cs (as'@[a'])
      by(auto simp:intra-kind-def)
    thus ?thesis by simp
  next
    case (Cons c' cs')
    from IH have upd-rev-cs cs' (a#as') = upd-rev-cs cs' as' .
    with Call Cons have upd-rev-cs cs ((a#as')@[a']) = upd-rev-cs cs (as'@[a'])
      by(auto simp:intra-kind-def)
    thus ?thesis by simp
  qed
qed
qed

```

lemma *upd-rev-cs-Cons-Return*:

```

assumes kind a = Q↔pf shows upd-rev-cs cs (a#as) = a#(upd-rev-cs cs as)
proof(induct as arbitrary:cs rule:rev-induct)
  case Nil
  with (kind a = Q↔pf) have upd-rev-cs cs ([]@[a]) = a#(upd-rev-cs cs [])
    by(simp only:upd-rev-cs.simps) clarsimp
  thus ?case by simp
next
  case (snoc a' as')
  note IH = (∧cs. upd-rev-cs cs (a#as') = a#upd-rev-cs cs as')
  show ?case
  proof(cases kind a' rule:edge-kind-cases)
    case Intra
    from IH have upd-rev-cs cs (a#as') = a#(upd-rev-cs cs as') .
    with Intra have upd-rev-cs cs ((a#as')@[a']) = a#(upd-rev-cs cs (as'@[a']))
      by(fastforce simp:intra-kind-def)
    thus ?thesis by simp
  next
  case Return

```

```

from IH have upd-rev-cs (a'#cs) (a#as') = a#(upd-rev-cs (a'#cs) as') .
with Return have upd-rev-cs cs ((a#as')@[a']) = a#(upd-rev-cs cs (as'@[a']))
```

by(*auto simp:intra-kind-def*)

thus *?thesis* **by** *simp*

next

case *Call*

show *?thesis*

proof(*cases cs*)

case *Nil*

from *IH* **have** *upd-rev-cs* [] (*a#as'*) = *a#(upd-rev-cs* [] *as'*) .

with *Call Nil* **have** *upd-rev-cs* *cs* ((*a#as'*)@[*a'*]) = *a#(upd-rev-cs* *cs* (*as'*@[*a'*]))

by(*auto simp:intra-kind-def*)

thus *?thesis* **by** *simp*

next

case (*Cons c' cs'*)

from *IH* **have** *upd-rev-cs* *cs'* (*a#as'*) = *a#(upd-rev-cs* *cs'* *as'*) .

with *Call Cons*

have *upd-rev-cs* *cs* ((*a#as'*)@[*a'*]) = *a#(upd-rev-cs* *cs* (*as'*@[*a'*]))

by(*auto simp:intra-kind-def*)

thus *?thesis* **by** *simp*

qed

qed

qed

lemma *upd-rev-cs-Cons-Call-Cons*:

```

assumes kind a = Q:r↔pfs
```

shows *upd-rev-cs* *cs* *as* = *c'#cs'* \implies *upd-rev-cs* *cs* (*a#as*) = *cs'*

proof(*induct as arbitrary:cs rule:rev-induct*)

case *Nil*

with (*kind a = Q:r↔pfs*) **have** *upd-rev-cs* *cs* ([]@[*a*]) = *cs'*

by(*simp only:upd-rev-cs.simps*) *clarsimp*

thus *?case* **by** *simp*

next

case (*snoc a' as'*)

note *IH* = ($\bigwedge cs. \text{upd-rev-cs } cs \text{ } as' = c'\#cs' \implies \text{upd-rev-cs } cs \text{ } (a\#as') = cs'$)

show *?case*

proof(*cases kind a' rule:edge-kind-cases*)

case *Intra*

with (*upd-rev-cs* *cs* (*as'*@[*a'*]) = *c'#cs'*)

have *upd-rev-cs* *cs* *as'* = *c'#cs'* **by**(*fastforce simp:intra-kind-def*)

from *IH*[*OF this*] **have** *upd-rev-cs* *cs* (*a#as'*) = *cs'* .

with *Intra* **show** *?thesis* **by**(*fastforce simp:intra-kind-def*)

next

case *Return*

with (*upd-rev-cs* *cs* (*as'*@[*a'*]) = *c'#cs'*)

have *upd-rev-cs* (*a'#cs*) *as'* = *c'#cs'* **by** *simp*

from *IH*[*OF this*] **have** *upd-rev-cs* (*a'#cs*) (*a#as'*) = *cs'* .

with *Return* **show** *?thesis* **by** *simp*

```

next
  case Call
  show ?thesis
  proof(cases cs)
    case Nil
    with ⟨upd-rev-cs cs (as'@[a']) = c'#cs'⟩ Call
    have upd-rev-cs cs as' = c'#cs' by simp
    from IH[OF this] have upd-rev-cs cs (a#as') = cs' .
    with Nil Call show ?thesis by simp
  next
  case (Cons cx csx)
  with ⟨upd-rev-cs cs (as'@[a']) = c'#cs'⟩ Call
  have upd-rev-cs csx as' = c'#cs' by simp
  from IH[OF this] have upd-rev-cs csx (a#as') = cs' .
  with Cons Call show ?thesis by simp
qed
qed
qed

```

lemma *upd-rev-cs-Cons-Call-Cons-Empty*:

```

  assumes kind a = Q:r↔pfs
  shows upd-rev-cs cs as = [] ⟹ upd-rev-cs cs (a#as) = []
  proof(induct as arbitrary:cs rule:rev-induct)
    case Nil
    with ⟨kind a = Q:r↔pfs⟩ have upd-rev-cs cs ([]@[a]) = []
      by(simp only:upd-rev-cs.simps) clarsimp
    thus ?case by simp
  next
  case (snoc a' as')
  note IH = ⟨∧cs. upd-rev-cs cs as' = [] ⟹ upd-rev-cs cs (a#as') = []⟩
  show ?case
  proof(cases kind a' rule:edge-kind-cases)
    case Intra
    with ⟨upd-rev-cs cs (as'@[a']) = []⟩
    have upd-rev-cs cs as' = [] by(fastforce simp:intra-kind-def)
    from IH[OF this] have upd-rev-cs cs (a#as') = [] .
    with Intra show ?thesis by(fastforce simp:intra-kind-def)
  next
  case Return
  with ⟨upd-rev-cs cs (as'@[a']) = []⟩
  have upd-rev-cs (a'#cs) as' = [] by simp
  from IH[OF this] have upd-rev-cs (a'#cs) (a#as') = [] .
  with Return show ?thesis by simp
  next
  case Call
  show ?thesis
  proof(cases cs)
    case Nil

```

```

with ⟨upd-rev-cs cs (as'@[a']) = []⟩ Call
have upd-rev-cs cs as' = [] by simp
from IH[OF this] have upd-rev-cs cs (a#as') = [] .
with Nil Call show ?thesis by simp
next
case (Cons cx csx)
with ⟨upd-rev-cs cs (as'@[a']) = []⟩ Call
have upd-rev-cs csx as' = [] by simp
from IH[OF this] have upd-rev-cs csx (a#as') = [] .
with Cons Call show ?thesis by simp
qed
qed
qed

```

definition *valid-call-list* :: 'edge list \Rightarrow 'node \Rightarrow bool
where *valid-call-list* cs n \equiv
 $\forall cs' c cs''. cs = cs'@c\#cs'' \longrightarrow (valid-edge c \wedge (\exists Q r p fs. (kind c = Q:r\hookrightarrow pfs) \wedge$
 $p = get-proc (case cs' of [] \Rightarrow n \mid - \Rightarrow last (sourcenodes cs'))))$

definition *valid-return-list* :: 'edge list \Rightarrow 'node \Rightarrow bool
where *valid-return-list* cs n \equiv
 $\forall cs' c cs''. cs = cs'@c\#cs'' \longrightarrow (valid-edge c \wedge (\exists Q p f. (kind c = Q\leftarrow pf) \wedge$
 $p = get-proc (case cs' of [] \Rightarrow n \mid - \Rightarrow last (targetnodes cs'))))$

lemma *valid-call-list-valid-edges*:
assumes *valid-call-list* cs n **shows** $\forall c \in set cs. valid-edge c$
proof –
from ⟨*valid-call-list* cs n⟩
have $\forall cs' c cs''. cs = cs'@c\#cs'' \longrightarrow valid-edge c$
by (simp add: *valid-call-list-def*)
thus ?thesis
proof (induct cs)
case Nil **thus** ?case by simp
next
case (Cons cx csx)
note IH = $\langle \forall cs' c cs''. csx = cs'@c\#cs'' \longrightarrow valid-edge c \implies$
 $\forall a \in set csx. valid-edge a \rangle$
from $\langle \forall cs' c cs''. cx\#csx = cs'@c\#cs'' \longrightarrow valid-edge c \rangle$
have *valid-edge* cx **by** blast
from $\langle \forall cs' c cs''. cx\#csx = cs'@c\#cs'' \longrightarrow valid-edge c \rangle$
have $\forall cs' c cs''. csx = cs'@c\#cs'' \longrightarrow valid-edge c$
by auto (erule-tac x=cx#cs' in allE, auto)
from IH[OF this] ⟨*valid-edge* cx⟩ **show** ?case by simp
qed
qed

lemma *valid-return-list-valid-edges*:
assumes *valid-return-list* *rs n* **shows** $\forall r \in \text{set } rs. \text{valid-edge } r$
proof –
from $\langle \text{valid-return-list } rs \ n \rangle$
have $\forall rs' \ r \ rs''. rs = rs' @ r \# rs'' \longrightarrow \text{valid-edge } r$
by (*simp add: valid-return-list-def*)
thus *?thesis*
proof (*induct rs*)
case Nil **thus** *?case by simp*
next
case (*Cons rx rsx*)
note $IH = \langle \forall rs' \ r \ rs''. rsx = rs' @ r \# rs'' \longrightarrow \text{valid-edge } r \implies$
 $\forall a \in \text{set } rsx. \text{valid-edge } a \rangle$
from $\langle \forall rs' \ r \ rs''. rx \# rsx = rs' @ r \# rs'' \longrightarrow \text{valid-edge } r \rangle$
have *valid-edge rx* **by** *blast*
from $\langle \forall rs' \ r \ rs''. rx \# rsx = rs' @ r \# rs'' \longrightarrow \text{valid-edge } r \rangle$
have $\forall rs' \ r \ rs''. rsx = rs' @ r \# rs'' \longrightarrow \text{valid-edge } r$
by *auto(erule-tac x=rx#rs' in allE, auto)*
from $IH[OF \text{ this}] \langle \text{valid-edge } rx \rangle$ **show** *?case by simp*
qed
qed

lemma *vpra-empty-valid-call-list-rev*:
 $\text{valid-call-list } cs \ n \implies \text{valid-path-rev-aux } [] \ (\text{rev } cs)$
proof (*induct cs arbitrary:n*)
case Nil **thus** *?case by simp*
next
case (*Cons c' cs'*)
note $IH = \langle \bigwedge n. \text{valid-call-list } cs' \ n \implies \text{valid-path-rev-aux } [] \ (\text{rev } cs') \rangle$
from $\langle \text{valid-call-list } (c' \# cs') \ n \rangle$ **have** *valid-call-list cs' (sourcenode c')*
apply (*clarsimp simp: valid-call-list-def*)
apply (*erule-tac x=c'#cs' in allE*) **apply** *clarsimp*
by (*case-tac cs', auto simp: sourcenodes-def*)
from $IH[OF \text{ this}]$ **have** *valid-path-rev-aux [] (rev cs')* .
moreover
from $\langle \text{valid-call-list } (c' \# cs') \ n \rangle$ **obtain** $Q \ r \ p \ fs$ **where** $\text{kind } c' = Q:r \hookrightarrow pfs$
apply (*clarsimp simp: valid-call-list-def*)
by (*erule-tac x=[] in allE*) *fastforce*
ultimately show *?case by simp*
qed

lemma *vpa-upd-cs-cases*:
 $\llbracket \text{valid-path-aux } cs \ as; \text{valid-call-list } cs \ n; \ n \ -as \rightarrow^* \ n \rrbracket$
 $\implies \text{case } (\text{upd-cs } cs \ as) \text{ of } [] \Rightarrow (\forall c \in \text{set } cs. \exists a \in \text{set } as. a \in \text{get-return-edges } c)$
 $| \ cx \# csx \Rightarrow \text{valid-call-list } (cx \# csx) \ n'$

```

proof(induct arbitrary:n rule:vpa-induct)
  case (vpa-empty cs)
  from  $\langle n - [] \rightarrow^* n' \rangle$  have  $n = n'$  by fastforce
  with  $\langle \text{valid-call-list } cs \ n \rangle$  show ?case by(cases cs) auto
next
  case (vpa-intra cs a' as')
  note  $IH = \langle \bigwedge n. \llbracket \text{valid-call-list } cs \ n; \ n - as' \rightarrow^* n' \rrbracket$ 
     $\implies \text{case } (\text{upd-cs } cs \ as') \text{ of } [] \implies \forall c \in \text{set } cs. \exists a \in \text{set } as'. a \in \text{get-return-edges } c$ 
     $| \ cx \# \ csx \implies \text{valid-call-list } (cx \ \# \ csx) \ n' \rangle$ 
  from  $\langle \text{intra-kind } (kind \ a') \rangle$  have  $\text{upd-cs } cs \ (a' \# as') = \text{upd-cs } cs \ as'$ 
    by(fastforce simp:intra-kind-def)
  from  $\langle n - a' \# as' \rightarrow^* n' \rangle$  have [simp]: $n = \text{sourcenode } a'$  and valid-edge  $a'$ 
    and targetnode  $a' - as' \rightarrow^* n'$  by(auto elim:path-split-Cons)
  from  $\langle \text{valid-edge } a' \rangle \langle \text{intra-kind } (kind \ a') \rangle$ 
  have  $\text{get-proc } (\text{sourcenode } a') = \text{get-proc } (\text{targetnode } a')$  by(rule get-proc-intra)
  with  $\langle \text{valid-call-list } cs \ n \rangle$  have  $\text{valid-call-list } cs \ (\text{targetnode } a')$ 
    apply(clarsimp simp:valid-call-list-def)
    apply(erule-tac x=cs' in allE) apply clarsimp
    by(case-tac cs') auto
  from  $IH[OF \text{ this } \langle \text{targetnode } a' - as' \rightarrow^* n' \rangle] \langle \text{upd-cs } cs \ (a' \# as') = \text{upd-cs } cs \ as' \rangle$ 
  show ?case by(cases upd-cs cs as') auto
next
  case (vpa-Call cs a' as' Q r p fs)
  note  $IH = \langle \bigwedge n. \llbracket \text{valid-call-list } (a' \# cs) \ n; \ n - as' \rightarrow^* n' \rrbracket$ 
     $\implies \text{case } (\text{upd-cs } (a' \# cs) \ as')$ 
     $\text{of } [] \implies \forall c \in \text{set } (a' \# cs). \exists a \in \text{set } as'. a \in \text{get-return-edges } c$ 
     $| \ cx \# \ csx \implies \text{valid-call-list } (cx \ \# \ csx) \ n' \rangle$ 
  from  $\langle kind \ a' = Q:r \hookrightarrow pfs \rangle$  have  $\text{upd-cs } (a' \# cs) \ as' = \text{upd-cs } cs \ (a' \# as')$ 
    by simp
  from  $\langle n - a' \# as' \rightarrow^* n' \rangle$  have [simp]: $n = \text{sourcenode } a'$  and valid-edge  $a'$ 
    and targetnode  $a' - as' \rightarrow^* n'$  by(auto elim:path-split-Cons)
  from  $\langle \text{valid-edge } a' \rangle \langle kind \ a' = Q:r \hookrightarrow pfs \rangle$ 
  have  $\text{get-proc } (\text{targetnode } a') = p$  by(rule get-proc-call)
  with  $\langle \text{valid-edge } a' \rangle \langle kind \ a' = Q:r \hookrightarrow pfs \rangle \langle \text{valid-call-list } cs \ n \rangle$ 
  have  $\text{valid-call-list } (a' \# cs) \ (\text{targetnode } a')$ 
    apply(clarsimp simp:valid-call-list-def)
    apply(case-tac cs') apply auto
    apply(erule-tac x=list in allE) apply clarsimp
    by(case-tac list,auto simp:sourcenodes-def)
  from  $IH[OF \text{ this } \langle \text{targetnode } a' - as' \rightarrow^* n' \rangle]$ 
     $\langle \text{upd-cs } (a' \# cs) \ as' = \text{upd-cs } cs \ (a' \# as') \rangle$ 
  have  $\text{case } \text{upd-cs } cs \ (a' \# as')$ 
     $\text{of } [] \implies \forall c \in \text{set } (a' \ \# \ cs). \exists a \in \text{set } as'. a \in \text{get-return-edges } c$ 
     $| \ cx \ \# \ csx \implies \text{valid-call-list } (cx \ \# \ csx) \ n' \text{ by } \text{simp}$ 
  thus ?case by(cases upd-cs cs (a' # as')) simp+
next
  case (vpa-ReturnEmpty cs a' as' Q p f)
  note  $IH = \langle \bigwedge n. \llbracket \text{valid-call-list } [] \ n; \ n - as' \rightarrow^* n' \rrbracket$ 
     $\implies \text{case } (\text{upd-cs } [] \ as')$ 

```



```

      of [] ⇒ ∀ c ∈ set []. ∃ a ∈ set as'. a ∈ get-return-edges c
      | cx # csx ⇒ valid-call-list (cx # csx) n'
from ⟨kind a' = Q ↔ pf⟩ ⟨cs = []⟩ have upd-cs [] as' = upd-cs cs (a' # as')
  by simp
from ⟨n - a' # as' →* n'⟩ have [simp]: n = sourcenode a' and valid-edge a'
  and targetnode a' - as' →* n' by (auto elim: path-split-Cons)
have valid-call-list [] (targetnode a') by (simp add: valid-call-list-def)
from IH[OF this ⟨targetnode a' - as' →* n'⟩]
  ⟨upd-cs [] as' = upd-cs cs (a' # as')⟩
have case (upd-cs cs (a' # as'))
  of [] ⇒ ∀ c ∈ set []. ∃ a ∈ set as'. a ∈ get-return-edges c
  | cx # csx ⇒ valid-call-list (cx # csx) n' by simp
with ⟨cs = []⟩ show ?case by (cases upd-cs cs (a' # as')) simp+
next
case (vpa-ReturnCons cs a' as' Q p f c' cs')
note IH = ⟨∧ n. [valid-call-list cs' n; n - as' →* n']
  ⇒ case (upd-cs cs' as')
  of [] ⇒ ∀ c ∈ set cs'. ∃ a ∈ set as'. a ∈ get-return-edges c
  | cx # csx ⇒ valid-call-list (cx # csx) n'
from ⟨kind a' = Q ↔ pf⟩ ⟨cs = c' # cs'⟩ ⟨a' ∈ get-return-edges c'⟩
have upd-cs cs' as' = upd-cs cs (a' # as') by simp
from ⟨n - a' # as' →* n'⟩ have [simp]: n = sourcenode a' and valid-edge a'
  and targetnode a' - as' →* n' by (auto elim: path-split-Cons)
from ⟨valid-call-list cs n⟩ ⟨cs = c' # cs'⟩ have valid-edge c'
  apply (clarsimp simp: valid-call-list-def)
  by (erule-tac x = [] in allE, auto)
with ⟨a' ∈ get-return-edges c'⟩ obtain ax where valid-edge ax
  and sources: sourcenode ax = sourcenode c'
  and targets: targetnode ax = targetnode a' and kind ax = (λcf. False) ✓
  by (fastforce dest: call-return-node-edge)
from ⟨valid-edge ax⟩ sources[THEN sym] targets[THEN sym] ⟨kind ax = (λcf.
False) ✓⟩
have get-proc (sourcenode c') = get-proc (targetnode a')
  by (fastforce intro: get-proc-intra simp: intra-kind-def)
with ⟨valid-call-list cs n⟩ ⟨cs = c' # cs'⟩
have valid-call-list cs' (targetnode a')
  apply (clarsimp simp: valid-call-list-def)
  apply (erule-tac x = c' # cs' in allE)
  by (case-tac cs', auto simp: sourcenodes-def)
from IH[OF this ⟨targetnode a' - as' →* n'⟩]
  ⟨upd-cs cs' as' = upd-cs cs (a' # as')⟩
have case (upd-cs cs (a' # as'))
  of [] ⇒ ∀ c ∈ set cs'. ∃ a ∈ set as'. a ∈ get-return-edges c
  | cx # csx ⇒ valid-call-list (cx # csx) n' by simp
with ⟨cs = c' # cs'⟩ ⟨a' ∈ get-return-edges c'⟩ show ?case
  by (cases upd-cs cs (a' # as')) simp+
qed

```

```

lemma vpa-valid-call-list-valid-return-list-vpra:
  [[valid-path-aux cs cs'; valid-call-list cs n; valid-return-list cs' n']]
  ==> valid-path-rev-aux cs' (rev cs)
proof(induct arbitrary:n n' rule:vpa-induct)
  case (vpa-empty cs)
  from ⟨valid-call-list cs n⟩ show ?case by(rule vpra-empty-valid-call-list-rev)
next
  case (vpa-intra cs a as)
  from ⟨intra-kind (kind a)⟩ ⟨valid-return-list (a#as) n'⟩
  have False apply(clarsimp simp:valid-return-list-def)
  by(erule-tac x=[] in allE,clarsimp simp:intra-kind-def)
  thus ?case by simp
next
  case (vpa-Call cs a as Q r p fs)
  from ⟨kind a = Q:r↔pfs⟩ ⟨valid-return-list (a#as) n'⟩
  have False apply(clarsimp simp:valid-return-list-def)
  by(erule-tac x=[] in allE,clarsimp)
  thus ?case by simp
next
  case (vpa-ReturnEmpty cs a as Q p f)
  from ⟨cs = []⟩ show ?case by simp
next
  case (vpa-ReturnCons cs a as Q p f c' cs')
  note IH = ⟨∧n n'. [[valid-call-list cs' n; valid-return-list as n']]
  ==> valid-path-rev-aux as (rev cs')
  from ⟨valid-return-list (a#as) n'⟩ have valid-return-list as (targetnode a)
  apply(clarsimp simp:valid-return-list-def)
  apply(erule-tac x=a#cs' in allE)
  by(case-tac cs',auto simp:targetnodes-def)
  from ⟨valid-call-list cs n⟩ ⟨cs = c'#cs'⟩
  have valid-call-list cs' (sourcenode c')
  apply(clarsimp simp:valid-call-list-def)
  apply(erule-tac x=c'#cs' in allE)
  by(case-tac cs',auto simp:sourcenodes-def)
  from ⟨valid-call-list cs n⟩ ⟨cs = c'#cs'⟩ have valid-edge c'
  apply(clarsimp simp:valid-call-list-def)
  by(erule-tac x=[] in allE,auto)
  with ⟨a ∈ get-return-edges c'⟩ obtain Q' r' p' f' where kind c' = Q':r'↔p'f'
  apply(cases kind c' rule:edge-kind-cases)
  by(auto dest:only-call-get-return-edges simp:intra-kind-def)
  from IH[OF ⟨valid-call-list cs' (sourcenode c')⟩
  ⟨valid-return-list as (targetnode a)⟩]
  have valid-path-rev-aux as (rev cs') .
  with ⟨kind a = Q↔pf⟩ ⟨cs = c'#cs'⟩ ⟨a ∈ get-return-edges c'⟩ ⟨kind c' =
  Q':r'↔p'f'⟩
  show ?case by simp
qed

```

lemma *vpa-to-vpra*:

[[*valid-path-aux cs as*; *valid-path-aux (upd-cs cs as) cs'*;
 $n -as \rightarrow^* n'$; *valid-call-list cs n*; *valid-return-list cs' n'*]]
 \implies *valid-path-rev-aux cs' as* \wedge *valid-path-rev-aux (upd-rev-cs cs' as) (rev cs)*

proof(*induct arbitrary:n rule:vpa-induct*)

case *vpa-empty* **thus** ?*case*
by(*fastforce intro:vpa-valid-call-list-valid-return-list-vpra*)

next

case (*vpa-intra cs a as*)

note $IH = \langle \bigwedge n. \llbracket \text{valid-path-aux (upd-cs cs as) cs'; } n -as \rightarrow^* n'; \text{valid-call-list cs n; valid-return-list cs' n'} \rrbracket$
 \implies *valid-path-rev-aux cs' as* \wedge
valid-path-rev-aux (upd-rev-cs cs' as) (rev cs)

from $\langle n -a\#as \rightarrow^* n' \rangle$ **have** $n = \text{sourcenode } a$ **and** *valid-edge a*
and *targetnode a* $-as \rightarrow^* n'$ **by**(*auto intro:path-split-Cons*)

from $\langle \text{valid-edge } a \rangle \langle \text{intra-kind (kind } a) \rangle$

have $\text{get-proc (sourcenode } a) = \text{get-proc (targetnode } a)$ **by**(*rule get-proc-intra*)

with $\langle \text{valid-call-list cs } n \rangle \langle n = \text{sourcenode } a \rangle$

have *valid-call-list cs (targetnode a)*
apply(*clarsimp simp:valid-call-list-def*)
apply(*erule-tac x=cs' in allE*) **apply** *clarsimp*
by(*case-tac cs'*) *auto*

from $\langle \text{valid-path-aux (upd-cs cs (a\#as)) cs' } \rangle \langle \text{intra-kind (kind } a) \rangle$

have *valid-path-aux (upd-cs cs as) cs'*
by(*fastforce simp:intra-kind-def*)

from $IH[OF \text{ this } \langle \text{targetnode } a -as \rightarrow^* n' \rangle \langle \text{valid-call-list cs (targetnode } a) \rangle \langle \text{valid-return-list cs' } n' \rangle]$

have *valid-path-rev-aux cs' as*
and *valid-path-rev-aux (upd-rev-cs cs' as) (rev cs)* **by** *simp-all*

from $\langle \text{intra-kind (kind } a) \rangle \langle \text{valid-path-rev-aux cs' as} \rangle$

have *valid-path-rev-aux cs' (a\#as)* **by**(*rule vpra-Cons-intra*)

from $\langle \text{intra-kind (kind } a) \rangle$ **have** $\text{upd-rev-cs cs' (a\#as)} = \text{upd-rev-cs cs' as}$
by(*simp add:upd-rev-cs-Cons-intra*)

with $\langle \text{valid-path-rev-aux (upd-rev-cs cs' as) (rev cs)} \rangle$

have *valid-path-rev-aux (upd-rev-cs cs' (a\#as)) (rev cs)* **by** *simp*

with $\langle \text{valid-path-rev-aux cs' (a\#as)} \rangle$ **show** ?*case* **by** *simp*

next

case (*vpa-Call cs a as Q r p fs*)

note $IH = \langle \bigwedge n. \llbracket \text{valid-path-aux (upd-cs (a\#cs) as) cs'; } n -as \rightarrow^* n'; \text{valid-call-list (a\#cs) n; valid-return-list cs' n'} \rrbracket$
 \implies *valid-path-rev-aux cs' as* \wedge
valid-path-rev-aux (upd-rev-cs cs' as) (rev (a\#cs))

from $\langle n -a\#as \rightarrow^* n' \rangle$ **have** $n = \text{sourcenode } a$ **and** *valid-edge a*
and *targetnode a* $-as \rightarrow^* n'$ **by**(*auto intro:path-split-Cons*)

from $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q:r \hookrightarrow pfs \rangle$ **have** $p = \text{get-proc (targetnode } a)$
by(*rule get-proc-call[THEN sym]*)

from $\langle \text{valid-call-list cs } n \rangle \langle n = \text{sourcenode } a \rangle$

have *valid-call-list cs (sourcenode a)* **by** *simp*

```

with ⟨kind a = Q:r↔pfs⟩ ⟨valid-edge a⟩ ⟨p = get-proc (targetnode a)⟩
have valid-call-list (a#cs) (targetnode a)
  apply(clarsimp simp:valid-call-list-def)
  apply(case-tac cs') apply auto
  apply(erule-tac x=list in allE) apply clarsimp
  by(case-tac list,auto simp:sourcenodes-def)
from ⟨kind a = Q:r↔pfs⟩ have upd-cs cs (a#as) = upd-cs (a#cs) as
  by simp
with ⟨valid-path-aux (upd-cs cs (a#as)) cs'⟩
have valid-path-aux (upd-cs (a#cs) as) cs' by simp
from IH[OF this ⟨targetnode a -as→* n'⟩ ⟨valid-call-list (a#cs) (targetnode a)⟩
  ⟨valid-return-list cs' n'⟩]
have valid-path-rev-aux cs' as
  and valid-path-rev-aux (upd-rev-cs cs' as) (rev (a#cs)) by simp-all
show ?case
proof(cases upd-rev-cs cs' as)
  case Nil
    with ⟨kind a = Q:r↔pfs⟩
    have upd-rev-cs cs' (a#as) = [] by(rule upd-rev-cs-Cons-Call-Cons-Empty)
    with ⟨valid-path-rev-aux (upd-rev-cs cs' as) (rev (a#cs))⟩ ⟨kind a = Q:r↔pfs⟩
  Nil
    have valid-path-rev-aux (upd-rev-cs cs' (a#as)) (rev cs) by simp
    from Nil ⟨kind a = Q:r↔pfs⟩ have valid-path-rev-aux (upd-rev-cs cs' as)
  ([]@[a])
    by(simp only:valid-path-rev-aux.simps) clarsimp
    with ⟨valid-path-rev-aux cs' as⟩ have valid-path-rev-aux cs' ([a]@as)
    by(fastforce intro:valid-path-rev-aux-Append)
    with ⟨valid-path-rev-aux (upd-rev-cs cs' (a#as)) (rev cs)⟩
    show ?thesis by simp
  next
    case (Cons cx csx)
    with ⟨valid-path-rev-aux (upd-rev-cs cs' as) (rev (a#cs))⟩ ⟨kind a = Q:r↔pfs⟩
    have match:cx ∈ get-return-edges a valid-path-rev-aux csx (rev cs) by auto
    from ⟨kind a = Q:r↔pfs⟩ Cons have upd-rev-cs cs' (a#as) = csx
    by(rule upd-rev-cs-Cons-Call-Cons)
    with ⟨valid-path-rev-aux (upd-rev-cs cs' as) (rev(a#cs))⟩ ⟨kind a = Q:r↔pfs⟩
  match
    have valid-path-rev-aux (upd-rev-cs cs' (a#as)) (rev cs) by simp
    from Cons ⟨kind a = Q:r↔pfs⟩ match
    have valid-path-rev-aux (upd-rev-cs cs' as) ([]@[a])
    by(simp only:valid-path-rev-aux.simps) clarsimp
    with ⟨valid-path-rev-aux cs' as⟩ have valid-path-rev-aux cs' ([a]@as)
    by(fastforce intro:valid-path-rev-aux-Append)
    with ⟨valid-path-rev-aux (upd-rev-cs cs' (a#as)) (rev cs)⟩
    show ?thesis by simp
  qed
next
  case (vpa-ReturnEmpty cs a as Q p f)
  note IH = ⟨∧n. []valid-path-aux (upd-cs [] as) cs'; n -as→* n'⟩;

```

```

    valid-call-list [] n; valid-return-list cs' n''
  => valid-path-rev-aux cs' as ^
    valid-path-rev-aux (upd-rev-cs cs' as) (rev [])
from <n -a#as->* n'> have n = sourcenode a and valid-edge a
  and targetnode a -as->* n' by(auto intro:path-split-Cons)
from <cs = []> <kind a = Q<->pf> have upd-cs cs (a#as) = upd-cs [] as
  by simp
with <valid-path-aux (upd-cs cs (a#as)) cs'>
have valid-path-aux (upd-cs [] as) cs' by simp
from IH[OF this <targetnode a -as->* n'> - <valid-return-list cs' n''>]
have valid-path-rev-aux cs' as
  and valid-path-rev-aux (upd-rev-cs cs' as) (rev [])
  by(auto simp:valid-call-list-def)
from <kind a = Q<->pf> <valid-path-rev-aux cs' as>
have valid-path-rev-aux cs' (a#as) by(rule vpra-Cons-Return)
moreover
from <cs = []> have valid-path-rev-aux (upd-rev-cs cs' (a#as)) (rev cs)
  by simp
ultimately show ?case by simp
next
case (vpa-ReturnCons cs a as Q p f cx csx)
note IH = <^n. [valid-path-aux (upd-cs csx as) cs'; n -as->* n';
  valid-call-list csx n; valid-return-list cs' n'']
  => valid-path-rev-aux cs' as ^
    valid-path-rev-aux (upd-rev-cs cs' as) (rev csx)
note match = <cs = cx#csx> <a ∈ get-return-edges cx>
from <n -a#as->* n'> have n = sourcenode a and valid-edge a
  and targetnode a -as->* n' by(auto intro:path-split-Cons)
from <cs = cx#csx> <valid-call-list cs n> have valid-edge cx
  apply(clarsimp simp:valid-call-list-def)
  by(erule-tac x=[] in allE) clarsimp
with match have get-proc (sourcenode cx) = get-proc (targetnode a)
  by(fastforce intro:get-proc-get-return-edge)
with <valid-call-list cs n> <cs = cx#csx>
have valid-call-list csx (targetnode a)
  apply(clarsimp simp:valid-call-list-def)
  apply(erule-tac x=cx#cs' in allE) apply clarsimp
  by(case-tac cs',auto simp:sourcenodes-def)
from <kind a = Q<->pf> match have upd-cs cs (a#as) = upd-cs csx as by simp
with <valid-path-aux (upd-cs cs (a#as)) cs'>
have valid-path-aux (upd-cs csx as) cs' by simp
from IH[OF this <targetnode a -as->* n'> <valid-call-list csx (targetnode a)>
  <valid-return-list cs' n''>]
have valid-path-rev-aux cs' as
  and valid-path-rev-aux (upd-rev-cs cs' as) (rev csx) by simp-all
from <kind a = Q<->pf> <valid-path-rev-aux cs' as>
have valid-path-rev-aux cs' (a#as) by(rule vpra-Cons-Return)
from match <valid-edge cx> obtain Q' r' p' f' where kind cx = Q':r'<->p'f'
  by(fastforce dest!:only-call-get-return-edges)

```

```

from ⟨kind a = Q↔pf⟩ have upd-rev-cs cs' (a#as) = a#(upd-rev-cs cs' as)
  by(rule upd-rev-cs-Cons-Return)
with ⟨valid-path-rev-aux (upd-rev-cs cs' as) (rev csx)⟩ ⟨kind a = Q↔pf⟩
  ⟨kind cx = Q':r'↔pf'⟩ match
have valid-path-rev-aux (upd-rev-cs cs' (a#as)) (rev cs)
  by simp
with ⟨valid-path-rev-aux cs' (a#as)⟩ show ?case by simp
qed

```

lemma *vp-to-vpra*:

```

  n -as→√* n' ⇒ valid-path-rev-aux [] as
by(fastforce elim:vpa-to-vpra[THEN conjunct1]
  simp:vp-def valid-path-def valid-call-list-def valid-return-list-def)

```

Same level paths

```

fun same-level-path-aux :: 'edge list ⇒ 'edge list ⇒ bool
  where same-level-path-aux cs [] ↔ True
  | same-level-path-aux cs (a#as) ↔
    (case (kind a) of Q:r↔pfs ⇒ same-level-path-aux (a#cs) as
     | Q↔pf ⇒ case cs of [] ⇒ False
     | c'#cs' ⇒ a ∈ get-return-edges c' ∧
                 same-level-path-aux cs' as
     | - ⇒ same-level-path-aux cs as)

```

lemma *slpa-induct* [consumes 1, case-names *slpa-empty slpa-intra slpa-Call*
slpa-Return]:

```

assumes major: same-level-path-aux xs ys
and rules: ∧cs. P cs []
  ∧cs a as. [[intra-kind(kind a); same-level-path-aux cs as; P cs as]]
  ⇒ P cs (a#as)
  ∧cs a as Q r p fs. [[kind a = Q:r↔pfs; same-level-path-aux (a#cs) as; P
(a#cs) as]]
  ⇒ P cs (a#as)
  ∧cs a as Q p f c' cs'. [[kind a = Q↔pf; cs = c'#cs'; same-level-path-aux cs'
as;
  a ∈ get-return-edges c'; P cs' as]]
  ⇒ P cs (a#as)

```

shows *P xs ys*

using *major*

apply(*induct ys arbitrary: xs*)

by(*auto intro:rules split:edge-kind.split-asm list.split-asm simp:intra-kind-def*)

lemma *slpa-cases* [consumes 4, case-names *intra-path return-intra-path*]:

```

assumes same-level-path-aux cs as and upd-cs cs as = []
and ∀c ∈ set cs. valid-edge c and ∀a ∈ set as. valid-edge a

```

obtains $\forall a \in \text{set } as. \text{intra-kind}(\text{kind } a)$
 $| \text{asx } a \text{ asx}' Q p f c' cs' \text{ where } as = \text{asx}@a\#\text{asx}' \text{ and same-level-path-aux } cs$
 asx
and $\text{kind } a = Q \leftrightarrow_{pf}$ **and** $\text{upd-cs } cs \text{ asx} = c' \# cs'$ **and** $\text{upd-cs } cs (\text{asx}@[a]) =$
 \square
and $a \in \text{get-return-edges } c'$ **and** $\text{valid-edge } c'$
and $\forall a \in \text{set } asx'. \text{intra-kind}(\text{kind } a)$
proof(*atomize-elim*)
from *assms*
show $(\forall a \in \text{set } as. \text{intra-kind}(\text{kind } a)) \vee$
 $(\exists \text{asx } a \text{ asx}' Q p f c' cs'. as = \text{asx}@a\#\text{asx}' \wedge \text{same-level-path-aux } cs \text{ asx} \wedge$
 $\text{kind } a = Q \leftrightarrow_{pf} \wedge \text{upd-cs } cs \text{ asx} = c' \# cs' \wedge \text{upd-cs } cs (\text{asx}@[a]) = \square \wedge$
 $a \in \text{get-return-edges } c' \wedge \text{valid-edge } c' \wedge (\forall a \in \text{set } asx'. \text{intra-kind}(\text{kind } a)))$
proof(*induct rule:slpa-induct*)
case (*slpa-empty cs*)
have $\forall a \in \text{set } \square. \text{intra-kind}(\text{kind } a)$ **by** *simp*
thus *?case by simp*
next
case (*slpa-intra cs a as*)
note $IH = \langle [\text{upd-cs } cs \text{ as} = \square]; \forall c \in \text{set } cs. \text{valid-edge } c; \forall a' \in \text{set } as. \text{valid-edge}$
 $a' \rangle$
 $\implies (\forall a \in \text{set } as. \text{intra-kind}(\text{kind } a)) \vee$
 $(\exists \text{asx } a \text{ asx}' Q p f c' cs'. as = \text{asx}@a\#\text{asx}' \wedge \text{same-level-path-aux } cs \text{ asx} \wedge$
 $\text{kind } a = Q \leftrightarrow_{pf} \wedge \text{upd-cs } cs \text{ asx} = c' \# cs' \wedge \text{upd-cs } cs (\text{asx}@[a]) = \square \wedge$
 $a \in \text{get-return-edges } c' \wedge \text{valid-edge } c' \wedge (\forall a' \in \text{set } asx'. \text{intra-kind}(\text{kind } a')))$
from $\langle \forall a' \in \text{set } (a \# as). \text{valid-edge } a' \rangle$ **have** $\forall a' \in \text{set } as. \text{valid-edge } a'$ **by** *simp*
from $\langle \text{intra-kind}(\text{kind } a) \rangle \langle \text{upd-cs } cs (a \# as) = \square \rangle$
have $\text{upd-cs } cs \text{ as} = \square$ **by**(*fastforce simp:intra-kind-def*)
from $IH[OF \text{ this } \langle \forall c \in \text{set } cs. \text{valid-edge } c \rangle \langle \forall a' \in \text{set } as. \text{valid-edge } a' \rangle]$ **show**
?case
proof
assume $\forall a \in \text{set } as. \text{intra-kind}(\text{kind } a)$
with $\langle \text{intra-kind}(\text{kind } a) \rangle$ **have** $\forall a' \in \text{set } (a \# as). \text{intra-kind}(\text{kind } a')$
by *simp*
thus *?case by simp*
next
assume $\exists \text{asx } a \text{ asx}' Q p f c' cs'. as = \text{asx}@a\#\text{asx}' \wedge \text{same-level-path-aux } cs$
 $asx \wedge$
 $\text{kind } a = Q \leftrightarrow_{pf} \wedge \text{upd-cs } cs \text{ asx} = c' \# cs' \wedge \text{upd-cs } cs (\text{asx}@[a]) =$
 $\square \wedge$
 $a \in \text{get-return-edges } c' \wedge \text{valid-edge } c' \wedge$
 $(\forall a \in \text{set } asx'. \text{intra-kind}(\text{kind } a))$
then obtain $\text{asx } a' Q p f \text{asx}' c' cs' \text{ where } as = \text{asx}@a'\#\text{asx}'$
and $\text{same-level-path-aux } cs \text{ asx}$ **and** $\text{upd-cs } cs (\text{asx}@[a']) = \square$
and $\text{upd-cs } cs \text{ asx} = c' \# cs'$ **and** $\text{assms}: a' \in \text{get-return-edges } c'$
 $\text{kind } a' = Q \leftrightarrow_{pf} \text{valid-edge } c' \forall a \in \text{set } asx'. \text{intra-kind}(\text{kind } a)$
by *blast*
from $\langle as = \text{asx}@a'\#\text{asx}' \rangle$ **have** $a \# as = (a \# \text{asx})@a'\#\text{asx}'$ **by** *simp*
moreover

from $\langle \text{intra-kind } (\text{kind } a) \rangle \langle \text{same-level-path-aux } cs \ asx \rangle$
have $\text{same-level-path-aux } cs \ (a\#asx)$ **by** $(\text{fastforce simp:intra-kind-def})$
moreover
from $\langle \text{upd-cs } cs \ asx = c'\#cs' \rangle \langle \text{intra-kind } (\text{kind } a) \rangle$
have $\text{upd-cs } cs \ (a\#asx) = c'\#cs'$ **by** $(\text{fastforce simp:intra-kind-def})$
moreover
from $\langle \text{upd-cs } cs \ (asx@[a']) = [] \rangle \langle \text{intra-kind } (\text{kind } a) \rangle$
have $\text{upd-cs } cs \ ((a\#asx)@[a']) = []$ **by** $(\text{fastforce simp:intra-kind-def})$
ultimately show $?case$ **using** $assms$ **by** $blast$
qed
next
case $(\text{slpa-Call } cs \ a \ as \ Q \ r \ p \ fs)$
note $IH = \langle [\text{upd-cs } (a\#cs) \ as = []; \forall c \in \text{set } (a\#cs). \text{valid-edge } c; \forall a' \in \text{set } as. \text{valid-edge } a'] \implies$
 $(\forall a' \in \text{set } as. \text{intra-kind } (\text{kind } a')) \vee$
 $(\exists asx \ a' \ asx' \ Q' \ p' \ f' \ c' \ cs'. \ as = asx@a'\#asx' \wedge$
 $\text{same-level-path-aux } (a\#cs) \ asx \wedge \text{kind } a' = Q' \leftrightarrow_{p'} f' \wedge$
 $\text{upd-cs } (a\#cs) \ asx = c'\#cs' \wedge \text{upd-cs } (a\#cs) \ (asx@[a']) = [] \wedge$
 $a' \in \text{get-return-edges } c' \wedge \text{valid-edge } c' \wedge$
 $(\forall a' \in \text{set } asx'. \text{intra-kind } (\text{kind } a')) \rangle$
from $\langle \forall a' \in \text{set } (a\#as). \text{valid-edge } a' \rangle$ **have** $\text{valid-edge } a$
and $\langle \forall a' \in \text{set } as. \text{valid-edge } a' \rangle$ **by** simp-all
from $\langle \forall c \in \text{set } cs. \text{valid-edge } c \rangle \langle \text{valid-edge } a \rangle$ **have** $\forall c \in \text{set } (a\#cs). \text{valid-edge } c$
by simp
from $\langle \text{upd-cs } cs \ (a\#as) = [] \rangle \langle \text{kind } a = Q:r \leftrightarrow pfs \rangle$
have $\text{upd-cs } (a\#cs) \ as = []$ **by** simp
from $IH[OF \ \text{this } \langle \forall c \in \text{set } (a\#cs). \text{valid-edge } c \rangle \langle \forall a' \in \text{set } as. \text{valid-edge } a' \rangle]$
show $?case$
proof
assume $\forall a' \in \text{set } as. \text{intra-kind } (\text{kind } a')$
with $\langle \text{kind } a = Q:r \leftrightarrow pfs \rangle$ **have** $\text{upd-cs } cs \ (a\#as) = a\#cs$
by $(\text{fastforce intro:upd-cs-intra-path})$
with $\langle \text{upd-cs } cs \ (a\#as) = [] \rangle$ **have** False **by** simp
thus $?case$ **by** simp
next
assume $\exists asx \ a' \ asx' \ Q \ p \ f \ c' \ cs'. \ as = asx@a'\#asx' \wedge$
 $\text{same-level-path-aux } (a\#cs) \ asx \wedge \text{kind } a' = Q \leftrightarrow_p f \wedge$
 $\text{upd-cs } (a\#cs) \ asx = c'\#cs' \wedge \text{upd-cs } (a\#cs) \ (asx@[a']) = [] \wedge$
 $a' \in \text{get-return-edges } c' \wedge \text{valid-edge } c' \wedge$
 $(\forall a \in \text{set } asx'. \text{intra-kind } (\text{kind } a))$
then obtain $asx \ a' \ Q' \ p' \ f' \ asx' \ c' \ cs'$ **where** $as = asx@a'\#asx'$
and $\text{same-level-path-aux } (a\#cs) \ asx$ **and** $\text{upd-cs } (a\#cs) \ (asx@[a']) = []$
and $\text{upd-cs } (a\#cs) \ asx = c'\#cs'$ **and** $assms: a' \in \text{get-return-edges } c'$
 $\text{kind } a' = Q' \leftrightarrow_{p'} f'$ $\text{valid-edge } c' \forall a \in \text{set } asx'. \text{intra-kind } (\text{kind } a)$
by $blast$
from $\langle as = asx@a'\#asx' \rangle$ **have** $a\#as = (a\#asx)@a'\#asx'$ **by** simp
moreover
from $\langle \text{kind } a = Q:r \leftrightarrow pfs \rangle \langle \text{same-level-path-aux } (a\#cs) \ asx \rangle$
have $\text{same-level-path-aux } cs \ (a\#asx)$ **by** simp

moreover
from $\langle \text{kind } a = Q:r \leftrightarrow_{pfs} \rangle \langle \text{upd-cs } (a\#cs) \text{ asx} = c'\#cs' \rangle$
have $\text{upd-cs } cs \ (a\#asx) = c'\#cs'$ **by** *simp*
moreover
from $\langle \text{kind } a = Q:r \leftrightarrow_{pfs} \rangle \langle \text{upd-cs } (a\#cs) \ (asx@[a']) = [] \rangle$
have $\text{upd-cs } cs \ ((a\#asx)@[a']) = []$ **by** *simp*
ultimately show *?case* **using** *assms* **by** *blast*
qed
next
case $(\text{slpa-Return } cs \ a \ \text{as} \ Q \ p \ f \ c' \ cs')$
note $IH = \langle []; \forall c \in \text{set } cs'. \text{valid-edge } c; \forall a' \in \text{set } as. \text{valid-edge } a' \rangle \implies$
 $(\forall a' \in \text{set } as. \text{intra-kind } (\text{kind } a')) \vee$
 $(\exists asx \ a' \ asx' \ Q' \ p' \ f' \ c'' \ cs''. \text{as} = asx@a'\#asx' \wedge$
 $\text{same-level-path-aux } cs' \ asx \wedge \text{kind } a' = Q' \leftrightarrow_{p'} f' \wedge \text{upd-cs } cs' \ asx = c''\#cs'')$
 \wedge
 $\text{upd-cs } cs' \ (asx@[a']) = [] \wedge a' \in \text{get-return-edges } c'' \wedge \text{valid-edge } c'' \wedge$
 $(\forall a' \in \text{set } asx'. \text{intra-kind } (\text{kind } a')) \rangle$
from $\langle \forall a' \in \text{set } (a\#as). \text{valid-edge } a' \rangle$ **have** *valid-edge* a
and $\forall a' \in \text{set } as. \text{valid-edge } a'$ **by** *simp-all*
from $\langle \forall c \in \text{set } cs. \text{valid-edge } c \rangle \langle cs = c' \# cs' \rangle$
have *valid-edge* c' **and** $\forall c \in \text{set } cs'. \text{valid-edge } c$ **by** *simp-all*
from $\langle \text{upd-cs } cs \ (a\#as) = [] \rangle \langle \text{kind } a = Q \leftrightarrow_{pfs} \rangle \langle cs = c' \# cs' \rangle$
 $\langle a \in \text{get-return-edges } c' \rangle$ **have** $\text{upd-cs } cs' \ \text{as} = []$ **by** *simp*
from $IH[OF \ \text{this } \langle \forall c \in \text{set } cs'. \text{valid-edge } c \rangle \langle \forall a' \in \text{set } as. \text{valid-edge } a' \rangle]$ **show**
?case
proof
assume $\forall a' \in \text{set } as. \text{intra-kind } (\text{kind } a')$
hence $\text{upd-cs } cs' \ \text{as} = cs'$ **by** *(rule upd-cs-intra-path)*
with $\langle \text{upd-cs } cs' \ \text{as} = [] \rangle$ **have** $cs' = []$ **by** *simp*
with $\langle cs = c'\#cs' \rangle \langle a \in \text{get-return-edges } c' \rangle \langle \text{kind } a = Q \leftrightarrow_{pfs} \rangle$
have $\text{upd-cs } cs \ [a] = []$ **by** *simp*
moreover
from $\langle cs = c'\#cs' \rangle$ **have** $\text{upd-cs } cs \ [] \neq []$ **by** *simp*
moreover
have *same-level-path-aux* $cs \ []$ **by** *simp*
ultimately show *?case*
using $\langle \text{kind } a = Q \leftrightarrow_{pfs} \rangle \langle \forall a' \in \text{set } as. \text{intra-kind } (\text{kind } a') \rangle \langle cs = c'\#cs' \rangle$
 $\langle a \in \text{get-return-edges } c' \rangle \langle \text{valid-edge } c' \rangle$
by *fastforce*
next
assume $\exists asx \ a' \ asx' \ Q' \ p' \ f' \ c'' \ cs''. \text{as} = asx@a'\#asx' \wedge$
 $\text{same-level-path-aux } cs' \ asx \wedge \text{kind } a' = Q' \leftrightarrow_{p'} f' \wedge \text{upd-cs } cs' \ asx = c''\#cs''$
 \wedge
 $\text{upd-cs } cs' \ (asx@[a']) = [] \wedge a' \in \text{get-return-edges } c'' \wedge \text{valid-edge } c'' \wedge$
 $(\forall a' \in \text{set } asx'. \text{intra-kind } (\text{kind } a'))$
then obtain $asx \ a' \ asx' \ Q' \ p' \ f' \ c'' \ cs''$ **where** $\text{as} = asx@a'\#asx'$
and *same-level-path-aux* $cs' \ asx$ **and** $\text{upd-cs } cs' \ asx = c''\#cs''$
and $\text{upd-cs } cs' \ (asx@[a']) = []$ **and** *assms*: $a' \in \text{get-return-edges } c''$

$kind\ a' = Q' \leftrightarrow_p f' \text{ valid-edge } c'' \forall a' \in set\ asx'. \text{ intra-kind } (kind\ a')$
by *blast*
from $\langle as = asx @ a' \# asx' \rangle$ **have** $a \# as = (a \# asx) @ a' \# asx'$ **by** *simp*
moreover
from $\langle same\text{-level-path-aux } cs' asx \rangle \langle cs = c' \# cs' \rangle \langle a \in get\text{-return-edges } c' \rangle$
 $\langle kind\ a = Q \leftrightarrow_p f \rangle$
have *same-level-path-aux* $cs (a \# asx)$ **by** *simp*
moreover
from $\langle upd\text{-cs } cs' asx = c'' \# cs'' \rangle \langle kind\ a = Q \leftrightarrow_p f \rangle \langle cs = c' \# cs' \rangle$
have *upd-cs* $cs (a \# asx) = c'' \# cs''$ **by** *simp*
moreover
from $\langle upd\text{-cs } cs' (asx @ [a']) = [] \rangle \langle cs = c' \# cs' \rangle \langle a \in get\text{-return-edges } c' \rangle$
 $\langle kind\ a = Q \leftrightarrow_p f \rangle$
have *upd-cs* $cs ((a \# asx) @ [a']) = []$ **by** *simp*
ultimately show *?case* **using** *assms* **by** *blast*
qed
qed
qed

lemma *same-level-path-aux-valid-path-aux*:
 $same\text{-level-path-aux } cs\ as \implies \text{valid-path-aux } cs\ as$
by (*induct rule:slpa-induct, auto split:edge-kind.split simp:intra-kind-def*)

lemma *same-level-path-aux-Append*:
 $\llbracket same\text{-level-path-aux } cs\ as; same\text{-level-path-aux } (upd\text{-cs } cs\ as)\ as \rrbracket$
 $\implies same\text{-level-path-aux } cs\ (as @ as')$
by (*induct rule:slpa-induct, auto simp:intra-kind-def*)

lemma *same-level-path-aux-callstack-Append*:
 $same\text{-level-path-aux } cs\ as \implies same\text{-level-path-aux } (cs @ cs')\ as$
by (*induct rule:slpa-induct, auto simp:intra-kind-def*)

lemma *same-level-path-upd-cs-callstack-Append*:
 $\llbracket same\text{-level-path-aux } cs\ as; upd\text{-cs } cs\ as = cs \rrbracket$
 $\implies upd\text{-cs } (cs @ cs'')\ as = (cs' @ cs'')$
by (*induct rule:slpa-induct, auto split:edge-kind.split simp:intra-kind-def*)

lemma *slpa-split*:
assumes $same\text{-level-path-aux } cs\ as$ **and** $as = xs @ ys$ **and** $upd\text{-cs } cs\ xs = []$
shows $same\text{-level-path-aux } cs\ xs$ **and** $same\text{-level-path-aux } []\ ys$
using *assms*
proof (*induct arbitrary:xs ys rule:slpa-induct*)
case (*slpa-empty cs*) **case** 1
from $\langle [] = xs @ ys \rangle$ **show** *?case* **by** *simp*

```

next
  case (slpa-empty cs) case 2
  from ⟨[] = xs@ys⟩ show ?case by simp
next
  case (slpa-intra cs a as)
  note IH1 = ⟨∧xs ys. [as = xs@ys; upd-cs cs xs = []] ⇒ same-level-path-aux cs
xs⟩
  note IH2 = ⟨∧xs ys. [as = xs@ys; upd-cs cs xs = []] ⇒ same-level-path-aux []
ys⟩
  { case 1
  show ?case
  proof(cases xs)
    case Nil thus ?thesis by simp
  next
    case (Cons x' xs')
    with ⟨a#as = xs@ys⟩ have a = x' and as = xs'@ys by simp-all
    with ⟨upd-cs cs xs = []⟩ Cons ⟨intra-kind (kind a)⟩
    have upd-cs cs xs' = [] by(fastforce simp:intra-kind-def)
    from IH1[OF ⟨as = xs'@ys⟩ this] have same-level-path-aux cs xs' .
    with ⟨a = x'⟩ ⟨intra-kind (kind a)⟩ Cons
    show ?thesis by(fastforce simp:intra-kind-def)
  qed
  next
  case 2
  show ?case
  proof(cases xs)
    case Nil
    with ⟨upd-cs cs xs = []⟩ have cs = [] by fastforce
    with Nil ⟨a#as = xs@ys⟩ ⟨same-level-path-aux cs as⟩ ⟨intra-kind (kind a)⟩
    show ?thesis by(cases ys,auto simp:intra-kind-def)
  next
    case (Cons x' xs')
    with ⟨a#as = xs@ys⟩ have a = x' and as = xs'@ys by simp-all
    with ⟨upd-cs cs xs = []⟩ Cons ⟨intra-kind (kind a)⟩
    have upd-cs cs xs' = [] by(fastforce simp:intra-kind-def)
    from IH2[OF ⟨as = xs'@ys⟩ this] show ?thesis .
  qed
  }
next
  case (slpa-Call cs a as Q r p fs)
  note IH1 = ⟨∧xs ys. [as = xs@ys; upd-cs (a#cs) xs = []]
⇒ same-level-path-aux (a#cs) xs⟩
  note IH2 = ⟨∧xs ys. [as = xs@ys; upd-cs (a#cs) xs = []]
⇒ same-level-path-aux [] ys⟩
  { case 1
  show ?case
  proof(cases xs)
    case Nil thus ?thesis by simp
  next

```

```

    case (Cons x' xs')
    with ⟨a#as = xs@ys⟩ have a = x' and as = xs'@ys by simp-all
    with ⟨upd-cs cs xs = []⟩ Cons ⟨kind a = Q:r↔pfs⟩
    have upd-cs (a#cs) xs' = [] by simp
    from IH1[OF ⟨as = xs'@ys⟩ this] have same-level-path-aux (a#cs) xs' .
    with ⟨a = x'⟩ ⟨kind a = Q:r↔pfs⟩ Cons show ?thesis by simp
  qed
next
case 2
show ?case
proof(cases xs)
  case Nil
  with ⟨upd-cs cs xs = []⟩ have cs = [] by fastforce
  with Nil ⟨a#as = xs@ys⟩ ⟨same-level-path-aux (a#cs) as⟩ ⟨kind a = Q:r↔pfs⟩
  show ?thesis by(cases ys) auto
next
case (Cons x' xs')
  with ⟨a#as = xs@ys⟩ have a = x' and as = xs'@ys by simp-all
  with ⟨upd-cs cs xs = []⟩ Cons ⟨kind a = Q:r↔pfs⟩
  have upd-cs (a#cs) xs' = [] by simp
  from IH2[OF ⟨as = xs'@ys⟩ this] show ?thesis .
  qed
}
next
case (slpa-Return cs a as Q p f c' cs')
  note IH1 = ⟨∧xs ys. [as = xs@ys; upd-cs cs' xs = []] ⇒ same-level-path-aux
cs' xs⟩
  note IH2 = ⟨∧xs ys. [as = xs@ys; upd-cs cs' xs = []] ⇒ same-level-path-aux
[] ys⟩
  { case 1
  show ?case
  proof(cases xs)
    case Nil thus ?thesis by simp
  next
  case (Cons x' xs')
    with ⟨a#as = xs@ys⟩ have a = x' and as = xs'@ys by simp-all
    with ⟨upd-cs cs xs = []⟩ Cons ⟨kind a = Q↔pf⟩ ⟨cs = c'#cs'⟩
    have upd-cs cs' xs' = [] by simp
    from IH1[OF ⟨as = xs'@ys⟩ this] have same-level-path-aux cs' xs' .
    with ⟨a = x'⟩ ⟨kind a = Q↔pf⟩ ⟨cs = c'#cs'⟩ ⟨a ∈ get-return-edges c'⟩ Cons
    show ?thesis by simp
  qed
  next
  case 2
  show ?case
  proof(cases xs)
    case Nil
    with ⟨upd-cs cs xs = []⟩ have cs = [] by fastforce
    with ⟨cs = c'#cs'⟩ have False by simp

```

```

    thus ?thesis by simp
  next
    case (Cons x' xs')
    with ⟨a#as = xs@ys⟩ have a = x' and as = xs'@ys by simp-all
    with ⟨upd-cs cs xs = []⟩ Cons ⟨kind a = Q↔pf⟩ ⟨cs = c'#cs'⟩
    have upd-cs cs' xs' = [] by simp
    from IH2[OF ⟨as = xs'@ys⟩ this] show ?thesis .
  qed
}
qed

```

lemma *slpa-number-Calls-eq-number>Returns*:

```

[[same-level-path-aux cs as; upd-cs cs as = []];
  ∀ a ∈ set as. valid-edge a; ∀ c ∈ set cs. valid-edge c]
⇒ length [a←as@cs. ∃ Q r p fs. kind a = Q:r↔pfs] =
  length [a←as. ∃ Q p f. kind a = Q↔pf]
apply(induct rule:slpa-induct)
by(auto split:list.split edge-kind.split intro:only-call-get-return-edges
  simp:intra-kind-def)

```

lemma *slpa-get-proc*:

```

[[same-level-path-aux cs as; upd-cs cs as = []; n -as→* n';
  ∀ c ∈ set cs. valid-edge c]
⇒ (if cs = [] then get-proc n else get-proc(last(sourcenodes cs))) = get-proc n'
proof(induct arbitrary:n rule:slpa-induct)
  case slpa-empty thus ?case by fastforce
next
  case (slpa-intra cs a as)
  note IH = ⟨∧n. [[upd-cs cs as = []; n -as→* n'; ∀ a∈set cs. valid-edge a]
    ⇒ (if cs = [] then get-proc n else get-proc (last (sourcenodes cs))) =
      get-proc n'⟩
  from ⟨intra-kind (kind a)⟩ ⟨upd-cs cs (a#as) = []⟩
  have upd-cs cs as = [] by(cases kind a,auto simp:intra-kind-def)
  from ⟨n -a#as→* n'⟩ have n -[]@a#as→* n' by simp
  hence valid-edge a and n = sourcenode a and targetnode a -as→* n'
    by(fastforce dest:path-split)+
  from ⟨valid-edge a⟩ ⟨intra-kind (kind a)⟩ ⟨n = sourcenode a⟩
  have get-proc n = get-proc (targetnode a)
    by(fastforce intro:get-proc-intra)
  from IH[OF ⟨upd-cs cs as = []⟩ ⟨targetnode a -as→* n'⟩ ⟨∀ a∈set cs. valid-edge
a⟩]
  have (if cs = [] then get-proc (targetnode a)
    else get-proc (last (sourcenodes cs))) = get-proc n' .
  with ⟨get-proc n = get-proc (targetnode a)⟩ show ?case by auto
next
  case (slpa-Call cs a as Q r p fs)
  note IH = ⟨∧n. [[upd-cs (a#cs) as = []; n -as→* n'; ∀ a∈set (a#cs). valid-edge

```

$a]$
 \implies (if $a\#cs = []$ then $get\text{-}proc\ n$ else $get\text{-}proc\ (last\ (sourcenodes\ (a\#cs)))$) =
 $get\text{-}proc\ n'$
from $\langle kind\ a = Q:r\leftrightarrow pfs \rangle \langle upd\text{-}cs\ cs\ (a\#as) = [] \rangle$
have $upd\text{-}cs\ (a\#cs)\ as = []$ **by** *simp*
from $\langle n - a\#as \rightarrow * n' \rangle$ **have** $n - []@a\#as \rightarrow * n'$ **by** *simp*
hence $valid\text{-}edge\ a$ **and** $n = sourcenode\ a$ **and** $targetnode\ a - as \rightarrow * n'$
by (*fastforce dest:path-split*)
from $\langle valid\text{-}edge\ a \rangle \langle \forall a \in set\ cs.\ valid\text{-}edge\ a \rangle$ **have** $\forall a \in set\ (a\#cs).\ valid\text{-}edge\ a$
by *simp*
from $IH[OF\ \langle upd\text{-}cs\ (a\#cs)\ as = [] \rangle \langle targetnode\ a - as \rightarrow * n' \rangle\ this]$
have $get\text{-}proc\ (last\ (sourcenodes\ (a\#cs))) = get\text{-}proc\ n'$ **by** *simp*
with $\langle n = sourcenode\ a \rangle$ **show** $?case$ **by** (*cases cs, auto simp:sourcenodes-def*)
next
case (*slpa-Return cs a as Q p f c' cs'*)
note $IH = \langle \bigwedge n.\ []upd\text{-}cs\ cs'\ as = [];\ n - as \rightarrow * n';\ \forall a \in set\ cs'.\ valid\text{-}edge\ a \rangle$
 \implies (if $cs' = []$ then $get\text{-}proc\ n$ else $get\text{-}proc\ (last\ (sourcenodes\ cs'))$) =
 $get\text{-}proc\ n'$
from $\langle \forall a \in set\ cs.\ valid\text{-}edge\ a \rangle \langle cs = c'\#cs' \rangle$
have $valid\text{-}edge\ c'$ **and** $\forall a \in set\ cs'.\ valid\text{-}edge\ a$ **by** *simp-all*
from $\langle kind\ a = Q\leftrightarrow pfs \rangle \langle upd\text{-}cs\ cs\ (a\#as) = [] \rangle \langle cs = c'\#cs' \rangle$
have $upd\text{-}cs\ cs'\ as = []$ **by** *simp*
from $\langle n - a\#as \rightarrow * n' \rangle$ **have** $n - []@a\#as \rightarrow * n'$ **by** *simp*
hence $n = sourcenode\ a$ **and** $targetnode\ a - as \rightarrow * n'$
by (*fastforce dest:path-split*)
from $\langle valid\text{-}edge\ c' \rangle \langle a \in get\text{-}return\text{-}edges\ c' \rangle$
have $get\text{-}proc\ (sourcenode\ c') = get\text{-}proc\ (targetnode\ a)$
by (*rule get-proc-get-return-edge*)
from $IH[OF\ \langle upd\text{-}cs\ cs'\ as = [] \rangle \langle targetnode\ a - as \rightarrow * n' \rangle \langle \forall a \in set\ cs'.\ valid\text{-}edge\ a \rangle]$
have (if $cs' = []$ then $get\text{-}proc\ (targetnode\ a)$
else $get\text{-}proc\ (last\ (sourcenodes\ cs'))$) = $get\text{-}proc\ n'$.
with $\langle cs = c'\#cs' \rangle \langle get\text{-}proc\ (sourcenode\ c') = get\text{-}proc\ (targetnode\ a) \rangle$
show $?case$ **by** (*auto simp:sourcenodes-def*)
qed

lemma *slpa-get-return-edges*:

$[\textit{same-level-path-aux}\ cs\ as;\ cs \neq [];\ upd\text{-}cs\ cs\ as = [];$
 $\forall xs\ ys.\ as = xs@ys \wedge ys \neq [] \implies upd\text{-}cs\ cs\ xs \neq []]$
 $\implies last\ as \in get\text{-}return\text{-}edges\ (last\ cs)$

proof (*induct rule:slpa-induct*)

case (*slpa-empty cs*)

from $\langle cs \neq [] \rangle \langle upd\text{-}cs\ cs\ [] = [] \rangle$ **have** *False* **by** *fastforce*

thus $?case$ **by** *simp*

next

case (*slpa-intra cs a as*)

note $IH = \langle []cs \neq [];\ upd\text{-}cs\ cs\ as = [];$
 $\forall xs\ ys.\ as = xs@ys \wedge ys \neq [] \implies upd\text{-}cs\ cs\ xs \neq []]$

```

     $\implies \text{last } as \in \text{get-return-edges } (\text{last } cs)$ 
  show ?case
  proof(cases as = [])
    case True
    with  $\langle \text{intra-kind } (kind\ a) \rangle \langle \text{upd-cs } cs\ (a\#as) = [] \rangle$  have  $cs = []$ 
    by(fastforce simp:intra-kind-def)
    with  $\langle cs \neq [] \rangle$  have False by simp
    thus ?thesis by simp
  next
    case False
    from  $\langle \text{intra-kind } (kind\ a) \rangle \langle \text{upd-cs } cs\ (a\#as) = [] \rangle$  have  $\text{upd-cs } cs\ as = []$ 
    by(fastforce simp:intra-kind-def)
    from  $\langle \forall xs\ ys. a\#as = xs@ys \wedge ys \neq [] \implies \text{upd-cs } cs\ xs \neq [] \rangle \langle \text{intra-kind } (kind\ a) \rangle$ 
    have  $\forall xs\ ys. as = xs@ys \wedge ys \neq [] \implies \text{upd-cs } cs\ xs \neq []$ 
    apply(clarsimp,erule-tac x=a#xs in allE)
    by(auto simp:intra-kind-def)
    from IH[OF  $\langle cs \neq [] \rangle \langle \text{upd-cs } cs\ as = [] \rangle$  this]
    have  $\text{last } as \in \text{get-return-edges } (\text{last } cs)$  .
    with False show ?thesis by simp
  qed
next
  case (slpa-Call cs a as Q r p fs)
  note IH =  $\langle [a\#cs \neq []; \text{upd-cs } (a\#cs)\ as = [];$ 
     $\forall xs\ ys. as = xs@ys \wedge ys \neq [] \implies \text{upd-cs } (a\#cs)\ xs \neq []] \rangle$ 
     $\implies \text{last } as \in \text{get-return-edges } (\text{last } (a\#cs)) \rangle$ 
  show ?case
  proof(cases as = [])
    case True
    with  $\langle kind\ a = Q:r \hookrightarrow pfs \rangle \langle \text{upd-cs } cs\ (a\#as) = [] \rangle$  have  $a\#cs = []$  by simp
    thus ?thesis by simp
  next
    case False
    from  $\langle kind\ a = Q:r \hookrightarrow pfs \rangle \langle \text{upd-cs } cs\ (a\#as) = [] \rangle$  have  $\text{upd-cs } (a\#cs)\ as = []$ 
    by simp
    from  $\langle \forall xs\ ys. a\#as = xs@ys \wedge ys \neq [] \implies \text{upd-cs } cs\ xs \neq [] \rangle \langle kind\ a =$ 
     $Q:r \hookrightarrow pfs \rangle$ 
    have  $\forall xs\ ys. as = xs@ys \wedge ys \neq [] \implies \text{upd-cs } (a\#cs)\ xs \neq []$ 
    by(clarsimp,erule-tac x=a#xs in allE,simp)
    from IH[OF -  $\langle \text{upd-cs } (a\#cs)\ as = [] \rangle$  this]
    have  $\text{last } as \in \text{get-return-edges } (\text{last } (a\#cs))$  by simp
    with False  $\langle cs \neq [] \rangle$  show ?thesis by(simp add:targetnodes-def)
  qed
next
  case (slpa-Return cs a as Q p f c' cs')
  note IH =  $\langle [cs' \neq []; \text{upd-cs } cs'\ as = [];$ 
     $\forall xs\ ys. as = xs@ys \wedge ys \neq [] \implies \text{upd-cs } cs'\ xs \neq []] \rangle$ 
     $\implies \text{last } as \in \text{get-return-edges } (\text{last } cs') \rangle$ 
  show ?case

```

```

proof(cases as = [])
  case True
    with ⟨kind a = Q↔pf⟩ ⟨cs = c'#cs'⟩ ⟨upd-cs cs (a#as) = []⟩
    have cs' = [] by simp
    with ⟨cs = c'#cs'⟩ ⟨a ∈ get-return-edges c'⟩ True
    show ?thesis by simp
  next
    case False
    from ⟨kind a = Q↔pf⟩ ⟨cs = c'#cs'⟩ ⟨upd-cs cs (a#as) = []⟩
    have upd-cs cs' as = [] by simp
    show ?thesis
    proof(cases cs' = [])
      case True
        with ⟨cs = c'#cs'⟩ ⟨kind a = Q↔pf⟩ have upd-cs cs [a] = [] by simp
        with ⟨∀ xs ys. a#as = xs@ys ∧ ys ≠ [] ⟶ upd-cs cs xs ≠ []⟩ False have
False
          apply(erule-tac x=[a] in allE) by fastforce
          thus ?thesis by simp
      next
        case False
        from ⟨∀ xs ys. a#as = xs@ys ∧ ys ≠ [] ⟶ upd-cs cs xs ≠ []⟩
          ⟨kind a = Q↔pf⟩ ⟨cs = c'#cs'⟩
        have ∀ xs ys. as = xs@ys ∧ ys ≠ [] ⟶ upd-cs cs' xs ≠ []
          by(clarsimp,erule-tac x=a#xs in allE,simp)
        from IH[OF False ⟨upd-cs cs' as = []⟩ this]
        have last as ∈ get-return-edges (last cs') .
        with ⟨as ≠ []⟩ False ⟨cs = c'#cs'⟩ show ?thesis by(simp add:targetnodes-def)
    qed
  qed
qed

```

lemma slpa-callstack-length:

```

assumes same-level-path-aux cs as and length cs = length cfsx
obtains cfx cfsx' where transfers (kinds as) (cfsx@cf#cfs) = cfsx'@cfx#cfs
and transfers (kinds as) (cfsx@cf#cfs') = cfsx'@cfx#cfs'
and length cfsx' = length (upd-cs cs as)
proof(atomize-elim)
from assms show ∃ cfsx' cfx. transfers (kinds as) (cfsx@cf#cfs) = cfsx'@cfx#cfs
∧
  transfers (kinds as) (cfsx@cf#cfs') = cfsx'@cfx#cfs' ∧
  length cfsx' = length (upd-cs cs as)
proof(induct arbitrary:cfsx cf rule:slpa-induct)
  case (slpa-empty cs) thus ?case by(simp add:kinds-def)
next
  case (slpa-intra cs a as)
  note IH = ⟨∧ cfsx cf. length cs = length cfsx ⟹
    ∃ cfsx' cfx. transfers (kinds as) (cfsx@cf#cfs) = cfsx'@cfx#cfs ∧
    transfers (kinds as) (cfsx@cf#cfs') = cfsx'@cfx#cfs' ∧

```



```

      length cfsx' = length (upd-cs cs as)
from ⟨intra-kind (kind a)⟩
have length (upd-cs cs (a#as)) = length (upd-cs cs as)
  by(fastforce simp:intra-kind-def)
show ?case
proof(cases cfsx)
  case Nil
  with ⟨length cs = length cfsx⟩ have length cs = length [] by simp
  from Nil ⟨intra-kind (kind a)⟩
  obtain cfx where transfer:transfer (kind a) (cfsx@cf#cfs) = []@cfx#cfs
    transfer (kind a) (cfsx@cf#cfs') = []@cfx#cfs'
  by(cases kind a,auto simp:kinds-def intra-kind-def)
  from IH[OF ⟨length cs = length []⟩] obtain cfsx' cfx'
    where transfers (kinds as) ([]@cfx#cfs) = cfsx'@cfx'#cfs
    and transfers (kinds as) ([]@cfx#cfs') = cfsx'@cfx'#cfs'
    and length cfsx' = length (upd-cs cs as) by blast
  with ⟨length (upd-cs cs (a#as)) = length (upd-cs cs as)⟩ transfer
  show ?thesis by(fastforce simp:kinds-def)
next
  case (Cons x xs)
  with ⟨intra-kind (kind a)⟩ obtain cfx'
    where transfer:transfer (kind a) (cfsx@cf#cfs) = (cfx'#xs)@cf#cfs
    transfer (kind a) (cfsx@cf#cfs') = (cfx'#xs)@cf#cfs'
  by(cases kind a,auto simp:kinds-def intra-kind-def)
  from ⟨length cs = length cfsx⟩ Cons have length cs = length (cfx'#xs)
    by simp
  from IH[OF this] obtain cfs'' cf''
    where transfers (kinds as) ((cfx'#xs)@cf#cfs) = cfs''@cf''#cfs
    and transfers (kinds as) ((cfx'#xs)@cf#cfs') = cfs''@cf''#cfs'
    and length cfs'' = length (upd-cs cs as) by blast
  with ⟨length (upd-cs cs (a#as)) = length (upd-cs cs as)⟩ transfer
  show ?thesis by(fastforce simp:kinds-def)
qed
next
  case (slpa-Call cs a as Q r p fs)
  note IH = ⟨ $\bigwedge$  cfsx cf. length (a#cs) = length cfsx  $\implies$ 
     $\exists$  cfsx' cfx. transfers (kinds as) (cfsx@cf#cfs) = cfsx'@cfx#cfs  $\wedge$ 
    transfers (kinds as) (cfsx@cf#cfs') = cfsx'@cfx'#cfs'  $\wedge$ 
    length cfsx' = length (upd-cs (a#cs) as)⟩
  from ⟨kind a = Q:r $\hookrightarrow$ pfs⟩
  obtain cfx where transfer:transfer (kind a) (cfsx@cf#cfs) = (cfx#cfsx)@cf#cfs
    transfer (kind a) (cfsx@cf#cfs') = (cfx#cfsx)@cf#cfs'
  by(cases cfsx) auto
  from ⟨length cs = length cfsx⟩ have length (a#cs) = length (cfx#cfsx)
    by simp
  from IH[OF this] obtain cfsx' cfx'
    where transfers (kinds as) ((cfx#cfsx)@cf#cfs) = cfsx'@cfx'#cfs
    and transfers (kinds as) ((cfx#cfsx)@cf#cfs') = cfsx'@cfx'#cfs'
    and length cfsx' = length (upd-cs (a#cs) as) by blast

```

```

with ⟨kind a = Q:r↔pfs⟩ transfer show ?case by(fastforce simp:kinds-def)
next
case (slpa-Return cs a as Q p f c' cs')
note IH = ⟨∧cfsx cf. length cs' = length cfsx ⇒
  ∃ cfsx' cfx. transfers (kinds as) (cfsx@cfc#cfs) = cfsx'@cfx#cfs ∧
  transfers (kinds as) (cfsx@cfc#cfs') = cfsx'@cfx#cfs' ∧
  length cfsx' = length (upd-cs cs' as)⟩
from ⟨kind a = Q↔pf⟩ ⟨cs = c'#cs'⟩
have length (upd-cs cs (a#as)) = length (upd-cs cs' as) by simp
show ?case
proof(cases cs')
  case Nil
    with ⟨cs = c'#cs'⟩ ⟨length cs = length cfsx⟩ obtain cfx
    where [simp]:cfsx = [cfx] by(cases cfsx) auto
    with ⟨kind a = Q↔pf⟩ obtain cf'
    where transfer:transfer (kind a) (cfsx@cfc#cfs) = []@cf'#cfs
      transfer (kind a) (cfsx@cfc#cfs') = []@cf'#cfs'
    by fastforce
    from Nil have length cs' = length [] by simp
    from IH[OF this] obtain cfsx' cfx'
    where transfers (kinds as) ([]@cf'#cfs) = cfsx'@cfx'#cfs
      and transfers (kinds as) ([]@cf'#cfs') = cfsx'@cfx'#cfs'
      and length cfsx' = length (upd-cs cs' as) by blast
    with ⟨length (upd-cs cs (a#as)) = length (upd-cs cs' as)⟩ transfer
    show ?thesis by(fastforce simp:kinds-def)
  next
    case (Cons cx csx)
    with ⟨cs = c'#cs'⟩ ⟨length cs = length cfsx⟩ obtain x x' xs
    where [simp]:cfsx = x#x'#xs and length xs = length csx
      by(cases cfsx,auto,case-tac list,fastforce+)
    with ⟨kind a = Q↔pf⟩ obtain cf'
    where transfer:transfer (kind a) ((x#x'#xs)@cfc#cfs) = (cf'#xs)@cfc#cfs
      transfer (kind a) ((x#x'#xs)@cfc#cfs') = (cf'#xs)@cfc#cfs'
    by fastforce
    from ⟨cs = c'#cs'⟩ ⟨length cs = length cfsx⟩ have length cs' = length (cf'#xs)
      by simp
    from IH[OF this] obtain cfsx' cfx
    where transfers (kinds as) ((cf'#xs)@cfc#cfs) = cfsx'@cfx#cfs
      and transfers (kinds as) ((cf'#xs)@cfc#cfs') = cfsx'@cfx#cfs'
      and length cfsx' = length (upd-cs cs' as) by blast
    with ⟨length (upd-cs cs (a#as)) = length (upd-cs cs' as)⟩ transfer
    show ?thesis by(fastforce simp:kinds-def)
  qed
qed
qed

```

lemma slpa-snoc-intra:

[[same-level-path-aux cs as; intra-kind (kind a)]]

\Rightarrow *same-level-path-aux cs (as@[a])*
by(*induct rule:slpa-induct,auto simp:intra-kind-def*)

lemma *slpa-snoc-Call*:

\llbracket *same-level-path-aux cs as; kind a = Q:r \hookrightarrow pfs* \rrbracket
 \Rightarrow *same-level-path-aux cs (as@[a])*
by(*induct rule:slpa-induct,auto simp:intra-kind-def*)

lemma *vpa-Main-slpa*:

\llbracket *valid-path-aux cs as; m -as \rightarrow^* m'; as \neq [];*
valid-call-list cs m; get-proc m' = Main;
get-proc (case cs of [] \Rightarrow m | - \Rightarrow sourcenode (last cs)) = Main \rrbracket
 \Rightarrow *same-level-path-aux cs as \wedge upd-cs cs as = []*

proof(*induct arbitrary:m rule:vpa-induct*)

case (*vpa-empty cs*) **thus** ?*case* **by** *simp*

next

case (*vpa-intra cs a as*)

note *IH = $\langle \bigwedge m. \llbracket m -as\rightarrow^* m'; as \neq [];$*
valid-call-list cs m; get-proc m' = Main;
get-proc (case cs of [] \Rightarrow m | a # list \Rightarrow sourcenode (last cs)) = Main \rrbracket
 \Rightarrow *same-level-path-aux cs as \wedge upd-cs cs as = []*

from $\langle m -a \# as\rightarrow^* m' \rangle$ **have** *sourcenode a = m* **and** *valid-edge a*
and *targetnode a -as \rightarrow^* m'* **by**(*auto elim:path-split-Cons*)

from \langle *valid-edge a* \rangle \langle *intra-kind (kind a)* \rangle

have *get-proc (sourcenode a) = get-proc (targetnode a)* **by**(*rule get-proc-intra*)

show ?*case*

proof(*cases as = []*)

case *True*

with \langle *targetnode a -as \rightarrow^* m'* \rangle **have** *targetnode a = m'* **by** *fastforce*

with \langle *get-proc (sourcenode a) = get-proc (targetnode a)* \rangle

\langle *sourcenode a = m* \rangle \langle *get-proc m' = Main* \rangle

have *get-proc m = Main* **by** *simp*

have *cs = []*

proof(*cases cs*)

case *Cons*

with \langle *valid-call-list cs m* \rangle

obtain *c Q r p fs* **where** *valid-edge c* **and** *kind c = Q:r \hookrightarrow get-proc m.fs*

by(*auto simp:valid-call-list-def,erule-tac x=[] in allE,*

auto simp:sourcenodes-def)

with \langle *get-proc m = Main* \rangle **have** *kind c = Q:r \hookrightarrow Main.fs* **by** *simp*

with \langle *valid-edge c* \rangle **have** *False* **by**(*rule Main-no-call-target*)

thus ?*thesis* **by** *simp*

qed *simp*

with *True* \langle *intra-kind (kind a)* \rangle **show** ?*thesis* **by**(*fastforce simp:intra-kind-def*)

next

case *False*

from \langle *valid-call-list cs m* \rangle \langle *sourcenode a = m* \rangle

\langle *get-proc (sourcenode a) = get-proc (targetnode a)* \rangle

```

have valid-call-list cs (targetnode a)
  apply(clarsimp simp:valid-call-list-def)
  apply(erule-tac x=cs' in allE)
  apply(erule-tac x=c in allE)
  by(auto split:list.split)
from  $\langle \text{get-proc (case cs of [] \Rightarrow m \mid - \Rightarrow \text{sourcenode (last cs)}) = Main} \rangle$ 
   $\langle \text{sourcenode a = m} \rangle \langle \text{get-proc (sourcenode a) = get-proc (targetnode a)} \rangle$ 
have get-proc (case cs of [] \Rightarrow targetnode a \mid - \Rightarrow sourcenode (last cs)) = Main
  by(cases cs) auto
from  $\text{IH}[\text{OF} \langle \text{targetnode a -as} \rightarrow^* m' \rangle \text{False} \langle \text{valid-call-list cs (targetnode a)} \rangle$ 
   $\langle \text{get-proc m' = Main} \rangle \text{this}]$ 
have same-level-path-aux cs as \wedge upd-cs cs as = [] .
with  $\langle \text{intra-kind (kind a)} \rangle$  show ?thesis by(fastforce simp:intra-kind-def)
qed
next
case (vpa-Call cs a as Q r p fs)
note  $\text{IH} = \langle \bigwedge m. \llbracket m -as \rightarrow^* m'; as \neq []; \text{valid-call-list (a \# cs) m};$ 
   $\text{get-proc m' = Main};$ 
   $\text{get-proc (case a \# cs of [] \Rightarrow m \mid - \Rightarrow \text{sourcenode (last (a \# cs))}) = Main} \rrbracket$ 
   $\Rightarrow \text{same-level-path-aux (a \# cs) as} \wedge \text{upd-cs (a \# cs) as} = [] \rangle$ 
from  $\langle m -a \# as \rightarrow^* m' \rangle$  have sourcenode a = m and valid-edge a
  and targetnode a -as} \rightarrow^* m' by(auto elim:path-split-Cons)
from  $\langle \text{valid-edge a} \rangle \langle \text{kind a = Q:r} \hookrightarrow pfs \rangle$  have get-proc (targetnode a) = p
  by(rule get-proc-call)
show ?case
proof(cases as = [])
  case True
  with  $\langle \text{targetnode a -as} \rightarrow^* m' \rangle$  have targetnode a = m' by fastforce
  with  $\langle \text{get-proc (targetnode a) = p} \rangle \langle \text{get-proc m' = Main} \rangle \langle \text{kind a = Q:r} \hookrightarrow pfs \rangle$ 
  have kind a = Q:r} \hookrightarrow \text{Mainfs} by simp
  with  $\langle \text{valid-edge a} \rangle$  have False by(rule Main-no-call-target)
  thus ?thesis by simp
next
case False
from  $\langle \text{get-proc (targetnode a) = p} \rangle \langle \text{valid-call-list cs m} \rangle \langle \text{valid-edge a} \rangle$ 
   $\langle \text{kind a = Q:r} \hookrightarrow pfs \rangle \langle \text{sourcenode a = m} \rangle$ 
have valid-call-list (a \# cs) (targetnode a)
  apply(clarsimp simp:valid-call-list-def)
  apply(case-tac cs') apply auto
  apply(erule-tac x=list in allE)
  by(case-tac list)(auto simp:sourcenodes-def)
from  $\langle \text{get-proc (case cs of [] \Rightarrow m \mid - \Rightarrow \text{sourcenode (last cs)}) = Main} \rangle$ 
   $\langle \text{sourcenode a = m} \rangle$ 
have get-proc (case a \# cs of [] \Rightarrow targetnode a
   $\mid - \Rightarrow \text{sourcenode (last (a \# cs))}) = \text{Main}$ 
  by(cases cs) auto
from  $\text{IH}[\text{OF} \langle \text{targetnode a -as} \rightarrow^* m' \rangle \text{False} \langle \text{valid-call-list (a\#cs) (targetnode$ 
   $a) \rangle$ 
   $\langle \text{get-proc m' = Main} \rangle \text{this}]$ 

```

```

  have same-level-path-aux (a # cs) as  $\wedge$  upd-cs (a # cs) as = [] .
  with  $\langle$ kind a = Q:r $\leftrightarrow$ pfs $\rangle$  show ?thesis by simp
qed
next
case (vpa-ReturnEmpty cs a as Q p f)
note IH =  $\langle$  $\wedge$ m.  $\llbracket$ m -as $\rightarrow$ * m'; as  $\neq$  []; valid-call-list [] m; get-proc m' = Main;
  get-proc (case [] of []  $\Rightarrow$  m | a # list  $\Rightarrow$  sourcenode (last [])) = Main $\rrbracket$ 
 $\Rightarrow$  same-level-path-aux [] as  $\wedge$  upd-cs [] as = [] $\rangle$ 
from  $\langle$ m -a # as $\rightarrow$ * m' $\rangle$  have sourcenode a = m and valid-edge a
  and targetnode a -as $\rightarrow$ * m' by(auto elim:path-split-Cons)
from  $\langle$ valid-edge a $\rangle$   $\langle$ kind a = Q $\leftrightarrow$ pf $\rangle$  have get-proc (sourcenode a) = p
  by(rule get-proc-return)
from  $\langle$ get-proc (case cs of []  $\Rightarrow$  m | a # list  $\Rightarrow$  sourcenode (last cs)) = Main $\rangle$ 
 $\langle$ cs = [] $\rangle$ 
have get-proc m = Main by simp
with  $\langle$ sourcenode a = m $\rangle$   $\langle$ get-proc (sourcenode a) = p $\rangle$  have p = Main by simp
with  $\langle$ kind a = Q $\leftrightarrow$ pf $\rangle$  have kind a = Q $\leftrightarrow$ Mainf by simp
with  $\langle$ valid-edge a $\rangle$  have False by(rule Main-no-return-source)
thus ?case by simp
next
case (vpa-ReturnCons cs a as Q p f c' cs')
note IH =  $\langle$  $\wedge$ m.  $\llbracket$ m -as $\rightarrow$ * m'; as  $\neq$  []; valid-call-list cs' m; get-proc m' =
Main;
  get-proc (case cs' of []  $\Rightarrow$  m | a # list  $\Rightarrow$  sourcenode (last cs')) = Main $\rrbracket$ 
 $\Rightarrow$  same-level-path-aux cs' as  $\wedge$  upd-cs cs' as = [] $\rangle$ 
from  $\langle$ m -a # as $\rightarrow$ * m' $\rangle$  have sourcenode a = m and valid-edge a
  and targetnode a -as $\rightarrow$ * m' by(auto elim:path-split-Cons)
from  $\langle$ valid-edge a $\rangle$   $\langle$ kind a = Q $\leftrightarrow$ pf $\rangle$  have get-proc (sourcenode a) = p
  by(rule get-proc-return)
from  $\langle$ valid-call-list cs m $\rangle$   $\langle$ cs = c' # cs' $\rangle$ 
have valid-edge c'
  by(auto simp:valid-call-list-def,erule-tac x=[] in allE,auto)
from  $\langle$ valid-edge c' $\rangle$   $\langle$ a  $\in$  get-return-edges c' $\rangle$ 
have get-proc (sourcenode c') = get-proc (targetnode a)
  by(rule get-proc-get-return-edge)
show ?case
proof(cases as = [])
case True
with  $\langle$ targetnode a -as $\rightarrow$ * m' $\rangle$  have targetnode a = m' by fastforce
with  $\langle$ get-proc m' = Main $\rangle$  have get-proc (targetnode a) = Main by simp
from  $\langle$ get-proc (sourcenode c') = get-proc (targetnode a) $\rangle$ 
 $\langle$ get-proc (targetnode a) = Main $\rangle$ 
have get-proc (sourcenode c') = Main by simp
have cs' = []
proof(cases cs')
case (Cons cx csx)
with  $\langle$ cs = c' # cs' $\rangle$   $\langle$ valid-call-list cs m $\rangle$ 
obtain Qx rx fsx where valid-edge cx
  and kind cx = Qx:rx $\leftrightarrow$ get-proc (sourcenode c')fsx

```

```

      by (auto simp: valid-call-list-def,erule-tac x=[c'] in allE,
          auto simp: sourcenodes-def)
    with ⟨get-proc (sourcenode c') = Main⟩ have kind cx = Qx:rx↔Mainfsx by
simp
    with ⟨valid-edge cx⟩ have False by (rule Main-no-call-target)
    thus ?thesis by simp
  qed simp
  with True ⟨cs = c' # cs'⟩ ⟨a ∈ get-return-edges c'⟩ ⟨kind a = Q↔pf⟩
  show ?thesis by simp
next
  case False
  from ⟨valid-call-list cs m⟩ ⟨cs = c' # cs'⟩
    ⟨get-proc (sourcenode c') = get-proc (targetnode a)⟩
  have valid-call-list cs' (targetnode a)
    apply (clarsimp simp: valid-call-list-def)
    apply (erule-tac x=c' # cs' in allE)
    by (case-tac cs')(auto simp: sourcenodes-def)
  from ⟨get-proc (case cs of [] ⇒ m | a # list ⇒ sourcenode (last cs)) = Main⟩
    ⟨cs = c' # cs'⟩ ⟨get-proc (sourcenode c') = get-proc (targetnode a)⟩
  have get-proc (case cs' of [] ⇒ targetnode a
    | - ⇒ sourcenode (last cs')) = Main
    by (cases cs') auto
  from IH[OF ⟨targetnode a -as→* m'⟩ False ⟨valid-call-list cs' (targetnode a)⟩
    ⟨get-proc m' = Main⟩ this]
  have same-level-path-aux cs' as ∧ upd-cs cs' as = [].
  with ⟨kind a = Q↔pf⟩ ⟨cs = c' # cs'⟩ ⟨a ∈ get-return-edges c'⟩
  show ?thesis by simp
qed
qed

```

definition *same-level-path* :: 'edge list ⇒ bool
 where *same-level-path* as ≡ *same-level-path-aux* [] as ∧ *upd-cs* [] as = []

lemma *same-level-path-valid-path*:
same-level-path as ⇒ *valid-path* as
by (fastforce intro: *same-level-path-aux-valid-path-aux*
simp:same-level-path-def valid-path-def)

lemma *same-level-path-Append*:
 [[*same-level-path* as; *same-level-path* as']] ⇒ *same-level-path* (as@as')
by (fastforce elim: *same-level-path-aux-Append upd-cs-Append simp:same-level-path-def*)

lemma *same-level-path-number-Calls-eq-number>Returns*:
 [[*same-level-path* as; ∀ a ∈ set as. *valid-edge* a]] ⇒

$length [a \leftarrow as. \exists Q r p fs. kind a = Q:r \leftrightarrow pfs] = length [a \leftarrow as. \exists Q p f. kind a = Q \leftrightarrow pf]$
by(*fastforce dest:slpa-number-Calls-eq-number>Returns simp:same-level-path-def*)

lemma same-level-path-valid-path-Append:
 $\llbracket same-level-path\ as; valid-path\ as \rrbracket \implies valid-path\ (as @ as')$
by(*fastforce intro:valid-path-aux-Append elim:same-level-path-aux-valid-path-aux simp:valid-path-def same-level-path-def*)

lemma valid-path-same-level-path-Append:
 $\llbracket valid-path\ as; same-level-path\ as \rrbracket \implies valid-path\ (as @ as')$
apply(*auto simp:valid-path-def same-level-path-def*)
apply(*erule valid-path-aux-Append*)
by(*fastforce intro!:same-level-path-aux-valid-path-aux dest:same-level-path-aux-callstack-Append*)

lemma intras-same-level-path:
assumes $\forall a \in set\ as. intra-kind(kind\ a)$ **shows** *same-level-path as*
proof –
from $\langle \forall a \in set\ as. intra-kind(kind\ a) \rangle$ **have** *same-level-path-aux [] as*
by(*induct as*)(*auto simp:intra-kind-def*)
moreover
from $\langle \forall a \in set\ as. intra-kind(kind\ a) \rangle$ **have** *upd-cs [] as = []*
by(*induct as*)(*auto simp:intra-kind-def*)
ultimately show *?thesis* **by**(*simp add:same-level-path-def*)
qed

definition same-level-path' :: 'node \Rightarrow 'edge list \Rightarrow 'node \Rightarrow bool
 $(- \dashrightarrow_{sl^*} - [51,0,0] 80)$
where *slp-def:n -as \rightarrow_{sl^*} n' \equiv n -as \rightarrow^* n' \wedge same-level-path as*

lemma slp-vp: n -as \rightarrow_{sl^*} n' \implies n -as $\rightarrow_{\checkmark}^*$ n'
by(*fastforce intro:same-level-path-valid-path simp:slp-def vp-def*)

lemma intra-path-slp: n -as \rightarrow_l^* n' \implies n -as \rightarrow_{sl^*} n'
by(*fastforce intro:intras-same-level-path simp:slp-def intra-path-def*)

lemma slp-Append:
 $\llbracket n -as \rightarrow_{sl^*} n''; n'' -as' \rightarrow_{sl^*} n' \rrbracket \implies n -as @ as' \rightarrow_{sl^*} n'$
by(*fastforce simp:slp-def intro:path-Append same-level-path-Append*)

lemma slp-vp-Append:
 $\llbracket n -as \rightarrow_{sl^*} n''; n'' -as' \rightarrow_{\checkmark}^* n' \rrbracket \implies n -as @ as' \rightarrow_{\checkmark}^* n'$
by(*fastforce simp:slp-def vp-def intro:path-Append same-level-path-valid-path-Append*)

lemma *vp-slp-Append*:
 $\llbracket n - as \rightarrow_{\sqrt{*}} n''; n'' - as' \rightarrow_{sl{*}} n' \rrbracket \implies n - as @ as' \rightarrow_{\sqrt{*}} n'$
by(*fastforce simp:slp-def vp-def intro:path-Append valid-path-same-level-path-Append*)

lemma *slp-get-proc*:
 $n - as \rightarrow_{sl{*}} n' \implies \text{get-proc } n = \text{get-proc } n'$
by(*fastforce dest:slpa-get-proc simp:same-level-path-def slp-def*)

lemma *same-level-path-inner-path*:
assumes $n - as \rightarrow_{sl{*}} n'$
obtains as' **where** $n - as' \rightarrow_{l{*}} n'$ **and** $\text{set}(\text{sourcenodes } as') \subseteq \text{set}(\text{sourcenodes } as)$
proof(*atomize-elim*)
from $\langle n - as \rightarrow_{sl{*}} n' \rangle$ **have** $n - as \rightarrow_{*} n'$ **and** *same-level-path* as
by(*simp-all add:slp-def*)
from $\langle \text{same-level-path } as \rangle$ **have** *same-level-path-aux* $\square as$ **and** *upd-cs* $\square as = \square$
by(*simp-all add:same-level-path-def*)
from $\langle n - as \rightarrow_{*} n' \rangle \langle \text{same-level-path-aux } \square as \rangle \langle \text{upd-cs } \square as = \square \rangle$
show $\exists as'. n - as' \rightarrow_{l{*}} n' \wedge \text{set}(\text{sourcenodes } as') \subseteq \text{set}(\text{sourcenodes } as)$
proof(*induct as arbitrary:n rule:length-induct*)
fix $as\ n$
assume $IH:\forall as''. \text{length } as'' < \text{length } as \longrightarrow$
 $(\forall n''. n'' - as'' \rightarrow_{*} n' \longrightarrow \text{same-level-path-aux } \square as'' \longrightarrow$
 $\text{upd-cs } \square as'' = \square \longrightarrow$
 $(\exists as'. n'' - as' \rightarrow_{l{*}} n' \wedge \text{set}(\text{sourcenodes } as') \subseteq \text{set}(\text{sourcenodes } as''))$)
and $n - as \rightarrow_{*} n'$ **and** *same-level-path-aux* $\square as$ **and** *upd-cs* $\square as = \square$
show $\exists as'. n - as' \rightarrow_{l{*}} n' \wedge \text{set}(\text{sourcenodes } as') \subseteq \text{set}(\text{sourcenodes } as)$
proof(*cases as*)
case *Nil*
with $\langle n - as \rightarrow_{*} n' \rangle$ **show** *?thesis* **by**(*fastforce simp:intra-path-def*)
next
case $(\text{Cons } a' as')$
with $\langle n - as \rightarrow_{*} n' \rangle$ *Cons* **have** $n = \text{sourcenode } a'$ **and** *valid-edge* a'
and *targetnode* $a' - as' \rightarrow_{*} n'$
by(*auto intro:path-split-Cons*)
show *?thesis*
proof(*cases kind a' rule:edge-kind-cases*)
case *Intra*
with *Cons* $\langle \text{same-level-path-aux } \square as \rangle$ **have** *same-level-path-aux* $\square as'$
by(*fastforce simp:intra-kind-def*)
moreover
from *Intra Cons* $\langle \text{upd-cs } \square as = \square \rangle$ **have** *upd-cs* $\square as' = \square$
by(*fastforce simp:intra-kind-def*)
ultimately obtain as'' **where** *targetnode* $a' - as'' \rightarrow_{l{*}} n'$
and $\text{set}(\text{sourcenodes } as'') \subseteq \text{set}(\text{sourcenodes } as')$


```

    using IH Cons ⟨targetnode a' -as'→* n'⟩
    by(erule-tac x=as' in allE) auto
  from ⟨n = sourcenode a'⟩ ⟨valid-edge a'⟩ Intra ⟨targetnode a' -as''→ι* n'⟩
  have n -a'#as''→ι* n' by(fastforce intro:Cons-path simp:intra-path-def)
  with ⟨set (sourcenodes as'') ⊆ set (sourcenodes as')⟩ Cons show ?thesis
    by(rule-tac x=a'#as'' in exI, auto simp:sourcenodes-def)
next
case (Call Q p f)
with Cons ⟨same-level-path-aux [] as⟩
have same-level-path-aux [a'] as' by simp
from Call Cons ⟨upd-cs [] as = []⟩ have upd-cs [a'] as' = [] by simp
hence as' ≠ [] by fastforce
with ⟨upd-cs [a'] as' = []⟩ obtain xs ys where as' = xs@ys and xs ≠ []
and upd-cs [a'] xs = [] and upd-cs [] ys = []
and ∀ xs' ys'. xs = xs'@ys' ∧ ys' ≠ [] → upd-cs [a'] xs' ≠ []
  by -(erule upd-cs-empty-split, auto)
from ⟨same-level-path-aux [a'] as'⟩ ⟨as' = xs@ys⟩ ⟨upd-cs [a'] xs = []⟩
have same-level-path-aux [a'] xs and same-level-path-aux [] ys
  by(auto intro:slpa-split)
from ⟨same-level-path-aux [a'] xs⟩ ⟨upd-cs [a'] xs = []⟩
  ⟨∀ xs' ys'. xs = xs'@ys' ∧ ys' ≠ [] → upd-cs [a'] xs' ≠ []⟩
have last xs ∈ get-return-edges (last [a'])
  by(fastforce intro!:slpa-get-return-edges)
with ⟨valid-edge a'⟩ Call
obtain a where valid-edge a and sourcenode a = sourcenode a'
  and targetnode a = targetnode (last xs) and kind a = (λcf. False)✓
  by -(drule call-return-node-edge, auto)
from ⟨targetnode a = targetnode (last xs)⟩ ⟨xs ≠ []⟩
have targetnode a = targetnode (last (a'#xs)) by simp
from ⟨as' = xs@ys⟩ ⟨xs ≠ []⟩ Cons have length ys < length as by simp
from ⟨targetnode a' -as'→* n'⟩ ⟨as' = xs@ys⟩ ⟨xs ≠ []⟩
have targetnode (last (a'#xs)) -ys→* n'
  by(cases xs rule:rev-cases, auto dest:path-split)
with IH ⟨length ys < length as⟩ ⟨same-level-path-aux [] ys⟩
  ⟨upd-cs [] ys = []⟩
obtain as'' where targetnode (last (a'#xs)) -as''→ι* n'
  and set(sourcenodes as'') ⊆ set(sourcenodes ys)
  apply(erule-tac x=ys in allE) apply clarsimp
  apply(erule-tac x=targetnode (last (a'#xs)) in allE)
  by clarsimp
from ⟨sourcenode a = sourcenode a'⟩ ⟨n = sourcenode a'⟩
  ⟨targetnode a = targetnode (last (a'#xs))⟩ ⟨valid-edge a⟩
  ⟨kind a = (λcf. False)✓⟩ ⟨targetnode (last (a'#xs)) -as''→ι* n'⟩
have n -a'#as''→ι* n'
  by(fastforce intro:Cons-path simp:intra-path-def intra-kind-def)
moreover
from ⟨set(sourcenodes as'') ⊆ set(sourcenodes ys)⟩ Cons ⟨as' = xs@ys⟩
  ⟨sourcenode a = sourcenode a'⟩
have set(sourcenodes (a'#as'')) ⊆ set(sourcenodes as)

```

```

      by(auto simp:sourcenodes-def)
    ultimately show ?thesis by blast
  next
    case (Return Q p f)
    with Cons ⟨same-level-path-aux [] as⟩ have False by simp
    thus ?thesis by simp
  qed
qed
qed
qed

```

lemma *slp-callstack-length-equal*:

```

  assumes  $n - as \rightarrow_{sl}^* n'$  obtains  $cf'$  where transfers (kinds as) ( $cf \# cfs$ ) =  $cf' \# cfs$ 
  and transfers (kinds as) ( $cf \# cfs'$ ) =  $cf' \# cfs'$ 
proof(atomize-elim)
  from ⟨ $n - as \rightarrow_{sl}^* n'$ ⟩ have same-level-path-aux [] as and upd-cs [] as = []
  by(simp-all add:slp-def same-level-path-def)
  then obtain cfx cfsx where transfers (kinds as) ( $cf \# cfs$ ) =  $cfx \# cfsx$ 
  and transfers (kinds as) ( $cf \# cfs'$ ) =  $cfx \# cfsx$ 
  and length cfsx = length (upd-cs [] as)
  by(fastforce elim:slpa-callstack-length)
  with ⟨upd-cs [] as = []⟩ have cfx = [] by(cases cfsx) auto
  with ⟨transfers (kinds as) ( $cf \# cfs$ ) =  $cfx \# cfsx$ ⟩
  ⟨transfers (kinds as) ( $cf \# cfs'$ ) =  $cfx \# cfsx$ ⟩
  show  $\exists cf'$ . transfers (kinds as) ( $cf \# cfs$ ) =  $cf' \# cfs$   $\wedge$ 
  transfers (kinds as) ( $cf \# cfs'$ ) =  $cf' \# cfs'$  by fastforce
qed

```

lemma *slp-cases* [*consumes 1, case-names intra-path return-intra-path*]:

```

  assumes  $m - as \rightarrow_{sl}^* m'$ 
  obtains  $m - as \rightarrow_{\iota}^* m'$ 
  |  $as' a as'' Q p f$  where  $as = as' @ a \# as''$  and kind a =  $Q \leftrightarrow p f$ 
  and  $m - as' @ [a] \rightarrow_{sl}^* \text{targetnode } a$  and  $\text{targetnode } a - as'' \rightarrow_{\iota}^* m'$ 
proof(atomize-elim)
  from ⟨ $m - as \rightarrow_{sl}^* m'$ ⟩ have  $m - as \rightarrow^* m'$  and same-level-path-aux [] as
  and upd-cs [] as = [] by(simp-all add:slp-def same-level-path-def)
  from ⟨ $m - as \rightarrow^* m'$ ⟩ have  $\forall a \in \text{set } as$ . valid-edge a by(rule path-valid-edges)
  have  $\forall a \in \text{set []}$ . valid-edge a by simp
  with ⟨same-level-path-aux [] as⟩ ⟨upd-cs [] as = []⟩ ⟨ $\forall a \in \text{set []}$ . valid-edge a⟩
  ⟨ $\forall a \in \text{set } as$ . valid-edge a⟩
  show  $m - as \rightarrow_{\iota}^* m' \vee$ 
  ( $\exists as' a as'' Q p f$ .  $as = as' @ a \# as'' \wedge \text{kind } a = Q \leftrightarrow p f \wedge$ 
   $m - as' @ [a] \rightarrow_{sl}^* \text{targetnode } a \wedge \text{targetnode } a - as'' \rightarrow_{\iota}^* m'$ )
proof(cases rule:slpa-cases)
  case intra-path
  with ⟨ $m - as \rightarrow^* m'$ ⟩ have  $m - as \rightarrow_{\iota}^* m'$  by(simp add:intra-path-def)

```

```

thus ?thesis by blast
next
case (return-intra-path as' a as'' Q p f c' cs')
from ⟨m -as'→* m'⟩ ⟨as = as' @ a # as''⟩
have m -as'→* sourcenode a and valid-edge a and targetnode a -as''→* m'
  by(auto intro:path-split)
from ⟨m -as'→* sourcenode a⟩ ⟨valid-edge a⟩
have m -as'@[a]→* targetnode a by(fastforce intro:path-Append path-edge)
with ⟨same-level-path-aux [] as'⟩ ⟨upd-cs [] as' = c' # cs'⟩ ⟨kind a = Q↔pf⟩
  ⟨a ∈ get-return-edges c'⟩
have same-level-path-aux [] (as'@[a])
  by(fastforce intro:same-level-path-aux-Append)
with ⟨upd-cs [] (as' @ [a]) = []⟩ ⟨m -as'@[a]→* targetnode a⟩
have m -as'@[a]→st* targetnode a by(simp add:slp-def same-level-path-def)
moreover
from ⟨∀ a ∈ set as''. intra-kind (kind a)⟩ ⟨targetnode a -as''→* m'⟩
have targetnode a -as''→,* m' by(simp add:intra-path-def)
ultimately show ?thesis using ⟨as = as' @ a # as''⟩ ⟨kind a = Q↔pf⟩ by
blast
qed
qed

```

```

function same-level-path-rev-aux :: 'edge list ⇒ 'edge list ⇒ bool
where same-level-path-rev-aux cs [] ↔ True
| same-level-path-rev-aux cs (as@[a]) ↔
  (case (kind a) of Q↔pf ⇒ same-level-path-rev-aux (a#cs) as
  | Q:r↔pfs ⇒ case cs of [] ⇒ False
  | c'#cs' ⇒ c' ∈ get-return-edges a ∧
    same-level-path-rev-aux cs' as
  | - ⇒ same-level-path-rev-aux cs as)
by auto(case-tac b rule:rev-cases,auto)
termination by lexicographic-order

```

```

lemma slpra-induct [consumes 1,case-names slpra-empty slpra-intra slpra-Return
slpra-Call]:
assumes major: same-level-path-rev-aux xs ys
and rules: ∧ cs. P cs []
  ∧ cs a as. [[intra-kind(kind a); same-level-path-rev-aux cs as; P cs as]]
  ⇒ P cs (as@[a])
  ∧ cs a as Q p f. [[kind a = Q↔pf; same-level-path-rev-aux (a#cs) as; P (a#cs)
as]]
  ⇒ P cs (as@[a])
  ∧ cs a as Q r p fs c' cs'. [[kind a = Q:r↔pfs; cs = c'#cs';
  same-level-path-rev-aux cs' as; c' ∈ get-return-edges a; P cs' as]]
  ⇒ P cs (as@[a])
shows P xs ys
using major

```

apply(*induct ys arbitrary: xs rule:rev-induct*)
by(*auto intro:rules split:edge-kind.split-asm list.split-asm simp:intra-kind-def*)

lemma *same-level-path-rev-aux-Append*:
 $\llbracket \text{same-level-path-rev-aux } cs \text{ as}' ; \text{same-level-path-rev-aux } (\text{upd-rev-cs } cs \text{ as}') \text{ as} \rrbracket$
 $\implies \text{same-level-path-rev-aux } cs \text{ (as@as')}$
by(*induct rule:slpra-induct*,
auto simp:intra-kind-def simp del:append-assoc simp:append-assoc[THEN sym])

lemma *slpra-to-slpa*:
 $\llbracket \text{same-level-path-rev-aux } cs \text{ as} ; \text{upd-rev-cs } cs \text{ as} = [] ; n -as \rightarrow^* n' ;$
 $\text{valid-return-list } cs \text{ n} \rrbracket$
 $\implies \text{same-level-path-aux } [] \text{ as} \wedge \text{same-level-path-aux } (\text{upd-cs } [] \text{ as}) \text{ cs} \wedge$
 $\text{upd-cs } (\text{upd-cs } [] \text{ as}) \text{ cs} = []$
proof(*induct arbitrary:n' rule:slpra-induct*)
case *slpra-empty* **thus** ?*case* **by** *simp*
next
case (*slpra-intra* *cs a as*)
note *IH* = $\langle \wedge n'. \llbracket \text{upd-rev-cs } cs \text{ as} = [] ; n -as \rightarrow^* n' ; \text{valid-return-list } cs \text{ n} \rrbracket$
 $\implies \text{same-level-path-aux } [] \text{ as} \wedge \text{same-level-path-aux } (\text{upd-cs } [] \text{ as}) \text{ cs} \wedge$
 $\text{upd-cs } (\text{upd-cs } [] \text{ as}) \text{ cs} = [] \rangle$
from $\langle n -as@[a] \rightarrow^* n' \rangle$ **have** $n -as \rightarrow^* \text{sourcenode } a$ **and** *valid-edge* *a*
and $n' = \text{targetnode } a$ **by**(*auto intro:path-split-snoc*)
from $\langle \text{valid-edge } a \rangle \langle \text{intra-kind } (\text{kind } a) \rangle$
have *get-proc* (*sourcenode a*) = *get-proc* (*targetnode a*)
by(*rule get-proc-intra*)
with $\langle \text{valid-return-list } cs \text{ n}' \rangle \langle n' = \text{targetnode } a \rangle$
have *valid-return-list* *cs* (*sourcenode a*)
apply(*clarsimp simp:valid-return-list-def*)
apply(*erule-tac x=cs' in allE*) **apply** *clarsimp*
by(*case-tac cs'*)(*auto simp:targetnodes-def*)
from $\langle \text{upd-rev-cs } cs \text{ (as@[a])} = [] \rangle \langle \text{intra-kind } (\text{kind } a) \rangle$
have $\text{upd-rev-cs } cs \text{ as} = []$ **by**(*fastforce simp:intra-kind-def*)
from $\langle \text{valid-edge } a \rangle \langle \text{intra-kind } (\text{kind } a) \rangle$
have *get-proc* (*sourcenode a*) = *get-proc* (*targetnode a*) **by**(*rule get-proc-intra*)
from *IH*[*OF* $\langle \text{upd-rev-cs } cs \text{ as} = [] \rangle \langle n -as \rightarrow^* \text{sourcenode } a \rangle$
 $\langle \text{valid-return-list } cs \text{ (sourcenode } a) \rangle$]
have *same-level-path-aux* $[] \text{ as}$
and *same-level-path-aux* (*upd-cs* $[] \text{ as}$) *cs*
and $\text{upd-cs } (\text{upd-cs } [] \text{ as}) \text{ cs} = []$ **by** *simp-all*
from $\langle \text{same-level-path-aux } [] \text{ as} \rangle \langle \text{intra-kind } (\text{kind } a) \rangle$
have *same-level-path-aux* $[] \text{ (as@[a])}$ **by**(*rule slpa-snoc-intra*)
from $\langle \text{intra-kind } (\text{kind } a) \rangle$
have $\text{upd-cs } [] \text{ (as@[a])} = \text{upd-cs } [] \text{ as}$
by(*fastforce simp:upd-cs-Append intra-kind-def*)
moreover
from $\langle \text{same-level-path-aux } [] \text{ as} \rangle \langle \text{intra-kind } (\text{kind } a) \rangle$

```

have same-level-path-aux [] (as@[a]) by(rule slpa-snoc-intra)
ultimately show ?case using ⟨same-level-path-aux (upd-cs [] as) cs⟩
  ⟨upd-cs (upd-cs [] as) cs = []⟩
by simp
next
case (slpra-Return cs a as Q p f)
note IH = ⟨ $\bigwedge n' n''$ . [upd-rev-cs (a#cs) as = []; n -as→* n'];
  valid-return-list (a#cs) n'⟩
⇒ same-level-path-aux [] as ∧
  same-level-path-aux (upd-cs [] as) (a#cs) ∧
  upd-cs (upd-cs [] as) (a#cs) = []⟩
from ⟨n -as@[a]→* n'⟩ have n -as→* sourcenode a and valid-edge a
and n' = targetnode a by(auto intro:path-split-snoc)
from ⟨valid-edge a⟩ ⟨kind a = Q↔pf⟩ have p = get-proc (sourcenode a)
by(rule get-proc-return[THEN sym])
from ⟨valid-return-list cs n'⟩ ⟨n' = targetnode a⟩
have valid-return-list cs (targetnode a) by simp
with ⟨valid-edge a⟩ ⟨kind a = Q↔pf⟩ ⟨p = get-proc (sourcenode a)⟩
have valid-return-list (a#cs) (sourcenode a)
apply(clarsimp simp:valid-return-list-def)
apply(case-tac cs') apply auto
apply(erule-tac x=list in allE) apply clarsimp
by(case-tac list,auto simp:targetnodes-def)
from ⟨upd-rev-cs cs (as@[a]) = []⟩ ⟨kind a = Q↔pf⟩
have upd-rev-cs (a#cs) as = [] by simp
from IH[OF this ⟨n -as→* sourcenode a⟩ ⟨valid-return-list (a#cs) (sourcenode
a)⟩]
have same-level-path-aux [] as
and same-level-path-aux (upd-cs [] as) (a#cs)
and upd-cs (upd-cs [] as) (a#cs) = [] by simp-all
show ?case
proof(cases upd-cs [] as)
case Nil
with ⟨kind a = Q↔pf⟩ ⟨same-level-path-aux (upd-cs [] as) (a#cs)⟩
have False by simp
thus ?thesis by simp
next
case (Cons cx csx)
with ⟨kind a = Q↔pf⟩ ⟨same-level-path-aux (upd-cs [] as) (a#cs)⟩
obtain Qx fx
where match:a ∈ get-return-edges cx same-level-path-aux csx cs by auto
from ⟨kind a = Q↔pf⟩ Cons have upd-cs [] (as@[a]) = csx
by(rule upd-cs-snoc-Return-Cons)
with ⟨same-level-path-aux (upd-cs [] as) (a#cs)⟩
  ⟨kind a = Q↔pf⟩ match
have same-level-path-aux (upd-cs [] (as@[a])) cs by simp
from ⟨upd-cs [] (as@[a]) = csx⟩ ⟨kind a = Q↔pf⟩ Cons
  ⟨upd-cs (upd-cs [] as) (a#cs) = []⟩
have upd-cs (upd-cs [] (as@[a])) cs = [] by simp

```

```

from  $\langle \text{kind } a = Q \leftrightarrow pf \rangle$  match
have same-level-path-aux (upd-cs [] as) [a] by simp
with  $\langle \text{same-level-path-aux [] } as \rangle$  have same-level-path-aux [] (as@[a])
  by (rule same-level-path-aux-Append)
with  $\langle \text{same-level-path-aux (upd-cs [] (as@[a])) } cs \rangle$ 
   $\langle \text{upd-cs (upd-cs [] (as@[a])) } cs = [] \rangle$ 
show ?thesis by simp
qed
next
case (slpra-Call cs a as Q r p fs cx csx)
note  $IH = \langle \bigwedge n'. \llbracket \text{upd-rev-cs } csx \text{ } as = [] ; n - as \rightarrow^* n' ; \text{valid-return-list } csx \text{ } n \rrbracket$ 
   $\implies \text{same-level-path-aux [] } as \wedge$ 
   $\text{same-level-path-aux (upd-cs [] } as) \text{ } csx \wedge \text{upd-cs (upd-cs [] } as) \text{ } csx = [] \rangle$ 
note match =  $\langle cs = cx \# csx \rangle \langle cx \in \text{get-return-edges } a \rangle$ 
from  $\langle n - as@[a] \rightarrow^* n' \rangle$  have  $n - as \rightarrow^* \text{sourcenode } a$  and valid-edge a
  and  $n' = \text{targetnode } a$  by (auto intro:path-split-snoc)
from  $\langle \text{valid-edge } a \rangle$  match
have get-proc (sourcenode a) = get-proc (targetnode cx)
  by (fastforce intro:get-proc-get-return-edge)
with  $\langle \text{valid-return-list } cs \text{ } n' \rangle \langle cs = cx \# csx \rangle$ 
have valid-return-list csx (sourcenode a)
  apply (clarsimp simp:valid-return-list-def)
  apply (erule-tac x=cx#cs' in allE) apply clarsimp
  by (case-tac cs', auto simp:targetnodes-def)
from  $\langle \text{kind } a = Q : r \hookrightarrow pfs \rangle$  match  $\langle \text{upd-rev-cs } cs \text{ } (as@[a]) = [] \rangle$ 
have upd-rev-cs csx as = [] by simp
from  $IH[OF \text{ this } \langle n - as \rightarrow^* \text{sourcenode } a \rangle \langle \text{valid-return-list } csx \text{ } (sourcenode a) \rangle]$ 
have same-level-path-aux [] as
  and same-level-path-aux (upd-cs [] as) csx and upd-cs (upd-cs [] as) csx = []
  by simp-all
from  $\langle \text{same-level-path-aux [] } as \rangle \langle \text{kind } a = Q : r \hookrightarrow pfs \rangle$ 
have same-level-path-aux [] (as@[a]) by (rule slpa-snoc-Call)
from  $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q : r \hookrightarrow pfs \rangle$  match obtain  $Q' f'$  where kind cx =
 $Q' \hookrightarrow pf'$ 
  by (fastforce dest!:call-return-edges)
from  $\langle \text{kind } a = Q : r \hookrightarrow pfs \rangle$  have upd-cs [] (as@[a]) = a#(upd-cs [] as)
  by (rule upd-cs-snoc-Call)
with  $\langle \text{same-level-path-aux (upd-cs [] as) } csx \rangle \langle \text{kind } a = Q : r \hookrightarrow pfs \rangle$ 
   $\langle \text{kind } cx = Q' \hookrightarrow pf' \rangle$  match
have same-level-path-aux (upd-cs [] (as@[a])) cs by simp
from  $\langle \text{upd-cs (upd-cs [] as) } csx = [] \rangle \langle \text{upd-cs [] (as@[a]) = a\#(upd-cs [] as) \rangle$ 
   $\langle \text{kind } a = Q : r \hookrightarrow pfs \rangle \langle \text{kind } cx = Q' \hookrightarrow pf' \rangle$  match
have upd-cs (upd-cs [] (as@[a])) cs = [] by simp
with  $\langle \text{same-level-path-aux [] (as@[a]) \rangle$ 
   $\langle \text{same-level-path-aux (upd-cs [] (as@[a])) } cs \rangle$  show ?case by simp
qed

```

Lemmas on paths with $(-Entry-)$

```

lemma path-Entry-target [dest]:
  assumes  $n -as \rightarrow^* (-Entry-)$ 
  shows  $n = (-Entry-)$  and  $as = []$ 
using  $\langle n -as \rightarrow^* (-Entry-) \rangle$ 
proof(induct  $n$  as  $n' \equiv (-Entry-)$  rule:path.induct)
  case (Cons-path  $n''$  as a n)
  from  $\langle n'' = (-Entry-) \rangle \langle targetnode\ a = n'' \rangle \langle valid-edge\ a \rangle$  have False
    by  $-(rule\ Entry-target, simp-all)$ 
  { case 1
    from  $\langle False \rangle$  show ?case ..
  next
    case 2
    from  $\langle False \rangle$  show ?case ..
  }
qed simp-all

```

```

lemma Entry-sourcenode-hd:
  assumes  $n -as \rightarrow^* n'$  and  $(-Entry-) \in set\ (sourcenodes\ as)$ 
  shows  $n = (-Entry-)$  and  $(-Entry-) \notin set\ (sourcenodes\ (tl\ as))$ 
  using  $\langle n -as \rightarrow^* n' \rangle \langle (-Entry-) \in set\ (sourcenodes\ as) \rangle$ 
proof(induct rule:path.induct)
  case (empty-path n) case 1
  thus ?case by(simp add:sourcenodes-def)
next
  case (empty-path n) case 2
  thus ?case by(simp add:sourcenodes-def)
next
  case (Cons-path  $n''$  as  $n'$  a n)
  note IH1 =  $\langle (-Entry-) \in set(sourcenodes\ as) \implies n'' = (-Entry-) \rangle$ 
  note IH2 =  $\langle (-Entry-) \in set(sourcenodes\ as) \implies (-Entry-) \notin set(sourcenodes(tl\ as)) \rangle$ 
  have  $(-Entry-) \notin set\ (sourcenodes(tl(a\#as)))$ 
  proof(rule ccontr)
    assume  $\neg (-Entry-) \notin set\ (sourcenodes\ (tl\ (a\#as)))$ 
    hence  $(-Entry-) \in set\ (sourcenodes\ as)$  by simp
    from IH1[OF this] have  $n'' = (-Entry-)$  by simp
    with  $\langle targetnode\ a = n'' \rangle \langle valid-edge\ a \rangle$  show False by  $-(erule\ Entry-target, simp)$ 
  qed
  hence  $(-Entry-) \notin set\ (sourcenodes(tl(a\#as)))$  by fastforce
  { case 1
    with  $\langle (-Entry-) \notin set\ (sourcenodes(tl(a\#as))) \rangle \langle sourcenode\ a = n \rangle$ 
    show ?case by(simp add:sourcenodes-def)
  next
    case 2
    with  $\langle (-Entry-) \notin set\ (sourcenodes(tl(a\#as))) \rangle \langle sourcenode\ a = n \rangle$ 
    show ?case by(simp add:sourcenodes-def)
  }

```

}
qed

lemma *Entry-no-inner-return-path*:

assumes $(-Entry-) -as@[a] \rightarrow^* n$ **and** $\forall a \in set\ as.\ intra_kind(kind\ a)$
and $kind\ a = Q \leftrightarrow pf$
shows *False*

proof –

from $\langle(-Entry-) -as@[a] \rightarrow^* n\rangle$ **have** $\langle(-Entry-) -as \rightarrow^* sourcenode\ a$
and $valid_edge\ a$ **and** $targetnode\ a = n$ **by** $(auto\ intro:path_split_snoc)$
from $\langle(-Entry-) -as \rightarrow^* sourcenode\ a\rangle$ $\langle\forall a \in set\ as.\ intra_kind(kind\ a)\rangle$
have $\langle(-Entry-) -as \rightarrow_i^* sourcenode\ a\rangle$ **by** $(simp\ add:intra_path_def)$
hence $get_proc\ (sourcenode\ a) = Main$
by $(fastforce\ dest:intra_path_get_procs\ simp:get_proc_Entry)$
with $\langle valid_edge\ a\rangle$ $\langle kind\ a = Q \leftrightarrow pf\rangle$ **have** $p = Main$
by $(fastforce\ dest:get_proc_return)$
with $\langle valid_edge\ a\rangle$ $\langle kind\ a = Q \leftrightarrow pf\rangle$ **show** *?thesis*
by $(fastforce\ intro:Main_no_return_source)$

qed

lemma *vpra-no-spra*:

$\llbracket valid_path_rev_aux\ cs\ as; n -as \rightarrow^* n'; valid_return_list\ cs\ n'; cs \neq [];$
 $\forall xs\ ys.\ as = xs@ys \longrightarrow (\neg same_level_path_rev_aux\ cs\ ys \vee upd_rev_cs\ cs\ ys \neq$

$[]) \rrbracket$

$\implies \exists a\ Q\ f.\ valid_edge\ a \wedge kind\ a = Q \leftrightarrow get_proc\ n\ f$

proof $(induct\ arbitrary:n'$ *rule:vpra-induct*)

case $(vpra_empty\ cs)$

from $\langle valid_return_list\ cs\ n'\rangle$ $\langle cs \neq []\rangle$ **obtain** $Q\ f$ **where** $valid_edge\ (hd\ cs)$
and $kind\ (hd\ cs) = Q \leftrightarrow get_proc\ n'\ f$
apply $(unfold\ valid_return_list_def)$
apply $(drule\ hd_Cons_tl[THEN\ sym])$
apply $(erule_tac\ x=[]\ in\ allE)$
apply $(erule_tac\ x=hd\ cs\ in\ allE)$
by *auto*

from $\langle n -[] \rightarrow^* n'\rangle$ **have** $n = n'$ **by** *fastforce*

with $\langle valid_edge\ (hd\ cs)\rangle$ $\langle kind\ (hd\ cs) = Q \leftrightarrow get_proc\ n'\ f\rangle$ **show** *?case* **by** *blast*

next

case $(vpra_intra\ cs\ a\ as)$

note $IH = \langle \bigwedge n'. \llbracket n -as \rightarrow^* n'; valid_return_list\ cs\ n'; cs \neq [];$

$\forall xs\ ys.\ as = xs@ys \longrightarrow \neg same_level_path_rev_aux\ cs\ ys \vee upd_rev_cs\ cs\ ys \neq$

$[]) \rrbracket$

$\implies \exists a\ Q\ f.\ valid_edge\ a \wedge kind\ a = Q \leftrightarrow get_proc\ n\ f$

note $all = \langle \forall xs\ ys.\ as@[a] = xs@ys$

$\longrightarrow \neg same_level_path_rev_aux\ cs\ ys \vee upd_rev_cs\ cs\ ys \neq []\rangle$

from $\langle n -as@[a] \rightarrow^* n'\rangle$ **have** $n -as \rightarrow^* sourcenode\ a$ **and** $valid_edge\ a$
and $targetnode\ a = n'$ **by** $(auto\ intro:path_split_snoc)$


```

from ⟨valid-return-list cs n'⟩ ⟨cs ≠ []⟩ obtain Q f where valid-edge (hd cs)
  and kind (hd cs) = Q↔ get-proc n'f
  apply(unfold valid-return-list-def)
  apply(drule hd-Cons-tl[THEN sym])
  apply(erule-tac x=[] in allE)
  apply(erule-tac x=hd cs in allE)
  by auto
from ⟨valid-edge a⟩ ⟨intra-kind (kind a)⟩
have get-proc (sourcenode a) = get-proc (targetnode a) by(rule get-proc-intra)
with ⟨kind (hd cs) = Q↔ get-proc n'f⟩ ⟨targetnode a = n'⟩
have kind (hd cs) = Q↔ get-proc (sourcenode a)f by simp
from ⟨valid-return-list cs n'⟩ ⟨targetnode a = n'⟩
  ⟨get-proc (sourcenode a) = get-proc (targetnode a)⟩
have valid-return-list cs (sourcenode a)
  apply(clarsimp simp:valid-return-list-def)
  apply(erule-tac x=cs' in allE)
  apply(erule-tac x=c in allE)
  by(auto split:list.split)
from all ⟨intra-kind (kind a)⟩
have ∀ xs ys. as = xs@ys
  → ¬ same-level-path-rev-aux cs ys ∨ upd-rev-cs cs ys ≠ []
  applyclarsimp apply(erule-tac x=xs in allE)
  by(auto simp:intra-kind-def)
from IH[OF ⟨n -as→* sourcenode a⟩ ⟨valid-return-list cs (sourcenode a)⟩
  ⟨cs ≠ []⟩ this] show ?case .
next
case (vpra-Return cs a as Q p f)
note IH = ⟨∧ n'. [n -as→* n'; valid-return-list (a#cs) n'; a#cs ≠ []];
  ∀ xs ys. as = xs @ ys →
  ¬ same-level-path-rev-aux (a#cs) ys ∨ upd-rev-cs (a#cs) ys ≠ []]
⇒ ∃ a Q f. valid-edge a ∧ kind a = Q↔ get-proc nf
from ⟨n -as@[a]→* n'⟩ have n -as→* sourcenode a and valid-edge a
  and targetnode a = n' by(auto intro:path-split-snoc)
from ⟨valid-edge a⟩ ⟨kind a = Q↔ pf⟩ have get-proc (sourcenode a) = p
  by(rule get-proc-return)
with ⟨kind a = Q↔ pf⟩ ⟨valid-return-list cs n'⟩ ⟨valid-edge a⟩ ⟨targetnode a = n'⟩
have valid-return-list (a#cs) (sourcenode a)
  apply(clarsimp simp:valid-return-list-def)
  apply(case-tac cs') apply auto
  apply(erule-tac x=list in allE)
  apply(erule-tac x=c in allE)
  by(auto split:list.split simp:targetnodes-def)
from ⟨∀ xs ys. as@[a] = xs@ys →
  ¬ same-level-path-rev-aux cs ys ∨ upd-rev-cs cs ys ≠ []⟩ ⟨kind a = Q↔ pf⟩
have ∀ xs ys. as = xs@ys →
  ¬ same-level-path-rev-aux (a#cs) ys ∨ upd-rev-cs (a#cs) ys ≠ []
  applyclarsimp apply(erule-tac x=xs in allE)
  by auto
from IH[OF ⟨n -as→* sourcenode a⟩ ⟨valid-return-list (a#cs) (sourcenode a)⟩

```

```

- this] show ?case by simp
next
case (vpra-CallEmpty cs a as Q p fs)
from ⟨cs = []⟩ ⟨cs ≠ []⟩ have False by simp
thus ?case by simp
next
case (vpra-CallCons cs a as Q r p fs c' cs')
note IH = ⟨ $\wedge n'. \llbracket n -as \rightarrow * n' \rrbracket$ ; valid-return-list cs' n'; cs' ≠ []⟩;
   $\forall xs\ ys. as = xs@ys \rightarrow$ 
   $\neg$  same-level-path-rev-aux cs' ys  $\vee$  upd-rev-cs cs' ys ≠ []
 $\implies \exists a\ Q\ f. \text{valid-edge } a \wedge \text{kind } a = Q \leftrightarrow \text{get-proc } nf$ 
note all = ⟨ $\forall xs\ ys. as@[a] = xs@ys \rightarrow$ 
   $\neg$  same-level-path-rev-aux cs ys  $\vee$  upd-rev-cs cs ys ≠ []⟩
from ⟨n -as@[a]  $\rightarrow$  * n'⟩ have n -as  $\rightarrow$  * sourcenode a and valid-edge a
and targetnode a = n' by (auto intro:path-split-snoc)
from ⟨valid-return-list cs n'⟩ ⟨cs = c'#cs'⟩ have valid-edge c'
  apply (clarsimp simp:valid-return-list-def)
  apply (erule-tac x=[] in allE)
  by auto
show ?case
proof (cases cs' = [])
case True
with ⟨cs = c'#cs'⟩ ⟨kind a = Q:r  $\leftrightarrow$  pfs⟩ ⟨c' ∈ get-return-edges a⟩
have same-level-path-rev-aux cs (>[]@[a])
and upd-rev-cs cs (>[]@[a]) = []
by (simp only:same-level-path-rev-aux.simps upd-rev-cs.simps,clarsimp)+
with all have False by (erule-tac x=as in allE) fastforce
thus ?thesis by simp
next
case False
with ⟨valid-return-list cs n'⟩ ⟨cs = c'#cs'⟩
have valid-return-list cs' (targetnode c')
  apply (clarsimp simp:valid-return-list-def)
  apply (erule-tac x=c'#cs' in allE)
  apply (auto simp:targetnodes-def)
  apply (case-tac cs') apply auto
  apply (case-tac list) apply (auto simp:targetnodes-def)
done
from ⟨valid-edge a⟩ ⟨c' ∈ get-return-edges a⟩
have get-proc (sourcenode a) = get-proc (targetnode c')
  by (rule get-proc-get-return-edge)
with ⟨valid-return-list cs' (targetnode c')⟩
have valid-return-list cs' (sourcenode a)
  apply (clarsimp simp:valid-return-list-def)
  apply (erule-tac x=cs' in allE)
  apply (erule-tac x=c in allE)
  by (auto split:list.split)
from all ⟨kind a = Q:r  $\leftrightarrow$  pfs⟩ ⟨cs = c'#cs'⟩ ⟨c' ∈ get-return-edges a⟩
have  $\forall xs\ ys. as = xs@ys$ 

```

```

  → ¬ same-level-path-rev-aux cs' ys ∨ upd-rev-cs cs' ys ≠ []
  apply clarsimp apply (erule-tac x=xs in allE)
  by auto
  from IH[OF ⟨n -as→* sourcenode a⟩ ⟨valid-return-list cs' (sourcenode a)⟩
    False this] show ?thesis .
qed
qed

lemma valid-Entry-path-cases:
  assumes (-Entry-) -as→√* n and as ≠ []
  shows (∃ a' as'. as = as'@[a'] ∧ intra-kind(kind a')) ∨
        (∃ a' as' Q r p fs. as = as'@[a'] ∧ kind a' = Q:r→pfs) ∨
        (∃ as' as'' n'. as = as'@as'' ∧ as'' ≠ [] ∧ n' -as''→sl* n)
proof -
  from ⟨as ≠ []⟩ obtain a' as' where as = as'@[a'] by (cases as rule:rev-cases)
  auto
  thus ?thesis
  proof (cases kind a' rule:edge-kind-cases)
    case Intra with ⟨as = as'@[a']⟩ show ?thesis by simp
  next
    case Call with ⟨as = as'@[a']⟩ show ?thesis by simp
  next
    case (Return Q p f)
    from ⟨(-Entry-) -as→√* n⟩ have (-Entry-) -as→* n and valid-path-rev-aux
    [] as
    by (auto intro:vp-to-vpra simp:vp-def valid-path-def)
    from ⟨(-Entry-) -as→* n⟩ ⟨as = as'@[a']⟩
    have (-Entry-) -as'→* sourcenode a' and valid-edge a'
    and targetnode a' = n
    by (auto intro:path-split-snoc)
    from ⟨valid-path-rev-aux [] as⟩ ⟨as = as'@[a']⟩ Return
    have valid-path-rev-aux [a'] as' by simp
    from ⟨valid-edge a'⟩ Return
    have valid-return-list [a'] (sourcenode a')
    apply (clarsimp simp:valid-return-list-def)
    apply (case-tac cs')
    by (auto intro:get-proc-return[THEN sym])
    show ?thesis
  proof (cases ∀ xs ys. as' = xs@ys →
    (¬ same-level-path-rev-aux [a'] ys ∨ upd-rev-cs [a'] ys ≠ []))
    case True
    with ⟨valid-path-rev-aux [a'] as'⟩ ⟨(-Entry-) -as'→* sourcenode a'⟩
    ⟨valid-return-list [a'] (sourcenode a')⟩
    obtain ax Qx fx where valid-edge ax and kind ax = Qx↔ get-proc (-Entry-)fx
    by (fastforce dest!:vpra-no-slpra)
    hence False by (fastforce intro:Main-no-return-source simp:get-proc-Entry)
    thus ?thesis by simp
  next

```

```

case False
then obtain xs ys where  $as' = xs@ys$  and same-level-path-rev-aux [a'] ys
  and upd-rev-cs [a'] ys = [] by auto
with Return have same-level-path-rev-aux [] (ys@[a'])
  and upd-rev-cs [] (ys@[a']) = [] by simp-all
from (upd-rev-cs [a'] ys = []) have  $ys \neq []$  by auto
with  $\langle (-Entry-) -as' \rightarrow^* sourcenode\ a' \rangle \langle as' = xs@ys \rangle$ 
have  $hd(sourcenodes\ ys) -ys \rightarrow^* sourcenode\ a'$ 
  by (cases ys)(auto dest:path-split-second simp:sourcenodes-def)
with  $\langle targetnode\ a' = n \rangle \langle valid-edge\ a' \rangle$ 
have  $hd(sourcenodes\ ys) -ys@[a'] \rightarrow^* n$ 
  by (fastforce intro:path-Append path-edge)
with  $\langle same-level-path-rev-aux\ []\ (ys@[a']) \rangle \langle upd-rev-cs\ []\ (ys@[a']) = [] \rangle$ 
have same-level-path (ys@[a'])
  by (fastforce dest:slpra-to-slpa simp:same-level-path-def valid-return-list-def)
with  $\langle hd(sourcenodes\ ys) -ys@[a'] \rightarrow^* n \rangle$  have  $hd(sourcenodes\ ys) -ys@[a'] \rightarrow_{sl^*} n$ 
n
  by (simp add:slp-def)
with  $\langle as = as'@[a'] \rangle \langle as' = xs@ys \rangle$  Return
have  $\exists as'\ as'' n'. as = as'@as'' \wedge as'' \neq [] \wedge n' -as'' \rightarrow_{sl^*} n$ 
  by (rule-tac x=xs in exI) auto
thus ?thesis by simp
qed
qed
qed

```

```

lemma valid-Entry-path-ascending-path:
  assumes  $\langle (-Entry-) -as \rightarrow_{\sqrt{*}} n \rangle$ 
  obtains as' where  $\langle (-Entry-) -as' \rightarrow_{\sqrt{*}} n \rangle$ 
  and  $set(sourcenodes\ as') \subseteq set(sourcenodes\ as)$ 
  and  $\forall a' \in set\ as'.\ intra-kind(kind\ a') \vee (\exists Q\ r\ p\ fs.\ kind\ a' = Q:r \hookrightarrow_p fs)$ 
proof (atomize-elim)
  from  $\langle (-Entry-) -as \rightarrow_{\sqrt{*}} n \rangle$ 
  show  $\exists as'. \langle (-Entry-) -as' \rightarrow_{\sqrt{*}} n \rangle \wedge set(sourcenodes\ as') \subseteq set(sourcenodes\ as) \wedge$ 
     $(\forall a' \in set\ as'.\ intra-kind(kind\ a') \vee (\exists Q\ r\ p\ fs.\ kind\ a' = Q:r \hookrightarrow_p fs))$ 
  proof (induct as arbitrary:n rule:length-induct)
    fix as n
    assume IH:  $\forall as''.\ length\ as'' < length\ as \longrightarrow$ 
       $(\forall n'. \langle (-Entry-) -as'' \rightarrow_{\sqrt{*}} n' \rangle \longrightarrow$ 
         $(\exists as'. \langle (-Entry-) -as' \rightarrow_{\sqrt{*}} n' \rangle \wedge set(sourcenodes\ as') \subseteq set(sourcenodes\ as''))$ 
     $\wedge$ 
       $(\forall a' \in set\ as'.\ intra-kind(kind\ a') \vee (\exists Q\ r\ p\ fs.\ kind\ a' = Q:r \hookrightarrow_p fs))$ 
    and  $\langle (-Entry-) -as \rightarrow_{\sqrt{*}} n \rangle$ 
    show  $\exists as'. \langle (-Entry-) -as' \rightarrow_{\sqrt{*}} n \rangle \wedge set(sourcenodes\ as') \subseteq set(sourcenodes\ as) \wedge$ 
       $(\forall a' \in set\ as'.\ intra-kind(kind\ a') \vee (\exists Q\ r\ p\ fs.\ kind\ a' = Q:r \hookrightarrow_p fs))$ 
    proof (cases as = [])
      case True

```

with $\langle (-\text{Entry-}) -as \rightarrow_{\sqrt{*}} n \rangle$ **show** *?thesis* **by** (*fastforce simp:sourcenodes-def vp-def*)

next

case *False*

with $\langle (-\text{Entry-}) -as \rightarrow_{\sqrt{*}} n \rangle$

have $(\exists a' as'. as = as'@[a'] \wedge \text{intra-kind}(\text{kind } a')) \vee$
 $(\exists a' as' Q r p fs. as = as'@[a'] \wedge \text{kind } a' = Q:r \hookrightarrow pfs) \vee$
 $(\exists as' as'' n'. as = as'@[a''] \wedge as'' \neq [] \wedge n' -as'' \rightarrow_{sl^*} n)$

by (*fastforce dest!:valid-Entry-path-cases*)

thus *?thesis* **apply** $-$

proof (*erule disjE*) $+$

assume $\exists a' as'. as = as'@[a'] \wedge \text{intra-kind}(\text{kind } a')$

then obtain $a' as'$ **where** $as = as'@[a']$ **and** $\text{intra-kind}(\text{kind } a')$ **by** *blast*

from $\langle (-\text{Entry-}) -as \rightarrow_{\sqrt{*}} n \rangle$ $\langle as = as'@[a'] \rangle$

have $\langle (-\text{Entry-}) -as' \rightarrow_{\sqrt{*}} \text{sourcenode } a' \text{ and } \text{valid-edge } a' \text{ and } \text{targetnode } a' = n \rangle$

by (*auto intro:vp-split-snoc*)

from $\langle \text{valid-edge } a' \rangle$ $\langle \text{intra-kind}(\text{kind } a') \rangle$

have $\text{sourcenode } a' -[a'] \rightarrow_{sl^*} \text{targetnode } a'$

by (*fastforce intro:path-edge intras-same-level-path simp:slp-def*)

from *IH* $\langle (-\text{Entry-}) -as' \rightarrow_{\sqrt{*}} \text{sourcenode } a' \rangle$ $\langle as = as'@[a'] \rangle$

obtain xs **where** $\langle (-\text{Entry-}) -xs \rightarrow_{\sqrt{*}} \text{sourcenode } a' \rangle$

and $\text{set}(\text{sourcenodes } xs) \subseteq \text{set}(\text{sourcenodes } as')$

and $\forall a' \in \text{set } xs. \text{intra-kind}(\text{kind } a') \vee (\exists Q r p fs. \text{kind } a' = Q:r \hookrightarrow pfs)$

apply (*erule-tac x=as' in allE*) **by** *auto*

from $\langle (-\text{Entry-}) -xs \rightarrow_{\sqrt{*}} \text{sourcenode } a' \rangle$ $\langle \text{sourcenode } a' -[a'] \rightarrow_{sl^*} \text{targetnode } a' \rangle$

have $\langle (-\text{Entry-}) -xs@[a'] \rightarrow_{\sqrt{*}} \text{targetnode } a' \text{ by}(\text{rule } vp\text{-slp-Append})$

with $\langle \text{targetnode } a' = n \rangle$ **have** $\langle (-\text{Entry-}) -xs@[a'] \rightarrow_{\sqrt{*}} n \text{ by } \text{simp}$

moreover

from $\langle \text{set}(\text{sourcenodes } xs) \subseteq \text{set}(\text{sourcenodes } as') \rangle$ $\langle as = as'@[a'] \rangle$

have $\text{set}(\text{sourcenodes } (xs@[a'])) \subseteq \text{set}(\text{sourcenodes } as)$

by (*auto simp:sourcenodes-def*)

moreover

from $\langle \forall a' \in \text{set } xs. \text{intra-kind}(\text{kind } a') \vee (\exists Q r p fs. \text{kind } a' = Q:r \hookrightarrow pfs) \rangle$

$\langle \text{intra-kind}(\text{kind } a') \rangle$

have $\forall a' \in \text{set} (xs@[a']). \text{intra-kind}(\text{kind } a') \vee$
 $(\exists Q r p fs. \text{kind } a' = Q:r \hookrightarrow pfs)$

by *fastforce*

ultimately show *?thesis* **by** *blast*

next

assume $\exists a' as' Q r p fs. as = as'@[a'] \wedge \text{kind } a' = Q:r \hookrightarrow pfs$

then obtain $a' as' Q r p fs$ **where** $as = as'@[a']$ **and** $\text{kind } a' = Q:r \hookrightarrow pfs$

by *blast*

from $\langle (-\text{Entry-}) -as \rightarrow_{\sqrt{*}} n \rangle$ $\langle as = as'@[a'] \rangle$

have $\langle (-\text{Entry-}) -as' \rightarrow_{\sqrt{*}} \text{sourcenode } a' \text{ and } \text{valid-edge } a' \text{ and } \text{targetnode } a' = n \rangle$

by (*auto intro:vp-split-snoc*)

from *IH* $\langle (-\text{Entry-}) -as' \rightarrow_{\sqrt{*}} \text{sourcenode } a' \rangle$ $\langle as = as'@[a'] \rangle$

obtain xs **where** $\langle (-Entry-) -xs \rightarrow_{\sqrt{*}} sourcenode\ a' \rangle$
and $\langle set\ (sourcenodes\ xs) \subseteq set\ (sourcenodes\ as') \rangle$
and $\forall a' \in set\ xs. \text{intra-kind}\ (kind\ a') \vee (\exists Q\ r\ p\ fs. \text{kind}\ a' = Q:r \hookrightarrow_p fs)$
apply($erule-tac\ x=as'$ **in** $allE$) **by** $auto$
from $\langle targetnode\ a' = n \rangle \langle valid-edge\ a' \rangle \langle kind\ a' = Q:r \hookrightarrow_p fs \rangle$
 $\langle (-Entry-) -xs \rightarrow_{\sqrt{*}} sourcenode\ a' \rangle$
have $\langle (-Entry-) -xs@[a'] \rightarrow_{\sqrt{*}} n \rangle$
by($fastforce\ intro:path-Append\ path-edge\ vpa-snoc-Call$
 $simp:vp-def\ valid-path-def$)
moreover
from $\langle set\ (sourcenodes\ xs) \subseteq set\ (sourcenodes\ as') \rangle \langle as = as'@[a'] \rangle$
have $\langle set\ (sourcenodes\ (xs@[a'])) \subseteq set\ (sourcenodes\ as) \rangle$
by($auto\ simp:sourcenodes-def$)
moreover
from $\langle \forall a' \in set\ xs. \text{intra-kind}\ (kind\ a') \vee (\exists Q\ r\ p\ fs. \text{kind}\ a' = Q:r \hookrightarrow_p fs) \rangle$
 $\langle kind\ a' = Q:r \hookrightarrow_p fs \rangle$
have $\forall a' \in set\ (xs@[a']). \text{intra-kind}\ (kind\ a') \vee$
 $(\exists Q\ r\ p\ fs. \text{kind}\ a' = Q:r \hookrightarrow_p fs)$
by $fastforce$
ultimately show $?thesis$ **by** $blast$
next
assume $\exists as'\ as''\ n'. as = as'@as'' \wedge as'' \neq [] \wedge n' -as'' \rightarrow_{sl^*} n$
then obtain $as'\ as''\ n'$ **where** $as = as'@as''$ **and** $as'' \neq []$
and $n' -as'' \rightarrow_{sl^*} n$ **by** $blast$
from $\langle (-Entry-) -as \rightarrow_{\sqrt{*}} n \rangle \langle as = as'@as'' \rangle \langle as'' \neq [] \rangle$
have $\langle (-Entry-) -as' \rightarrow_{\sqrt{*}} hd(sourcenodes\ as'') \rangle$
by($cases\ as'', auto\ intro:vp-split\ simp:sourcenodes-def$)
from $\langle n' -as'' \rightarrow_{sl^*} n \rangle \langle as'' \neq [] \rangle$ **have** $hd(sourcenodes\ as'') = n'$
by($fastforce\ intro:path-sourcenode\ simp:slp-def$)
from $\langle as = as'@as'' \rangle \langle as'' \neq [] \rangle$ **have** $length\ as' < length\ as$ **by** $simp$
with $IH\ \langle (-Entry-) -as' \rightarrow_{\sqrt{*}} hd(sourcenodes\ as'') \rangle$
 $\langle hd(sourcenodes\ as'') = n' \rangle$
obtain xs **where** $\langle (-Entry-) -xs \rightarrow_{\sqrt{*}} n' \rangle$
and $\langle set\ (sourcenodes\ xs) \subseteq set\ (sourcenodes\ as') \rangle$
and $\forall a' \in set\ xs. \text{intra-kind}\ (kind\ a') \vee (\exists Q\ r\ p\ fs. \text{kind}\ a' = Q:r \hookrightarrow_p fs)$
apply($erule-tac\ x=as'$ **in** $allE$) **by** $auto$
from $\langle n' -as'' \rightarrow_{sl^*} n \rangle$ **obtain** ys **where** $n' -ys \rightarrow_{l^*} n$
and $\langle set\ (sourcenodes\ ys) \subseteq set\ (sourcenodes\ as'') \rangle$
by($erule\ same-level-path-inner-path$)
from $\langle (-Entry-) -xs \rightarrow_{\sqrt{*}} n' \rangle \langle n' -ys \rightarrow_{l^*} n \rangle$ **have** $\langle (-Entry-) -xs@ys \rightarrow_{\sqrt{*}} n \rangle$
by($fastforce\ intro:vp-slp-Append\ intra-path-slp$)
moreover
from $\langle set\ (sourcenodes\ xs) \subseteq set\ (sourcenodes\ as') \rangle$
 $\langle set\ (sourcenodes\ ys) \subseteq set\ (sourcenodes\ as'') \rangle \langle as = as'@as'' \rangle$
have $\langle set\ (sourcenodes\ (xs@ys)) \subseteq set\ (sourcenodes\ as) \rangle$
by($auto\ simp:sourcenodes-def$)
moreover
from $\langle \forall a' \in set\ xs. \text{intra-kind}\ (kind\ a') \vee (\exists Q\ r\ p\ fs. \text{kind}\ a' = Q:r \hookrightarrow_p fs) \rangle$
 $\langle n' -ys \rightarrow_{l^*} n \rangle$

```

    have  $\forall a' \in \text{set } (xs@ys). \text{intra-kind } (\text{kind } a') \vee (\exists Q r p fs. \text{kind } a' = Q:r \hookrightarrow pfs)$ 
      by(fastforce simp:intra-path-def)
    ultimately show ?thesis by blast
  qed
qed
qed
qed

```

end

end

theory *CFGExit* imports *CFG* begin

1.2.3 Adds an exit node to the abstract CFG

```

locale CFGExit = CFG sourcenode targetnode kind valid-edge Entry
  get-proc get-return-edges procs Main
  for sourcenode :: 'edge  $\Rightarrow$  'node and targetnode :: 'edge  $\Rightarrow$  'node
  and kind :: 'edge  $\Rightarrow$  ('var,'val,'ret,'pname) edge-kind
  and valid-edge :: 'edge  $\Rightarrow$  bool
  and Entry :: 'node ('('Entry'-')) and get-proc :: 'node  $\Rightarrow$  'pname
  and get-return-edges :: 'edge  $\Rightarrow$  'edge set
  and procs :: ('pname  $\times$  'var list  $\times$  'var list) list and Main :: 'pname +
  fixes Exit::'node ('('Exit'-'))
  assumes Exit-source [dest]:  $\llbracket \text{valid-edge } a; \text{sourcenode } a = (-\text{Exit}-) \rrbracket \Longrightarrow \text{False}$ 
  and get-proc-Exit:get-proc  $(-\text{Exit}-) = \text{Main}$ 
  and Exit-no-return-target:
     $\llbracket \text{valid-edge } a; \text{kind } a = Q \leftrightarrow pf; \text{targetnode } a = (-\text{Exit}-) \rrbracket \Longrightarrow \text{False}$ 
  and Entry-Exit-edge:  $\exists a. \text{valid-edge } a \wedge \text{sourcenode } a = (-\text{Entry}-) \wedge$ 
     $\text{targetnode } a = (-\text{Exit}-) \wedge \text{kind } a = (\lambda s. \text{False})_{\checkmark}$ 

```

begin

```

lemma Entry-noteq-Exit [dest]:
  assumes eq:(-Entry-) = (-Exit-) shows False
proof -
  from Entry-Exit-edge obtain a where sourcenode a = (-Entry-)
  and valid-edge a by blast
  with eq show False by simp(erule Exit-source)
qed

```

```

lemma Exit-noteq-Entry [dest]:(-Exit-) = (-Entry-)  $\Longrightarrow$  False
  by(rule Entry-noteq-Exit[OF sym],simp)

```

```

lemma [simp]: valid-node  $(-\text{Entry}-)$ 

```

proof –
from *Entry-Exit-edge* **obtain** *a* **where** *sourcenode a = (-Entry-)*
and *valid-edge a by blast*
thus *?thesis* **by**(*fastforce simp:valid-node-def*)
qed

lemma [*simp*]: *valid-node (-Exit-)*
proof –
from *Entry-Exit-edge* **obtain** *a* **where** *targetnode a = (-Exit-)*
and *valid-edge a by blast*
thus *?thesis* **by**(*fastforce simp:valid-node-def*)
qed

Definition of *method-exit*

definition *method-exit* :: *'node ⇒ bool*
where *method-exit n ≡ n = (-Exit-) ∨*
(∃ a Q p f. n = sourcenode a ∧ valid-edge a ∧ kind a = Q↔_pf)

lemma *method-exit-cases*:
 $\llbracket \text{method-exit } n; n = (-\text{Exit-}) \implies P; \bigwedge a Q p f. \llbracket n = \text{sourcenode } a; \text{valid-edge } a; \text{kind } a = Q \leftrightarrow_p f \rrbracket \implies P \rrbracket \implies P$
by(*fastforce simp:method-exit-def*)

lemma *method-exit-inner-path*:
assumes *method-exit n* **and** *n -as→_l* n'* **shows** *as = []*
using *(method-exit n)*
proof(*rule method-exit-cases*)
assume *n = (-Exit-)*
show *?thesis*
proof(*cases as*)
case (*Cons a' as'*)
with *(n -as→_l* n')* **have** *n = sourcenode a' and valid-edge a'*
by(*auto elim:path-split-Cons simp:intra-path-def*)
with *(n = (-Exit-))* **have** *sourcenode a' = (-Exit-)* **by** *simp*
with *(valid-edge a')* **have** *False* **by**(*rule Exit-source*)
thus *?thesis* **by** *simp*
qed *simp*
next
fix *a Q f p*
assume *n = sourcenode a and valid-edge a and kind a = Q↔_pf*
show *?thesis*
proof(*cases as*)
case (*Cons a' as'*)
with *(n -as→_l* n')* **have** *n = sourcenode a' and valid-edge a'*
and *intra-kind (kind a')*
by(*auto elim:path-split-Cons simp:intra-path-def*)


```

from ⟨valid-edge a⟩ ⟨kind a =  $Q \leftrightarrow pf$ ⟩ ⟨valid-edge a'⟩ ⟨n = sourcenode a⟩
  ⟨n = sourcenode a'⟩ ⟨intra-kind (kind a')⟩
have False by (fastforce dest:return-edges-only simp:intra-kind-def)
thus ?thesis by simp
qed simp
qed

```

Definition of *inner-node*

```

definition inner-node :: 'node  $\Rightarrow$  bool
where inner-node-def:
  inner-node n  $\equiv$  valid-node n  $\wedge$  n  $\neq$  (-Entry-)  $\wedge$  n  $\neq$  (-Exit-)

```

```

lemma inner-is-valid:
  inner-node n  $\implies$  valid-node n
by (simp add:inner-node-def valid-node-def)

```

```

lemma [dest]:
  inner-node (-Entry-)  $\implies$  False
by (simp add:inner-node-def)

```

```

lemma [dest]:
  inner-node (-Exit-)  $\implies$  False
by (simp add:inner-node-def)

```

```

lemma [simp]: [[valid-edge a; targetnode a  $\neq$  (-Exit-)]
 $\implies$  inner-node (targetnode a)
by (simp add:inner-node-def, rule ccontr, simp, erule Entry-target)

```

```

lemma [simp]: [[valid-edge a; sourcenode a  $\neq$  (-Entry-)]
 $\implies$  inner-node (sourcenode a)
by (simp add:inner-node-def, rule ccontr, simp, erule Exit-source)

```

```

lemma valid-node-cases [consumes 1, case-names Entry Exit inner]:
  [[valid-node n; n = (-Entry-)  $\implies$  Q; n = (-Exit-)  $\implies$  Q;
  inner-node n  $\implies$  Q]  $\implies$  Q
apply (auto simp:valid-node-def)
apply (case-tac sourcenode a = (-Entry-)) apply auto
apply (case-tac targetnode a = (-Exit-)) apply auto
done

```

Lemmas on paths with (-Exit-)

```

lemma path-Exit-source:
  [[n -as $\rightarrow^*$  n'; n = (-Exit-)]  $\implies$  n' = (-Exit-)  $\wedge$  as = []
proof (induct rule:path.induct)
case (Cons-path n'' as n' a n)
from ⟨n = (-Exit-)⟩ ⟨sourcenode a = n⟩ ⟨valid-edge a⟩ have False
by -(rule Exit-source, simp-all)

```

thus ?case by simp
qed simp

lemma [dest]:(-Exit-) -as→* n' ⇒ n' = (-Exit-) ∧ as = []
by(fastforce elim!:path-Exit-source)

lemma Exit-no-sourcenode[dest]:
assumes isin:(-Exit-) ∈ set (sourcenodes as) and path:n -as→* n'
shows False

proof -
from isin obtain ns' ns'' where sourcenodes as = ns'@(-Exit-)#ns''
by(auto dest:split-list simp:sourcenodes-def)
then obtain as' as'' a where as = as'@a#as''
and source:sourcenode a = (-Exit-)
by(fastforce elim:map-append-append-maps simp:sourcenodes-def)
with path have valid-edge a by(fastforce dest:path-split)
with source show ?thesis by -(erule Exit-source)
qed

lemma vpa-no-slpa:

[[valid-path-aux cs as; n -as→* n'; valid-call-list cs n; cs ≠ [];
∀ xs ys. as = xs@ys → (¬ same-level-path-aux cs xs ∨ upd-cs cs xs ≠ [])]]
⇒ ∃ a Q r fs. valid-edge a ∧ kind a = Q:r↦get-proc n'fs

proof(induct arbitrary:n rule:vpa-induct)

case (vpa-empty cs)

from ⟨valid-call-list cs n⟩ ⟨cs ≠ []⟩ obtain Q r fs where valid-edge (hd cs)
and kind (hd cs) = Q:r↦get-proc n'fs
apply(unfold valid-call-list-def)
apply(drule hd-Cons-tl[THEN sym])
apply(erule-tac x=[] in allE)
apply(erule-tac x=hd cs in allE)
by auto

from ⟨n -[]→* n'⟩ have n = n' by fastforce

with ⟨valid-edge (hd cs)⟩ ⟨kind (hd cs) = Q:r↦get-proc n'fs⟩ show ?case by blast

next

case (vpa-intra cs a as)

note IH = ⟨∧n. [[n -as→* n'; valid-call-list cs n; cs ≠ [];

∀ xs ys. as = xs@ys → ¬ same-level-path-aux cs xs ∨ upd-cs cs xs ≠ []]

⇒ ∃ a' Q' r' fs'. valid-edge a' ∧ kind a' = Q':r'↦get-proc n'fs'⟩

note all = ⟨∀ xs ys. a#as = xs@ys

→ ¬ same-level-path-aux cs xs ∨ upd-cs cs xs ≠ []⟩

from ⟨n -a#as→* n'⟩ have sourcenode a = n and valid-edge a

and targetnode a -as→* n'

by(auto intro:path-split-Cons)

from ⟨valid-call-list cs n⟩ ⟨cs ≠ []⟩ obtain Q r fs where valid-edge (hd cs)

and kind (hd cs) = Q:r↦get-proc n'fs

apply(unfold valid-call-list-def)

```

apply(drule hd-Cons-tl[THEN sym])
apply(erule-tac x=[] in allE)
apply(erule-tac x=hd cs in allE)
by auto
from  $\langle \text{valid-edge } a \rangle \langle \text{intra-kind } (\text{kind } a) \rangle$ 
have get-proc (sourcenode a) = get-proc (targetnode a) by(rule get-proc-intra)
with  $\langle \text{kind } (\text{hd } cs) = Q:r \hookrightarrow \text{get-proc } nfs \rangle \langle \text{sourcenode } a = n \rangle$ 
have kind (hd cs) = Q:r \hookrightarrow get-proc (targetnode a)fs by simp
from  $\langle \text{valid-call-list } cs \ n \rangle \langle \text{sourcenode } a = n \rangle$ 
 $\langle \text{get-proc } (\text{sourcenode } a) = \text{get-proc } (\text{targetnode } a) \rangle$ 
have valid-call-list cs (targetnode a)
apply(clarsimp simp:valid-call-list-def)
apply(erule-tac x=cs' in allE)
apply(erule-tac x=c in allE)
by(auto split:list.split)
from all  $\langle \text{intra-kind } (\text{kind } a) \rangle$ 
have  $\forall xs \ ys. as = xs@ys \longrightarrow \neg \text{same-level-path-aux } cs \ xs \vee \text{upd-cs } cs \ xs \neq []$ 
apply clarsimp apply(erule-tac x=a#xs in allE)
by(auto simp:intra-kind-def)
from IH[OF  $\langle \text{targetnode } a - as \rightarrow * n' \rangle \langle \text{valid-call-list } cs \ (\text{targetnode } a) \rangle$ 
 $\langle cs \neq [] \rangle$  this] show ?case .
next
case (vpa-Call cs a as Q r p fs)
note IH =  $\langle \bigwedge n. [n - as \rightarrow * n'; \text{valid-call-list } (a\#cs) \ n; a\#cs \neq [];$ 
 $\forall xs \ ys. as = xs@ys \longrightarrow \neg \text{same-level-path-aux } (a\#cs) \ xs \vee \text{upd-cs } (a\#cs) \ xs$ 
 $\neq []]$ 
 $\implies \exists a' \ Q' \ r' \ fs'. \text{valid-edge } a' \wedge \text{kind } a' = Q':r' \hookrightarrow \text{get-proc } n'fs'$ 
note all =  $\langle \forall xs \ ys.$ 
 $a\#as = xs@ys \longrightarrow \neg \text{same-level-path-aux } cs \ xs \vee \text{upd-cs } cs \ xs \neq [] \rangle$ 
from  $\langle n - a\#as \rightarrow * n' \rangle$  have sourcenode a = n and valid-edge a
and targetnode a - as  $\rightarrow * n'$ 
by(auto intro:path-split-Cons)
from  $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q:r \hookrightarrow pfs \rangle$  have get-proc (targetnode a) = p
by(rule get-proc-call)
with  $\langle \text{kind } a = Q:r \hookrightarrow pfs \rangle$  have kind a = Q:r \hookrightarrow get-proc (targetnode a)fs by simp
with  $\langle \text{valid-call-list } cs \ n \rangle \langle \text{valid-edge } a \rangle \langle \text{sourcenode } a = n \rangle$ 
have valid-call-list (a#cs) (targetnode a)
apply(clarsimp simp:valid-call-list-def)
apply(case-tac cs') apply auto
apply(erule-tac x=list in allE)
apply(erule-tac x=c in allE)
by(auto split:list.split simp:sourcenodes-def)
from all  $\langle \text{kind } a = Q:r \hookrightarrow pfs \rangle$ 
have  $\forall xs \ ys. as = xs@ys$ 
 $\longrightarrow \neg \text{same-level-path-aux } (a\#cs) \ xs \vee \text{upd-cs } (a\#cs) \ xs \neq []$ 
apply clarsimp apply(erule-tac x=a#xs in allE)
by auto
from IH[OF  $\langle \text{targetnode } a - as \rightarrow * n' \rangle \langle \text{valid-call-list } (a\#cs) \ (\text{targetnode } a) \rangle$ 
- this] show ?case by simp

```

```

next
  case (vpa-ReturnEmpty cs a as Q p fx)
  from (cs ≠ [] | cs = []) have False by simp
  thus ?case by simp
next
  case (vpa-ReturnCons cs a as Q p f c' cs')
  note IH = (∧ n. [n -as→* n'; valid-call-list cs' n; cs' ≠ [];
    ∀ xs ys. as = xs@ys → ¬ same-level-path-aux cs' xs ∨ upd-cs cs' xs ≠ []]
    ⇒ ∃ a' Q' r' fs'. valid-edge a' ∧ kind a' = Q':r'↔ get-proc n'fs')
  note all = (∀ xs ys. a#as = xs@ys
    → ¬ same-level-path-aux cs xs ∨ upd-cs cs xs ≠ [])
  from (n -a#as→* n') have sourcenode a = n and valid-edge a
    and targetnode a -as→* n'
    by(auto intro:path-split-Cons)
  from (valid-call-list cs n) (cs = c'#cs') have valid-edge c'
    apply(clarsimp simp:valid-call-list-def)
    apply(erule-tac x=[] in allE)
    by auto
  show ?case
  proof(cases cs' = [])
    case True
    with all (cs = c'#cs') (kind a = Q↔pf) (a ∈ get-return-edges c') have False
      by(erule-tac x=[a] in allE,fastforce)
    thus ?thesis by simp
  next
    case False
    with (valid-call-list cs n) (cs = c'#cs')
    have valid-call-list cs' (sourcenode c')
      apply(clarsimp simp:valid-call-list-def)
      apply(erule-tac x=c'#cs' in allE)
      apply(auto simp:sourcenodes-def)
      apply(case-tac cs') apply auto
      apply(case-tac list) apply(auto simp:sourcenodes-def)
      done
    from (valid-edge c') (a ∈ get-return-edges c')
    have get-proc (sourcenode c') = get-proc (targetnode a)
      by(rule get-proc-get-return-edge)
    with (valid-call-list cs' (sourcenode c'))
    have valid-call-list cs' (targetnode a)
      apply(clarsimp simp:valid-call-list-def)
      apply(erule-tac x=cs' in allE)
      apply(erule-tac x=c in allE)
      by(auto split:list.split)
    from all (kind a = Q↔pf) (cs = c'#cs') (a ∈ get-return-edges c')
    have ∀ xs ys. as = xs@ys → ¬ same-level-path-aux cs' xs ∨ upd-cs cs' xs ≠ []
      apply clarsimp apply(erule-tac x=a#xs in allE)
      by auto
    from IH[OF (targetnode a -as→* n') (valid-call-list cs' (targetnode a))
      False this] show ?thesis .

```

qed
qed

lemma *valid-Exit-path-cases*:

assumes $n - as \rightarrow \sqrt{*} (-Exit-)$ **and** $as \neq []$
shows $(\exists a' as'. as = a' \# as' \wedge \text{intra-kind}(\text{kind } a')) \vee$
 $(\exists a' as' Q p f. as = a' \# as' \wedge \text{kind } a' = Q \leftrightarrow pf) \vee$
 $(\exists as' as'' n'. as = as' @ as'' \wedge as' \neq [] \wedge n - as' \rightarrow_{sl*} n')$

proof –

from $\langle as \neq [] \rangle$ **obtain** $a' as'$ **where** $as = a' \# as'$ **by** $(\text{cases } as) \text{ auto}$
thus *?thesis*

proof $(\text{cases } \text{kind } a' \text{ rule: edge-kind-cases})$

case *Intra* **with** $\langle as = a' \# as' \rangle$ **show** *?thesis* **by** *simp*

next

case *Return* **with** $\langle as = a' \# as' \rangle$ **show** *?thesis* **by** *simp*

next

case $(\text{Call } Q r p f)$

from $\langle n - as \rightarrow \sqrt{*} (-Exit-) \rangle$ **have** $n - as \rightarrow * (-Exit-)$ **and** *valid-path-aux* $[] as$
by $(\text{simp-all add: vp-def valid-path-def})$

from $\langle n - as \rightarrow * (-Exit-) \rangle$ $\langle as = a' \# as' \rangle$

have *sourcenode* $a' = n$ **and** *valid-edge* a' **and** *targetnode* $a' - as' \rightarrow * (-Exit-)$
by $(\text{auto intro: path-split-Cons})$

from $\langle \text{valid-path-aux } [] as \rangle$ $\langle as = a' \# as' \rangle$ *Call*

have *valid-path-aux* $[a'] as'$ **by** *simp*

from $\langle \text{valid-edge } a' \rangle$ *Call*

have *valid-call-list* $[a'] (\text{targetnode } a')$

apply $(\text{clarsimp simp: valid-call-list-def})$

apply $(\text{case-tac } cs')$

by $(\text{auto intro: get-proc-call[THEN sym]})$

show *?thesis*

proof $(\text{cases } \forall xs ys. as' = xs @ ys \rightarrow$

$(\neg \text{same-level-path-aux } [a'] xs \vee \text{upd-cs } [a'] xs \neq []))$

case *True*

with $\langle \text{valid-path-aux } [a'] as' \rangle$ $\langle \text{targetnode } a' - as' \rightarrow * (-Exit-) \rangle$

$\langle \text{valid-call-list } [a'] (\text{targetnode } a') \rangle$

obtain $ax Qx rx fsx$ **where** *valid-edge* ax **and** *kind* $ax = Qx:rx \leftrightarrow \text{get-proc } (-Exit-) fsx$

by $(\text{fastforce dest!: vpa-no-slpa})$

hence *False* **by** $(\text{fastforce intro: Main-no-call-target simp: get-proc-Exit})$

thus *?thesis* **by** *simp*

next

case *False*

then obtain $xs ys$ **where** $as' = xs @ ys$ **and** *same-level-path-aux* $[a'] xs$

and *upd-cs* $[a'] xs = []$ **by** *auto*

with *Call* **have** *same-level-path* $(a' \# xs)$ **by** $(\text{simp add: same-level-path-def})$

from $\langle \text{upd-cs } [a'] xs = [] \rangle$ **have** $xs \neq []$ **by** *auto*

with $\langle \text{targetnode } a' - as' \rightarrow * (-Exit-) \rangle$ $\langle as' = xs @ ys \rangle$

have *targetnode* $a' - xs \rightarrow * \text{last}(\text{targetnodes } xs)$

apply $(\text{cases } xs \text{ rule: rev-cases})$

```

    by(auto intro:path-Append path-split path-edge simp:targetnodes-def)
  with ⟨sourcenode  $a' = n$ ⟩ ⟨valid-edge  $a'$ ⟩ ⟨same-level-path ( $a' \# xs$ )⟩
  have  $n - a' \# xs \rightarrow_{sl^*} \text{last}(\text{targetnodes } xs)$ 
    by(fastforce intro:Cons-path simp:slp-def)
  with ⟨ $as = a' \# as'$ ⟩ ⟨ $as' = xs @ ys$ ⟩ Call
  have  $\exists as' as'' n'. as = as' @ as'' \wedge as' \neq [] \wedge n - as' \rightarrow_{sl^*} n'$ 
    by(rule-tac  $x = a' \# xs$  in exI) auto
  thus ?thesis by simp
qed
qed
qed

lemma valid-Exit-path-descending-path:
  assumes  $n - as \rightarrow_{\sqrt{*}} (-Exit-)$ 
  obtains  $as'$  where  $n - as' \rightarrow_{\sqrt{*}} (-Exit-)$ 
  and  $\text{set}(\text{sourcenodes } as') \subseteq \text{set}(\text{sourcenodes } as)$ 
  and  $\forall a' \in \text{set } as'. \text{intra-kind}(\text{kind } a') \vee (\exists Q f p. \text{kind } a' = Q \leftrightarrow pf)$ 
proof(atomize-elim)
  from ⟨ $n - as \rightarrow_{\sqrt{*}} (-Exit-)$ ⟩
  show  $\exists as'. n - as' \rightarrow_{\sqrt{*}} (-Exit-) \wedge \text{set}(\text{sourcenodes } as') \subseteq \text{set}(\text{sourcenodes } as) \wedge$ 
    ( $\forall a' \in \text{set } as'. \text{intra-kind}(\text{kind } a') \vee (\exists Q f p. \text{kind } a' = Q \leftrightarrow pf)$ )
  proof(induct as arbitrary:n rule:length-induct)
    fix as n
    assume IH: $\forall as''. \text{length } as'' < \text{length } as \rightarrow$ 
      ( $\forall n'. n' - as'' \rightarrow_{\sqrt{*}} (-Exit-) \rightarrow$ 
        ( $\exists as'. n' - as' \rightarrow_{\sqrt{*}} (-Exit-) \wedge \text{set}(\text{sourcenodes } as') \subseteq \text{set}(\text{sourcenodes } as'')$ )
      )
    and  $n - as \rightarrow_{\sqrt{*}} (-Exit-)$ 
  show  $\exists as'. n - as' \rightarrow_{\sqrt{*}} (-Exit-) \wedge \text{set}(\text{sourcenodes } as') \subseteq \text{set}(\text{sourcenodes } as) \wedge$ 
    ( $\forall a' \in \text{set } as'. \text{intra-kind}(\text{kind } a') \vee (\exists Q f p. \text{kind } a' = Q \leftrightarrow pf)$ )
  proof(cases as = [])
    case True
    with ⟨ $n - as \rightarrow_{\sqrt{*}} (-Exit-)$ ⟩ show ?thesis by(fastforce simp:sourcenodes-def
  vp-def)
  next
  case False
  with ⟨ $n - as \rightarrow_{\sqrt{*}} (-Exit-)$ ⟩
  have (( $\exists a' as'. as = a' \# as' \wedge \text{intra-kind}(\text{kind } a')$ )  $\vee$ 
    ( $\exists a' as' Q p f. as = a' \# as' \wedge \text{kind } a' = Q \leftrightarrow pf$ ))  $\vee$ 
    ( $\exists as' as'' n'. as = as' @ as'' \wedge as' \neq [] \wedge n - as' \rightarrow_{sl^*} n'$ )
    by(auto dest!:valid-Exit-path-cases)
  thus ?thesis apply -
  proof(erule disjE)+
    assume  $\exists a' as'. as = a' \# as' \wedge \text{intra-kind}(\text{kind } a')$ 
    then obtain  $a' as'$  where  $as = a' \# as'$  and  $\text{intra-kind}(\text{kind } a')$  by blast
    from ⟨ $n - as \rightarrow_{\sqrt{*}} (-Exit-)$ ⟩ ⟨ $as = a' \# as'$ ⟩
    have sourcenode  $a' = n$  and valid-edge  $a'$ 

```

and $\text{targetnode } a' - as' \rightarrow_{\sqrt{*}} (-Exit-)$
by $(\text{auto intro:vp-split-Cons})$
from $\langle \text{valid-edge } a' \rangle \langle \text{intra-kind } (kind \ a') \rangle$
have $\text{sourcenode } a' - [a'] \rightarrow_{st^*} \text{targetnode } a'$
by $(\text{fastforce intro:path-edge intras-same-level-path simp:slp-def})$
from $IH \langle \text{targetnode } a' - as' \rightarrow_{\sqrt{*}} (-Exit-) \rangle \langle as = a' \# as' \rangle$
obtain xs **where** $\text{targetnode } a' - xs \rightarrow_{\sqrt{*}} (-Exit-)$
and $\text{set } (sourcenodes \ xs) \subseteq \text{set } (sourcenodes \ as')$
and $\forall a' \in \text{set } xs. \text{intra-kind } (kind \ a') \vee (\exists Q \ f \ p. \text{kind } a' = Q \leftrightarrow pf)$
apply $(\text{erule-tac } x=as' \text{ in } allE)$ **by** auto
from $\langle \text{sourcenode } a' - [a'] \rightarrow_{st^*} \text{targetnode } a' \rangle \langle \text{targetnode } a' - xs \rightarrow_{\sqrt{*}} (-Exit-) \rangle$
have $\text{sourcenode } a' - [a'] @ xs \rightarrow_{\sqrt{*}} (-Exit-)$ **by** $(\text{rule slp-vp-Append})$
with $\langle \text{sourcenode } a' = n \rangle$ **have** $n - a' \# xs \rightarrow_{\sqrt{*}} (-Exit-)$ **by** simp
moreover
from $\langle \text{set } (sourcenodes \ xs) \subseteq \text{set } (sourcenodes \ as') \rangle \langle as = a' \# as' \rangle$
have $\text{set } (sourcenodes \ (a' \# xs)) \subseteq \text{set } (sourcenodes \ as)$
by $(\text{auto simp:sourcenodes-def})$
moreover
from $\langle \forall a' \in \text{set } xs. \text{intra-kind } (kind \ a') \vee (\exists Q \ f \ p. \text{kind } a' = Q \leftrightarrow pf) \rangle$
 $\langle \text{intra-kind } (kind \ a') \rangle$
have $\forall a' \in \text{set } (a' \# xs). \text{intra-kind } (kind \ a') \vee (\exists Q \ f \ p. \text{kind } a' = Q \leftrightarrow pf)$
by fastforce
ultimately show $?thesis$ **by** blast
next
assume $\exists a' \ as' \ Q \ p \ f. as = a' \# as' \wedge \text{kind } a' = Q \leftrightarrow pf$
then obtain $a' \ as' \ Q \ p \ f$ **where** $as = a' \# as'$ **and** $\text{kind } a' = Q \leftrightarrow pf$ **by**
blast
from $\langle n - as \rightarrow_{\sqrt{*}} (-Exit-) \rangle \langle as = a' \# as' \rangle$
have $\text{sourcenode } a' = n$ **and** $\text{valid-edge } a'$
and $\text{targetnode } a' - as' \rightarrow_{\sqrt{*}} (-Exit-)$
by $(\text{auto intro:vp-split-Cons})$
from $IH \langle \text{targetnode } a' - as' \rightarrow_{\sqrt{*}} (-Exit-) \rangle \langle as = a' \# as' \rangle$
obtain xs **where** $\text{targetnode } a' - xs \rightarrow_{\sqrt{*}} (-Exit-)$
and $\text{set } (sourcenodes \ xs) \subseteq \text{set } (sourcenodes \ as')$
and $\forall a' \in \text{set } xs. \text{intra-kind } (kind \ a') \vee (\exists Q \ f \ p. \text{kind } a' = Q \leftrightarrow pf)$
apply $(\text{erule-tac } x=as' \text{ in } allE)$ **by** auto
from $\langle \text{sourcenode } a' = n \rangle \langle \text{valid-edge } a' \rangle \langle \text{kind } a' = Q \leftrightarrow pf \rangle$
 $\langle \text{targetnode } a' - xs \rightarrow_{\sqrt{*}} (-Exit-) \rangle$
have $n - a' \# xs \rightarrow_{\sqrt{*}} (-Exit-)$
by $(\text{fastforce intro:Cons-path simp:vp-def valid-path-def})$
moreover
from $\langle \text{set } (sourcenodes \ xs) \subseteq \text{set } (sourcenodes \ as') \rangle \langle as = a' \# as' \rangle$
have $\text{set } (sourcenodes \ (a' \# xs)) \subseteq \text{set } (sourcenodes \ as)$
by $(\text{auto simp:sourcenodes-def})$
moreover
from $\langle \forall a' \in \text{set } xs. \text{intra-kind } (kind \ a') \vee (\exists Q \ f \ p. \text{kind } a' = Q \leftrightarrow pf) \rangle$
 $\langle \text{kind } a' = Q \leftrightarrow pf \rangle$
have $\forall a' \in \text{set } (a' \# xs). \text{intra-kind } (kind \ a') \vee (\exists Q \ f \ p. \text{kind } a' = Q \leftrightarrow pf)$
by fastforce

ultimately show *?thesis by blast*
next
assume $\exists as' as'' n'. as = as'@as'' \wedge as' \neq [] \wedge n - as' \rightarrow_{sl}^* n'$
then obtain $as' as'' n'$ **where** $as = as'@as''$ **and** $as' \neq []$
and $n - as' \rightarrow_{sl}^* n'$ **by** *blast*
from $\langle n - as \rightarrow_{\sqrt{}}^* (-Exit) \rangle \langle as = as'@as'' \rangle \langle as' \neq [] \rangle$
have $last(targetnodes as') - as'' \rightarrow_{\sqrt{}}^* (-Exit)$
by *(cases as' rule:rev-cases, auto intro:vp-split simp:targetnodes-def)*
from $\langle n - as' \rightarrow_{sl}^* n' \rangle \langle as' \neq [] \rangle$ **have** $last(targetnodes as') = n'$
by *(fastforce intro:path-targetnode simp:slp-def)*
from $\langle as = as'@as'' \rangle \langle as' \neq [] \rangle$ **have** $length as'' < length as$ **by** *simp*
with *IH* $\langle last(targetnodes as') - as'' \rightarrow_{\sqrt{}}^* (-Exit) \rangle$
 $\langle last(targetnodes as') = n' \rangle$
obtain xs **where** $n' - xs \rightarrow_{\sqrt{}}^* (-Exit)$
and $set(sourcenodes xs) \subseteq set(sourcenodes as'')$
and $\forall a' \in set xs. intra-kind(kind a') \vee (\exists Q f p. kind a' = Q \leftrightarrow pf)$
apply *(erule-tac x=as'' in allE)* **by** *auto*
from $\langle n - as' \rightarrow_{sl}^* n' \rangle$ **obtain** ys **where** $n - ys \rightarrow_{\iota}^* n'$
and $set(sourcenodes ys) \subseteq set(sourcenodes as')$
by *(erule same-level-path-inner-path)*
from $\langle n - ys \rightarrow_{\iota}^* n' \rangle \langle n' - xs \rightarrow_{\sqrt{}}^* (-Exit) \rangle$ **have** $n - ys@xs \rightarrow_{\sqrt{}}^* (-Exit)$
by *(fastforce intro:slp-vp-Append intra-path-slp)*
moreover
from $\langle set(sourcenodes xs) \subseteq set(sourcenodes as'') \rangle$
 $\langle set(sourcenodes ys) \subseteq set(sourcenodes as') \rangle \langle as = as'@as'' \rangle$
have $set(sourcenodes (ys@xs)) \subseteq set(sourcenodes as)$
by *(auto simp:sourcenodes-def)*
moreover
from $\langle \forall a' \in set xs. intra-kind(kind a') \vee (\exists Q f p. kind a' = Q \leftrightarrow pf) \rangle$
 $\langle n - ys \rightarrow_{\iota}^* n' \rangle$
have $\forall a' \in set (ys@xs). intra-kind(kind a') \vee (\exists Q f p. kind a' = Q \leftrightarrow pf)$
by *(fastforce simp:intra-path-def)*
ultimately show *?thesis by blast*
qed
qed
qed
qed

lemma *valid-Exit-path-intra-path:*

assumes $n - as \rightarrow_{\sqrt{}}^* (-Exit)$
obtains $as' pex$ **where** $n - as' \rightarrow_{\iota}^* pex$ **and** *method-exit pex*
and $set(sourcenodes as') \subseteq set(sourcenodes as)$
proof *(atomize-elim)*
from $\langle n - as \rightarrow_{\sqrt{}}^* (-Exit) \rangle$
obtain as' **where** $n - as' \rightarrow_{\sqrt{}}^* (-Exit)$
and $set(sourcenodes as') \subseteq set(sourcenodes as)$
and $all: \forall a' \in set as'. intra-kind(kind a') \vee (\exists Q f p. kind a' = Q \leftrightarrow pf)$
by *(erule valid-Exit-path-descending-path)*


```

show  $\exists as' pex. n -as' \rightarrow_i^* pex \wedge \text{method-exit } pex \wedge$ 
       $set(\text{sourcenodes } as') \subseteq set(\text{sourcenodes } as)$ 
proof(cases  $\exists a' \in set\ as'. \exists Q\ f\ p. \text{kind } a' = Q \leftrightarrow pf$ )
  case True
    then obtain asx ax asx' where [simp]: $as' = asx @ ax \# asx'$ 
      and  $\exists Q\ f\ p. \text{kind } ax = Q \leftrightarrow pf$  and  $\forall a' \in set\ asx. \neg (\exists Q\ f\ p. \text{kind } a' =$ 
Q  $\leftrightarrow pf)$ 
      by(erule split-list-first-propE)
    with all have  $\forall a' \in set\ asx. \text{intra-kind}(\text{kind } a')$  by auto
    from  $\langle n -as' \rightarrow_{\sqrt{}}^* (-Exit-) \rangle$  have  $n -asx \rightarrow^* \text{sourcenode } ax$ 
      and valid-edge ax by(auto elim:path-split simp:vp-def)
    from  $\langle n -asx \rightarrow^* \text{sourcenode } ax \rangle \langle \forall a' \in set\ asx. \text{intra-kind}(\text{kind } a') \rangle$ 
      have  $n -asx \rightarrow_i^* \text{sourcenode } ax$  by(simp add:intra-path-def)
    moreover
      from  $\langle \text{valid-edge } ax \rangle \langle \exists Q\ f\ p. \text{kind } ax = Q \leftrightarrow pf \rangle$ 
      have method-exit (sourcenode ax) by(fastforce simp:method-exit-def)
    moreover
      from  $\langle set(\text{sourcenodes } as') \subseteq set(\text{sourcenodes } as) \rangle$ 
      have  $set(\text{sourcenodes } asx) \subseteq set(\text{sourcenodes } as)$  by(simp add:sourcenodes-def)
    ultimately show ?thesis by blast
  next
    case False
    with all  $\langle n -as' \rightarrow_{\sqrt{}}^* (-Exit-) \rangle$  have  $n -as' \rightarrow_i^* (-Exit-)$ 
      by(fastforce simp:vp-def intra-path-def)
    moreover have method-exit (-Exit-) by(simp add:method-exit-def)
    ultimately show ?thesis using  $\langle set(\text{sourcenodes } as') \subseteq set(\text{sourcenodes } as) \rangle$ 
      by blast
  qed
qed

end

end

```

1.3 CFG well-formedness

theory *CFG-wf* **imports** *CFG* **begin**

```

locale CFG-wf = CFG sourcenode targetnode kind valid-edge Entry
  get-proc get-return-edges procs Main
  for sourcenode :: 'edge  $\Rightarrow$  'node and targetnode :: 'edge  $\Rightarrow$  'node
  and kind :: 'edge  $\Rightarrow$  ('var,'val,'ret,'pname) edge-kind
  and valid-edge :: 'edge  $\Rightarrow$  bool
  and Entry :: 'node (('Entry'-)) and get-proc :: 'node  $\Rightarrow$  'pname
  and get-return-edges :: 'edge  $\Rightarrow$  'edge set
  and procs :: ('pname  $\times$  'var list  $\times$  'var list) list and Main :: 'pname +

```

fixes $Def::'node \Rightarrow 'var\ set$
fixes $Use::'node \Rightarrow 'var\ set$
fixes $ParamDefs::'node \Rightarrow 'var\ list$
fixes $ParamUses::'node \Rightarrow 'var\ set\ list$
assumes $Entry\text{-}empty:Def\ (-Entry) = \{\} \wedge Use\ (-Entry) = \{\}$
and $ParamUses\text{-}call\text{-}source\text{-}length:$
 $\llbracket valid\text{-}edge\ a; kind\ a = Q:r\hookrightarrow_p fs; (p,ins,outs) \in set\ procs \rrbracket$
 $\implies length(ParamUses\ (sourcename\ a)) = length\ ins$
and $distinct\text{-}ParamDefs:valid\text{-}edge\ a \implies distinct\ (ParamDefs\ (targetnode\ a))$
and $ParamDefs\text{-}return\text{-}target\text{-}length:$
 $\llbracket valid\text{-}edge\ a; kind\ a = Q'\leftarrow_p f'; (p,ins,outs) \in set\ procs \rrbracket$
 $\implies length(ParamDefs\ (targetnode\ a)) = length\ outs$
and $ParamDefs\text{-}in\text{-}Def:$
 $\llbracket valid\text{-}node\ n; V \in set\ (ParamDefs\ n) \rrbracket \implies V \in Def\ n$
and $ins\text{-}in\text{-}Def:$
 $\llbracket valid\text{-}edge\ a; kind\ a = Q:r\hookrightarrow_p fs; (p,ins,outs) \in set\ procs; V \in set\ ins \rrbracket$
 $\implies V \in Def\ (targetnode\ a)$
and $call\text{-}source\text{-}Def\text{-}empty:$
 $\llbracket valid\text{-}edge\ a; kind\ a = Q:r\hookrightarrow_p fs \rrbracket \implies Def\ (sourcename\ a) = \{\}$
and $ParamUses\text{-}in\text{-}Use:$
 $\llbracket valid\text{-}node\ n; V \in Union\ (set\ (ParamUses\ n)) \rrbracket \implies V \in Use\ n$
and $outs\text{-}in\text{-}Use:$
 $\llbracket valid\text{-}edge\ a; kind\ a = Q\leftarrow_p f; (p,ins,outs) \in set\ procs; V \in set\ outs \rrbracket$
 $\implies V \in Use\ (sourcename\ a)$
and $CFG\text{-}intra\text{-}edge\text{-}no\text{-}Def\text{-}equal:$
 $\llbracket valid\text{-}edge\ a; V \notin Def\ (sourcename\ a); intra\text{-}kind\ (kind\ a); pred\ (kind\ a)\ s \rrbracket$
 $\implies state\text{-}val\ (transfer\ (kind\ a)\ s)\ V = state\text{-}val\ s\ V$
and $CFG\text{-}intra\text{-}edge\text{-}transfer\text{-}uses\text{-}only\text{-}Use:$
 $\llbracket valid\text{-}edge\ a; \forall V \in Use\ (sourcename\ a). state\text{-}val\ s\ V = state\text{-}val\ s'\ V;$
 $intra\text{-}kind\ (kind\ a); pred\ (kind\ a)\ s; pred\ (kind\ a)\ s' \rrbracket$
 $\implies \forall V \in Def\ (sourcename\ a). state\text{-}val\ (transfer\ (kind\ a)\ s)\ V =$
 $state\text{-}val\ (transfer\ (kind\ a)\ s')\ V$
and $CFG\text{-}edge\text{-}Uses\text{-}pred\text{-}equal:$
 $\llbracket valid\text{-}edge\ a; pred\ (kind\ a)\ s; snd\ (hd\ s) = snd\ (hd\ s');$
 $\forall V \in Use\ (sourcename\ a). state\text{-}val\ s\ V = state\text{-}val\ s'\ V; length\ s = length$
 $s' \rrbracket$
 $\implies pred\ (kind\ a)\ s'$
and $CFG\text{-}call\text{-}edge\text{-}length:$
 $\llbracket valid\text{-}edge\ a; kind\ a = Q:r\hookrightarrow_p fs; (p,ins,outs) \in set\ procs \rrbracket$
 $\implies length\ fs = length\ ins$
and $CFG\text{-}call\text{-}determ:$
 $\llbracket valid\text{-}edge\ a; kind\ a = Q:r\hookrightarrow_p fs; valid\text{-}edge\ a'; kind\ a' = Q':r'\hookrightarrow_{p'} fs';$
 $sourcename\ a = sourcename\ a'; pred\ (kind\ a)\ s; pred\ (kind\ a')\ s \rrbracket$
 $\implies a = a'$
and $CFG\text{-}call\text{-}edge\text{-}params:$
 $\llbracket valid\text{-}edge\ a; kind\ a = Q:r\hookrightarrow_p fs; i < length\ ins;$
 $(p,ins,outs) \in set\ procs; pred\ (kind\ a)\ s; pred\ (kind\ a)\ s';$
 $\forall V \in (ParamUses\ (sourcename\ a))!i. state\text{-}val\ s\ V = state\text{-}val\ s'\ V \rrbracket$
 $\implies (params\ fs\ (fst\ (hd\ s)))!i = (params\ fs\ (fst\ (hd\ s')))!i$

and *CFG-return-edge-fun*:
 $\llbracket \text{valid-edge } a; \text{ kind } a = Q' \hookrightarrow_p f'; (p, \text{ins}, \text{outs}) \in \text{set procs} \rrbracket$
 $\implies f' \text{ vmap } \text{vmap}' = \text{vmap}'(\text{ParamDefs } (\text{targetnode } a) \text{ } [:=] \text{ map } \text{vmap } \text{outs})$
and *deterministic*: $\llbracket \text{valid-edge } a; \text{ valid-edge } a'; \text{ sourcenode } a = \text{sourcenode } a';$
 $\text{targetnode } a \neq \text{targetnode } a'; \text{ intra-kind } (\text{kind } a); \text{ intra-kind } (\text{kind } a') \rrbracket$
 $\implies \exists Q Q'. \text{ kind } a = (Q)_{\surd} \wedge \text{ kind } a' = (Q')_{\surd} \wedge$
 $(\forall s. (Q s \longrightarrow \neg Q' s) \wedge (Q' s \longrightarrow \neg Q s))$

begin

lemma *CFG-equal-Use-equal-call*:

assumes *valid-edge* a **and** $\text{kind } a = Q:r \hookrightarrow_p fs$ **and** *valid-edge* a'
and $\text{kind } a' = Q':r' \hookrightarrow_{p'} fs'$ **and** $\text{sourcenode } a = \text{sourcenode } a'$
and $\text{pred } (\text{kind } a) s$ **and** $\text{pred } (\text{kind } a') s'$
and $\text{snd } (\text{hd } s) = \text{snd } (\text{hd } s')$ **and** $\text{length } s = \text{length } s'$
and $\forall V \in \text{Use } (\text{sourcenode } a). \text{ state-val } s V = \text{state-val } s' V$
shows $a = a'$

proof –

from $\langle \text{valid-edge } a \rangle \langle \text{pred } (\text{kind } a) s \rangle \langle \text{snd } (\text{hd } s) = \text{snd } (\text{hd } s') \rangle$
 $\langle \forall V \in \text{Use } (\text{sourcenode } a). \text{ state-val } s V = \text{state-val } s' V \rangle \langle \text{length } s = \text{length } s' \rangle$
have $\text{pred } (\text{kind } a) s'$ **by** (*rule CFG-edge-Uses-pred-equal*)
with $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q:r \hookrightarrow_p fs \rangle \langle \text{valid-edge } a' \rangle \langle \text{kind } a' = Q':r' \hookrightarrow_{p'} fs' \rangle$
 $\langle \text{sourcenode } a = \text{sourcenode } a' \rangle \langle \text{pred } (\text{kind } a') s' \rangle$
show *?thesis* **by** –(*rule CFG-call-determ*)
qed

lemma *CFG-call-edge-param-in*:

assumes *valid-edge* a **and** $\text{kind } a = Q:r \hookrightarrow_p fs$ **and** $i < \text{length } \text{ins}$
and $(p, \text{ins}, \text{outs}) \in \text{set procs}$ **and** $\text{pred } (\text{kind } a) s$ **and** $\text{pred } (\text{kind } a) s'$
and $\forall V \in (\text{ParamUses } (\text{sourcenode } a))!i. \text{ state-val } s V = \text{state-val } s' V$
shows $\text{state-val } (\text{transfer } (\text{kind } a) s) (\text{ins}!i) =$
 $\text{state-val } (\text{transfer } (\text{kind } a) s') (\text{ins}!i)$

proof –

from *assms* **have** $\text{params}:(\text{params } fs (\text{fst } (\text{hd } s)))!i = (\text{params } fs (\text{fst } (\text{hd } s')))!i$
by (*rule CFG-call-edge-params*)
from $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q:r \hookrightarrow_p fs \rangle \langle (p, \text{ins}, \text{outs}) \in \text{set procs} \rangle$
have $[\text{simp}]:(\text{THE } \text{ins}. \exists \text{outs}. (p, \text{ins}, \text{outs}) \in \text{set procs}) = \text{ins}$
by (*rule formal-in-THE*)
from $\langle \text{pred } (\text{kind } a) s \rangle$ **obtain** $cf \ cfs$ **where** $[\text{simp}]:s = cf \# cfs$ **by** (*cases* s) *auto*
from $\langle \text{pred } (\text{kind } a) s' \rangle$ **obtain** $cf' \ cfs'$ **where** $[\text{simp}]:s' = cf' \# cfs'$
by (*cases* s') *auto*
from $\langle \text{kind } a = Q:r \hookrightarrow_p fs \rangle$
have $\text{fst } (\text{hd } (\text{transfer } (\text{kind } a) s)) = (\text{empty } (\text{ins } [:=] \text{params } fs (\text{fst } cf)))$
 $\text{fst } (\text{hd } (\text{transfer } (\text{kind } a) s')) = (\text{empty } (\text{ins } [:=] \text{params } fs (\text{fst } cf')))$
by *simp-all*
from $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q:r \hookrightarrow_p fs \rangle \langle (p, \text{ins}, \text{outs}) \in \text{set procs} \rangle$

have $\text{length } fs = \text{length } ins$ **by** (rule CFG-call-edge-length)
from $\langle (p, ins, outs) \in \text{set } procs \rangle$ **have** $\text{distinct } ins$ **by** (rule distinct-formal-ins)
with $\langle i < \text{length } ins \rangle \langle \text{length } fs = \text{length } ins \rangle$
have $(\text{empty}(ins \[:=] \text{params } fs \ (fst \ cf))) \ (ins!i) = (\text{params } fs \ (fst \ cf))!i$
 $(\text{empty}(ins \[:=] \text{params } fs \ (fst \ cf'))) \ (ins!i) = (\text{params } fs \ (fst \ cf'))!i$
by (fastforce intro:fun-upds-nth)+
with eqs $\langle \text{kind } a = Q:r \leftrightarrow_p fs \rangle \text{params}$
show ?thesis **by** simp
qed

lemma CFG-call-edge-no-param:

assumes $\text{valid-edge } a$ **and** $\text{kind } a = Q:r \leftrightarrow_p fs$ **and** $V \notin \text{set } ins$
and $(p, ins, outs) \in \text{set } procs$ **and** $\text{pred } (\text{kind } a) \ s$
shows $\text{state-val } (\text{transfer } (\text{kind } a) \ s) \ V = \text{None}$
proof –
from $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q:r \leftrightarrow_p fs \rangle \langle (p, ins, outs) \in \text{set } procs \rangle$
have [simp]: $(\text{THE } ins. \exists \text{outs. } (p, ins, outs) \in \text{set } procs) = ins$
by (rule formal-in-THE)
from $\langle \text{pred } (\text{kind } a) \ s \rangle$ **obtain** $cf \ cfs$ **where** [simp]: $s = cf \# cfs$ **by** (cases s) auto
from $\langle V \notin \text{set } ins \rangle$ **have** $(\text{empty}(ins \[:=] \text{params } fs \ (fst \ cf))) \ V = \text{None}$
by (auto dest:fun-upds-notin)
with $\langle \text{kind } a = Q:r \leftrightarrow_p fs \rangle$ **show** ?thesis **by** simp
qed

lemma CFG-return-edge-param-out:

assumes $\text{valid-edge } a$ **and** $\text{kind } a = Q \leftarrow_p f$ **and** $i < \text{length } outs$
and $(p, ins, outs) \in \text{set } procs$ **and** $\text{state-val } s \ (outs!i) = \text{state-val } s' \ (outs!i)$
and $s = cf \# cfx \# cfs$ **and** $s' = cf' \# cfx' \# cfs'$
shows $\text{state-val } (\text{transfer } (\text{kind } a) \ s) \ ((\text{ParamDefs } (\text{targetnode } a))!i) =$
 $\text{state-val } (\text{transfer } (\text{kind } a) \ s') \ ((\text{ParamDefs } (\text{targetnode } a))!i)$
proof –
from $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q \leftarrow_p f \rangle \langle (p, ins, outs) \in \text{set } procs \rangle$
have [simp]: $(\text{THE } outs. \exists \text{ins. } (p, ins, outs) \in \text{set } procs) = outs$
by (rule formal-out-THE)
from $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q \leftarrow_p f \rangle \langle (p, ins, outs) \in \text{set } procs \rangle \langle s = cf \# cfx \# cfs \rangle$
have $\text{transfer}:\text{fst } (\text{hd } (\text{transfer } (\text{kind } a) \ s)) =$
 $(fst \ cfx)(\text{ParamDefs } (\text{targetnode } a) \[:=] \text{map } (fst \ cf) \ outs)$
by (fastforce intro:CFG-return-edge-fun)
from $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q \leftarrow_p f \rangle \langle (p, ins, outs) \in \text{set } procs \rangle \langle s' = cf' \# cfx' \# cfs' \rangle$
have $\text{transfer}':\text{fst } (\text{hd } (\text{transfer } (\text{kind } a) \ s')) =$
 $(fst \ cfx')(\text{ParamDefs } (\text{targetnode } a) \[:=] \text{map } (fst \ cf') \ outs)$
by (fastforce intro:CFG-return-edge-fun)
from $\langle \text{state-val } s \ (outs!i) = \text{state-val } s' \ (outs!i) \rangle \langle i < \text{length } outs \rangle$
 $\langle s = cf \# cfx \# cfs \rangle \langle s' = cf' \# cfx' \# cfs' \rangle$
have $(fst \ cf) \ (outs!i) = (fst \ cf') \ (outs!i)$ **by** simp
from $\langle \text{valid-edge } a \rangle$ **have** $\text{distinct } (\text{ParamDefs } (\text{targetnode } a))$

by(*fastforce intro:distinct-ParamDefs*)
from $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q \leftrightarrow_{pf} \rangle \langle (p, ins, outs) \in \text{set procs} \rangle$
have $\text{length}(\text{ParamDefs } (\text{targetnode } a)) = \text{length } outs$
by(*fastforce intro:ParamDefs-return-target-length*)
with $\langle i < \text{length } outs \rangle \langle \text{distinct } (\text{ParamDefs } (\text{targetnode } a)) \rangle$
have $(fst \text{ cfx})(\text{ParamDefs } (\text{targetnode } a) [:=] \text{map } (fst \text{ cf}) \text{ outs})$
 $((\text{ParamDefs } (\text{targetnode } a))!i) = (\text{map } (fst \text{ cf}) \text{ outs})!i$
and $(fst \text{ cfx}')(\text{ParamDefs } (\text{targetnode } a) [:=] \text{map } (fst \text{ cf}') \text{ outs})$
 $((\text{ParamDefs } (\text{targetnode } a))!i) = (\text{map } (fst \text{ cf}') \text{ outs})!i$
by(*fastforce intro:fun-upds-nth*)
with $\text{transfer } \text{transfer}' \langle (fst \text{ cf}) \text{ (outs)!i} \rangle = \langle (fst \text{ cf}') \text{ (outs)!i} \rangle \langle i < \text{length } outs \rangle$
show *?thesis by simp*
qed

lemma *CFG-slp-no-Def-equal*:

assumes $n - as \rightarrow_{sl^*} n'$ **and** *valid-edge* a **and** $a' \in \text{get-return-edges } a$
and $V \notin \text{set } (\text{ParamDefs } (\text{targetnode } a'))$ **and** $\text{preds } (\text{kinds } (a \# as @ [a'])) \text{ } s$
shows $\text{state-val } (\text{transfers } (\text{kinds } (a \# as @ [a'])) \text{ } s) \text{ } V = \text{state-val } s \text{ } V$
proof –
from $\langle \text{valid-edge } a \rangle \langle a' \in \text{get-return-edges } a \rangle$
obtain $Q \text{ } r \text{ } p \text{ } fs$ **where** $\text{kind } a = Q : r \leftrightarrow_{pfs}$
by(*fastforce dest!:only-call-get-return-edges*)
with $\langle \text{valid-edge } a \rangle \langle a' \in \text{get-return-edges } a \rangle$ **obtain** $Q' \text{ } f'$ **where** $\text{kind } a' =$
 $Q' \leftrightarrow_{pf'}$
by(*fastforce dest!:call-return-edges*)
from $\langle \text{valid-edge } a \rangle \langle a' \in \text{get-return-edges } a \rangle$ **have** *valid-edge* a'
by(*rule get-return-edges-valid*)
from $\langle \text{preds } (\text{kinds } (a \# as @ [a'])) \text{ } s \rangle$ **obtain** $cf \text{ } cfs$ **where** $[simp]: s = cf \# cfs$
by(*cases s, auto simp: kinds-def*)
from $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q : r \leftrightarrow_{pfs} \rangle$ **obtain** $ins \text{ } outs$
where $(p, ins, outs) \in \text{set procs}$ **by**(*fastforce dest!:callee-in-procs*)
from $\langle \text{kind } a = Q : r \leftrightarrow_{pfs} \rangle$ **obtain** cfx **where** $\text{transfer } (\text{kind } a) \text{ } s = cfx \# cf \# cfs$
by *simp*
moreover
from $\langle n - as \rightarrow_{sl^*} n' \rangle$ **obtain** cfx'
where $\text{transfers } (\text{kinds } as) \text{ } (cfx \# cf \# cfs) = cfx' \# cf \# cfs$
by(*fastforce elim:slp-callstack-length-equal*)
moreover
from $\langle \text{kind } a' = Q' \leftrightarrow_{pf'} \rangle \langle \text{valid-edge } a' \rangle \langle (p, ins, outs) \in \text{set procs} \rangle$
have $\text{fst } (\text{hd } (\text{transfer } (\text{kind } a') \text{ } (cfx' \# cf \# cfs))) =$
 $(fst \text{ cf})(\text{ParamDefs } (\text{targetnode } a') [:=] \text{map } (fst \text{ cfx}') \text{ outs})$
by(*simp, simp only: formal-out-THE, fastforce intro:CFG-return-edge-fun*)
ultimately have $\text{fst } (\text{hd } (\text{transfers } (\text{kinds } (a \# as @ [a'])) \text{ } s)) =$
 $(fst \text{ cf})(\text{ParamDefs } (\text{targetnode } a') [:=] \text{map } (fst \text{ cfx}') \text{ outs})$
by(*simp add: kinds-def transfers-split*)
with $\langle V \notin \text{set } (\text{ParamDefs } (\text{targetnode } a')) \rangle$ **show** *?thesis*
by(*simp add: fun-upds-notin*)
qed

lemma [dest!]: $V \in Use (-Entry-) \implies False$
by(simp add:Entry-empty)

lemma [dest!]: $V \in Def (-Entry-) \implies False$
by(simp add:Entry-empty)

lemma CFG-intra-path-no-Def-equal:

assumes $n -as \rightarrow_i^* n'$ **and** $\forall n \in set (sourcenodes\ as). V \notin Def\ n$
and $preds (kinds\ as)\ s$
shows $state-val (transfers (kinds\ as)\ s)\ V = state-val\ s\ V$

proof –

from $\langle n -as \rightarrow_i^* n' \rangle$ **have** $n -as \rightarrow^* n'$ **and** $\forall a \in set\ as. intra-kind (kind\ a)$
by(simp-all add:intra-path-def)
from $\langle \forall n \in set (sourcenodes\ as). V \notin Def\ n \rangle \langle preds (kinds\ as)\ s \rangle$
have $state-val (transfers (kinds\ as)\ s)\ V = state-val\ s\ V$
proof(induct arbitrary:s rule:path.induct)
case (empty-path n)
thus ?case **by**(simp add:sourcenodes-def kinds-def)

next

case (Cons-path n'' as n' a n)
note $IH = \langle \bigwedge s. \llbracket \forall a \in set\ as. intra-kind (kind\ a); \forall n \in set (sourcenodes\ as). V \notin Def\ n; preds (kinds\ as)\ s \rrbracket \implies state-val (transfers (kinds\ as)\ s)\ V = state-val\ s\ V \rangle$
from $\langle preds (kinds\ (a\ \#as))\ s \rangle$ **have** $pred (kind\ a)\ s$
and $preds (kinds\ as)\ (transfer (kind\ a)\ s)$ **by**(simp-all add:kinds-def)
from $\langle \forall n \in set (sourcenodes\ (a\ \#as)). V \notin Def\ n \rangle$
have $noDef: V \notin Def (sourcenode\ a)$
and $all: \forall n \in set (sourcenodes\ as). V \notin Def\ n$
by(auto simp:sourcenodes-def)
from $\langle \forall a \in set (a\ \#as). intra-kind (kind\ a) \rangle$
have $intra-kind (kind\ a)$ **and** $all': \forall a \in set\ as. intra-kind (kind\ a)$
by auto
from $\langle valid-edge\ a \rangle noDef \langle intra-kind (kind\ a) \rangle \langle pred (kind\ a)\ s \rangle$
have $state-val (transfer (kind\ a)\ s)\ V = state-val\ s\ V$
by –(rule CFG-intra-edge-no-Def-equal)
with $IH[OF\ all'\ all \langle preds (kinds\ as)\ (transfer (kind\ a)\ s) \rangle]$ **show** ?case
by(simp add:kinds-def)

qed

thus ?thesis **by** blast

qed

lemma slpa-preds:

$\llbracket same-level-path-aux\ cs\ as; s = cfsx @ cf \# cfs; s' = cfsx @ cf \# cfs'; \text{length}\ cfs = \text{length}\ cfs'; \forall a \in set\ as. valid-edge\ a; \text{length}\ cs = \text{length}\ cfsx; \rrbracket$

```

    preds (kinds as) s]]
  => preds (kinds as) s'
proof(induct arbitrary:s s' cf cfsx rule:slpa-induct)
  case (slpa-empty cs) thus ?case by(simp add:kinds-def)
next
  case (slpa-intra cs a as)
  note IH = ⟨ $\bigwedge s s' cf cfsx. \llbracket s = cfsx@cf\#cfs; s' = cfsx@cf\#cfs' \rrbracket;$ 
    length cfs = length cfs';  $\forall a \in \text{set } as. \text{valid-edge } a; \text{length } cs = \text{length } cfsx;$ 
    preds (kinds as) s]]  $\implies$  preds (kinds as) s'⟩
  from ⟨ $\forall a \in \text{set } (a\#as). \text{valid-edge } a$ ⟩ have valid-edge a
    and  $\forall a \in \text{set } as. \text{valid-edge } a$  by simp-all
  from ⟨preds (kinds (a#as)) s⟩ have pred (kind a) s
    and preds (kinds as) (transfer (kind a) s) by(simp-all add:kinds-def)
  show ?case
  proof(cases cfsx)
  case Nil
  with ⟨length cs = length cfsx⟩ have length cs = length [] by simp
  from Nil ⟨s = cfsx@cf#cfs⟩ ⟨s' = cfsx@cf#cfs'⟩ ⟨intra-kind (kind a)⟩
  obtain cfx where transfer (kind a) s = []@cfx#cfs
    and transfer (kind a) s' = []@cfx#cfs'
  by(cases kind a,auto simp:kinds-def intra-kind-def)
  from IH[OF this ⟨length cfs = length cfs'⟩ ⟨ $\forall a \in \text{set } as. \text{valid-edge } a$ ⟩
    ⟨length cs = length []⟩ ⟨preds (kinds as) (transfer (kind a) s)⟩]
  have preds (kinds as) (transfer (kind a) s') .
  moreover
  from Nil ⟨valid-edge a⟩ ⟨pred (kind a) s⟩ ⟨s = cfsx@cf#cfs⟩ ⟨s' = cfsx@cf#cfs'⟩
    ⟨length cfs = length cfs'⟩
  have pred (kind a) s' by(fastforce intro:CFG-edge-Uses-pred-equal)
  ultimately show ?thesis by(simp add:kinds-def)
next
  case (Cons x xs)
  with ⟨s = cfsx@cf#cfs⟩ ⟨s' = cfsx@cf#cfs'⟩ ⟨intra-kind (kind a)⟩
  obtain cfx where transfer (kind a) s = (cfx#xs)@cf#cfs
    and transfer (kind a) s' = (cfx#xs)@cf#cfs'
  by(cases kind a,auto simp:kinds-def intra-kind-def)
  from IH[OF this ⟨length cfs = length cfs'⟩ ⟨ $\forall a \in \text{set } as. \text{valid-edge } a$ ⟩ -
    ⟨preds (kinds as) (transfer (kind a) s)⟩] ⟨length cs = length cfsx⟩ Cons
  have preds (kinds as) (transfer (kind a) s') by simp
  moreover
  from Cons ⟨valid-edge a⟩ ⟨pred (kind a) s⟩ ⟨s = cfsx@cf#cfs⟩ ⟨s' = cfsx@cf#cfs'⟩
    ⟨length cfs = length cfs'⟩
  have pred (kind a) s' by(fastforce intro:CFG-edge-Uses-pred-equal)
  ultimately show ?thesis by(simp add:kinds-def)
qed
next
  case (slpa-Call cs a as Q r p fs)
  note IH = ⟨ $\bigwedge s s' cf cfsx. \llbracket s = cfsx@cf\#cfs; s' = cfsx@cf\#cfs' \rrbracket;$ 
    length cfs = length cfs';  $\forall a \in \text{set } as. \text{valid-edge } a; \text{length } (a\#cs) = \text{length } cfsx;$ 
    preds (kinds as) s]]  $\implies$  preds (kinds as) s'⟩

```

```

from ⟨ $\forall a \in \text{set } (a \# \text{as}). \text{valid-edge } a$ ⟩ have valid-edge a
  and  $\forall a \in \text{set } \text{as}. \text{valid-edge } a$  by simp-all
from ⟨preds (kinds (a # as)) s⟩ have pred (kind a) s
  and preds (kinds as) (transfer (kind a) s) by (simp-all add:kinds-def)
from ⟨kind a = Q:r↔pfs⟩ ⟨s = cfsx@cf#cfs⟩ ⟨s' = cfsx@cf#cfs'⟩ obtain cfx
  where transfer (kind a) s = (cfx#cfsx)@cf#cfs
  and transfer (kind a) s' = (cfx#cfsx)@cf#cfs' by (cases cfsx) auto
from IH[OF this ⟨length cfs = length cfs'⟩ ⟨ $\forall a \in \text{set } \text{as}. \text{valid-edge } a$ ⟩ -
  ⟨preds (kinds as) (transfer (kind a) s)⟩] ⟨length cs = length cfsx⟩
have preds (kinds as) (transfer (kind a) s') by simp
moreover
from ⟨valid-edge a⟩ ⟨pred (kind a) s⟩ ⟨s = cfsx@cf#cfs⟩ ⟨s' = cfsx@cf#cfs'⟩
  ⟨length cfs = length cfs'⟩ have pred (kind a) s'
  by (cases cfsx)(auto intro:CFG-edge-Uses-pred-equal)
ultimately show ?case by (simp add:kinds-def)
next
case (slpa-Return cs a as Q p f c' cs')
note IH = ⟨ $\bigwedge s s' cf cfsx. \llbracket s = cfsx@cf#cfs; s' = cfsx@cf#cfs';$ 
  length cfs = length cfs';  $\forall a \in \text{set } \text{as}. \text{valid-edge } a; \text{length } cs' = \text{length } cfsx;$ 
  preds (kinds as) s  $\rrbracket \implies \text{preds (kinds as) s'}$ ⟩
from ⟨ $\forall a \in \text{set } (a \# \text{as}). \text{valid-edge } a$ ⟩ have valid-edge a
  and  $\forall a \in \text{set } \text{as}. \text{valid-edge } a$  by simp-all
from ⟨preds (kinds (a # as)) s⟩ have pred (kind a) s
  and preds (kinds as) (transfer (kind a) s) by (simp-all add:kinds-def)
show ?case
proof (cases cs')
  case Nil
  with ⟨cs = c' # cs'⟩ ⟨s = cfsx@cf#cfs⟩ ⟨s' = cfsx@cf#cfs'⟩
    ⟨length cs = length cfsx⟩
  obtain cf' where s = cf' # cf # cfs and s' = cf' # cf # cfs' by (cases cfsx) auto
  with ⟨kind a = Q↔pf⟩ obtain cf'' where transfer (kind a) s = []@cf''#cfs
    and transfer (kind a) s' = []@cf''#cfs' by auto
  from IH[OF this ⟨length cfs = length cfs'⟩ ⟨ $\forall a \in \text{set } \text{as}. \text{valid-edge } a$ ⟩ -
    ⟨preds (kinds as) (transfer (kind a) s)⟩] Nil
  have preds (kinds as) (transfer (kind a) s') by simp
  moreover
  from ⟨valid-edge a⟩ ⟨pred (kind a) s⟩ ⟨s = cfsx@cf#cfs⟩ ⟨s' = cfsx@cf#cfs'⟩
    ⟨length cfs = length cfs'⟩ have pred (kind a) s'
    by (cases cfsx)(auto intro:CFG-edge-Uses-pred-equal)
  ultimately show ?thesis by (simp add:kinds-def)
next
case (Cons cx csx)
with ⟨cs = c' # cs'⟩ ⟨length cs = length cfsx⟩ ⟨s = cfsx@cf#cfs⟩ ⟨s' = cfsx@cf#cfs'⟩
  obtain x x' xs where s = (x # x' # xs)@cf#cfs and s' = (x # x' # xs)@cf#cfs'
  and length xs = length csx
  by (cases cfsx, auto, case-tac list, fastforce+)
with ⟨kind a = Q↔pf⟩ obtain cf' where transfer (kind a) s = (cf' # xs)@cf#cfs
  and transfer (kind a) s' = (cf' # xs)@cf#cfs'
  by fastforce

```



```

from IH[OF this ‹length cfs = length cfs'› ‹ $\forall a \in \text{set as. valid-edge } a$ › -
  ‹preds (kinds as) (transfer (kind a) s)›] Cons ‹length xs = length csx›
have preds (kinds as) (transfer (kind a) s') by simp
moreover
from ‹valid-edge a› ‹pred (kind a) s› ‹s = cfsx@cf#cfs› ‹s' = cfsx@cf#cfs'›
  ‹length cfs = length cfs'› have pred (kind a) s'
  by (cases cfsx)(auto intro:CFG-edge-Uses-pred-equal)
ultimately show ?thesis by (simp add:kinds-def)
qed
qed

```

lemma slp-preds:

```

assumes n -as→sl* n' and preds (kinds as) (cf#cfs)
and length cfs = length cfs'
shows preds (kinds as) (cf#cfs')
proof -
from ‹n -as→sl* n'› have n -as→* n' and same-level-path-aux [] as
  by (simp-all add:slp-def same-level-path-def)
from ‹n -as→* n'› have  $\forall a \in \text{set as. valid-edge } a$  by (rule path-valid-edges)
with ‹same-level-path-aux [] as› ‹preds (kinds as) (cf#cfs)›
  ‹length cfs = length cfs'›
show ?thesis by (fastforce elim!:slpa-preds)
qed
end

```

end

theory CFGExit-wf imports CFGExit CFG-wf **begin**

1.3.1 New well-formedness lemmas using (-Exit-)

```

locale CFGExit-wf = CFGExit sourcenode targetnode kind valid-edge Entry
  get-proc get-return-edges procs Main Exit +
  CFG-wf sourcenode targetnode kind valid-edge Entry
  get-proc get-return-edges procs Main Def Use ParamDefs ParamUses
for sourcenode :: 'edge  $\Rightarrow$  'node and targetnode :: 'edge  $\Rightarrow$  'node
and kind :: 'edge  $\Rightarrow$  ('var,'val,'ret,'pname) edge-kind
and valid-edge :: 'edge  $\Rightarrow$  bool
and Entry :: 'node (('(-Entry'-)) and get-proc :: 'node  $\Rightarrow$  'pname
and get-return-edges :: 'edge  $\Rightarrow$  'edge set
and procs :: ('pname  $\times$  'var list  $\times$  'var list) list and Main :: 'pname
and Exit::'node (('(-Exit'-))
and Def :: 'node  $\Rightarrow$  'var set and Use :: 'node  $\Rightarrow$  'var set
and ParamDefs :: 'node  $\Rightarrow$  'var list
and ParamUses :: 'node  $\Rightarrow$  'var set list +
assumes Exit-empty:Def (-Exit-) = {}  $\wedge$  Use (-Exit-) = {}

```

begin

lemma *Exit-Use-empty* [*dest!*]: $V \in Use (-Exit) \implies False$
by(*simp add:Exit-empty*)

lemma *Exit-Def-empty* [*dest!*]: $V \in Def (-Exit) \implies False$
by(*simp add:Exit-empty*)

end

end

1.4 CFG and semantics conform

theory *SemanticsCFG* **imports** *CFG* **begin**

locale *CFG-semantics-wf* = *CFG* *sourcenode* *targetnode* *kind* *valid-edge* *Entry*
get-proc *get-return-edges* *procs* *Main*

for *sourcenode* :: 'edge \Rightarrow 'node **and** *targetnode* :: 'edge \Rightarrow 'node

and *kind* :: 'edge \Rightarrow ('var,'val,'ret,'pname) edge-kind

and *valid-edge* :: 'edge \Rightarrow bool

and *Entry* :: 'node ('('Entry'-')) **and** *get-proc* :: 'node \Rightarrow 'pname

and *get-return-edges* :: 'edge \Rightarrow 'edge set

and *procs* :: ('pname \times 'var list \times 'var list) list **and** *Main* :: 'pname +

fixes *sem*::'com \Rightarrow ('var \rightarrow 'val) list \Rightarrow 'com \Rightarrow ('var \rightarrow 'val) list \Rightarrow bool

$((1\langle -,/- \rangle) \Rightarrow / (1\langle -,/- \rangle)) [0,0,0,0] 81$

fixes *identifies*::'node \Rightarrow 'com \Rightarrow bool (- \triangleq - [51,0] 80)

assumes *fundamental-property*:

$\llbracket n \triangleq c; \langle c, [cf] \rangle \Rightarrow \langle c', s' \rangle \rrbracket \implies$

$\exists n' as. n - as \rightarrow_{\sqrt{*}} n' \wedge n' \triangleq c' \wedge preds (kinds as) [(cf, undefined)] \wedge$

$transfers (kinds as) [(cf, undefined)] = cfs' \wedge map fst cfs' = s'$

end

1.5 Return and their corresponding call nodes

theory *ReturnAndCallNodes* **imports** *CFG* **begin**

context *CFG* **begin**

1.5.1 Defining *return-node*

definition *return-node* :: 'node \Rightarrow bool

where *return-node* $n \equiv \exists a a'. valid-edge a \wedge n = targetnode a \wedge$

$valid-edge a' \wedge a \in get-return-edges a'$

lemma *return-node-determines-call-node*:

assumes *return-node n*

shows $\exists!n'. \exists a a'. \text{valid-edge } a \wedge n' = \text{sourcnode } a \wedge \text{valid-edge } a' \wedge a' \in \text{get-return-edges } a \wedge n = \text{targetnode } a'$

proof(*rule ex-ex1I*)

from $\langle \text{return-node } n \rangle$

show $\exists n' a a'. \text{valid-edge } a \wedge n' = \text{sourcnode } a \wedge \text{valid-edge } a' \wedge a' \in \text{get-return-edges } a \wedge n = \text{targetnode } a'$

by(*simp add:return-node-def*) *blast*

next

fix $n' nx$

assume $\exists a a'. \text{valid-edge } a \wedge n' = \text{sourcnode } a \wedge \text{valid-edge } a' \wedge a' \in \text{get-return-edges } a \wedge n = \text{targetnode } a'$

and $\exists a a'. \text{valid-edge } a \wedge nx = \text{sourcnode } a \wedge \text{valid-edge } a' \wedge a' \in \text{get-return-edges } a \wedge n = \text{targetnode } a'$

then obtain $a a' ax ax'$ **where** *valid-edge a* **and** $n' = \text{sourcnode } a$ **and** *valid-edge a'* **and** $a' \in \text{get-return-edges } a$ **and** $n = \text{targetnode } a'$ **and** *valid-edge ax* **and** $nx = \text{sourcnode } ax$ **and** *valid-edge ax'* **and** $ax' \in \text{get-return-edges } ax$ **and** $n = \text{targetnode } ax'$

by *blast*

from $\langle \text{valid-edge } a \rangle \langle a' \in \text{get-return-edges } a \rangle$ **have** *valid-edge a'*

by(*rule get-return-edges-valid*)

from $\langle \text{valid-edge } a \rangle \langle a' \in \text{get-return-edges } a \rangle$ **obtain** a''

where *intra-edge1:valid-edge a''* $\text{sourcnode } a'' = \text{sourcnode } a$ $\text{targetnode } a'' = \text{targetnode } a'$ *kind a'' = (λcf. False)* ✓

by(*fastforce dest:call-return-node-edge*)

from $\langle \text{valid-edge } ax \rangle \langle ax' \in \text{get-return-edges } ax \rangle$ **obtain** ax''

where *intra-edge2:valid-edge ax''* $\text{sourcnode } ax'' = \text{sourcnode } ax$ $\text{targetnode } ax'' = \text{targetnode } ax'$ *kind ax'' = (λcf. False)* ✓

by(*fastforce dest:call-return-node-edge*)

from $\langle \text{valid-edge } a \rangle \langle a' \in \text{get-return-edges } a \rangle$

obtain $Q r p fs$ **where** *kind a = Q:r↦pfs*

by(*fastforce dest!:only-call-get-return-edges*)

with $\langle \text{valid-edge } a \rangle \langle a' \in \text{get-return-edges } a \rangle$ **obtain** $Q' p f'$

where *kind a' = Q'↔pf'* **by**(*fastforce dest!:call-return-edges*)

with $\langle \text{valid-edge } a' \rangle$

have $\exists!a''. \text{valid-edge } a'' \wedge \text{targetnode } a'' = \text{targetnode } a' \wedge \text{intra-kind}(\text{kind } a'')$

by(*rule return-only-one-intra-edge*)

with *intra-edge1* *intra-edge2* $\langle n = \text{targetnode } a' \rangle \langle n = \text{targetnode } ax' \rangle$

have $a'' = ax''$ **by**(*fastforce simp:intra-kind-def*)

with $\langle \text{sourcnode } a'' = \text{sourcnode } a \rangle \langle \text{sourcnode } ax'' = \text{sourcnode } ax \rangle$

$\langle n' = \text{sourcnode } a \rangle \langle nx = \text{sourcnode } ax \rangle$

show $n' = nx$ **by** *simp*

qed

lemma *return-node-THE-call-node*:

$\llbracket \text{return-node } n; \text{valid-edge } a; \text{valid-edge } a'; a' \in \text{get-return-edges } a;$

$n = \text{targetnode } a'$
 $\implies (\text{THE } n'. \exists a a'. \text{valid-edge } a \wedge n' = \text{sourcenode } a \wedge \text{valid-edge } a' \wedge$
 $a' \in \text{get-return-edges } a \wedge n = \text{targetnode } a') = \text{sourcenode } a$
by(*fastforce intro! : the1-equality return-node-determines-call-node*)

1.5.2 Defining call nodes belonging to a certain *return-node*

definition *call-of-return-node* :: 'node \Rightarrow 'node \Rightarrow bool
where *call-of-return-node* $n\ n' \equiv \exists a a'. \text{return-node } n \wedge$
 $\text{valid-edge } a \wedge n' = \text{sourcenode } a \wedge \text{valid-edge } a' \wedge$
 $a' \in \text{get-return-edges } a \wedge n = \text{targetnode } a'$

lemma *return-node-call-of-return-node*:
 $\text{return-node } n \implies \exists ! n'. \text{call-of-return-node } n\ n'$
by –(*frule return-node-determines-call-node, unfold call-of-return-node-def, simp*)

lemma *call-of-return-nodes-det* [*dest*]:
assumes *call-of-return-node* $n\ n'$ **and** *call-of-return-node* $n\ n''$
shows $n' = n''$

proof –
from $\langle \text{call-of-return-node } n\ n' \rangle$ **have** *return-node* n
by(*simp add: call-of-return-node-def*)
hence $\exists ! n'. \text{call-of-return-node } n\ n'$ **by**(*rule return-node-call-of-return-node*)
with $\langle \text{call-of-return-node } n\ n' \rangle \langle \text{call-of-return-node } n\ n'' \rangle$
show *?thesis* **by** *auto*
qed

lemma *get-return-edges-call-of-return-nodes*:
 $\llbracket \text{valid-call-list } cs\ m; \text{valid-return-list } rs\ m; \forall i < \text{length } rs. rs!i \in \text{get-return-edges } (cs!i); \text{length } rs = \text{length } cs \rrbracket$
 $\implies \forall i < \text{length } cs. \text{call-of-return-node } (\text{targetnodes } rs!i) (\text{sourcenode } (cs!i))$

proof(*induct cs arbitrary: m rs*)
case *Nil* **thus** *?case* **by** *fastforce*
next
case (*Cons* $c'\ cs'$)
note $IH = \langle \bigwedge m\ rs. \llbracket \text{valid-call-list } cs'\ m; \text{valid-return-list } rs\ m; \forall i < \text{length } rs. rs!i \in \text{get-return-edges } (cs'!i); \text{length } rs = \text{length } cs' \rrbracket$
 $\implies \forall i < \text{length } cs'. \text{call-of-return-node } (\text{targetnodes } rs!i) (\text{sourcenode } (cs'!i)) \rangle$
from $\langle \text{length } rs = \text{length } (c' \# cs') \rangle$ **obtain** $r'\ rs'$ **where** $rs = r' \# rs'$
and $\text{length } rs' = \text{length } cs'$ **by**(*cases rs*) *auto*
with $\langle \forall i < \text{length } rs. rs!i \in \text{get-return-edges } ((c' \# cs')!i) \rangle$
have $\forall i < \text{length } rs'. rs'!i \in \text{get-return-edges } (cs'!i)$
and $r' \in \text{get-return-edges } c'$ **by** *auto*
from $\langle \text{valid-call-list } (c' \# cs')\ m \rangle$ **have** *valid-edge* c'
by(*fastforce simp: valid-call-list-def*)

```

from this  $\langle r' \in \text{get-return-edges } c' \rangle$ 
have  $\text{get-proc } (\text{sourcenode } c') = \text{get-proc } (\text{targetnode } r')$ 
  by(rule get-proc-get-return-edge)
from  $\langle \text{valid-call-list } (c' \# cs') \ m \rangle$ 
have  $\text{valid-call-list } cs' \ (\text{sourcenode } c')$ 
  apply(clarsimp simp:valid-call-list-def)
  apply(erule-tac  $x=c' \# cs'$  in allE) apply clarsimp
  by(case-tac  $cs'$ )(auto simp:sourcenodes-def)
from  $\langle \text{valid-return-list } rs \ m \rangle \langle rs = r' \# rs' \rangle$ 
   $\langle \text{get-proc } (\text{sourcenode } c') = \text{get-proc } (\text{targetnode } r') \rangle$ 
have  $\text{valid-return-list } rs' \ (\text{sourcenode } c')$ 
  apply(clarsimp simp:valid-return-list-def)
  apply(erule-tac  $x=r' \# cs'$  in allE) apply clarsimp
  by(case-tac  $cs'$ )(auto simp:targetnodes-def)
from IH[OF  $\langle \text{valid-call-list } cs' \ (\text{sourcenode } c') \rangle$ 
   $\langle \text{valid-return-list } rs' \ (\text{sourcenode } c') \rangle$ 
   $\langle \forall i < \text{length } rs'. \ rs' ! i \in \text{get-return-edges } (cs' ! i) \rangle \langle \text{length } rs' = \text{length } cs' \rangle$ ]
have  $\text{all:} \forall i < \text{length } cs'.$ 
   $\text{call-of-return-node } (\text{targetnodes } rs' ! i) \ (\text{sourcenode } (cs' ! i)) .$ 
from  $\langle \text{valid-edge } c' \rangle \langle r' \in \text{get-return-edges } c' \rangle$  have  $\text{valid-edge } r'$ 
  by(rule get-return-edges-valid)
from  $\langle \text{valid-edge } r' \rangle \langle \text{valid-edge } c' \rangle \langle r' \in \text{get-return-edges } c' \rangle$ 
have  $\text{return-node } (\text{targetnode } r')$  by(fastforce simp:return-node-def)
with  $\langle \text{valid-edge } c' \rangle \langle r' \in \text{get-return-edges } c' \rangle \langle \text{valid-edge } r' \rangle$ 
have  $\text{call-of-return-node } (\text{targetnode } r') \ (\text{sourcenode } c')$ 
  by(simp add:call-of-return-node-def) blast
with all  $\langle rs = r' \# rs' \rangle$  show ?case
  by auto(case-tac i,auto simp:targetnodes-def)
qed

end

end

```

1.6 Observable Sets of Nodes

theory *Observable* **imports** *ReturnAndCallNodes* **begin**

context *CFG* **begin**

1.6.1 Intraprocedural observable sets

inductive-set *obs-intra* :: $'node \Rightarrow 'node \text{ set} \Rightarrow 'node \text{ set}$

for $n::'node$ **and** $S::'node \text{ set}$

where *obs-intra-elem*:

$\llbracket n \text{ --as} \rightarrow_i^* n'; \forall nx \in \text{set}(\text{sourcenodes } as). \ nx \notin S; n' \in S \rrbracket \implies n' \in \text{obs-intra } n \ S$

lemma *obs-intraE*:
assumes $n' \in \text{obs-intra } n \ S$
obtains as where $n - \text{as} \rightarrow_l^* n'$ **and** $\forall nx \in \text{set}(\text{sourcenodes } \text{as}). nx \notin S$ **and**
 $n' \in S$
using $\langle n' \in \text{obs-intra } n \ S \rangle$
by(*fastforce elim:obs-intra.cases*)

lemma *n-in-obs-intra*:
assumes *valid-node* n **and** $n \in S$ **shows** $\text{obs-intra } n \ S = \{n\}$
proof –
from $\langle \text{valid-node } n \rangle$ **have** $n - [] \rightarrow^* n$ **by**(*rule empty-path*)
hence $n - [] \rightarrow_l^* n$ **by**(*simp add:intra-path-def*)
with $\langle n \in S \rangle$ **have** $n \in \text{obs-intra } n \ S$
by(*fastforce elim:obs-intra-elem simp:sourcenodes-def*)
{ fix n' **assume** $n' \in \text{obs-intra } n \ S$
have $n' = n$
proof(*rule ccontr*)
assume $n' \neq n$
from $\langle n' \in \text{obs-intra } n \ S \rangle$ **obtain as where** $n - \text{as} \rightarrow_l^* n'$
and $\forall nx \in \text{set}(\text{sourcenodes } \text{as}). nx \notin S$
and $n' \in S$ **by**(*fastforce elim:obs-intra.cases*)
from $\langle n - \text{as} \rightarrow_l^* n' \rangle$ **have** $n - \text{as} \rightarrow^* n'$ **by**(*simp add:intra-path-def*)
from this $\langle \forall nx \in \text{set}(\text{sourcenodes } \text{as}). nx \notin S \rangle \langle n' \neq n \rangle \langle n \in S \rangle$
show *False*
proof(*induct rule:path.induct*)
case (*Cons-path* n'' *as* $n' a n$)
from $\langle \forall nx \in \text{set}(\text{sourcenodes } (a \# \text{as})). nx \notin S \rangle \langle \text{sourcenode } a = n \rangle$
have $n \notin S$ **by**(*simp add:sourcenodes-def*)
with $\langle n \in S \rangle$ **show** *False* **by** *simp*
qed *simp*
qed }
with $\langle n \in \text{obs-intra } n \ S \rangle$ **show** *?thesis* **by** *fastforce*
qed

lemma *in-obs-intra-valid*:
assumes $n' \in \text{obs-intra } n \ S$ **shows** *valid-node* n **and** *valid-node* n'
using $\langle n' \in \text{obs-intra } n \ S \rangle$
by(*auto elim!:obs-intraE intro:path-valid-node simp:intra-path-def*)

lemma *edge-obs-intra-subset*:
assumes *valid-edge* a **and** *intra-kind* (*kind* a) **and** *sourcenode* $a \notin S$
shows $\text{obs-intra } (\text{targetnode } a) \ S \subseteq \text{obs-intra } (\text{sourcenode } a) \ S$
proof
fix n **assume** $n \in \text{obs-intra } (\text{targetnode } a) \ S$

then obtain as **where** $targetnode\ a -as \rightarrow_i^* n$
and $all: \forall nx \in set(sourcenodes\ as). nx \notin S$ **and** $n \in S$ **by** $(erule\ obs-intraE)$
from $\langle valid-edge\ a \rangle \langle intra-kind\ (kind\ a) \rangle \langle targetnode\ a -as \rightarrow_i^* n \rangle$
have $sourcenode\ a -[a]@as \rightarrow_i^* n$ **by** $(fastforce\ intro: Cons-path\ simp: intra-path-def)$
moreover
from $all\ \langle sourcenode\ a \notin S \rangle$ **have** $\forall nx \in set(sourcenodes\ (a\#\ as)). nx \notin S$
by $(simp\ add: sourcenodes-def)$
ultimately show $n \in obs-intra\ (sourcenode\ a)\ S$ **using** $\langle n \in S \rangle$
by $(fastforce\ intro: obs-intra-elem)$
qed

lemma $path-obs-intra-subset$:

assumes $n -as \rightarrow_i^* n'$ **and** $\forall n' \in set(sourcenodes\ as). n' \notin S$
shows $obs-intra\ n'\ S \subseteq obs-intra\ n\ S$

proof –

from $\langle n -as \rightarrow_i^* n' \rangle$ **have** $n -as \rightarrow^* n'$ **and** $\forall a \in set\ as. intra-kind\ (kind\ a)$
by $(simp-all\ add: intra-path-def)$

from $this\ \langle \forall n' \in set(sourcenodes\ as). n' \notin S \rangle$ **show** $?thesis$

proof $(induct\ rule: path.induct)$

case $(Cons-path\ n''\ as\ n'\ a\ n)$

note $IH = \langle \llbracket \forall a \in set\ as. intra-kind\ (kind\ a); \forall n' \in set(sourcenodes\ as). n' \notin S \rrbracket$

$\implies obs-intra\ n'\ S \subseteq obs-intra\ n''\ S$

from $\langle \forall n' \in set(sourcenodes\ (a\#\ as)). n' \notin S \rangle$

have $all: \forall n' \in set(sourcenodes\ as). n' \notin S$ **and** $sourcenode\ a \notin S$

by $(simp-all\ add: sourcenodes-def)$

from $\langle \forall a \in set(a\#\ as). intra-kind\ (kind\ a) \rangle$

have $intra-kind\ (kind\ a)$ **and** $\forall a \in set\ as. intra-kind\ (kind\ a)$

by $(simp-all\ add: intra-path-def)$

from $IH[OF\ \langle \forall a \in set\ as. intra-kind\ (kind\ a) \rangle\ all]$

have $obs-intra\ n'\ S \subseteq obs-intra\ n''\ S$.

from $\langle valid-edge\ a \rangle \langle intra-kind\ (kind\ a) \rangle \langle targetnode\ a = n'' \rangle$

$\langle sourcenode\ a = n \rangle \langle sourcenode\ a \notin S \rangle$

have $obs-intra\ n''\ S \subseteq obs-intra\ n\ S$ **by** $(fastforce\ dest: edge-obs-intra-subset)$

with $\langle obs-intra\ n'\ S \subseteq obs-intra\ n''\ S \rangle$ **show** $?case$ **by** $fastforce$

qed $simp$

qed

lemma $path-ex-obs-intra$:

assumes $n -as \rightarrow_i^* n'$ **and** $n' \in S$

obtains m **where** $m \in obs-intra\ n\ S$

proof $(atomize-elim)$

show $\exists m. m \in obs-intra\ n\ S$

proof $(cases\ \forall nx \in set(sourcenodes\ as). nx \notin S)$

case $True$

with $\langle n -as \rightarrow_i^* n' \rangle \langle n' \in S \rangle$ **have** $n' \in obs-intra\ n\ S$ **by** $-(rule\ obs-intra-elem)$

thus $?thesis$ **by** $fastforce$

next
case *False*
hence $\exists nx \in \text{set}(\text{sourcenodes } as). nx \in S$ **by** *fastforce*
then obtain $nx \ ns \ ns'$ **where** $\text{sourcenodes } as = ns@nx\#ns'$
and $nx \in S$ **and** $\forall n' \in \text{set } ns. n' \notin S$
by (*fastforce elim!:split-list-first-propE*)
from $\langle \text{sourcenodes } as = ns@nx\#ns' \rangle$ **obtain** $as' \ a \ as''$
where $ns = \text{sourcenodes } as'$
and $as = as'@a\#as''$ **and** $\text{sourcenode } a = nx$
by (*fastforce elim:map-append-append-maps simp:sourcenodes-def*)
with $\langle n -as \rightarrow_i^* n' \rangle$ **have** $n -as' \rightarrow_i^* nx$
by (*fastforce dest:path-split simp:intra-path-def*)
with $\langle nx \in S \rangle \langle \forall n' \in \text{set } ns. n' \notin S \rangle \langle ns = \text{sourcenodes } as' \rangle$
have $nx \in \text{obs-intra } n \ S$ **by** (*fastforce intro:obs-intra-elem*)
thus *?thesis* **by** *fastforce*
qed
qed

1.6.2 Interprocedural observable sets restricted to the slice

fun $\text{obs} :: 'node \text{ list} \Rightarrow 'node \text{ set} \Rightarrow 'node \text{ list set}$
where $\text{obs} [] \ S = \{\}$
 $| \text{obs } (n\#ns) \ S = (\text{let } S' = \text{obs-intra } n \ S \text{ in}$
(if $(S' = \{\}) \vee (\exists n' \in \text{set } ns. \exists nx. \text{call-of-return-node } n' \ nx \wedge nx \notin S)$
then $\text{obs } ns \ S$ *else* $(\lambda nx. nx\#ns)$ *' S'))*

lemma *obsI*:
assumes $n' \in \text{obs-intra } n \ S$
and $\forall nx \in \text{set } nsx'. \exists nx'. \text{call-of-return-node } nx \ nx' \wedge nx' \in S$
shows $\llbracket ns = nsx@n\#nsx'; \forall xs \ x \ xs'. nsx = xs@x\#xs' \wedge \text{obs-intra } x \ S \neq \{\} \rrbracket$
 $\longrightarrow (\exists x'' \in \text{set } (xs'@[n]). \exists nx. \text{call-of-return-node } x'' \ nx \wedge nx \notin S)$
 $\implies n'\#nsx' \in \text{obs } ns \ S$
proof (*induct ns arbitrary:nsx*)
case (*Cons x xs*)
note $IH = \langle \bigwedge nsx. \llbracket xs = nsx@n\#nsx';$
 $\forall xs \ x \ xs'. nsx = xs @ x \# xs' \wedge \text{obs-intra } x \ S \neq \{\} \longrightarrow$
 $(\exists x'' \in \text{set } (xs'@[n]). \exists nx. \text{call-of-return-node } x'' \ nx \wedge nx \notin S) \rrbracket$
 $\implies n'\#nsx' \in \text{obs } xs \ S \rangle$
note $nsx = \langle \forall xs \ x \ xs'. nsx = xs @ x \# xs' \wedge \text{obs-intra } x \ S \neq \{\} \longrightarrow$
 $(\exists x'' \in \text{set } (xs'@[n]). \exists nx. \text{call-of-return-node } x'' \ nx \wedge nx \notin S) \rangle$
show *?case*
proof (*cases nsx*)
case *Nil*
with $\langle x\#xs = nsx@n\#nsx' \rangle$ **have** $n = x$ **and** $xs = nsx'$ **by** *simp-all*
with $\langle n' \in \text{obs-intra } n \ S \rangle$
 $\langle \forall nx \in \text{set } nsx'. \exists nx'. \text{call-of-return-node } nx \ nx' \wedge nx' \in S \rangle$
show *?thesis* **by** (*fastforce simp:Let-def*)
next

case (*Cons* z zs)
with $\langle x \# xs = nsx @ n \# nsx' \rangle$ **have** [*simp*]: $x = z$ $xs = zs @ n \# nsx'$ **by** *simp-all*
from *nsx Cons*
have $\forall xs \ x \ xs'. \ zs = xs @ x \# xs' \wedge \text{obs-intra } x \ S \neq \{\}$ \longrightarrow
 $(\exists x'' \in \text{set } (xs' @ [n]). \exists nx. \text{call-of-return-node } x'' \ nx \wedge nx \notin S)$
by *clarsimp(erule-tac x=z#xs in allE,auto)*
from *IH[OF $\langle xs = zs @ n \# nsx' \rangle$ this]* **have** $n' \# nsx' \in \text{obs } xs \ S$ **by** *simp*
show *?thesis*
proof(*cases obs-intra z S = {}*)
case *True*
with *Cons $\langle n' \# nsx' \in \text{obs } xs \ S \rangle$* **show** *?thesis* **by**(*simp add:Let-def*)
next
case *False*
from *nsx Cons*
have $\text{obs-intra } z \ S \neq \{\}$ \longrightarrow
 $(\exists x'' \in \text{set } (zs @ [n]). \exists nx. \text{call-of-return-node } x'' \ nx \wedge nx \notin S)$
by *clarsimp(erule-tac x=[] in allE,auto)*
with *False* **have** $\exists x'' \in \text{set } (zs @ [n]). \exists nx. \text{call-of-return-node } x'' \ nx \wedge nx \notin$
 S
by *simp*
with $\langle xs = zs @ n \# nsx' \rangle$
have $\exists n' \in \text{set } xs. \exists nx. \text{call-of-return-node } n' \ nx \wedge nx \notin S$ **by** *fastforce*
with *Cons $\langle n' \# nsx' \in \text{obs } xs \ S \rangle$* **show** *?thesis* **by**(*simp add:Let-def*)
qed
qed
qed *simp*

lemma *obsE [consumes 2]*:
assumes $ns' \in \text{obs } ns \ S$ **and** $\forall n \in \text{set } (tl \ ns). \text{return-node } n$
obtains $nsx \ n \ nsx' \ n'$ **where** $ns = nsx @ n \# nsx'$ **and** $ns' = n' \# nsx'$
and $n' \in \text{obs-intra } n \ S$
and $\forall nx \in \text{set } nsx'. \exists nx'. \text{call-of-return-node } nx \ nx' \wedge nx' \in S$
and $\forall xs \ x \ xs'. \ nsx = xs @ x \# xs' \wedge \text{obs-intra } x \ S \neq \{\}$
 $\longrightarrow (\exists x'' \in \text{set } (xs' @ [n]). \exists nx. \text{call-of-return-node } x'' \ nx \wedge nx \notin S)$
proof(*atomize-elim*)
from $\langle ns' \in \text{obs } ns \ S \rangle \ \forall n \in \text{set } (tl \ ns). \text{return-node } n$
show $\exists nsx \ n \ nsx' \ n'. \ ns = nsx @ n \# nsx' \wedge ns' = n' \# nsx' \wedge$
 $n' \in \text{obs-intra } n \ S \wedge (\forall nx \in \text{set } nsx'. \exists nx'. \text{call-of-return-node } nx \ nx' \wedge nx' \in$
 $S) \wedge$
 $(\forall xs \ x \ xs'. \ nsx = xs @ x \# xs' \wedge \text{obs-intra } x \ S \neq \{\} \longrightarrow$
 $(\exists x'' \in \text{set } (xs' @ [n]). \exists nx. \text{call-of-return-node } x'' \ nx \wedge nx \notin S))$
proof(*induct ns*)
case *Nil* **thus** *?case* **by** *simp*
next
case (*Cons* $nx \ ns''$)
note *IH* = $\langle \llbracket ns' \in \text{obs } ns'' \ S; \forall a \in \text{set } (tl \ ns''). \text{return-node } a \rrbracket$
 $\implies \exists nsx \ n \ nsx' \ n'. \ ns'' = nsx @ n \# nsx' \wedge ns' = n' \# nsx' \wedge$

$n' \in \text{obs-intra } n \ S \wedge$
 $(\forall nx \in \text{set } nsx'. \exists nx'. \text{call-of-return-node } nx \ nx' \wedge nx' \in S) \wedge$
 $(\forall xs \ x \ xs'. nsx = xs \ @ \ x \ \# \ xs' \wedge \text{obs-intra } x \ S \neq \{\}) \longrightarrow$
 $(\exists x'' \in \text{set } (xs' \ @ \ [n]). \exists nx. \text{call-of-return-node } x'' \ nx \wedge nx \notin S))$
from $\langle \forall a \in \text{set } (tl \ (nx \ \# \ ns'')). \text{return-node } a \rangle$ **have** $\forall n \in \text{set } ns''$. **return-node**
 n
by *simp*
show *?case*
proof(*cases ns''*)
case *Nil*
with $\langle ns' \in \text{obs } (nx \ \# \ ns'') \ S \rangle$ **obtain** x **where** $ns' = [x]$ **and** $x \in \text{obs-intra}$
 $nx \ S$
by(*auto simp:Let-def split:split-if-asm*)
with *Nil* **show** *?thesis* **by** *fastforce*
next
case *Cons*
with $\langle \forall n \in \text{set } ns''$. **return-node** $n \rangle$ **have** $\forall a \in \text{set } (tl \ ns'')$. **return-node** a
by *simp*
show *?thesis*
proof(*cases* $\exists n' \in \text{set } ns''$. $\exists nx'$. *call-of-return-node* $n' \ nx' \wedge nx' \notin S$)
case *True*
with $\langle ns' \in \text{obs } (nx \ \# \ ns'') \ S \rangle$ **have** $ns' \in \text{obs } ns'' \ S$ **by** *simp*
from *IH*[*OF this* $\langle \forall a \in \text{set } (tl \ ns'')$. **return-node** $a \rangle$]
obtain $nsx \ n \ nsx' \ n'$ **where** *split:ns''* $= nsx \ @ \ n \ \# \ nsx'$
 $ns' = n' \ \# \ nsx' \ n' \in \text{obs-intra } n \ S$
 $\forall nx \in \text{set } nsx'. \exists nx'. \text{call-of-return-node } nx \ nx' \wedge nx' \in S$
and *imp:* $\forall xs \ x \ xs'. nsx = xs \ @ \ x \ \# \ xs' \wedge \text{obs-intra } x \ S \neq \{\} \longrightarrow$
 $(\exists x'' \in \text{set } (xs' \ @ \ [n]). \exists nx. \text{call-of-return-node } x'' \ nx \wedge nx \notin S)$
by *blast*
from *True* $\langle ns'' = nsx \ @ \ n \ \# \ nsx' \rangle$
 $\langle \forall nx \in \text{set } nsx'. \exists nx'. \text{call-of-return-node } nx \ nx' \wedge nx' \in S \rangle$
have $(\exists nx'. \text{call-of-return-node } n \ nx' \wedge nx' \notin S) \vee$
 $(\exists n' \in \text{set } nsx. \exists nx'. \text{call-of-return-node } n' \ nx' \wedge nx' \notin S)$ **by** *fastforce*
thus *?thesis*
proof
assume $\exists nx'. \text{call-of-return-node } n \ nx' \wedge nx' \notin S$
with *split* **show** *?thesis* **by** *clarsimp*
next
assume $\exists n' \in \text{set } nsx. \exists nx'. \text{call-of-return-node } n' \ nx' \wedge nx' \notin S$
with *imp* **have** $\forall xs \ x \ xs'. nsx \ \# \ nsx = xs \ @ \ x \ \# \ xs' \wedge \text{obs-intra } x \ S \neq \{\}$
 \longrightarrow
 $(\exists x'' \in \text{set } (xs' \ @ \ [n]). \exists nx. \text{call-of-return-node } x'' \ nx \wedge nx \notin S)$
apply *clarsimp* **apply**(*case-tac xs*) **apply** *auto*
by(*erule-tac x=list in allE,auto*)
with *split Cons* **show** *?thesis* **by** *auto*
qed
next
case *False*
hence $\forall n' \in \text{set } ns''$. $\forall nx'$. *call-of-return-node* $n' \ nx' \longrightarrow nx' \in S$ **by** *simp*

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show ?thesis
proof(cases obs-intra nx S = {})
  case True
    with  $\langle ns' \in \text{obs } (nx \# ns'') S \rangle$  have  $ns' \in \text{obs } ns'' S$  by simp
    from IH[OF this  $\langle \forall a \in \text{set } (tl \ ns''). \text{return-node } a \rangle$ ]
    obtain nsx n nsx' n' where split:ns'' = nsx @ n # nsx'
      ns' = n' # nsx' n'  $\in \text{obs-intra } n S$ 
       $\forall nx \in \text{set } nsx'. \exists nx'. \text{call-of-return-node } nx \ nx' \wedge nx' \in S$ 
      and imp: $\forall xs \ x \ xs'. nsx = xs @ x \# xs' \wedge \text{obs-intra } x S \neq \{\}$   $\longrightarrow$ 
       $(\exists x'' \in \text{set } (xs' @ [n]). \exists nx. \text{call-of-return-node } x'' \ nx \wedge nx \notin S)$ 
      by blast
    from True imp Cons
    have  $\forall xs \ x \ xs'. nx \# nsx = xs @ x \# xs' \wedge \text{obs-intra } x S \neq \{\}$   $\longrightarrow$ 
       $(\exists x'' \in \text{set } (xs' @ [n]). \exists nx. \text{call-of-return-node } x'' \ nx \wedge nx \notin S)$ 
      by clarsimp(case-tac xs,clarsimp+,erule-tac x=list in allE,auto)
    with split Cons show ?thesis by auto
  next
    case False
    with  $\langle n' \in \text{set } ns''. \forall nx'. \text{call-of-return-node } n' \ nx' \longrightarrow nx' \in S \rangle$ 
       $\langle ns' \in \text{obs } (nx \# ns'') S \rangle$ 
    obtain nx'' where ns' = nx'' # ns'' and nx''  $\in \text{obs-intra } nx S$ 
    by(fastforce simp:Let-def split:split-if-asm)
    { fix n' assume n'  $\in \text{set } ns''$ 
      with  $\langle \forall n \in \text{set } ns''. \text{return-node } n \rangle$  have return-node n' by simp
      hence  $\exists ! n''. \text{call-of-return-node } n' \ n''$ 
      by(rule return-node-call-of-return-node)
      from  $\langle n' \in \text{set } ns'' \rangle$ 
       $\langle \forall n' \in \text{set } ns''. \forall nx'. \text{call-of-return-node } n' \ nx' \longrightarrow nx' \in S \rangle$ 
      have  $\forall nx'. \text{call-of-return-node } n' \ nx' \longrightarrow nx' \in S$  by simp
      with  $\langle \exists ! n''. \text{call-of-return-node } n' \ n'' \rangle$ 
      have  $\exists n''. \text{call-of-return-node } n' \ n'' \wedge n'' \in S$  by fastforce }
    with  $\langle ns' = nx'' \# ns'' \rangle$   $\langle nx'' \in \text{obs-intra } nx S \rangle$  show ?thesis by fastforce
  qed
qed
qed
qed
qed

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lemma obs-split-det:

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assumes  $xs @ x \# xs' = ys @ y \# ys'$ 
and obs-intra x S  $\neq \{\}$ 
and  $\forall x' \in \text{set } xs'. \exists x''. \text{call-of-return-node } x' \ x'' \wedge x'' \in S$ 
and  $\forall zs \ z \ zs'. xs = zs @ z \# zs' \wedge \text{obs-intra } z S \neq \{\}$ 
 $\longrightarrow (\exists z'' \in \text{set } (zs' @ [x]). \exists nx. \text{call-of-return-node } z'' \ nx \wedge nx \notin S)$ 
and obs-intra y S  $\neq \{\}$ 
and  $\forall y' \in \text{set } ys'. \exists y''. \text{call-of-return-node } y' \ y'' \wedge y'' \in S$ 
and  $\forall zs \ z \ zs'. ys = zs @ z \# zs' \wedge \text{obs-intra } z S \neq \{\}$ 

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$\longrightarrow (\exists z'' \in \text{set } (zs'@[y])). \exists ny. \text{call-of-return-node } z'' ny \wedge ny \notin S$
shows $xs = ys \wedge x = y \wedge xs' = ys'$
using *assms*
proof(*induct xs arbitrary:ys*)
case *Nil*
note $\text{imp}_y = \langle \forall zs z zs'. ys = zs@z\#zs' \wedge \text{obs-intra } z S \neq \{\} \rangle$
 $\longrightarrow (\exists z'' \in \text{set } (zs'@[y])). \exists ny. \text{call-of-return-node } z'' ny \wedge ny \notin S$
show *?case*
proof(*cases ys = []*)
case *True*
with $\langle []@x\#xs' = ys@y\#ys' \rangle$ **show** *?thesis by simp*
next
case *False*
with $\langle []@x\#xs' = ys@y\#ys' \rangle$
obtain zs **where** $x\#zs = ys$ **and** $xs' = zs@y\#ys'$ **by**(*auto simp:Cons-eq-append-conv*)
from $\langle x\#zs = ys \rangle \langle \text{obs-intra } x S \neq \{\} \rangle \text{imp}_y$
have $\exists z'' \in \text{set } (zs@[y]). \exists ny. \text{call-of-return-node } z'' ny \wedge ny \notin S$
by *blast*
with $\langle xs' = zs@y\#ys' \rangle \langle \forall x' \in \text{set } xs'. \exists x''. \text{call-of-return-node } x' x'' \wedge x'' \in S \rangle$
have *False by fastforce*
thus *?thesis by simp*
qed
next
case (*Cons w ws*)
note $IH = \langle \bigwedge ys. \llbracket ws @ x \# xs' = ys @ y \# ys'; \text{obs-intra } x S \neq \{\}; \forall x' \in \text{set } xs'. \exists x''. \text{call-of-return-node } x' x'' \wedge x'' \in S; \forall zs z zs'. ws = zs @ z \# zs' \wedge \text{obs-intra } z S \neq \{\} \longrightarrow (\exists z'' \in \text{set } (zs' @ [x]). \exists nx. \text{call-of-return-node } z'' nx \wedge nx \notin S); \text{obs-intra } y S \neq \{\}; \forall y' \in \text{set } ys'. \exists y''. \text{call-of-return-node } y' y'' \wedge y'' \in S; \forall zs z zs'. ys = zs @ z \# zs' \wedge \text{obs-intra } z S \neq \{\} \longrightarrow (\exists z'' \in \text{set } (zs' @ [y]). \exists ny. \text{call-of-return-node } z'' ny \wedge ny \notin S) \rrbracket \implies ws = ys \wedge x = y \wedge xs' = ys' \rangle$
note $\text{imp}_w = \langle \forall zs z zs'. w \# ws = zs @ z \# zs' \wedge \text{obs-intra } z S \neq \{\} \longrightarrow (\exists z'' \in \text{set } (zs' @ [x]). \exists nx. \text{call-of-return-node } z'' nx \wedge nx \notin S) \rangle$
note $\text{imp}_y = \langle \forall zs z zs'. ys = zs @ z \# zs' \wedge \text{obs-intra } z S \neq \{\} \longrightarrow (\exists z'' \in \text{set } (zs' @ [y]). \exists ny. \text{call-of-return-node } z'' ny \wedge ny \notin S) \rangle$
show *?case*
proof(*cases ys*)
case *Nil*
with $\langle (w\#ws) @ x \# xs' = ys @ y \# ys' \rangle$ **have** $y = w$ **and** $ys' = ws @ x \# xs'$
by *simp-all*
from $\langle y = w \rangle \langle \text{obs-intra } y S \neq \{\} \rangle \text{imp}_w$
have $\exists z'' \in \text{set } (ws @ [x]). \exists nx. \text{call-of-return-node } z'' nx \wedge nx \notin S$ **by** *blast*
with $\langle ys' = ws @ x \# xs' \rangle$
 $\langle \forall y' \in \text{set } ys'. \exists y''. \text{call-of-return-node } y' y'' \wedge y'' \in S \rangle$
have *False by fastforce*
thus *?thesis by simp*

```

next
  case (Cons w' ws')
  with ⟨(w # ws) @ x # xs' = ys @ y # ys'⟩ have w = w'
  and ws @ x # xs' = ws' @ y # ys' by simp-all
  from impw have imp1:∀zs z zs'. ws = zs @ z # zs' ∧ obs-intra z S ≠ {} →
    (∃z''∈set (zs' @ [x]). ∃nx. call-of-return-node z'' nx ∧ nx ∉ S)
  by clarsimp(erule-tac x=w#zs in allE,clarsimp)
  from Cons impy have imp2:∀zs z zs'. ws' = zs @ z # zs' ∧ obs-intra z S ≠
{} →
  (∃z''∈set (zs' @ [y]). ∃ny. call-of-return-node z'' ny ∧ ny ∉ S)
  by clarsimp(erule-tac x=w'#zs in allE,clarsimp)
  from IH[OF ⟨ws @ x # xs' = ws' @ y # ys'⟩ ⟨obs-intra x S ≠ {}⟩
  ⟨∀x'∈set xs'. ∃x''. call-of-return-node x' x'' ∧ x'' ∈ S⟩ imp1
  ⟨obs-intra y S ≠ {}⟩ ⟨∀y'∈set ys'. ∃y''. call-of-return-node y' y'' ∧ y'' ∈ S⟩
  imp2]
  have ws = ws' ∧ x = y ∧ xs' = ys' .
  with ⟨w = w'⟩ Cons show ?thesis by simp
qed
qed

```

lemma *in-obs-valid*:

```

assumes ns' ∈ obs ns S and ∀n ∈ set ns. valid-node n
shows ∀n ∈ set ns'. valid-node n
using ⟨ns' ∈ obs ns S⟩ ⟨∀n ∈ set ns. valid-node n⟩
by(induct ns)(auto intro:in-obs-intra-valid simp:Let-def split:split-if-asm)

```

end

end

1.7 Postdomination

theory *Postdomination* **imports** *CFGExit* **begin**

For static interprocedural slicing, we only consider standard control dependence, hence we only need standard postdomination.

```

locale Postdomination = CFGExit sourcenode targetnode kind valid-edge Entry
  get-proc get-return-edges procs Main Exit
  for sourcenode :: 'edge ⇒ 'node and targetnode :: 'edge ⇒ 'node
  and kind :: 'edge ⇒ ('var,'val,'ret,'pname) edge-kind
  and valid-edge :: 'edge ⇒ bool
  and Entry :: 'node (('Entry'-)) and get-proc :: 'node ⇒ 'pname
  and get-return-edges :: 'edge ⇒ 'edge set
  and procs :: ('pname × 'var list × 'var list) list and Main :: 'pname

```

and *Exit*::'node ('(-Exit'-)) +
assumes *Entry-path:valid-node* $n \implies \exists as. (-Entry-) -as \rightarrow_{\sqrt{*}} n$
and *Exit-path:valid-node* $n \implies \exists as. n -as \rightarrow_{\sqrt{*}} (-Exit-)$
and *method-exit-unique*:
 $\llbracket \text{method-exit } n; \text{method-exit } n'; \text{get-proc } n = \text{get-proc } n' \rrbracket \implies n = n'$

begin

lemma *get-return-edges-unique*:

assumes *valid-edge* a **and** $a' \in \text{get-return-edges } a$ **and** $a'' \in \text{get-return-edges } a$
shows $a' = a''$

proof –

from $\langle \text{valid-edge } a \rangle \langle a' \in \text{get-return-edges } a \rangle$
obtain $Q r p fs$ **where** $\text{kind } a = Q:r \leftrightarrow_p fs$
by (*fastforce dest!:only-call-get-return-edges*)
with $\langle \text{valid-edge } a \rangle \langle a' \in \text{get-return-edges } a \rangle$ **obtain** $Q' f'$ **where** $\text{kind } a' =$
 $Q' \leftrightarrow_p f'$
by (*fastforce dest!:call-return-edges*)
from $\langle \text{valid-edge } a \rangle \langle a' \in \text{get-return-edges } a \rangle$ **have** *valid-edge* a'
by (*rule get-return-edges-valid*)
from $\text{this } \langle \text{kind } a' = Q' \leftrightarrow_p f' \rangle$ **have** *get-proc* (*sourcenode* a') = p
by (*rule get-proc-return*)
from $\langle \text{valid-edge } a' \rangle \langle \text{kind } a' = Q' \leftrightarrow_p f' \rangle$ **have** *method-exit* (*sourcenode* a')
by (*fastforce simp:method-exit-def*)
from $\langle \text{valid-edge } a \rangle \langle a'' \in \text{get-return-edges } a \rangle \langle \text{kind } a = Q:r \leftrightarrow_p fs \rangle$
obtain $Q'' f''$ **where** $\text{kind } a'' = Q'' \leftrightarrow_p f''$ **by** (*fastforce dest!:call-return-edges*)
from $\langle \text{valid-edge } a \rangle \langle a'' \in \text{get-return-edges } a \rangle$ **have** *valid-edge* a''
by (*rule get-return-edges-valid*)
from $\text{this } \langle \text{kind } a'' = Q'' \leftrightarrow_p f'' \rangle$ **have** *get-proc* (*sourcenode* a'') = p
by (*rule get-proc-return*)
from $\langle \text{valid-edge } a'' \rangle \langle \text{kind } a'' = Q'' \leftrightarrow_p f'' \rangle$ **have** *method-exit* (*sourcenode* a'')
by (*fastforce simp:method-exit-def*)
with $\langle \text{method-exit } (\text{sourcenode } a') \rangle \langle \text{get-proc } (\text{sourcenode } a') = p \rangle$
 $\langle \text{get-proc } (\text{sourcenode } a'') = p \rangle$ **have** *sourcenode* $a' = \text{sourcenode } a''$
by (*fastforce elim!:method-exit-unique*)
from $\langle \text{valid-edge } a \rangle \langle a' \in \text{get-return-edges } a \rangle$
obtain ax' **where** *valid-edge* ax' **and** *sourcenode* $ax' = \text{sourcenode } a$
and *targetnode* $ax' = \text{targetnode } a'$ **and** *intra-kind*(*kind* ax')
by –(*drule call-return-node-edge,auto simp:intra-kind-def*)
from $\langle \text{valid-edge } a \rangle \langle a'' \in \text{get-return-edges } a \rangle$
obtain ax'' **where** *valid-edge* ax'' **and** *sourcenode* $ax'' = \text{sourcenode } a$
and *targetnode* $ax'' = \text{targetnode } a''$ **and** *intra-kind*(*kind* ax'')
by –(*drule call-return-node-edge,auto simp:intra-kind-def*)
from $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q:r \leftrightarrow_p fs \rangle \langle \text{valid-edge } ax' \rangle$
 $\langle \text{sourcenode } ax' = \text{sourcenode } a \rangle \langle \text{intra-kind}(\text{kind } ax') \rangle$
 $\langle \text{valid-edge } ax'' \rangle \langle \text{sourcenode } ax'' = \text{sourcenode } a \rangle \langle \text{intra-kind}(\text{kind } ax'') \rangle$
have $ax' = ax''$ **by** –(*drule call-only-one-intra-edge,auto*)
with $\langle \text{targetnode } ax' = \text{targetnode } a' \rangle \langle \text{targetnode } ax'' = \text{targetnode } a'' \rangle$
have *targetnode* $a' = \text{targetnode } a''$ **by** *simp*

with $\langle \text{valid-edge } a' \rangle \langle \text{valid-edge } a'' \rangle \langle \text{sourcenode } a' = \text{sourcenode } a'' \rangle$
show *?thesis* **by**(rule edge-det)
qed

definition *postdominate* :: 'node \Rightarrow 'node \Rightarrow bool (- postdominates - [51,0])
where *postdominate-def*: n' postdominates $n \equiv$
 $(\text{valid-node } n \wedge \text{valid-node } n' \wedge$
 $(\forall \text{ as } \text{pex. } (n - \text{as} \rightarrow_i^* \text{pex} \wedge \text{method-exit } \text{pex}) \longrightarrow n' \in \text{set } (\text{sourcenodes } \text{as})))$

lemma *postdominate-implies-inner-path*:

assumes n' postdominates n
obtains *as* **where** $n - \text{as} \rightarrow_i^* n'$ **and** $n' \notin \text{set } (\text{sourcenodes } \text{as})$
proof(*atomize-elim*)
from $\langle n'$ postdominates $n \rangle$ **have** *valid-node* n
and *all*: $\forall \text{ as } \text{pex. } (n - \text{as} \rightarrow_i^* \text{pex} \wedge \text{method-exit } \text{pex}) \longrightarrow n' \in \text{set } (\text{sourcenodes } \text{as})$
by(*auto simp:postdominate-def*)
from $\langle \text{valid-node } n \rangle$ **obtain** *asx* **where** $n - \text{asx} \rightarrow_{\sqrt{}}^* (-\text{Exit-})$ **by**(*auto dest:Exit-path*)
then obtain *as* **where** $n - \text{as} \rightarrow_{\sqrt{}}^* (-\text{Exit-})$
and $\forall a \in \text{set } \text{as. } \text{intra-kind}(\text{kind } a) \vee (\exists Q \text{ f } p. \text{kind } a = Q \leftrightarrow_{pf})$
by $-(\text{erule } \text{valid-Exit-path-descending-path})$
show $\exists \text{ as. } n - \text{as} \rightarrow_i^* n' \wedge n' \notin \text{set } (\text{sourcenodes } \text{as})$
proof(*cases* $\exists a \in \text{set } \text{as. } \exists Q \text{ f } p. \text{kind } a = Q \leftrightarrow_{pf}$)
case *True*
then obtain *asx ax asx'* **where** [*simp*]: $\text{as} = \text{asx} @ \text{ax} \# \text{asx}'$
and $\exists Q \text{ f } p. \text{kind } \text{ax} = Q \leftrightarrow_{pf}$ **and** $\forall a \in \text{set } \text{asx. } \forall Q \text{ f } p. \text{kind } a \neq Q \leftrightarrow_{pf}$
by $-(\text{erule } \text{split-list-first-propE}, \text{simp})$
with $\langle \forall a \in \text{set } \text{as. } \text{intra-kind}(\text{kind } a) \vee (\exists Q \text{ f } p. \text{kind } a = Q \leftrightarrow_{pf}) \rangle$
have $\forall a \in \text{set } \text{asx. } \text{intra-kind}(\text{kind } a)$ **by** *auto*
from $\langle n - \text{as} \rightarrow_{\sqrt{}}^* (-\text{Exit-}) \rangle$ **have** $n - \text{asx} \rightarrow_{\sqrt{}}^* \text{sourcenode } \text{ax}$
and *valid-edge* *ax* **by**(*auto dest:vp-split*)
from $\langle n - \text{asx} \rightarrow_{\sqrt{}}^* \text{sourcenode } \text{ax} \rangle \langle \forall a \in \text{set } \text{asx. } \text{intra-kind}(\text{kind } a) \rangle$
have $n - \text{asx} \rightarrow_i^* \text{sourcenode } \text{ax}$ **by**(*simp add:vp-def intra-path-def*)
from $\langle \text{valid-edge } \text{ax} \rangle \langle \exists Q \text{ f } p. \text{kind } \text{ax} = Q \leftrightarrow_{pf} \rangle$
have *method-exit* (*sourcenode* *ax*) **by**(*fastforce simp:method-exit-def*)
with $\langle n - \text{asx} \rightarrow_i^* \text{sourcenode } \text{ax} \rangle$ **all** **have** $n' \in \text{set } (\text{sourcenodes } \text{asx})$ **by**
fastforce
then obtain *xs ys* **where** *sourcenodes* *asx* = $\text{xs} @ n' \# \text{ys}$ **and** $n' \notin \text{set } \text{xs}$
by(*fastforce dest:split-list-first*)
then obtain *as' a as''* **where** $\text{xs} = \text{sourcenodes } \text{as}'$
and [*simp*]: $\text{asx} = \text{as}' @ a \# \text{as}''$ **and** *sourcenode* $a = n'$
by(*fastforce elim:map-append-append-maps simp:sourcenodes-def*)
from $\langle n - \text{asx} \rightarrow_i^* \text{sourcenode } \text{ax} \rangle$ **have** $n - \text{as}' \rightarrow_i^* \text{sourcenode } a$
by(*fastforce dest:path-split simp:intra-path-def*)
with $\langle \text{sourcenode } a = n' \rangle \langle n' \notin \text{set } \text{xs} \rangle \langle \text{xs} = \text{sourcenodes } \text{as}' \rangle$
show *?thesis* **by** *fastforce*
next

```

case False
with  $\langle \forall a \in \text{set } as. \text{intra-kind}(kind\ a) \vee (\exists Q\ f\ p. kind\ a = Q \leftrightarrow pf) \rangle$ 
have  $\forall a \in \text{set } as. \text{intra-kind}(kind\ a)$  by fastforce
with  $\langle n - as \rightarrow_{\sqrt{*}} (-Exit-) \rangle$  all have  $n' \in \text{set } (sourcenodes\ as)$ 
by (auto simp:vp-def intra-path-def simp:method-exit-def)
then obtain xs ys where  $sourcenodes\ as = xs @ n' \# ys$  and  $n' \notin \text{set } xs$ 
by (fastforce dest:split-list-first)
then obtain as' a as'' where  $xs = sourcenodes\ as'$ 
and  $[simp]: as = as' @ a \# as''$  and  $sourcenode\ a = n'$ 
by (fastforce elim:map-append-append-maps simp:sourcenodes-def)
from  $\langle n - as \rightarrow_{\sqrt{*}} (-Exit-) \rangle$   $\langle \forall a \in \text{set } as. \text{intra-kind}(kind\ a) \rangle$   $\langle as = as' @ a \# as'' \rangle$ 
have  $n - as' \rightarrow_{i^*} sourcenode\ a$ 
by (fastforce dest:path-split simp:vp-def intra-path-def)
with  $\langle sourcenode\ a = n' \rangle$   $\langle n' \notin \text{set } xs \rangle$   $\langle xs = sourcenodes\ as' \rangle$ 
show ?thesis by fastforce
qed
qed

```

```

lemma postdominate-variant:
assumes  $n'$  postdominates  $n$ 
shows  $\forall as. n - as \rightarrow_{\sqrt{*}} (-Exit-) \longrightarrow n' \in \text{set } (sourcenodes\ as)$ 
proof -
from  $\langle n'$  postdominates  $n \rangle$ 
have  $all: \forall as\ pex. (n - as \rightarrow_{i^*} pex \wedge \text{method-exit } pex) \longrightarrow n' \in \text{set } (sourcenodes\ as)$ 
by (simp add:postdominate-def)
{ fix as assume  $n - as \rightarrow_{\sqrt{*}} (-Exit-)$ 
then obtain as' pex where  $n - as' \rightarrow_{i^*} pex$  and method-exit pex
and  $\text{set}(sourcenodes\ as') \subseteq \text{set}(sourcenodes\ as)$ 
by (erule valid-Exit-path-intra-path)
from  $\langle n - as' \rightarrow_{i^*} pex \rangle$   $\langle \text{method-exit } pex \rangle$   $\langle n'$  postdominates  $n \rangle$ 
have  $n' \in \text{set } (sourcenodes\ as')$  by (fastforce simp:postdominate-def)
with  $\langle \text{set}(sourcenodes\ as') \subseteq \text{set}(sourcenodes\ as) \rangle$ 
have  $n' \in \text{set } (sourcenodes\ as)$  by fastforce }
thus ?thesis by simp
qed

```

```

lemma postdominate-refl:
assumes valid-node  $n$  and  $\neg \text{method-exit } n$  shows  $n$  postdominates  $n$ 
using  $\langle \text{valid-node } n \rangle$ 
proof (induct rule:valid-node-cases)
case Entry
{ fix as pex assume  $(-Entry-) - as \rightarrow_{i^*} pex$  and method-exit pex
from  $\langle \text{method-exit } pex \rangle$  have  $(-Entry-) \in \text{set } (sourcenodes\ as)$ 
proof (rule method-exit-cases)
assume  $pex = (-Exit-)$ 
with  $\langle (-Entry-) - as \rightarrow_{i^*} pex \rangle$  have  $as \neq []$ 

```



```

    apply (clarsimp simp:intra-path-def) apply (erule path.cases)
    by (drule sym,simp,drule Exit-noteq-Entry,auto)
  with ⟨(-Entry-) -as→i* pex⟩ have hd (sourcenodes as) = (-Entry-)
    by (fastforce intro:path-sourcenode simp:intra-path-def)
  with ⟨as ≠ []⟩ show ?thesis by (fastforce intro:hd-in-set simp:sourcenodes-def)
next
  fix a Q p f assume pex = sourcenode a and valid-edge a and kind a =
Q↔pf
  from ⟨(-Entry-) -as→i* pex⟩ have get-proc (-Entry-) = get-proc pex
    by (rule intra-path-get-procs)
  hence get-proc pex = Main by (simp add:get-proc-Entry)
  from ⟨valid-edge a⟩ ⟨kind a = Q↔pf⟩ have get-proc (sourcenode a) = p
    by (rule get-proc-return)
  with ⟨pex = sourcenode a⟩ ⟨get-proc pex = Main⟩ have p = Main by simp
  with ⟨valid-edge a⟩ ⟨kind a = Q↔pf⟩ have False
    by simp (rule Main-no-return-source)
  thus ?thesis by simp
qed }
with Entry show ?thesis
  by (fastforce intro:empty-path simp:postdominate-def intra-path-def)
next
case Exit
  with ⟨¬ method-exit n⟩ have False by (simp add:method-exit-def)
  thus ?thesis by simp
next
case inner
  show ?thesis
  proof (cases ∃ as. n -as→i* (-Exit-))
  case True
    { fix as pex assume n -as→i* pex and method-exit pex
      with ⟨¬ method-exit n⟩ have as ≠ []
        by (fastforce elim:path.cases simp:intra-path-def)
      with ⟨n -as→i* pex⟩ inner have hd (sourcenodes as) = n
        by (fastforce intro:path-sourcenode simp:intra-path-def)
      from ⟨as ≠ []⟩ have sourcenodes as ≠ [] by (simp add:sourcenodes-def)
      with ⟨hd (sourcenodes as) = n⟩ [THEN sym]
        have n ∈ set (sourcenodes as) by simp }
    hence ∀ as pex. (n -as→i* pex ∧ method-exit pex) → n ∈ set (sourcenodes
as)
      by fastforce
    with True inner show ?thesis
      by (fastforce intro:empty-path
        simp:postdominate-def inner-is-valid intra-path-def)
  next
  case False
    with inner show ?thesis by (fastforce dest:inner-is-valid Exit-path)
  qed
qed

```

lemma *postdominate-trans*:
 assumes n'' postdominates n and n' postdominates n''
 shows n' postdominates n
proof –
 from $\langle n''$ postdominates $n \rangle \langle n'$ postdominates $n'' \rangle$
 have *valid-node* n and *valid-node* n' by (*simp-all add:postdominate-def*)
 { **fix** as pex **assume** $n - as \rightarrow_{i^*} pex$ and *method-exit* pex
 with $\langle n''$ postdominates $n \rangle$ **have** $n'' \in \text{set}(\text{sourcenodes } as)$
 by (*fastforce simp:postdominate-def*)
 then **obtain** $ns' ns''$ **where** *sourcenodes* $as = ns' @ n'' \# ns''$
 by (*auto dest:split-list*)
 then **obtain** $as' as'' a$ **where** *sourcenodes* $as'' = ns''$ and [*simp*]: $as = as' @ a \# as''$
 and [*simp*]: *sourcenode* $a = n''$
 by (*fastforce elim:map-append-append-maps simp:sourcenodes-def*)
 from $\langle n - as \rightarrow_{i^*} pex \rangle$ **have** $n - as' @ a \# as'' \rightarrow_{i^*} pex$ by *simp*
 hence $n'' - a \# as'' \rightarrow_{i^*} pex$
 by (*fastforce dest:path-split-second simp:intra-path-def*)
 with $\langle n'$ postdominates $n'' \rangle \langle \text{method-exit } pex \rangle$
 have $n' \in \text{set}(\text{sourcenodes } (a \# as'))$ by (*fastforce simp:postdominate-def*)
 hence $n' \in \text{set}(\text{sourcenodes } as)$ by (*fastforce simp:sourcenodes-def*) }
 with $\langle \text{valid-node } n \rangle \langle \text{valid-node } n' \rangle$
 show ?*thesis* by (*fastforce simp:postdominate-def*)
qed

lemma *postdominate-antisym*:
 assumes n' postdominates n and n postdominates n'
 shows $n = n'$
proof –
 from $\langle n'$ postdominates $n \rangle$ **have** *valid-node* n and *valid-node* n'
 by (*auto simp:postdominate-def*)
 from $\langle \text{valid-node } n \rangle$ **obtain** asx **where** $n - asx \rightarrow_{\sqrt{*}} (-Exit)$ by (*auto dest:Exit-path*)
 then **obtain** $as' pex$ **where** $n - as' \rightarrow_{i^*} pex$ and *method-exit* pex
 by (*erule valid-Exit-path-intra-path*)
 with $\langle n'$ postdominates $n \rangle$ **have** $\exists nx \in \text{set}(\text{sourcenodes } as'). nx = n'$
 by (*fastforce simp:postdominate-def*)
 then **obtain** $ns ns'$ **where** *sourcenodes* $as' = ns @ n' \# ns'$
 and $\forall nx \in \text{set } ns'. nx \neq n'$
 by (*fastforce elim!:split-list-last-propE*)
 from $\langle \text{sourcenodes } as' = ns @ n' \# ns' \rangle$ **obtain** $asx a asx'$
 where [*simp*]: $ns' = \text{sourcenodes } asx'$ $as' = asx @ a \# asx'$ *sourcenode* $a = n'$
 by (*fastforce elim:map-append-append-maps simp:sourcenodes-def*)
 from $\langle n - as' \rightarrow_{i^*} pex \rangle$ **have** $n' - a \# asx' \rightarrow_{i^*} pex$
 by (*fastforce dest:path-split-second simp:intra-path-def*)
 with $\langle n$ postdominates $n' \rangle \langle \text{method-exit } pex \rangle$ **have** $n \in \text{set}(\text{sourcenodes } (a \# asx'))$
 by (*fastforce simp:postdominate-def*)

hence $n = n' \vee n \in \text{set}(\text{sourcenodes } asx')$ **by** (*simp add:sourcenodes-def*)
thus *?thesis*
proof
 assume $n = n'$ **thus** *?thesis* .
next
 assume $n \in \text{set}(\text{sourcenodes } asx')$
 then obtain $nsx' nsx''$ **where** $\text{sourcenodes } asx' = nsx'@n\#nsx''$
 by (*auto dest:split-list*)
 then obtain $asi\ asi'\ a'$ **where** [*simp*]: $asx' = asi@a'\#asi'$ **sourcenode** $a' = n$
 by (*fastforce elim:map-append-append-maps simp:sourcenodes-def*)
 with $\langle n - as' \rightarrow_i * pex \rangle$ **have** $n - (asx@a\#asi)@a'\#asi' \rightarrow_i * pex$ **by** *simp*
 hence $n - (asx@a\#asi)@a'\#asi' \rightarrow * pex$
 and $\forall a \in \text{set}((asx@a\#asi)@a'\#asi')$. *intra-kind (kind a)*
 by (*simp-all add:intra-path-def*)
 from $\langle n - (asx@a\#asi)@a'\#asi' \rightarrow * pex \rangle$
 have $n - a'\#asi' \rightarrow * pex$ **by** (*fastforce dest:path-split-second*)
 with $\langle \forall a \in \text{set}((asx@a\#asi)@a'\#asi')$. *intra-kind (kind a)* \rangle
 have $n - a'\#asi' \rightarrow_i * pex$ **by** (*simp add:intra-path-def*)
 with $\langle n' \text{ postdominates } n \rangle$ *method-exit pex*
 have $n' \in \text{set}(\text{sourcenodes } (a'\#asi'))$ **by** (*fastforce simp:postdominate-def*)
 hence $n' = n \vee n' \in \text{set}(\text{sourcenodes } asi')$
 by (*simp add:sourcenodes-def*)
 thus *?thesis*
proof
 assume $n' = n$ **thus** *?thesis* **by** (*rule sym*)
next
 assume $n' \in \text{set}(\text{sourcenodes } asi')$
 with $\langle \forall nx \in \text{set } ns'. nx \neq n' \rangle$ **have** *False* **by** (*fastforce simp:sourcenodes-def*)
 thus *?thesis* **by** *simp*
qed
qed
qed

lemma *postdominate-path-branch*:

assumes $n - as \rightarrow * n''$ **and** $n' \text{ postdominates } n''$ **and** $\neg n' \text{ postdominates } n$
obtains $a\ as'\ as''$ **where** $as = as'@a\#as''$ **and** *valid-edge a*
and $\neg n' \text{ postdominates } (\text{sourcenode } a)$ **and** $n' \text{ postdominates } (\text{targetnode } a)$
proof (*atomize-elim*)
 from *assms*
 show $\exists as' a as''. as = as'@a\#as'' \wedge \text{valid-edge } a \wedge$
 $\neg n' \text{ postdominates } (\text{sourcenode } a) \wedge n' \text{ postdominates } (\text{targetnode } a)$
 proof (*induct rule:path.induct*)
 case (*Cons-path n'' as nx a n*)
 note $IH = \langle [n' \text{ postdominates } nx; \neg n' \text{ postdominates } n'] \rangle$
 $\implies \exists as' a as''. as = as'@a\#as'' \wedge \text{valid-edge } a \wedge$
 $\neg n' \text{ postdominates } \text{sourcenode } a \wedge n' \text{ postdominates } \text{targetnode } a \rangle$
 show *?case*
 proof (*cases n' postdominates n''*)

```

    case True
    with  $\langle \neg n' \text{ postdominates } n \rangle \langle \text{sourcenode } a = n \rangle \langle \text{targetnode } a = n'' \rangle$ 
       $\langle \text{valid-edge } a \rangle$  show ?thesis by blast
  next
  case False
  from IH[OF  $\langle n' \text{ postdominates } nx \rangle$  this] show ?thesis
    by clarsimp(rule-tac  $x=a\#as'$  in exI,clarsimp)
  qed
qed simp
qed

```

lemma Exit-no-postdominator:

```

  assumes  $\langle \text{-Exit-} \rangle \text{ postdominates } n$  shows False
proof -
  from  $\langle \text{-Exit-} \rangle \text{ postdominates } n$  have valid-node n by (simp add:postdominate-def)
  from  $\langle \text{valid-node } n \rangle$  obtain asx where  $n - asx \rightarrow_{\sqrt{*}} \langle \text{-Exit-} \rangle$  by (auto dest:Exit-path)
  then obtain as' pex where  $n - as' \rightarrow_{i*} pex$  and method-exit pex
    by  $\langle \text{erule valid-Exit-path-intra-path} \rangle$ 
  with  $\langle \text{-Exit-} \rangle \text{ postdominates } n$  have  $\langle \text{-Exit-} \rangle \in \text{set } (\text{sourcenodes } as')$ 
    by (fastforce simp:postdominate-def)
  with  $\langle n - as' \rightarrow_{i*} pex \rangle$  show False by (fastforce simp:intra-path-def)
qed

```

lemma postdominate-inner-path-targetnode:

```

  assumes  $n' \text{ postdominates } n$  and  $n - as \rightarrow_{i*} n''$  and  $n' \notin \text{set } (\text{sourcenodes } as)$ 
  shows  $n' \text{ postdominates } n''$ 
proof -
  from  $\langle n' \text{ postdominates } n \rangle$  obtain asx
    where valid-node n and valid-node n'
    and all: $\forall as \text{ pex. } (n - as \rightarrow_{i*} pex \wedge \text{method-exit } pex) \longrightarrow n' \in \text{set } (\text{sourcenodes } as)$ 
  by (auto simp:postdominate-def)
  from  $\langle n - as \rightarrow_{i*} n'' \rangle$  have valid-node n''
    by (fastforce dest:path-valid-node simp:intra-path-def)
  have  $\forall as' \text{ pex'. } (n'' - as' \rightarrow_{i*} pex' \wedge \text{method-exit } pex') \longrightarrow$ 
     $n' \in \text{set } (\text{sourcenodes } as')$ 
  proof (rule ccontr)
    assume  $\neg (\forall as' \text{ pex'. } (n'' - as' \rightarrow_{i*} pex' \wedge \text{method-exit } pex') \longrightarrow$ 
       $n' \in \text{set } (\text{sourcenodes } as'))$ 
    then obtain as' pex' where  $n'' - as' \rightarrow_{i*} pex'$  and method-exit pex'
      and  $n' \notin \text{set } (\text{sourcenodes } as')$  by blast
    from  $\langle n - as \rightarrow_{i*} n'' \rangle \langle n'' - as' \rightarrow_{i*} pex' \rangle$  have  $n - as @ as' \rightarrow_{i*} pex'$ 
      by (fastforce intro:path-Append simp:intra-path-def)
    from  $\langle n' \notin \text{set } (\text{sourcenodes } as) \rangle \langle n' \notin \text{set } (\text{sourcenodes } as') \rangle$ 
      have  $n' \notin \text{set } (\text{sourcenodes } (as @ as'))$ 
      by (simp add:sourcenodes-def)
    with  $\langle n - as @ as' \rightarrow_{i*} pex' \rangle \langle \text{method-exit } pex' \rangle \langle n' \text{ postdominates } n \rangle$ 

```

```

  show False by(fastforce simp:postdominate-def)
qed
with ⟨valid-node n'⟩ ⟨valid-node n'⟩
show ?thesis by(auto simp:postdominate-def)
qed

```

lemma *not-postdominate-source-not-postdominate-target*:

```

  assumes ¬ n postdominates (sourcenode a)
  and valid-node n and valid-edge a and intra-kind (kind a)
  obtains ax where sourcenode a = sourcenode ax and valid-edge ax
  and ¬ n postdominates targetnode ax
proof(atomize-elim)
  show ∃ ax. sourcenode a = sourcenode ax ∧ valid-edge ax ∧
    ¬ n postdominates targetnode ax
  proof –
  from assms obtain asx pex
    where sourcenode a – asx →l* pex and method-exit pex
    and n ∉ set(sourcenodes asx) by(fastforce simp:postdominate-def)
  show ?thesis
  proof(cases asx)
  case Nil
  with ⟨sourcenode a – asx →l* pex⟩ have pex = sourcenode a
    by(fastforce simp:intra-path-def)
  with ⟨method-exit pex⟩ have method-exit (sourcenode a) by simp
  thus ?thesis
  proof(rule method-exit-cases)
  assume sourcenode a = (-Exit-)
  with ⟨valid-edge a⟩ have False by(rule Exit-source)
  thus ?thesis by simp
  next
  fix a' Q f p assume sourcenode a = sourcenode a'
    and valid-edge a' and kind a' = Q ↔ pf
  hence False using ⟨intra-kind (kind a)⟩ ⟨valid-edge a⟩
    by(fastforce dest:return-edges-only simp:intra-kind-def)
  thus ?thesis by simp
  qed
  next
  case (Cons ax asx')
  with ⟨sourcenode a – asx →l* pex⟩
  have sourcenode a – []@ax#asx' →* pex
  and ∀ a ∈ set (ax#asx'). intra-kind (kind a) by(simp-all add:intra-path-def)
  from ⟨sourcenode a – []@ax#asx' →* pex⟩
  have sourcenode a = sourcenode ax and valid-edge ax
    and targetnode ax – asx' →* pex by(fastforce dest:path-split)+
  with ⟨∀ a ∈ set (ax#asx'). intra-kind (kind a)⟩
  have targetnode ax – asx' →l* pex by(simp add:intra-path-def)
  with ⟨n ∉ set(sourcenodes asx)⟩ Cons ⟨method-exit pex⟩
  have ¬ n postdominates targetnode ax

```

```

    by(fastforce simp:postdominate-def sourcenodes-def)
  with ⟨sourcenode a = sourcenode ax⟩ ⟨valid-edge ax⟩ show ?thesis by blast
qed
qed
qed

```

lemma *inner-node-Exit-edge*:

assumes *inner-node n*

obtains *a* **where** *valid-edge a* **and** *intra-kind (kind a)*

and *inner-node (sourcenode a)* **and** *targetnode a = (-Exit-)*

proof(*atomize-elim*)

from ⟨*inner-node n*⟩ **have** *valid-node n* **by**(*rule inner-is-valid*)

then obtain *as* **where** $n -as \rightarrow_{\sqrt{*}} (-Exit-)$ **by**(*fastforce dest:Exit-path*)

show $\exists a. \text{valid-edge } a \wedge \text{intra-kind } (kind\ a) \wedge \text{inner-node } (sourcenode\ a) \wedge$
targetnode a = (-Exit-)

proof(*cases as = []*)

case *True*

with ⟨*inner-node n*⟩ ⟨ $n -as \rightarrow_{\sqrt{*}} (-Exit-)$ ⟩ **have** *False* **by**(*fastforce simp:vp-def*)

thus ?thesis **by** *simp*

next

case *False*

with ⟨ $n -as \rightarrow_{\sqrt{*}} (-Exit-)$ ⟩ **obtain** *a'* *as'* **where** $as = as'@[a']$

and $n -as' \rightarrow_{\sqrt{*}} \text{sourcenode } a'$ **and** *valid-edge a'*

and $(-Exit-) = \text{targetnode } a'$ **by** $-(\text{erule } vp\text{-split}\text{-snoc})$

from ⟨*valid-edge a'*⟩ **have** *valid-node (sourcenode a')* **by** *simp*

thus ?thesis

proof(*cases sourcenode a' rule:valid-node-cases*)

case *Entry*

with ⟨ $n -as' \rightarrow_{\sqrt{*}} \text{sourcenode } a'$ ⟩ **have** $n -as' \rightarrow_* (-Entry-)$ **by**(*simp add:vp-def*)

with ⟨*inner-node n*⟩

have *False* **by** $-(\text{drule } path\text{-Entry}\text{-target}, \text{auto } \text{simp:inner-node-def})$

thus ?thesis **by** *simp*

next

case *Exit*

from ⟨*valid-edge a'*⟩ **this** **have** *False* **by**(*rule Exit-source*)

thus ?thesis **by** *simp*

next

case *inner*

have *intra-kind (kind a')*

proof(*cases kind a' rule:edge-kind-cases*)

case *Intra* **thus** ?thesis **by** *simp*

next

case (*Call Q r p fs*)

with ⟨*valid-edge a'*⟩ **have** $\text{get-proc}(\text{targetnode } a') = p$ **by**(*rule get-proc-call*)

with $(-Exit-) = \text{targetnode } a'$ get-proc-Exit **have** $p = \text{Main}$ **by** *simp*

with $(\text{kind } a' = Q:r \hookrightarrow_p fs)$ **have** $\text{kind } a' = Q:r \hookrightarrow_{\text{Main}} fs$ **by** *simp*

with ⟨*valid-edge a'*⟩ **have** *False* **by**(*rule Main-no-call-target*)

thus ?thesis **by** *simp*

```

next
  case (Return Q p f)
    from ⟨valid-edge a'⟩ ⟨kind a' = Q↔pf⟩ ⟨(-Exit-) = targetnode a'⟩ [THEN
sym]
    have False by (rule Exit-no-return-target)
    thus ?thesis by simp
  qed
  with ⟨valid-edge a'⟩ ⟨(-Exit-) = targetnode a'⟩ ⟨inner-node (sourcenode a')⟩
  show ?thesis by simp blast
  qed
  qed
  qed

```

lemma *inner-node-Entry-edge*:

```

assumes inner-node n
obtains a where valid-edge a and intra-kind (kind a)
and inner-node (targetnode a) and sourcenode a = (-Entry-)
proof (atomize-elim)
  from ⟨inner-node n⟩ have valid-node n by (rule inner-is-valid)
  then obtain as where (-Entry-) -as→√* n by (fastforce dest:Entry-path)
  show ∃ a. valid-edge a ∧ intra-kind (kind a) ∧ inner-node (targetnode a) ∧
    sourcenode a = (-Entry-)
  proof (cases as = [])
    case True
    with ⟨inner-node n⟩ ⟨(-Entry-) -as→√* n⟩ have False
    by (fastforce simp:inner-node-def vp-def)
    thus ?thesis by simp
  next
  case False
  with ⟨(-Entry-) -as→√* n⟩ obtain a' as' where as = a'#as'
    and targetnode a' -as'→√* n and valid-edge a'
    and (-Entry-) = sourcenode a' by -(erule vp-split-Cons)
  from ⟨valid-edge a'⟩ have valid-node (targetnode a') by simp
  thus ?thesis
  proof (cases targetnode a' rule:valid-node-cases)
    case Entry
    from ⟨valid-edge a'⟩ this have False by (rule Entry-target)
    thus ?thesis by simp
  next
  case Exit
  with ⟨targetnode a' -as'→√* n⟩ have (-Exit-) -as'→* n by (simp add:vp-def)
  with ⟨inner-node n⟩
  have False by -(drule path-Exit-source,auto simp:inner-node-def)
  thus ?thesis by simp
  next
  case inner
  have intra-kind (kind a')
  proof (cases kind a' rule:edge-kind-cases)

```

```

    case Intra thus ?thesis by simp
next
case (Call Q r p fs)
from ⟨valid-edge a' ⟨kind a' = Q:r↔pfs⟩
  ⟨(-Entry-) = sourcenode a'[THEN sym]⟩
have False by(rule Entry-no-call-source)
thus ?thesis by simp
next
case (Return Q p f)
with ⟨valid-edge a'⟩ have get-proc(sourcenode a') = p
  by(rule get-proc-return)
with ⟨(-Entry-) = sourcenode a' get-proc-Entry⟩ have p = Main by simp
with ⟨kind a' = Q↔pf⟩ have kind a' = Q↔Mainf by simp
with ⟨valid-edge a'⟩ have False by(rule Main-no-return-source)
thus ?thesis by simp
qed
with ⟨valid-edge a'⟩ ⟨(-Entry-) = sourcenode a'⟩ ⟨inner-node (targetnode a')⟩
show ?thesis by simp blast
qed
qed
qed

```

lemma *intra-path-to-matching-method-exit*:

```

  assumes method-exit n' and get-proc n = get-proc n' and valid-node n
  obtains as where n -as→ι* n'
proof(atomize-elim)
  from ⟨valid-node n⟩ obtain as' where n -as'→√* (-Exit-)
  by(fastforce dest:Exit-path)
  then obtain as mex where n -as→ι* mex and method-exit mex
  by(fastforce elim:valid-Exit-path-intra-path)
  from ⟨n -as→ι* mex⟩ have get-proc n = get-proc mex
  by(rule intra-path-get-procs)
  with ⟨method-exit n'⟩ ⟨get-proc n = get-proc n'⟩ ⟨method-exit mex⟩
  have mex = n' by(fastforce intro:method-exit-unique)
  with ⟨n -as→ι* mex⟩ show ∃ as. n -as→ι* n' by fastforce
qed

```

end

end

1.8 SDG

theory *SDG* imports *CFGExit-wf* *Postdomination* begin

1.8.1 The nodes of the SDG

```

datatype 'node SDG-node =
  CFG-node 'node
  | Formal-in 'node × nat
  | Formal-out 'node × nat
  | Actual-in 'node × nat
  | Actual-out 'node × nat

fun parent-node :: 'node SDG-node ⇒ 'node
  where parent-node (CFG-node n) = n
  | parent-node (Formal-in (m,x)) = m
  | parent-node (Formal-out (m,x)) = m
  | parent-node (Actual-in (m,x)) = m
  | parent-node (Actual-out (m,x)) = m

locale SDG = CFGExit-wf sourcenode targetnode kind valid-edge Entry
  get-proc get-return-edges procs Main Exit Def Use ParamDefs ParamUses +
  Postdomination sourcenode targetnode kind valid-edge Entry
  get-proc get-return-edges procs Main Exit
  for sourcenode :: 'edge ⇒ 'node and targetnode :: 'edge ⇒ 'node
  and kind :: 'edge ⇒ ('var,'val,'ret,'pname) edge-kind
  and valid-edge :: 'edge ⇒ bool
  and Entry :: 'node (('(-Entry'-)) and get-proc :: 'node ⇒ 'pname
  and get-return-edges :: 'edge ⇒ 'edge set
  and procs :: ('pname × 'var list × 'var list) list and Main :: 'pname
  and Exit::'node (('(-Exit'-))
  and Def :: 'node ⇒ 'var set and Use :: 'node ⇒ 'var set
  and ParamDefs :: 'node ⇒ 'var list and ParamUses :: 'node ⇒ 'var set list

begin

fun valid-SDG-node :: 'node SDG-node ⇒ bool
  where valid-SDG-node (CFG-node n) ↔ valid-node n
  | valid-SDG-node (Formal-in (m,x)) ↔
    (∃ a Q r p fs ins outs. valid-edge a ∧ (kind a = Q:r↔pfs) ∧ targetnode a = m
  ∧
    (p,ins,outs) ∈ set procs ∧ x < length ins)
  | valid-SDG-node (Formal-out (m,x)) ↔
    (∃ a Q p f ins outs. valid-edge a ∧ (kind a = Q↔pf) ∧ sourcenode a = m ∧
    (p,ins,outs) ∈ set procs ∧ x < length outs)
  | valid-SDG-node (Actual-in (m,x)) ↔
    (∃ a Q r p fs ins outs. valid-edge a ∧ (kind a = Q:r↔pfs) ∧ sourcenode a = m
  ∧
    (p,ins,outs) ∈ set procs ∧ x < length ins)
  | valid-SDG-node (Actual-out (m,x)) ↔
    (∃ a Q p f ins outs. valid-edge a ∧ (kind a = Q↔pf) ∧ targetnode a = m ∧
    (p,ins,outs) ∈ set procs ∧ x < length outs)

```

lemma *valid-SDG-CFG-node*:
valid-SDG-node $n \implies$ *valid-node* (*parent-node* n)
by(*cases* n) *auto*

lemma *Formal-in-parent-det*:
assumes *valid-SDG-node* (*Formal-in* (m,x)) **and** *valid-SDG-node* (*Formal-in* (m',x'))
and *get-proc* $m =$ *get-proc* m'
shows $m = m'$
proof –
from \langle *valid-SDG-node* (*Formal-in* (m,x)) \rangle **obtain** $a\ Q\ r\ p\ fs\ ins\ outs$
where *valid-edge* a **and** *kind* $a = Q:r \hookrightarrow_p fs$ **and** *targetnode* $a = m$
and $(p,ins,outs) \in$ *set procs* **and** $x <$ *length* ins **by** *fastforce*
from \langle *valid-SDG-node* (*Formal-in* (m',x')) \rangle **obtain** $a'\ Q'\ r'\ p'\ f'\ ins'\ outs'$
where *valid-edge* a' **and** *kind* $a' = Q':r' \hookrightarrow_{p'} f'$ **and** *targetnode* $a' = m'$
and $(p',ins',outs') \in$ *set procs* **and** $x' <$ *length* ins' **by** *fastforce*
from \langle *valid-edge* a \rangle \langle *kind* $a = Q:r \hookrightarrow_p fs$ \rangle \langle *targetnode* $a = m$ \rangle
have *get-proc* $m = p$ **by**(*fastforce* *intro:get-proc-call*)
moreover
from \langle *valid-edge* a' \rangle \langle *kind* $a' = Q':r' \hookrightarrow_{p'} f'$ \rangle \langle *targetnode* $a' = m'$ \rangle
have *get-proc* $m' = p'$ **by**(*fastforce* *intro:get-proc-call*)
ultimately have $p = p'$ **using** \langle *get-proc* $m =$ *get-proc* m' \rangle **by** *simp*
with \langle *valid-edge* a \rangle \langle *kind* $a = Q:r \hookrightarrow_p fs$ \rangle \langle *valid-edge* a' \rangle \langle *kind* $a' = Q':r' \hookrightarrow_{p'} f'$ \rangle
 \langle *targetnode* $a = m$ \rangle \langle *targetnode* $a' = m'$ \rangle
show *?thesis* **by**(*fastforce* *intro:same-proc-call-unique-target*)
qed

lemma *valid-SDG-node-parent-Entry*:
assumes *valid-SDG-node* n **and** *parent-node* $n = (-Entry-)$
shows $n =$ *CFG-node* ($-Entry-$)
proof(*cases* n)
case *CFG-node* **with** \langle *parent-node* $n = (-Entry-)$ \rangle **show** *?thesis* **by** *simp*
next
case (*Formal-in* z)
with \langle *parent-node* $n = (-Entry-)$ \rangle **obtain** x
where $[simp]:z = ((-Entry-),x)$ **by**(*cases* z) *auto*
with \langle *valid-SDG-node* n \rangle *Formal-in* **obtain** a **where** *valid-edge* a
and *targetnode* $a = (-Entry-)$ **by** *auto*
hence *False* **by** \neg (*rule* *Entry-target,simp+*)
thus *?thesis* **by** *simp*
next
case (*Formal-out* z)
with \langle *parent-node* $n = (-Entry-)$ \rangle **obtain** x
where $[simp]:z = ((-Entry-),x)$ **by**(*cases* z) *auto*
with \langle *valid-SDG-node* n \rangle *Formal-out* **obtain** $a\ Q\ p\ f$ **where** *valid-edge* a

```

    and kind a =  $Q \leftrightarrow pf$  and sourcenode a = (-Entry-) by auto
  from ⟨valid-edge a⟩ ⟨kind a =  $Q \leftrightarrow pf$ ⟩ have get-proc (sourcenode a) = p
    by(rule get-proc-return)
  with ⟨sourcenode a = (-Entry-)⟩ have p = Main
    by(auto simp:get-proc-Entry)
  with ⟨valid-edge a⟩ ⟨kind a =  $Q \leftrightarrow pf$ ⟩ have False
    by(fastforce intro:Main-no-return-source)
  thus ?thesis by simp
next
case (Actual-in z)
with ⟨parent-node n = (-Entry-)⟩ obtain x
  where [simp]:z = ((-Entry-),x) by(cases z) auto
with ⟨valid-SDG-node n⟩ Actual-in obtain a Q r p fs where valid-edge a
  and kind a =  $Q:r \hookrightarrow pfs$  and sourcenode a = (-Entry-) by fastforce
hence False by -(rule Entry-no-call-source,auto)
thus ?thesis by simp
next
case (Actual-out z)
with ⟨parent-node n = (-Entry-)⟩ obtain x
  where [simp]:z = ((-Entry-),x) by(cases z) auto
with ⟨valid-SDG-node n⟩ Actual-out obtain a where valid-edge a
  targetnode a = (-Entry-) by auto
hence False by -(rule Entry-target,simp+)
thus ?thesis by simp
qed

```

lemma *valid-SDG-node-parent-Exit*:

```

  assumes valid-SDG-node n and parent-node n = (-Exit-)
  shows n = CFG-node (-Exit-)
proof(cases n)
  case CFG-node with ⟨parent-node n = (-Exit-)⟩ show ?thesis by simp
next
case (Formal-in z)
with ⟨parent-node n = (-Exit-)⟩ obtain x
  where [simp]:z = ((-Exit-),x) by(cases z) auto
with ⟨valid-SDG-node n⟩ Formal-in obtain a Q r p fs where valid-edge a
  and kind a =  $Q:r \hookrightarrow pfs$  and targetnode a = (-Exit-) by fastforce
from ⟨valid-edge a⟩ ⟨kind a =  $Q:r \hookrightarrow pfs$ ⟩ have get-proc (targetnode a) = p
  by(rule get-proc-call)
with ⟨targetnode a = (-Exit-)⟩ have p = Main
  by(auto simp:get-proc-Exit)
with ⟨valid-edge a⟩ ⟨kind a =  $Q:r \hookrightarrow pfs$ ⟩ have False
  by(fastforce intro:Main-no-call-target)
thus ?thesis by simp
next
case (Formal-out z)
with ⟨parent-node n = (-Exit-)⟩ obtain x
  where [simp]:z = ((-Exit-),x) by(cases z) auto

```

with $\langle \text{valid-SDG-node } n \rangle$ *Formal-out* **obtain** a **where** *valid-edge* a
and *sourcenode* $a = (-\text{Exit-})$ **by** *auto*
hence *False* **by** $-(\text{rule } \text{Exit-source}, \text{simp}+)$
thus *?thesis* **by** *simp*
next
case $\langle \text{Actual-in } z \rangle$
with $\langle \text{parent-node } n = (-\text{Exit-}) \rangle$ **obtain** x
where $[\text{simp}] : z = ((-\text{Exit-}), x)$ **by** $(\text{cases } z)$ *auto*
with $\langle \text{valid-SDG-node } n \rangle$ *Actual-in* **obtain** a **where** *valid-edge* a
and *sourcenode* $a = (-\text{Exit-})$ **by** *auto*
hence *False* **by** $-(\text{rule } \text{Exit-source}, \text{simp}+)$
thus *?thesis* **by** *simp*
next
case $\langle \text{Actual-out } z \rangle$
with $\langle \text{parent-node } n = (-\text{Exit-}) \rangle$ **obtain** x
where $[\text{simp}] : z = ((-\text{Exit-}), x)$ **by** $(\text{cases } z)$ *auto*
with $\langle \text{valid-SDG-node } n \rangle$ *Actual-out* **obtain** a Q p f **where** *valid-edge* a
and *kind* $a = Q \leftrightarrow p f$ **and** *targetnode* $a = (-\text{Exit-})$ **by** *auto*
hence *False* **by** $-(\text{rule } \text{Exit-no-return-target}, \text{auto})$
thus *?thesis* **by** *simp*
qed

1.8.2 Data dependence

inductive *SDG-Use* $:: 'var \Rightarrow 'node \text{SDG-node} \Rightarrow \text{bool } (- \in \text{Use}_{\text{SDG}} -)$
where *CFG-Use-SDG-Use*:
 $\llbracket \text{valid-node } m; V \in \text{Use } m; n = \text{CFG-node } m \rrbracket \Longrightarrow V \in \text{Use}_{\text{SDG}} n$
| *Actual-in-SDG-Use*:
 $\llbracket \text{valid-SDG-node } n; n = \text{Actual-in } (m, x); V \in (\text{ParamUses } m)!x \rrbracket \Longrightarrow V \in \text{Use}_{\text{SDG}} n$
| *Formal-out-SDG-Use*:
 $\llbracket \text{valid-SDG-node } n; n = \text{Formal-out } (m, x); \text{get-proc } m = p; (p, \text{ins}, \text{outs}) \in \text{set } \text{procs}; V = \text{outs}!x \rrbracket \Longrightarrow V \in \text{Use}_{\text{SDG}} n$

abbreviation *notin-SDG-Use* $:: 'var \Rightarrow 'node \text{SDG-node} \Rightarrow \text{bool } (- \notin \text{Use}_{\text{SDG}} -)$
where $V \notin \text{Use}_{\text{SDG}} n \equiv \neg V \in \text{Use}_{\text{SDG}} n$

lemma *in-Use-valid-SDG-node*:
 $V \in \text{Use}_{\text{SDG}} n \Longrightarrow \text{valid-SDG-node } n$
by $(\text{induct rule:SDG-Use.induct}, \text{auto intro:valid-SDG-CFG-node})$

lemma *SDG-Use-parent-Use*:
 $V \in \text{Use}_{\text{SDG}} n \Longrightarrow V \in \text{Use } (\text{parent-node } n)$
proof $(\text{induct rule:SDG-Use.induct})$
case *CFG-Use-SDG-Use* **thus** *?case* **by** *simp*

next
case (*Actual-in-SDG-Use* $n\ m\ x\ V$)
from $\langle \text{valid-SDG-node } n \rangle \langle n = \text{Actual-in } (m, x) \rangle$ **obtain** $a\ Q\ r\ p\ fs\ ins\ outs$
where *valid-edge* a **and** *kind* $a = Q:r \hookrightarrow_p fs$ **and** *sourcenode* $a = m$
and $(p, ins, outs) \in \text{set procs}$ **and** $x < \text{length } ins$ **by** *fastforce*
from $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q:r \hookrightarrow_p fs \rangle \langle (p, ins, outs) \in \text{set procs} \rangle$
have $\text{length}(\text{ParamUses } (\text{sourcenode } a)) = \text{length } ins$
by(*fastforce intro:ParamUses-call-source-length*)
with $\langle x < \text{length } ins \rangle$
have $(\text{ParamUses } (\text{sourcenode } a))!x \in \text{set } (\text{ParamUses } (\text{sourcenode } a))$ **by** *simp*
with $\langle V \in (\text{ParamUses } m)!x \rangle \langle \text{sourcenode } a = m \rangle$
have $V \in \text{Union } (\text{set } (\text{ParamUses } m))$ **by** *fastforce*
with $\langle \text{valid-edge } a \rangle \langle \text{sourcenode } a = m \rangle \langle n = \text{Actual-in } (m, x) \rangle$ **show** *?case*
by(*fastforce intro:ParamUses-in-Use*)
next
case (*Formal-out-SDG-Use* $n\ m\ x\ p\ ins\ outs\ V$)
from $\langle \text{valid-SDG-node } n \rangle \langle n = \text{Formal-out } (m, x) \rangle$ **obtain** $a\ Q\ p'\ f\ ins'\ outs'$
where *valid-edge* a **and** *kind* $a = Q \leftarrow_p f$ **and** *sourcenode* $a = m$
and $(p', ins', outs') \in \text{set procs}$ **and** $x < \text{length } outs'$ **by** *fastforce*
from $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q \leftarrow_p f \rangle$ **have** *get-proc* $(\text{sourcenode } a) = p'$
by(*rule get-proc-return*)
with $\langle \text{get-proc } m = p \rangle \langle \text{sourcenode } a = m \rangle$ **have** $[\text{simp}]: p = p'$ **by** *simp*
with $\langle (p', ins', outs') \in \text{set procs} \rangle \langle (p, ins, outs) \in \text{set procs} \rangle$ *unique-callers*
have $[\text{simp}]: ins' = ins\ outs' = outs$ **by**(*auto dest:distinct-fst-isin-same-fst*)
from $\langle x < \text{length } outs' \rangle \langle V = outs ! x \rangle$ **have** $V \in \text{set } outs$ **by** *fastforce*
with $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q \leftarrow_p f \rangle \langle (p, ins, outs) \in \text{set procs} \rangle$
have $V \in \text{Use } (\text{sourcenode } a)$ **by**(*fastforce intro:outs-in-Use*)
with $\langle \text{sourcenode } a = m \rangle \langle \text{valid-SDG-node } n \rangle \langle n = \text{Formal-out } (m, x) \rangle$
show *?case* **by** *simp*
qed

inductive *SDG-Def* $:: 'var \Rightarrow 'node\ \text{SDG-node} \Rightarrow \text{bool } (- \in \text{Def}_{\text{SDG}} -)$
where *CFG-Def-SDG-Def*:
 $\llbracket \text{valid-node } m; V \in \text{Def } m; n = \text{CFG-node } m \rrbracket \Longrightarrow V \in \text{Def}_{\text{SDG}} n$
| *Formal-in-SDG-Def*:
 $\llbracket \text{valid-SDG-node } n; n = \text{Formal-in } (m, x); \text{get-proc } m = p; (p, ins, outs) \in \text{set procs};$
 $V = ins!x \rrbracket \Longrightarrow V \in \text{Def}_{\text{SDG}} n$
| *Actual-out-SDG-Def*:
 $\llbracket \text{valid-SDG-node } n; n = \text{Actual-out } (m, x); V = (\text{ParamDefs } m)!x \rrbracket \Longrightarrow V \in \text{Def}_{\text{SDG}} n$

abbreviation *notin-SDG-Def* $:: 'var \Rightarrow 'node\ \text{SDG-node} \Rightarrow \text{bool } (- \notin \text{Def}_{\text{SDG}} -)$
where $V \notin \text{Def}_{\text{SDG}} n \equiv \neg V \in \text{Def}_{\text{SDG}} n$

lemma *in-Def-valid-SDG-node*:

$V \in \text{Def}_{SDG} n \implies \text{valid-SDG-node } n$
by(*induct rule:SDG-Def.induct,auto intro:valid-SDG-CFG-node*)

lemma *SDG-Def-parent-Def*:

$V \in \text{Def}_{SDG} n \implies V \in \text{Def}$ (*parent-node* n)

proof(*induct rule:SDG-Def.induct*)

case *CFG-Def-SDG-Def* **thus** ?*case* **by** *simp*

next

case (*Formal-in-SDG-Def* n m x p ins $outs$ V)

from $\langle \text{valid-SDG-node } n \rangle \langle n = \text{Formal-in } (m, x) \rangle$ **obtain** a Q r p' fs ins' $outs'$

where *valid-edge* a **and** *kind* $a = Q:r \hookrightarrow_p fs$ **and** *targetnode* $a = m$

and $\langle p', ins', outs' \rangle \in \text{set procs}$ **and** $x < \text{length } ins'$ **by** *fastforce*

from $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q:r \hookrightarrow_p fs \rangle$ **have** *get-proc* (*targetnode* a) = p'

by(*rule get-proc-call*)

with $\langle \text{get-proc } m = p \rangle \langle \text{targetnode } a = m \rangle$ **have** [*simp*]: $p = p'$ **by** *simp*

with $\langle p', ins', outs' \rangle \in \text{set procs}$ $\langle p, ins, outs \rangle \in \text{set procs}$ *unique-callers*

have [*simp*]: $ins' = ins$ $outs' = outs$ **by**(*auto dest:distinct-fst-isin-same-fst*)

from $\langle x < \text{length } ins' \rangle \langle V = ins ! x \rangle$ **have** $V \in \text{set } ins$ **by** *fastforce*

with $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q:r \hookrightarrow_p fs \rangle \langle p, ins, outs \rangle \in \text{set procs}$

have $V \in \text{Def}$ (*targetnode* a) **by**(*fastforce intro:ins-in-Def*)

with $\langle \text{targetnode } a = m \rangle \langle \text{valid-SDG-node } n \rangle \langle n = \text{Formal-in } (m, x) \rangle$

show ?*case* **by** *simp*

next

case (*Actual-out-SDG-Def* n m x V)

from $\langle \text{valid-SDG-node } n \rangle \langle n = \text{Actual-out } (m, x) \rangle$ **obtain** a Q p f ins $outs$

where *valid-edge* a **and** *kind* $a = Q \leftrightarrow_p f$ **and** *targetnode* $a = m$

and $\langle p, ins, outs \rangle \in \text{set procs}$ **and** $x < \text{length } outs$ **by** *fastforce*

from $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q \leftrightarrow_p f \rangle \langle p, ins, outs \rangle \in \text{set procs}$

have $\text{length}(\text{ParamDefs } (\text{targetnode } a)) = \text{length } outs$

by(*rule ParamDefs-return-target-length*)

with $\langle x < \text{length } outs \rangle \langle V = \text{ParamDefs } m ! x \rangle \langle \text{targetnode } a = m \rangle$

have $V \in \text{set } (\text{ParamDefs } (\text{targetnode } a))$ **by**(*fastforce simp:set-conv-nth*)

with $\langle n = \text{Actual-out } (m, x) \rangle \langle \text{targetnode } a = m \rangle \langle \text{valid-edge } a \rangle$

show ?*case* **by**(*fastforce intro:ParamDefs-in-Def*)

qed

definition *data-dependence* :: '*node* *SDG-node* \Rightarrow '*var* \Rightarrow '*node* *SDG-node* \Rightarrow *bool*

(- *influences* - *in* - [51,0,0])

where n *influences* V *in* n' $\equiv \exists as. (V \in \text{Def}_{SDG} n) \wedge (V \in \text{Use}_{SDG} n') \wedge$

$(\text{parent-node } n \text{ --} as \rightarrow_i * \text{parent-node } n') \wedge$

$(\forall n''. \text{valid-SDG-node } n'' \wedge \text{parent-node } n'' \in \text{set } (\text{sourcenodes } (tl \ as)))$

$\longrightarrow V \notin \text{Def}_{SDG} n''$)

1.8.3 Control dependence

definition *control-dependence* :: 'node ⇒ 'node ⇒ bool

(- controls - [51,0])

where *n controls n'* ≡ ∃ a a' as. n - a#as →_ι* n' ∧ n' ∉ set(sourcenodes (a#as))
 ∧
 intra-kind(kind a) ∧ n' postdominates (targetnode a) ∧
 valid-edge a' ∧ intra-kind(kind a') ∧ sourcenode a' = n ∧
 ¬ n' postdominates (targetnode a')

lemma *control-dependence-path*:

assumes *n controls n'* **obtains** *as* **where** n - as →_ι* n' **and** as ≠ []

using ⟨n controls n'⟩

by(fastforce simp:control-dependence-def)

lemma *Exit-does-not-control* [dest]:

assumes (-Exit-) controls n' **shows** False

proof -

from ⟨(-Exit-) controls n'⟩ **obtain** a **where** valid-edge a

and sourcenode a = (-Exit-) **by**(auto simp:control-dependence-def)

thus ?thesis **by**(rule Exit-source)

qed

lemma *Exit-not-control-dependent*:

assumes *n controls n'* **shows** n' ≠ (-Exit-)

proof -

from ⟨n controls n'⟩ **obtain** a as **where** n - a#as →_ι* n'

and n' postdominates (targetnode a)

by(auto simp:control-dependence-def)

from ⟨n - a#as →_ι* n'⟩ **have** valid-edge a

by(fastforce elim:path.cases simp:intra-path-def)

hence valid-node (targetnode a) **by** simp

with ⟨n' postdominates (targetnode a)⟩ ⟨n - a#as →_ι* n'⟩ **show** ?thesis

by(fastforce elim:Exit-no-postdominator)

qed

lemma *which-node-intra-standard-control-dependence-source*:

assumes nx - as@a#as' →_ι* n **and** sourcenode a = n' **and** sourcenode a' = n'

and n ∉ set(sourcenodes (a#as')) **and** valid-edge a' **and** intra-kind(kind a')

and inner-node n **and** ¬ method-exit n **and** ¬ n postdominates (targetnode a')

and last:∀ ax ax'. ax ∈ set as' ∧ sourcenode ax = sourcenode ax' ∧

valid-edge ax' ∧ intra-kind(kind ax') → n postdominates targetnode ax'

shows n' controls n

proof -

from ⟨nx - as@a#as' →_ι* n⟩ ⟨sourcenode a = n'⟩ **have** n' - a#as' →_ι* n

by(fastforce dest:path-split-second simp:intra-path-def)

```

from ⟨ $nx - as@a\#as' \rightarrow_i * n$ ⟩ have valid-edge a
  by(fastforce intro:path-split simp:intra-path-def)
show ?thesis
proof(cases n postdominates (targetnode a))
  case True
    with ⟨ $n' - a\#as' \rightarrow_i * n$ ⟩ ⟨ $n \notin \text{set}(\text{sourcenodes } (a\#as'))$ ⟩
      ⟨valid-edge a'⟩ ⟨intra-kind(kind a')⟩ ⟨sourcenode a' = n'⟩
      ⟨ $\neg n \text{ postdominates } (\text{targetnode } a')$ ⟩ show ?thesis
      by(fastforce simp:control-dependence-def intra-path-def)
  next
    case False
      show ?thesis
      proof(cases as' = [])
        case True
          with ⟨ $n' - a\#as' \rightarrow_i * n$ ⟩ have targetnode a = n
            by(fastforce elim:path.cases simp:intra-path-def)
          with ⟨inner-node n⟩ ⟨ $\neg \text{method-exit } n$ ⟩ have  $n \text{ postdominates } (\text{targetnode } a)$ 
            by(fastforce dest:inner-is-valid intro:postdominate-refl)
          with ⟨ $\neg n \text{ postdominates } (\text{targetnode } a)$ ⟩ show ?thesis by simp
        next
          case False
            with ⟨ $nx - as@a\#as' \rightarrow_i * n$ ⟩ have targetnode a - as' →i * n
              by(fastforce intro:path-split simp:intra-path-def)
            with ⟨ $\neg n \text{ postdominates } (\text{targetnode } a)$ ⟩ ⟨valid-edge a⟩ ⟨inner-node n⟩
              ⟨targetnode a - as' →i * n⟩
            obtain asx pex where targetnode a - asx →i * pex and method-exit pex
              and  $n \notin \text{set}(\text{sourcenodes } asx)$ 
              by(fastforce dest:inner-is-valid simp:postdominate-def)
            show ?thesis
            proof(cases ∃ asx'. asx = as'@asx')
              case True
                then obtain asx' where [simp]:asx = as'@asx' by blast
                from ⟨targetnode a - asx →i * pex⟩ ⟨targetnode a - as' →i * n⟩
                  ⟨ $as' \neq []$ ⟩ ⟨method-exit pex⟩ ⟨ $\neg \text{method-exit } n$ ⟩
                obtain a'' as'' where  $asx' = a''\#as'' \wedge \text{sourcenode } a'' = n$ 
                  by(cases asx')(auto dest:path-split path-det simp:intra-path-def)
                hence  $n \in \text{set}(\text{sourcenodes } asx)$  by(simp add:sourcenodes-def)
                with ⟨ $n \notin \text{set}(\text{sourcenodes } asx)$ ⟩ have False by simp
                thus ?thesis by simp
              next
                case False
                  hence  $\forall asx'. asx \neq as'@asx'$  by simp
                  then obtain j asx' where  $asx = (\text{take } j \text{ } as')@asx'$ 
                    and  $j < \text{length } as'$  and  $\forall k > j. \forall asx''. asx \neq (\text{take } k \text{ } as')@asx''$ 
                    by(auto elim:path-split-general)
                  from ⟨ $asx = (\text{take } j \text{ } as')@asx'$ ⟩ ⟨ $j < \text{length } as'$ ⟩
                    have  $\exists as'1 \text{ } as'2. asx = as'1@asx' \wedge$ 
                       $as' = as'1@as'2 \wedge as'2 \neq [] \wedge as'1 = \text{take } j \text{ } as'$ 
                      by simp(rule-tac x = drop j as' in exI, simp)

```


then obtain $as'1\ as''$ **where** $asx = as'1@asx'$
and $as'1 = take\ j\ as'$
and $as' = as'1@as''$ **and** $as'' \neq []$ **by** *blast*
from $\langle as' = as'1@as'' \rangle \langle as'' \neq [] \rangle$ **obtain** $a1\ as'2$
where $as' = as'1@a1\#as'2$ **and** $as'' = a1\#as'2$
by *(cases as'')* *auto*
have $asx' \neq []$
proof *(cases asx' = [])*
case *True*
with $\langle asx = as'1@asx' \rangle \langle as' = as'1@as'' \rangle \langle as'' = a1\#as'2 \rangle$
have $as' = asx@a1\#as'2$ **by** *simp*
with $\langle n' - a\#as' \rightarrow_i^* n \rangle$ **have** $n' - (a\#asx)@a1\#as'2 \rightarrow_i^* n$ **by** *simp*
hence $n' - (a\#asx)@a1\#as'2 \rightarrow^* n$
and $\forall ax \in set((a\#asx)@a1\#as'2)$. *intra-kind(kind ax)*
by *(simp-all add:intra-path-def)*
from $\langle n' - (a\#asx)@a1\#as'2 \rightarrow^* n \rangle$
have $n' - a\#asx \rightarrow^*$ *sourcenode a1* **and** *valid-edge a1*
by *(erule path-split)+*
from $\langle \forall ax \in set((a\#asx)@a1\#as'2)$. *intra-kind(kind ax)*
have $\forall ax \in set(a\#asx)$. *intra-kind(kind ax)* **by** *simp*
with $\langle n' - a\#asx \rightarrow^*$ *sourcenode a1*
have $n' - a\#asx \rightarrow_i^*$ *sourcenode a1*
by *(simp add:intra-path-def)*
hence *targetnode a -asx* \rightarrow_i^* *sourcenode a1*
by *(fastforce intro:path-split-Cons simp:intra-path-def)*
with $\langle targetnode a -asx \rightarrow_i^*$ *pex*
have *pex = sourcenode a1*
by *(fastforce intro:path-det simp:intra-path-def)*
from $\langle \forall ax \in set((a\#asx)@a1\#as'2)$. *intra-kind(kind ax)*
have *intra-kind (kind a1)* **by** *simp*
from $\langle method-exit\ pex \rangle$ **have** *False*
proof *(rule method-exit-cases)*
assume $pex = (-Exit-)$
with $\langle pex = sourcenode\ a1 \rangle$ **have** *sourcenode a1 = (-Exit-)* **by** *simp*
with $\langle valid-edge\ a1 \rangle$ **show** *False* **by** *(rule Exit-source)*
next
fix $a\ Q\ f\ p$ **assume** $pex = sourcenode\ a$ **and** *valid-edge a*
and $kind\ a = Q \leftrightarrow_p f$
from $\langle valid-edge\ a \rangle \langle kind\ a = Q \leftrightarrow_p f \rangle \langle pex = sourcenode\ a \rangle$
 $\langle pex = sourcenode\ a1 \rangle \langle valid-edge\ a1 \rangle \langle intra-kind\ (kind\ a1) \rangle$
show *False* **by** *(fastforce dest:return-edges-only simp:intra-kind-def)*
qed
thus *?thesis* **by** *simp*
qed *simp*
with $\langle asx = as'1@asx' \rangle$ **obtain** $a2\ asx'1$
where $asx = as'1@a2\#asx'1$
and $asx' = a2\#asx'1$ **by** *(cases asx')* *auto*
from $\langle n' - a\#as' \rightarrow_i^* n \rangle \langle as' = as'1@a1\#as'2 \rangle$
have $n' - (a\#as'1)@a1\#as'2 \rightarrow_i^* n$ **by** *simp*
hence $n' - (a\#as'1)@a1\#as'2 \rightarrow^* n$
and $\forall ax \in set((a\#as'1)@a1\#as'2)$. *intra-kind(kind ax)*

```

    by(simp-all add: intra-path-def)
  from ⟨n' - (a#as'1)@a1#a2#as'2 →* n⟩ have n' - a#a2#as'1 →* sourcenode a1
    and valid-edge a1 by -(erule path-split)+
  from ⟨∀ ax ∈ set((a#a2#as'1)@a1#a2#as'2). intra-kind(kind ax)⟩
  have ∀ ax ∈ set(a#a2#as'1). intra-kind(kind ax) by simp
  with ⟨n' - a#a2#as'1 →* sourcenode a1⟩ have n' - a#a2#as'1 →* sourcenode a1
    by(simp add:intra-path-def)
  hence targetnode a - a#a2#as'1 →* sourcenode a1
    by(fastforce intro:path-split-Cons simp:intra-path-def)
  from ⟨targetnode a - a#a2#as'1 →* pex⟩ ⟨asx = a#a2#as'1⟩
  have targetnode a - a#a2#as'1 →* pex by(simp add:intra-path-def)
  hence targetnode a - a#a2#as'1 →* sourcenode a2 and valid-edge a2
    and targetnode a2 - a#a2#as'1 →* pex by(auto intro:path-split)
  from ⟨targetnode a2 - a#a2#as'1 →* pex⟩ ⟨asx = a#a2#as'1⟩
    ⟨targetnode a - a#a2#as'1 →* pex⟩
  have targetnode a2 - a#a2#as'1 →* pex by(simp add:intra-path-def)
  from ⟨targetnode a - a#a2#as'1 →* sourcenode a2⟩
    ⟨targetnode a - a#a2#as'1 →* sourcenode a1⟩
  have sourcenode a1 = sourcenode a2
    by(fastforce intro:path-det simp:intra-path-def)
  from ⟨asx = a#a2#as'1⟩ ⟨n ∉ set (sourcenodes asx)⟩
  have n ∉ set (sourcenodes a#a2#as'1) by(simp add:sourcenodes-def)
  with ⟨targetnode a2 - a#a2#as'1 →* pex⟩ ⟨method-exit pex⟩
    ⟨asx = a#a2#as'1⟩
  have ¬ n postdominates targetnode a2 by(fastforce simp:postdominate-def)
  from ⟨asx = a#a2#as'1⟩ ⟨targetnode a - a#a2#as'1 →* pex⟩
  have intra-kind (kind a2) by(simp add:intra-path-def)
  from ⟨a#a2#as'1 = a#a2#as'1⟩ have a1 ∈ set a#a2#as'1 by simp
  with ⟨sourcenode a1 = sourcenode a2⟩ last ⟨valid-edge a2⟩
    ⟨intra-kind (kind a2)⟩
  have n postdominates targetnode a2 by blast
  with ⟨¬ n postdominates targetnode a2⟩ have False by simp
  thus ?thesis by simp
qed
qed
qed
qed

```

1.8.4 SDG without summary edges

```

inductive cdep-edge :: 'node SDG-node ⇒ 'node SDG-node ⇒ bool
  (- →cd - [51,0] 80)
and ddep-edge :: 'node SDG-node ⇒ 'var ⇒ 'node SDG-node ⇒ bool
  (- →dd - [51,0,0] 80)
and call-edge :: 'node SDG-node ⇒ 'pname ⇒ 'node SDG-node ⇒ bool
  (- →call - [51,0,0] 80)
and return-edge :: 'node SDG-node ⇒ 'pname ⇒ 'node SDG-node ⇒ bool
  (- →ret - [51,0,0] 80)
and param-in-edge :: 'node SDG-node ⇒ 'pname ⇒ 'var ⇒ 'node SDG-node ⇒

```

```

bool
  (- --::->in - [51,0,0,0] 80)
  and param-out-edge :: 'node SDG-node => 'pname => 'var => 'node SDG-node
=> bool
  (- --::->out - [51,0,0,0] 80)
  and SDG-edge :: 'node SDG-node => 'var option =>
    ('pname × bool) option => 'node SDG-node => bool

```

where

```

n ->cd n' == SDG-edge n None None n'
| n -V->dd n' == SDG-edge n (Some V) None n'
| n -p->call n' == SDG-edge n None (Some(p,True)) n'
| n -p->ret n' == SDG-edge n None (Some(p,False)) n'
| n -p:V->in n' == SDG-edge n (Some V) (Some(p,True)) n'
| n -p:V->out n' == SDG-edge n (Some V) (Some(p,False)) n'

| SDG-cdep-edge:
  [[n = CFG-node m; n' = CFG-node m'; m controls m']] ==> n ->cd n'
| SDG-proc-entry-exit-cdep:
  [[valid-edge a; kind a = Q:r->pfs; n = CFG-node (targetnode a);
  a' ∈ get-return-edges a; n' = CFG-node (sourcenode a)]] ==> n ->cd n'
| SDG-parent-cdep-edge:
  [[valid-SDG-node n'; m = parent-node n'; n = CFG-node m; n ≠ n']]
  ==> n ->cd n'
| SDG-ddep-edge:n influences V in n' ==> n -V->dd n'
| SDG-call-edge:
  [[valid-edge a; kind a = Q:r->pfs; n = CFG-node (sourcenode a);
  n' = CFG-node (targetnode a)]] ==> n -p->call n'
| SDG-return-edge:
  [[valid-edge a; kind a = Q:<-pf; n = CFG-node (sourcenode a);
  n' = CFG-node (targetnode a)]] ==> n -p->ret n'
| SDG-param-in-edge:
  [[valid-edge a; kind a = Q:r->pfs; (p,ins,outs) ∈ set procs; V = ins!x;
  x < length ins; n = Actual-in (sourcenode a,x); n' = Formal-in (targetnode
a,x)]]
  ==> n -p:V->in n'
| SDG-param-out-edge:
  [[valid-edge a; kind a = Q:<-pf; (p,ins,outs) ∈ set procs; V = outs!x;
  x < length outs; n = Formal-out (sourcenode a,x);
  n' = Actual-out (targetnode a,x)]]
  ==> n -p:V->out n'

```

lemma *cdep-edge-cases*:

```

[[n ->cd n'; (parent-node n) controls (parent-node n')] ==> P;
∧ a Q r p fs a'. [[valid-edge a; kind a = Q:r->pfs; a' ∈ get-return-edges a;
parent-node n = targetnode a; parent-node n' = sourcenode a]] ==>

```

P ;
 $\wedge m. \llbracket n = \text{CFG-node } m; m = \text{parent-node } n'; n \neq n' \rrbracket \implies P \implies P$
by $-(\text{erule } \text{SDG-edge.cases}, \text{auto}, \text{fastforce})$

lemma *SDG-edge-valid-SDG-node*:
assumes *SDG-edge* n *Vopt* $\text{popt } n'$
shows *valid-SDG-node* n **and** *valid-SDG-node* n'
using $\langle \text{SDG-edge } n \text{ Vopt } \text{popt } n' \rangle$
proof (*induct* $\text{rule}:\text{SDG-edge.induct}$)
case (*SDG-cdep-edge* $n \ m \ n' \ m'$)
thus *valid-SDG-node* n *valid-SDG-node* n'
by (*fastforce* *elim:control-dependence-path* *elim:path-valid-node*
simp:intra-path-def)
next
case (*SDG-proc-entry-exit-cdep* $a \ Q \ r \ p \ f \ n \ a' \ n'$) **case** 1
from $\langle \text{valid-edge } a \rangle \langle n = \text{CFG-node } (\text{targetnode } a) \rangle$ **show** $?case$ **by** *simp*
next
case (*SDG-proc-entry-exit-cdep* $a \ Q \ r \ p \ f \ n \ a' \ n'$) **case** 2
from $\langle \text{valid-edge } a \rangle \langle a' \in \text{get-return-edges } a \rangle$ **have** *valid-edge* a'
by (*rule* *get-return-edges-valid*)
with $\langle n' = \text{CFG-node } (\text{sourcenode } a') \rangle$ **show** $?case$ **by** *simp*
next
case (*SDG-ddep-edge* $n \ V \ n'$)
thus *valid-SDG-node* n *valid-SDG-node* n'
by (*auto* *intro:in-Use-valid-SDG-node* *in-Def-valid-SDG-node*
simp:data-dependence-def)
qed (*fastforce* *intro:valid-SDG-CFG-node*)
 $+$

lemma *valid-SDG-node-cases*:
assumes *valid-SDG-node* n
shows $n = \text{CFG-node } (\text{parent-node } n) \vee \text{CFG-node } (\text{parent-node } n) \longrightarrow_{cd} n$
proof (*cases* n)
case (*CFG-node* m) **thus** $?thesis$ **by** *simp*
next
case (*Formal-in* z)
from $\langle n = \text{Formal-in } z \rangle$ **obtain** $m \ x$ **where** $z = (m, x)$ **by** (*cases* z) *auto*
with $\langle \text{valid-SDG-node } n \rangle \langle n = \text{Formal-in } z \rangle$ **have** *CFG-node* $(\text{parent-node } n)$
 $\longrightarrow_{cd} n$
by $-(\text{rule } \text{SDG-parent-cdep-edge}, \text{auto})$
thus $?thesis$ **by** *fastforce*
next
case (*Formal-out* z)
from $\langle n = \text{Formal-out } z \rangle$ **obtain** $m \ x$ **where** $z = (m, x)$ **by** (*cases* z) *auto*
with $\langle \text{valid-SDG-node } n \rangle \langle n = \text{Formal-out } z \rangle$ **have** *CFG-node* $(\text{parent-node } n)$
 $\longrightarrow_{cd} n$
by $-(\text{rule } \text{SDG-parent-cdep-edge}, \text{auto})$
thus $?thesis$ **by** *fastforce*

```

next
  case (Actual-in z)
  from ⟨n = Actual-in z⟩ obtain m x where z = (m,x) by(cases z) auto
  with ⟨valid-SDG-node n⟩ ⟨n = Actual-in z⟩ have CFG-node (parent-node n)
  →cd n
  by -(rule SDG-parent-cdep-edge,auto)
  thus ?thesis by fastforce
next
  case (Actual-out z)
  from ⟨n = Actual-out z⟩ obtain m x where z = (m,x) by(cases z) auto
  with ⟨valid-SDG-node n⟩ ⟨n = Actual-out z⟩ have CFG-node (parent-node n)
  →cd n
  by -(rule SDG-parent-cdep-edge,auto)
  thus ?thesis by fastforce
qed

```

lemma *SDG-cdep-edge-CFG-node*: $n \rightarrow_{cd} n' \implies \exists m. n = \text{CFG-node } m$
by(*induct* n *Vopt*≡None::'var option *popt*≡None::('pname × bool) option n'
rule:SDG-edge.induct) auto

lemma *SDG-call-edge-CFG-node*: $n -p \rightarrow_{call} n' \implies \exists m. n = \text{CFG-node } m$
by(*induct* n *Vopt*≡None::'var option *popt*≡Some(p,True) n'
rule:SDG-edge.induct) auto

lemma *SDG-return-edge-CFG-node*: $n -p \rightarrow_{ret} n' \implies \exists m. n = \text{CFG-node } m$
by(*induct* n *Vopt*≡None::'var option *popt*≡Some(p,False) n'
rule:SDG-edge.induct) auto

lemma *SDG-call-or-param-in-edge-unique-CFG-call-edge*:
SDG-edge n *Vopt* (Some(p,True)) n'
 $\implies \exists !a. \text{valid-edge } a \wedge \text{sourcenode } a = \text{parent-node } n \wedge$
 $\text{targetnode } a = \text{parent-node } n' \wedge (\exists Q r fs. \text{kind } a = Q:r \hookrightarrow pfs)$

proof(*induct* n *Vopt* Some(p,True) n' *rule*:SDG-edge.induct)
case (SDG-call-edge a Q r fs n n')
{ **fix** a'
assume *valid-edge* a' **and** *sourcenode* a' = *parent-node* n
and *targetnode* a' = *parent-node* n'
from ⟨*sourcenode* a' = *parent-node* n⟩ ⟨n = CFG-node (*sourcenode* a)⟩
have *sourcenode* a' = *sourcenode* a **by** fastforce
moreover from ⟨*targetnode* a' = *parent-node* n'⟩ ⟨n' = CFG-node (*targetnode*
a)⟩
have *targetnode* a' = *targetnode* a **by** fastforce
ultimately have a' = a **using** ⟨*valid-edge* a'⟩ ⟨*valid-edge* a⟩
by(fastforce *intro*:edge-det) }
with ⟨*valid-edge* a⟩ ⟨n = CFG-node (*sourcenode* a)⟩ ⟨n' = CFG-node (*targetnode*
a)⟩

```

  ⟨kind a = Q:r↔pfs⟩ show ?case by(fastforce intro!:ex1I[of - a])
next
  case (SDG-param-in-edge a Q r fs ins outs V x n n')
  { fix a'
    assume valid-edge a' and sourcenode a' = parent-node n
      and targetnode a' = parent-node n'
    from ⟨sourcenode a' = parent-node n⟩ ⟨n = Actual-in (sourcenode a,x)⟩
    have sourcenode a' = sourcenode a by fastforce
    moreover from ⟨targetnode a' = parent-node n'⟩ ⟨n' = Formal-in (targetnode
a,x)⟩
    have targetnode a' = targetnode a by fastforce
    ultimately have a' = a using ⟨valid-edge a'⟩ ⟨valid-edge a⟩
      by(fastforce intro:edge-det) }
  with ⟨valid-edge a⟩ ⟨n = Actual-in (sourcenode a,x)⟩
    ⟨n' = Formal-in (targetnode a,x)⟩ ⟨kind a = Q:r↔pfs⟩
  show ?case by(fastforce intro!:ex1I[of - a])
qed simp-all

```

lemma SDG-return-or-param-out-edge-unique-CFG-return-edge:

```

  SDG-edge n Vopt (Some(p,False)) n'
  ⇒ ∃!a. valid-edge a ∧ sourcenode a = parent-node n ∧
    targetnode a = parent-node n' ∧ (∃ Q f. kind a = Q↔pf)
proof(induct n Vopt Some(p,False) n' rule:SDG-edge.induct)
  case (SDG-return-edge a Q f n n')
  { fix a'
    assume valid-edge a' and sourcenode a' = parent-node n
      and targetnode a' = parent-node n'
    from ⟨sourcenode a' = parent-node n⟩ ⟨n = CFG-node (sourcenode a)⟩
    have sourcenode a' = sourcenode a by fastforce
    moreover from ⟨targetnode a' = parent-node n'⟩ ⟨n' = CFG-node (targetnode
a)⟩
    have targetnode a' = targetnode a by fastforce
    ultimately have a' = a using ⟨valid-edge a'⟩ ⟨valid-edge a⟩
      by(fastforce intro:edge-det) }
  with ⟨valid-edge a⟩ ⟨n = CFG-node (sourcenode a)⟩ ⟨n' = CFG-node (targetnode
a)⟩
    ⟨kind a = Q↔pf⟩ show ?case by(fastforce intro!:ex1I[of - a])
next
  case (SDG-param-out-edge a Q f ins outs V x n n')
  { fix a'
    assume valid-edge a' and sourcenode a' = parent-node n
      and targetnode a' = parent-node n'
    from ⟨sourcenode a' = parent-node n⟩ ⟨n = Formal-out (sourcenode a,x)⟩
    have sourcenode a' = sourcenode a by fastforce
    moreover from ⟨targetnode a' = parent-node n'⟩ ⟨n' = Actual-out (targetnode
a,x)⟩
    have targetnode a' = targetnode a by fastforce
    ultimately have a' = a using ⟨valid-edge a'⟩ ⟨valid-edge a⟩

```

```

    by(fastforce intro:edge-det) }
  with ⟨valid-edge a⟩ ⟨n = Formal-out (sourcenode a,x)⟩
    ⟨n' = Actual-out (targetnode a,x)⟩ ⟨kind a = Q↔pf⟩
  show ?case by(fastforce intro!:ex1I[of - a])
qed simp-all

lemma Exit-no-SDG-edge-source:
  SDG-edge (CFG-node (-Exit-)) Vopt popt n' ⇒ False
proof(induct CFG-node (-Exit-) Vopt popt n' rule:SDG-edge.induct)
  case (SDG-cdep-edge m n' m')
  hence (-Exit-) controls m' by simp
  thus ?case by fastforce
next
  case (SDG-proc-entry-exit-cdep a Q r p fs a' n')
  from ⟨CFG-node (-Exit-) = CFG-node (targetnode a)⟩
  have targetnode a = (-Exit-) by simp
  from ⟨valid-edge a⟩ ⟨kind a = Q:r↔pfs⟩ have get-proc (targetnode a) = p
  by(rule get-proc-call)
  with ⟨targetnode a = (-Exit-)⟩ have p = Main
  by(auto simp:get-proc-Exit)
  with ⟨valid-edge a⟩ ⟨kind a = Q:r↔pfs⟩ have False
  by(fastforce intro:Main-no-call-target)
  thus ?thesis by simp
next
  case (SDG-parent-cdep-edge n' m)
  from ⟨CFG-node (-Exit-) = CFG-node m⟩
  have [simp]:m = (-Exit-) by simp
  with ⟨valid-SDG-node n'⟩ ⟨m = parent-node n'⟩ ⟨CFG-node (-Exit-) ≠ n'⟩
  have False by -(drule valid-SDG-node-parent-Exit,simp+)
  thus ?thesis by simp
next
  case (SDG-ddep-edge V n')
  hence (CFG-node (-Exit-)) influences V in n' by simp
  with Exit-empty show ?case
  by(fastforce dest:path-Exit-source SDG-Def-parent-Def
    simp:data-dependence-def intra-path-def)
next
  case (SDG-call-edge a Q r p fs n')
  from ⟨CFG-node (-Exit-) = CFG-node (sourcenode a)⟩
  have sourcenode a = (-Exit-) by simp
  with ⟨valid-edge a⟩ show ?case by(rule Exit-source)
next
  case (SDG-return-edge a Q p f n')
  from ⟨CFG-node (-Exit-) = CFG-node (sourcenode a)⟩
  have sourcenode a = (-Exit-) by simp
  with ⟨valid-edge a⟩ show ?case by(rule Exit-source)
qed simp-all

```

1.8.5 Intraprocedural paths in the SDG

inductive *intra-SDG-path* ::

'node SDG-node \Rightarrow 'node SDG-node list \Rightarrow 'node SDG-node \Rightarrow bool
 (- i \rightarrow_d^* - [51,0,0] 80)

where *iSp-Nil*:

valid-SDG-node n \Longrightarrow n i \rightarrow_d^* n

| *iSp-Append-cdep*:

$\llbracket n \text{ i-ns} \rightarrow_d^* n''; n'' \rightarrow_{cd} n' \rrbracket \Longrightarrow n \text{ i-ns}@[n'] \rightarrow_d^* n'$

| *iSp-Append-ddep*:

$\llbracket n \text{ i-ns} \rightarrow_d^* n''; n'' -V \rightarrow_{dd} n'; n'' \neq n' \rrbracket \Longrightarrow n \text{ i-ns}@[n'] \rightarrow_d^* n'$

lemma *intra-SDG-path-Append*:

$\llbracket n'' \text{ i-ns}' \rightarrow_d^* n'; n \text{ i-ns} \rightarrow_d^* n'' \rrbracket \Longrightarrow n \text{ i-ns}@[n'] \rightarrow_d^* n'$

by(*induct rule:intra-SDG-path.induct*,

auto intro:intra-SDG-path.intros simp:append-assoc[THEN sym] simp del:append-assoc)

lemma *intra-SDG-path-valid-SDG-node*:

assumes n i-ns \rightarrow_d^* n' **shows** *valid-SDG-node* n **and** *valid-SDG-node* n'

using $\langle n \text{ i-ns} \rightarrow_d^* n' \rangle$

by(*induct rule:intra-SDG-path.induct*,

auto intro:SDG-edge-valid-SDG-node valid-SDG-CFG-node)

lemma *intra-SDG-path-intra-CFG-path*:

assumes n i-ns \rightarrow_d^* n'

obtains as **where** *parent-node* n -as \rightarrow_i^* *parent-node* n'

proof(*atomize-elim*)

from $\langle n \text{ i-ns} \rightarrow_d^* n' \rangle$

show \exists as. *parent-node* n -as \rightarrow_i^* *parent-node* n'

proof(*induct rule:intra-SDG-path.induct*)

case (*iSp-Nil* n)

from $\langle \text{valid-SDG-node } n \rangle$ **have** *valid-node* (*parent-node* n)

by(*rule valid-SDG-CFG-node*)

hence *parent-node* n -[] \rightarrow^* *parent-node* n **by**(*rule empty-path*)

thus ?case **by**(*auto simp:intra-path-def*)

next

case (*iSp-Append-cdep* n ns n'' n')

from $\langle \exists$ as. *parent-node* n -as \rightarrow_i^* *parent-node* n'' \rangle

obtain as **where** *parent-node* n -as \rightarrow_i^* *parent-node* n'' **by** *blast*

from $\langle n'' \rightarrow_{cd} n' \rangle$ **show** ?case

proof(*rule cdep-edge-cases*)

assume *parent-node* n'' *controls* *parent-node* n'

then obtain as' **where** *parent-node* n'' -as' \rightarrow_i^* *parent-node* n' **and** as' \neq

□


```

    by(erule control-dependence-path)
  with ⟨parent-node n -as→i* parent-node n''⟩
  have parent-node n -as@as'→i* parent-node n' by -(rule intra-path-Append)
  thus ?thesis by blast
next
  fix a Q r p fs a'
  assume valid-edge a and kind a = Q:r↪pfs and a' ∈ get-return-edges a
    and parent-node n'' = targetnode a and parent-node n' = sourcenode a'
  then obtain a'' where valid-edge a'' and sourcenode a'' = targetnode a
    and targetnode a'' = sourcenode a' and kind a'' = (λcf. False)√
    by(auto dest:intra-proc-additional-edge)
  hence targetnode a -[a'']→i* sourcenode a'
    by(fastforce dest:path-edge simp:intra-path-def intra-kind-def)
  with ⟨parent-node n'' = targetnode a⟩ ⟨parent-node n' = sourcenode a'⟩
  have ∃ as'. parent-node n'' -as'→i* parent-node n' ∧ as' ≠ [] by fastforce
  then obtain as' where parent-node n'' -as'→i* parent-node n' and as' ≠ []
  by blast
  with ⟨parent-node n -as→i* parent-node n''⟩
  have parent-node n -as@as'→i* parent-node n' by -(rule intra-path-Append)
  thus ?thesis by blast
next
  fix m assume n'' = CFG-node m and m = parent-node n'
  with ⟨parent-node n -as→i* parent-node n''⟩ show ?thesis by fastforce
qed
next
  case (iSp-Append-ddep n ns n'' V n')
  from ⟨∃ as. parent-node n -as→i* parent-node n''⟩
  obtain as where parent-node n -as→i* parent-node n'' by blast
  from ⟨n'' -V→dd n'⟩ have n'' influences V in n'
    by(fastforce elim:SDG-edge.cases)
  then obtain as' where parent-node n'' -as'→i* parent-node n'
    by(auto simp:data-dependence-def)
  with ⟨parent-node n -as→i* parent-node n''⟩
  have parent-node n -as@as'→i* parent-node n' by -(rule intra-path-Append)
  thus ?case by blast
qed
qed

```

1.8.6 Control dependence paths in the SDG

inductive *cddep-SDG-path* ::

'node SDG-node ⇒ 'node SDG-node list ⇒ 'node SDG-node ⇒ bool
 (- cd--→_a* - [51,0,0] 80)

where *cdSp-Nil*:

valid-SDG-node n ⇒ n cd-[]→_a* n

| *cdSp-Append-cdep*:

$$\llbracket n \text{ cd-ns} \rightarrow_d^* n''; n'' \rightarrow_{cd} n' \rrbracket \implies n \text{ cd-ns} @ [n''] \rightarrow_d^* n'$$

lemma *cdep-SDG-path-intra-SDG-path*:

$$n \text{ cd-ns} \rightarrow_d^* n' \implies n \text{ i-ns} \rightarrow_d^* n'$$

by(*induct rule:cdep-SDG-path.induct, auto intro:intra-SDG-path.intros*)

lemma *Entry-cdep-SDG-path*:

assumes $(\text{-Entry-}) -as \rightarrow_l^* n'$ **and** *inner-node* n' **and** \neg *method-exit* n'

obtains ns **where** *CFG-node* $(\text{-Entry-}) \text{ cd-ns} \rightarrow_d^* \text{ CFG-node } n'$

and $ns \neq []$ **and** $\forall n'' \in \text{set } ns. \text{parent-node } n'' \in \text{set}(\text{sourcenodes } as)$

proof(*atomize-elim*)

from $\langle (\text{-Entry-}) -as \rightarrow_l^* n' \rangle \langle \text{inner-node } n' \rangle \langle \neg \text{method-exit } n' \rangle$

show $\exists ns. \text{CFG-node } (\text{-Entry-}) \text{ cd-ns} \rightarrow_d^* \text{ CFG-node } n' \wedge ns \neq [] \wedge$

$(\forall n'' \in \text{set } ns. \text{parent-node } n'' \in \text{set}(\text{sourcenodes } as))$

proof(*induct as arbitrary:n' rule:length-induct*)

fix $as \ n'$

assume $IH: \forall as'. \text{length } as' < \text{length } as \longrightarrow$

$(\forall n''. (\text{-Entry-}) -as' \rightarrow_l^* n'' \longrightarrow \text{inner-node } n'' \longrightarrow \neg \text{method-exit } n'' \longrightarrow$

$(\exists ns. \text{CFG-node } (\text{-Entry-}) \text{ cd-ns} \rightarrow_d^* \text{ CFG-node } n'' \wedge ns \neq [] \wedge$

$(\forall nx \in \text{set } ns. \text{parent-node } nx \in \text{set}(\text{sourcenodes } as'))))$

and $(\text{-Entry-}) -as \rightarrow_l^* n'$ **and** *inner-node* n' **and** \neg *method-exit* n'

thus $\exists ns. \text{CFG-node } (\text{-Entry-}) \text{ cd-ns} \rightarrow_d^* \text{ CFG-node } n' \wedge ns \neq [] \wedge$

$(\forall n'' \in \text{set } ns. \text{parent-node } n'' \in \text{set}(\text{sourcenodes } as))$

proof $-$

have $\exists ax \ asx \ zs. (\text{-Entry-}) -ax \# \ asx \rightarrow_l^* n' \wedge n' \notin \text{set}(\text{sourcenodes } (ax \# \ asx))$

\wedge

$$as = (ax \# \ asx) @ zs$$

proof(*cases* $n' \in \text{set}(\text{sourcenodes } as)$)

case *True*

hence $\exists n'' \in \text{set}(\text{sourcenodes } as). n' = n''$ **by** *simp*

then obtain $ns' \ ns''$ **where** $\text{sourcenodes } as = ns' @ n' \# ns''$

and $\forall n'' \in \text{set } ns'. n' \neq n''$

by(*fastforce elim!:split-list-first-propE*)

from $\langle \text{sourcenodes } as = ns' @ n' \# ns'' \rangle$ **obtain** $xs \ ys \ ax$

where $\text{sourcenodes } xs = ns'$ **and** $as = xs @ ax \# ys$

and *sourcenode* $ax = n'$

by(*fastforce elim:map-append-append-maps simp:sourcenodes-def*)

from $\langle \forall n'' \in \text{set } ns'. n' \neq n'' \rangle \langle \text{sourcenodes } xs = ns' \rangle$

have $n' \notin \text{set}(\text{sourcenodes } xs)$ **by** *fastforce*

from $\langle (\text{-Entry-}) -as \rightarrow_l^* n' \rangle \langle as = xs @ ax \# ys \rangle$ **have** $(\text{-Entry-}) -xs @ ax \# ys \rightarrow_l^*$

n'

by *simp*

with $\langle \text{sourcenode } ax = n' \rangle$ **have** $(\text{-Entry-}) -xs \rightarrow_l^* n'$

by(*fastforce dest:path-split simp:intra-path-def*)

with $\langle \text{inner-node } n' \rangle$ **have** $xs \neq []$

by(*fastforce elim:path.cases simp:intra-path-def*)

with $\langle n' \notin \text{set}(\text{sourcenodes } xs) \rangle \langle (\text{-Entry-}) -xs \rightarrow_l^* n' \rangle \langle as = xs @ ax \# ys \rangle$

```

    show ?thesis by(cases xs) auto
next
case False
with ⟨(-Entry-) -as→i* n'⟩ ⟨inner-node n'⟩
show ?thesis by(cases as)(auto elim:path.cases simp:intra-path-def)
qed
then obtain ax asx zs where ⟨(-Entry-) -ax#asx→i* n'⟩
and n' ∉ set (sourcenodes (ax#asx)) and as = (ax#asx)@zs by blast
show ?thesis
proof(cases ∀ a' a''. a' ∈ set asx ∧ sourcenode a' = sourcenode a'' ∧
valid-edge a'' ∧ intra-kind(kind a'') → n' postdominates targetnode a'')
case True
have ⟨(-Exit-) -[]→i* (-Exit-)⟩
by(fastforce intro:empty-path simp:intra-path-def)
hence ¬ n' postdominates ⟨(-Exit-)⟩
by(fastforce simp:postdominate-def sourcenodes-def method-exit-def)
from ⟨(-Entry-) -ax#asx→i* n'⟩ have ⟨(-Entry-) -[]@ax#asx→i* n'⟩ by
simp
from ⟨(-Entry-) -ax#asx→i* n'⟩ have [simp]:sourcenode ax = ⟨(-Entry-)⟩
and valid-edge ax
by(auto intro:path-split-Cons simp:intra-path-def)
from Entry-Exit-edge obtain a' where sourcenode a' = ⟨(-Entry-)⟩
and targetnode a' = ⟨(-Exit-)⟩ and valid-edge a'
and intra-kind(kind a') by(auto simp:intra-kind-def)
with ⟨(-Entry-) -[]@ax#asx→i* n'⟩ ⟨¬ n' postdominates ⟨(-Exit-)⟩⟩
⟨valid-edge ax⟩ True ⟨sourcenode ax = ⟨(-Entry-)⟩⟩
⟨n' ∉ set (sourcenodes (ax#asx))⟩ ⟨inner-node n'⟩ ⟨¬ method-exit n'⟩
have sourcenode ax controls n'
by -(erule which-node-intra-standard-control-dependence-source
[of - - - - - a'],auto)
hence CFG-node ⟨(-Entry-)⟩ →cd CFG-node n'
by(fastforce intro:SDG-cdep-edge)
hence CFG-node ⟨(-Entry-)⟩ cd-[]@[CFG-node ⟨(-Entry-)⟩]→d* CFG-node n'
by(fastforce intro:cdSp-Append-cdep cdSp-Nil)
moreover
from ⟨as = (ax#asx)@zs⟩ have ⟨(-Entry-)⟩ ∈ set(sourcenodes as)
by(simp add:sourcenodes-def)
ultimately show ?thesis by fastforce
next
case False
hence ∃ a' ∈ set asx. ∃ a''. sourcenode a' = sourcenode a'' ∧ valid-edge a''
∧
intra-kind(kind a'') ∧ ¬ n' postdominates targetnode a''
by fastforce
then obtain ax' asx' asx'' where asx = asx'@ax'#asx'' ∧
(∃ a''. sourcenode ax' = sourcenode a'' ∧ valid-edge a'' ∧
intra-kind(kind a'') ∧ ¬ n' postdominates targetnode a'') ∧
(∀ z ∈ set asx''. ¬ (∃ a''. sourcenode z = sourcenode a'' ∧ valid-edge a'' ∧
intra-kind(kind a'') ∧ ¬ n' postdominates targetnode a''))

```

```

    by(blast elim!:split-list-last-propE)
  then obtain ai where  $ax = ax'@ax'\#ax''$ 
    and sourcenode  $ax' = \text{sourcenode } ai$ 
    and valid-edge ai and intra-kind(kind ai)
    and  $\neg n'$  postdominates targetnode ai
    and  $\forall z \in \text{set } ax''. \neg (\exists a''. \text{sourcenode } z = \text{sourcenode } a'' \wedge$ 
      valid-edge  $a'' \wedge \text{intra-kind}(\text{kind } a'') \wedge \neg n'$  postdominates targetnode a'')
    by blast
  from  $\langle (-\text{Entry-}) -ax\#ax \rightarrow_i^* n' \rangle \langle ax = ax'@ax'\#ax'' \rangle$ 
  have  $\langle (-\text{Entry-}) -(ax\#ax')@ax'\#ax'' \rightarrow_i^* n' \rangle$  by simp
  from  $\langle n' \notin \text{set } (\text{sourcenodes } (ax\#ax')) \rangle \langle ax = ax'@ax'\#ax'' \rangle$ 
  have  $n' \notin \text{set } (\text{sourcenodes } (ax'\#ax''))$ 
    by(auto simp:sourcenodes-def)
  with  $\langle \text{inner-node } n' \rangle \langle \neg n' \text{ postdominates } \text{targetnode } ai \rangle$ 
     $\langle n' \notin \text{set } (\text{sourcenodes } (ax'\#ax'')) \rangle \langle \text{sourcenode } ax' = \text{sourcenode } ai \rangle$ 
     $\langle \forall z \in \text{set } ax''. \neg (\exists a''. \text{sourcenode } z = \text{sourcenode } a'' \wedge$ 
      valid-edge  $a'' \wedge \text{intra-kind}(\text{kind } a'') \wedge \neg n'$  postdominates targetnode a'')
     $\langle \text{valid-edge } ai \rangle \langle \text{intra-kind}(\text{kind } ai) \rangle \langle \neg \text{method-exit } n' \rangle$ 
     $\langle (-\text{Entry-}) -(ax\#ax')@ax'\#ax'' \rightarrow_i^* n' \rangle$ 
  have sourcenode  $ax'$  controls  $n'$ 
    by(fastforce intro!:which-node-intra-standard-control-dependence-source)
  hence CFG-node (sourcenode  $ax'$ )  $\rightarrow_{cd}$  CFG-node  $n'$ 
    by(fastforce intro:SDG-cdep-edge)
  from  $\langle (-\text{Entry-}) -(ax\#ax')@ax'\#ax'' \rightarrow_i^* n' \rangle$ 
  have  $\langle (-\text{Entry-}) -ax\#ax' \rightarrow_i^* \text{sourcenode } ax' \rangle$  and valid-edge  $ax'$ 
    by(auto intro:path-split simp:intra-path-def simp del:append-Cons)
  from  $\langle ax = ax'@ax'\#ax'' \rangle \langle as = (ax\#ax')@zs \rangle$ 
  have  $\text{length } (ax\#ax') < \text{length } as$  by simp
  from  $\langle \text{valid-edge } ax' \rangle$  have valid-node (sourcenode  $ax'$ ) by simp
  hence inner-node (sourcenode  $ax'$ )
  proof(cases sourcenode  $ax'$  rule:valid-node-cases)
    case Entry
      with  $\langle (-\text{Entry-}) -ax\#ax' \rightarrow_i^* \text{sourcenode } ax' \rangle$ 
      have  $\langle (-\text{Entry-}) -ax\#ax' \rightarrow^* (-\text{Entry-}) \rangle$  by(simp add:intra-path-def)
      hence False by(fastforce dest:path-Entry-target)
      thus ?thesis by simp
    next
      case Exit
        with  $\langle \text{valid-edge } ax' \rangle$  have False by(rule Exit-source)
        thus ?thesis by simp
    qed simp
  from  $\langle ax = ax'@ax'\#ax'' \rangle \langle (-\text{Entry-}) -ax\#ax \rightarrow_i^* n' \rangle$ 
  have intra-kind (kind  $ax'$ ) by(simp add:intra-path-def)
  have  $\neg \text{method-exit } (\text{sourcenode } ax')$ 
  proof
    assume method-exit (sourcenode  $ax'$ )
    thus False
    proof(rule method-exit-cases)
      assume sourcenode  $ax' = (-\text{Exit-})$ 

```

```

    with ⟨valid-edge ax'⟩ show False by(rule Exit-source)
  next
    fix x Q f p assume sourcenode ax' = sourcenode x
      and valid-edge x and kind x = Q↔pf
    from ⟨valid-edge x⟩ ⟨kind x = Q↔pf⟩ ⟨sourcenode ax' = sourcenode x⟩
      ⟨valid-edge ax'⟩ ⟨intra-kind (kind ax')⟩ show False
      by(fastforce dest:return-edges-only simp:intra-kind-def)
    qed
  qed
  with IH ⟨length (ax#asx') < length as⟩ ⟨(-Entry-) -ax#asx'→ι* sourcenode
ax'⟩
    ⟨inner-node (sourcenode ax')⟩
  obtain ns where CFG-node (-Entry-) cd-ns→d* CFG-node (sourcenode
ax')
    and ns ≠ []
    and ∀n'' ∈ set ns. parent-node n'' ∈ set(sourcenodes (ax#asx'))
    by blast
  from ⟨CFG-node (-Entry-) cd-ns→d* CFG-node (sourcenode ax')⟩
    ⟨CFG-node (sourcenode ax') →cd CFG-node n'⟩
  have CFG-node (-Entry-) cd-ns@[CFG-node (sourcenode ax')]→d* CFG-node
n'
    by(fastforce intro:cdSp-Append-cdep)
  from ⟨as = (ax#asx)@zs⟩ ⟨asx = asx'@ax'#asx''⟩
  have sourcenode ax' ∈ set(sourcenodes as) by(simp add:sourcenodes-def)
  with ⟨∀n'' ∈ set ns. parent-node n'' ∈ set(sourcenodes (ax#asx'))⟩
    ⟨as = (ax#asx)@zs⟩ ⟨asx = asx'@ax'#asx''⟩
  have ∀n'' ∈ set (ns@[CFG-node (sourcenode ax')]).
    parent-node n'' ∈ set(sourcenodes as)
    by(fastforce simp:sourcenodes-def)
  with ⟨CFG-node (-Entry-) cd-ns@[CFG-node (sourcenode ax')]→d* CFG-node
n'⟩
    show ?thesis by fastforce
  qed
  qed
  qed
  qed

```

lemma *in-proc-cdep-SDG-path*:

```

  assumes n -as→ι* n' and n ≠ n' and n' ≠ (-Exit-) and valid-edge a
  and kind a = Q:r→pfs and targetnode a = n
  obtains ns where CFG-node n cd-ns→d* CFG-node n'
  and ns ≠ [] and ∀n'' ∈ set ns. parent-node n'' ∈ set(sourcenodes as)
proof(atomize-elim)
  show ∃ns. CFG-node n cd-ns→d* CFG-node n' ∧
    ns ≠ [] ∧ (∀n'' ∈ set ns. parent-node n'' ∈ set (sourcenodes as))
  proof(cases ∀ax. valid-edge ax ∧ sourcenode ax = n' →
    ax ∉ get-return-edges a)
  case True

```

from $\langle n - as \rightarrow_i^* n' \rangle \langle n \neq n' \rangle \langle n' \neq (-Exit) \rangle$
 $\langle \forall ax. \text{valid-edge } ax \wedge \text{sourcenode } ax = n' \longrightarrow ax \notin \text{get-return-edges } a \rangle$
show $\exists ns. \text{CFG-node } n \text{ cd-ns} \rightarrow_d^* \text{CFG-node } n' \wedge ns \neq [] \wedge$
 $(\forall n'' \in \text{set } ns. \text{parent-node } n'' \in \text{set}(\text{sourcenodes } as))$
proof(*induct as arbitrary:n' rule:length-induct*)
fix $as \ n'$
assume $IH:\forall as'. \text{length } as' < \text{length } as \longrightarrow$
 $(\forall n''. n - as' \rightarrow_i^* n'' \longrightarrow n \neq n'' \longrightarrow n'' \neq (-Exit) \longrightarrow$
 $(\forall ax. \text{valid-edge } ax \wedge \text{sourcenode } ax = n'' \longrightarrow ax \notin \text{get-return-edges } a))$
 \longrightarrow
 $(\exists ns. \text{CFG-node } n \text{ cd-ns} \rightarrow_d^* \text{CFG-node } n'' \wedge ns \neq [] \wedge$
 $(\forall n'' \in \text{set } ns. \text{parent-node } n'' \in \text{set}(\text{sourcenodes } as')))$
and $n - as \rightarrow_i^* n'$ **and** $n \neq n'$ **and** $n' \neq (-Exit)$
and $\forall ax. \text{valid-edge } ax \wedge \text{sourcenode } ax = n' \longrightarrow ax \notin \text{get-return-edges } a$
show $\exists ns. \text{CFG-node } n \text{ cd-ns} \rightarrow_d^* \text{CFG-node } n' \wedge ns \neq [] \wedge$
 $(\forall n'' \in \text{set } ns. \text{parent-node } n'' \in \text{set}(\text{sourcenodes } as))$
proof(*cases method-exit n'*)
case *True*
thus *?thesis*
proof(*rule method-exit-cases*)
assume $n' = (-Exit)$
with $\langle n' \neq (-Exit) \rangle$ **have** *False by simp*
thus *?thesis by simp*
next
fix $a' \ Q' \ f' \ p'$
assume $n' = \text{sourcenode } a'$ **and** *valid-edge a'* **and** *kind a' = Q' \leftarrow_p f'*
from $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q:r \hookrightarrow_p fs \rangle$ **have** $\text{get-proc}(\text{targetnode } a) = p$
by(*rule get-proc-call*)
from $\langle n - as \rightarrow_i^* n' \rangle$ **have** $\text{get-proc } n = \text{get-proc } n'$
by(*rule intra-path-get-procs*)
with $\langle \text{get-proc}(\text{targetnode } a) = p \rangle \langle \text{targetnode } a = n \rangle$
have $\text{get-proc }(\text{targetnode } a) = \text{get-proc } n'$ **by** *simp*
from $\langle \text{valid-edge } a' \rangle \langle \text{kind } a' = Q' \leftarrow_p f' \rangle$
have $\text{get-proc }(\text{sourcenode } a') = p'$ **by**(*rule get-proc-return*)
with $\langle n' = \text{sourcenode } a' \rangle \langle \text{get-proc }(\text{targetnode } a) = \text{get-proc } n' \rangle$
 $\langle \text{get-proc }(\text{targetnode } a) = p \rangle$ **have** $p = p'$ **by** *simp*
with $\langle \text{valid-edge } a' \rangle \langle \text{kind } a' = Q' \leftarrow_p f' \rangle$
obtain ax **where** *valid-edge ax* **and** $\exists Q \ r \ fs. \text{kind } ax = Q:r \hookrightarrow_p fs$
and $a' \in \text{get-return-edges } ax$ **by**(*auto dest:return-needs-call*)
hence $\text{CFG-node }(\text{targetnode } ax) \rightarrow_{cd} \text{CFG-node }(\text{sourcenode } a')$
by(*fastforce intro:SDG-proc-entry-exit-cdep*)
with $\langle \text{valid-edge } ax \rangle$
have $\text{CFG-node }(\text{targetnode } ax) \text{ cd-} [] @ [\text{CFG-node }(\text{targetnode } ax)] \rightarrow_d^*$
 $\text{CFG-node }(\text{sourcenode } a')$
by(*fastforce intro:cdep-SDG-path.intros*)
from $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q:r \hookrightarrow_p fs \rangle \langle \text{valid-edge } ax \rangle$
 $\langle \exists Q \ r \ fs. \text{kind } ax = Q:r \hookrightarrow_p fs \rangle$ **have** $\text{targetnode } a = \text{targetnode } ax$
by(*fastforce intro:same-proc-call-unique-target*)
from $\langle n - as \rightarrow_i^* n' \rangle \langle n \neq n' \rangle$

```

have  $as \neq []$  by (fastforce elim:path.cases simp:intra-path-def)
with  $\langle n - as \rightarrow_{\iota} * n' \rangle$  have  $hd (sourcenodes as) = n$ 
  by (fastforce intro:path-sourcenode simp:intra-path-def)
moreover
from  $\langle as \neq [] \rangle$  have  $hd (sourcenodes as) \in set (sourcenodes as)$ 
  by (fastforce intro:hd-in-set simp:sourcenodes-def)
ultimately have  $n \in set (sourcenodes as)$  by simp
with  $\langle n' = sourcenode a' \rangle$   $\langle targetnode a = targetnode ax \rangle$ 
   $\langle targetnode a = n \rangle$ 
   $\langle CFG-node (targetnode ax) cd - [] @ [CFG-node (targetnode ax)] \rightarrow_d * \rangle$ 
   $\langle CFG-node (sourcenode a') \rangle$ 
show ?thesis by fastforce
qed
next
case False
from  $\langle valid-edge a \rangle$   $\langle kind a = Q:r \hookrightarrow pfs \rangle$  obtain  $a'$ 
  where  $a' \in get-return-edges a$ 
  by (fastforce dest:get-return-edge-call)
with  $\langle valid-edge a \rangle$   $\langle kind a = Q:r \hookrightarrow pfs \rangle$  obtain  $Q' f'$  where  $kind a' =$ 
 $Q' \hookrightarrow pf'$ 
  by (fastforce dest!:call-return-edges)
with  $\langle valid-edge a \rangle$   $\langle kind a = Q:r \hookrightarrow pfs \rangle$   $\langle a' \in get-return-edges a \rangle$  obtain  $a''$ 
  where  $valid-edge a''$  and  $sourcenode a'' = targetnode a$ 
  and  $targetnode a'' = sourcenode a'$  and  $kind a'' = (\lambda cf. False)_{\checkmark}$ 
  by  $-(drule\ intra-proc-additional-edge, auto)$ 
from  $\langle valid-edge a \rangle$   $\langle a' \in get-return-edges a \rangle$  have  $valid-edge a'$ 
  by (rule get-return-edges-valid)
have  $\exists ax asx zs. n - ax \# asx \rightarrow_{\iota} * n' \wedge n' \notin set (sourcenodes (ax \# asx)) \wedge$ 
 $as = (ax \# asx) @ zs$ 
proof (cases  $n' \in set (sourcenodes as)$ )
  case True
  hence  $\exists n'' \in set (sourcenodes as). n' = n''$  by simp
  then obtain  $ns' ns''$  where  $sourcenodes as = ns' @ n' \# ns''$ 
  and  $\forall n'' \in set ns'. n' \neq n''$ 
  by (fastforce elim!:split-list-first-propE)
  from  $\langle sourcenodes as = ns' @ n' \# ns'' \rangle$  obtain  $xs ys ax$ 
  where  $sourcenodes xs = ns'$  and  $as = xs @ ax \# ys$ 
  and  $sourcenode ax = n'$ 
  by (fastforce elim:map-append-append-maps simp:sourcenodes-def)
  from  $\langle \forall n'' \in set ns'. n' \neq n'' \rangle$   $\langle sourcenodes xs = ns' \rangle$ 
  have  $n' \notin set (sourcenodes xs)$  by fastforce
  from  $\langle n - as \rightarrow_{\iota} * n' \rangle$   $\langle as = xs @ ax \# ys \rangle$  have  $n - xs @ ax \# ys \rightarrow_{\iota} * n'$  by
simp
with  $\langle sourcenode ax = n' \rangle$  have  $n - xs \rightarrow_{\iota} * n'$ 
  by (fastforce dest:path-split simp:intra-path-def)
with  $\langle n \neq n' \rangle$  have  $xs \neq []$  by (fastforce simp:intra-path-def)
with  $\langle n' \notin set (sourcenodes xs) \rangle$   $\langle n - xs \rightarrow_{\iota} * n' \rangle$   $\langle as = xs @ ax \# ys \rangle$  show
?thesis
  by (cases xs) auto

```

```

next
  case False
  with  $\langle n - as \rightarrow_i * n' \rangle \langle n \neq n' \rangle$ 
  show ?thesis by (cases as) (auto simp: intra-path-def)
qed
then obtain ax asx zs where  $n - ax \# asx \rightarrow_i * n'$ 
  and  $n' \notin \text{set}(\text{sourcenodes}(ax \# asx))$  and  $as = (ax \# asx) @ zs$  by blast
from  $\langle n - ax \# asx \rightarrow_i * n' \rangle \langle n' \neq (-Exit-) \rangle$  have inner-node  $n'$ 
  by (fastforce intro:path-valid-node simp:inner-node-def intra-path-def)
from  $\langle \text{valid-edge } a \rangle \langle \text{targetnode } a = n \rangle$  have valid-node  $n$  by fastforce
show ?thesis
proof (cases  $\forall a' a'' . a' \in \text{set } asx \wedge \text{sourcenode } a' = \text{sourcenode } a'' \wedge$ 
   $\text{valid-edge } a'' \wedge \text{intra-kind}(\text{kind } a'') \longrightarrow$ 
   $n' \text{ postdominates targetnode } a''$ )
  case True
  from  $\langle \text{targetnode } a = n \rangle \langle \text{sourcenode } a'' = \text{targetnode } a \rangle$ 
   $\langle \text{kind } a'' = (\lambda cf . \text{False})_{\surd} \rangle$ 
  have sourcenode  $a'' = n$  and intra-kind (kind  $a''$ )
  by (auto simp: intra-kind-def)
  { fix  $as'$  assume  $\text{targetnode } a'' - as' \rightarrow_i * n'$ 
    from  $\langle \text{valid-edge } a' \rangle \langle \text{targetnode } a'' = \text{sourcenode } a' \rangle$ 
     $\langle a' \in \text{get-return-edges } a \rangle$ 
     $\langle \forall ax . \text{valid-edge } ax \wedge \text{sourcenode } ax = n' \longrightarrow ax \notin \text{get-return-edges } a \rangle$ 
    have  $\text{targetnode } a'' \neq n'$  by fastforce
    with  $\langle \text{targetnode } a'' - as' \rightarrow_i * n' \rangle$  obtain  $ax'$  where valid-edge  $ax'$ 
      and  $\text{targetnode } a'' = \text{sourcenode } ax'$  and intra-kind (kind  $ax'$ )
      by (clarsimp simp: intra-path-def) (erule path.cases, fastforce+)
    from  $\langle \text{valid-edge } a' \rangle \langle \text{kind } a' = Q' \leftrightarrow_{pf} \rangle \langle \text{valid-edge } ax' \rangle$ 
     $\langle \text{targetnode } a'' = \text{sourcenode } a' \rangle \langle \text{targetnode } a'' = \text{sourcenode } ax' \rangle$ 
     $\langle \text{intra-kind}(\text{kind } ax') \rangle$ 
    have False by (fastforce dest:return-edges-only simp:intra-kind-def) }
  hence  $\neg n'$  postdominates targetnode  $a''$ 
  by (fastforce elim:postdominate-implies-inner-path)
  from  $\langle n - ax \# asx \rightarrow_i * n' \rangle$  have sourcenode  $ax = n$ 
  by (auto intro:path-split-Cons simp:intra-path-def)
  from  $\langle n - ax \# asx \rightarrow_i * n' \rangle$  have  $n - [] @ ax \# asx \rightarrow_i * n'$  by simp
  from this  $\langle \text{sourcenode } a'' = n \rangle \langle \text{sourcenode } ax = n \rangle$  True
   $\langle n' \notin \text{set}(\text{sourcenodes}(ax \# asx)) \rangle \langle \text{valid-edge } a'' \rangle \langle \text{intra-kind}(\text{kind } a'') \rangle$ 
   $\langle \text{inner-node } n' \rangle \langle \neg \text{method-exit } n' \rangle \langle \neg n' \text{ postdominates targetnode } a'' \rangle$ 
  have n controls  $n'$ 
  by (fastforce intro!:which-node-intra-standard-control-dependence-source)
  hence CFG-node  $n \longrightarrow_{cd}$  CFG-node  $n'$ 
  by (fastforce intro:SDG-cdep-edge)
  with  $\langle \text{valid-node } n \rangle$  have CFG-node  $n \text{ cd-} [] @ [CFG-node } n] \rightarrow_d * \text{CFG-node}$ 
   $n'$ 
  by (fastforce intro:cdSp-Append-cdep cdSp-Nil)
  moreover
  from  $as = (ax \# asx) @ zs$   $\langle \text{sourcenode } ax = n \rangle$  have  $n \in \text{set}(\text{sourcenodes}$ 
   $as)$ 

```



```

    by(simp add:sourcenodes-def)
  ultimately show ?thesis by fastforce
next
case False
hence  $\exists a' \in \text{set } asx. \exists a''. \text{sourcenode } a' = \text{sourcenode } a'' \wedge$ 
  valid-edge  $a'' \wedge \text{intra-kind}(\text{kind } a'') \wedge$ 
   $\neg n' \text{ postdominates targetnode } a''$ 
  by fastforce
then obtain  $ax' asx' asx''$  where  $asx = asx' @ ax' \# asx'' \wedge$ 
   $(\exists a''. \text{sourcenode } ax' = \text{sourcenode } a'' \wedge \text{valid-edge } a'' \wedge$ 
   $\text{intra-kind}(\text{kind } a'') \wedge \neg n' \text{ postdominates targetnode } a'') \wedge$ 
   $(\forall z \in \text{set } asx''. \neg (\exists a''. \text{sourcenode } z = \text{sourcenode } a'' \wedge$ 
   $\text{valid-edge } a'' \wedge \text{intra-kind}(\text{kind } a'') \wedge$ 
   $\neg n' \text{ postdominates targetnode } a''))$ 
  by(blast elim!:split-list-last-propE)
then obtain  $ai$  where  $asx = asx' @ ax' \# asx''$ 
  and  $\text{sourcenode } ax' = \text{sourcenode } ai$ 
  and  $\text{valid-edge } ai$  and  $\text{intra-kind}(\text{kind } ai)$ 
  and  $\neg n' \text{ postdominates targetnode } ai$ 
  and  $\forall z \in \text{set } asx''. \neg (\exists a''. \text{sourcenode } z = \text{sourcenode } a'' \wedge$ 
   $\text{valid-edge } a'' \wedge \text{intra-kind}(\text{kind } a'') \wedge$ 
   $\neg n' \text{ postdominates targetnode } a'')$ 
  by blast
from  $\langle asx = asx' @ ax' \# asx'' \rangle \langle n - ax \# asx \rightarrow_i^* n' \rangle$ 
have  $n - (ax \# asx') @ ax' \# asx'' \rightarrow_i^* n'$  by simp
from  $\langle n' \notin \text{set } (\text{sourcenodes } (ax \# asx)) \rangle \langle asx = asx' @ ax' \# asx'' \rangle$ 
have  $n' \notin \text{set } (\text{sourcenodes } (ax' \# asx''))$ 
  by(auto simp:sourcenodes-def)
with  $\langle \text{inner-node } n' \rangle \langle \neg n' \text{ postdominates targetnode } ai \rangle$ 
 $\langle n - (ax \# asx') @ ax' \# asx'' \rightarrow_i^* n' \rangle \langle \text{sourcenode } ax' = \text{sourcenode } ai \rangle$ 
 $\langle \forall z \in \text{set } asx''. \neg (\exists a''. \text{sourcenode } z = \text{sourcenode } a'' \wedge$ 
   $\text{valid-edge } a'' \wedge \text{intra-kind}(\text{kind } a'') \wedge$ 
   $\neg n' \text{ postdominates targetnode } a'') \rangle$ 
 $\langle \text{valid-edge } ai \rangle \langle \text{intra-kind}(\text{kind } ai) \rangle \langle \neg \text{method-exit } n' \rangle$ 
have  $\text{sourcenode } ax' \text{ controls } n'$ 
  by(fastforce intro!:which-node-intra-standard-control-dependence-source)
hence  $\text{CFG-node } (\text{sourcenode } ax') \rightarrow_{cd} \text{CFG-node } n'$ 
  by(fastforce intro:SDG-cdep-edge)
from  $\langle n - (ax \# asx') @ ax' \# asx'' \rightarrow_i^* n' \rangle$ 
have  $n - ax \# asx' \rightarrow_i^* \text{sourcenode } ax'$  and  $\text{valid-edge } ax'$ 
  by(auto intro:path-split simp:intra-path-def simp del:append-Cons)
from  $\langle asx = asx' @ ax' \# asx'' \rangle \langle as = (ax \# asx) @ zs \rangle$ 
have  $\text{length } (ax \# asx') < \text{length } as$  by simp
from  $\langle as = (ax \# asx) @ zs \rangle \langle asx = asx' @ ax' \# asx'' \rangle$ 
have  $\text{sourcenode } ax' \in \text{set}(\text{sourcenodes } as)$  by(simp add:sourcenodes-def)
show ?thesis
proof(cases  $n = \text{sourcenode } ax'$ )
case True
with  $\langle \text{CFG-node } (\text{sourcenode } ax') \rightarrow_{cd} \text{CFG-node } n' \rangle \langle \text{valid-edge } ax' \rangle$ 

```

```

have CFG-node  $n$   $cd-\square@[CFG-node\ n]\rightarrow_d^* CFG-node\ n'$ 
  by(fastforce intro:cdSp-Append-cdep cdSp-Nil)
  with  $\langle sourcenode\ ax' \in set(sourcenodes\ as) \rangle$  True show ?thesis by
fastforce
next
case False
from  $\langle valid-edge\ ax' \rangle$  have sourcenode  $ax' \neq (-Exit-)$ 
  by  $\neg(rule\ ccontr,fastforce\ elim!:Exit-source)$ 
from  $\langle n - ax \# asx' \rightarrow_l^* sourcenode\ ax' \rangle$  have  $n = sourcenode\ ax$ 
  by(fastforce intro:path-split-Cons simp:intra-path-def)
show ?thesis
proof(cases  $\forall ax. valid-edge\ ax \wedge sourcenode\ ax = sourcenode\ ax' \longrightarrow$ 
   $ax \notin get-return-edges\ a$ )
  case True
from  $\langle asx = asx' @ ax' \# asx'' \rangle$   $\langle n - ax \# asx \rightarrow_l^* n' \rangle$ 
have intra-kind (kind  $ax'$ ) by(simp add:intra-path-def)
have  $\neg method-exit\ (sourcenode\ ax')$ 
proof
  assume method-exit (sourcenode  $ax'$ )
  thus False
proof(rule method-exit-cases)
  assume sourcenode  $ax' = (-Exit-)$ 
  with  $\langle valid-edge\ ax' \rangle$  show False by(rule Exit-source)
next
  fix  $x\ Q\ f\ p$  assume sourcenode  $ax' = sourcenode\ x$ 
  and valid-edge  $x$  and kind  $x = Q \leftarrow pf$ 
from  $\langle valid-edge\ x \rangle$   $\langle kind\ x = Q \leftarrow pf \rangle$   $\langle sourcenode\ ax' = sourcenode$ 
x
   $\langle valid-edge\ ax' \rangle$   $\langle intra-kind\ (kind\ ax') \rangle$  show False
  by(fastforce dest:return-edges-only simp:intra-kind-def)
qed
qed
with IH  $\langle length\ (ax \# asx') < length\ as \rangle$   $\langle n - ax \# asx' \rightarrow_l^* sourcenode$ 
ax'
   $\langle n \neq sourcenode\ ax' \rangle$   $\langle sourcenode\ ax' \neq (-Exit-) \rangle$  True
obtain  $ns$  where CFG-node  $n\ cd-ns \rightarrow_d^* CFG-node\ (sourcenode\ ax')$ 
  and  $ns \neq []$ 
  and  $\forall n'' \in set\ ns. parent-node\ n'' \in set\ (sourcenodes\ (ax \# asx'))$ 
  by blast
from  $\langle CFG-node\ n\ cd-ns \rightarrow_d^* CFG-node\ (sourcenode\ ax') \rangle$ 
   $\langle CFG-node\ (sourcenode\ ax') \longrightarrow_{cd} CFG-node\ n' \rangle$ 
have CFG-node  $n\ cd-ns @ [CFG-node\ (sourcenode\ ax')] \rightarrow_d^* CFG-node$ 
n'
  by(rule cdSp-Append-cdep)
moreover
from  $\langle \forall n'' \in set\ ns. parent-node\ n'' \in set\ (sourcenodes\ (ax \# asx')) \rangle$ 
   $\langle asx = asx' @ ax' \# asx'' \rangle$   $\langle as = (ax \# asx) @ zs \rangle$ 
   $\langle sourcenode\ ax' \in set(sourcenodes\ as) \rangle$ 
have  $\forall n'' \in set\ (ns @ [CFG-node\ (sourcenode\ ax')])$ .

```

```

      parent-node  $n'' \in \text{set}(\text{sourcenodes } as)$ 
      by(fastforce simp:sourcenodes-def)
      ultimately show ?thesis by fastforce
    next
      case False
      then obtain  $ai'$  where valid-edge  $ai'$ 
        and sourcenode  $ai' = \text{sourcenode } ax'$ 
        and  $ai' \in \text{get-return-edges } a$  by blast
      with ⟨valid-edge  $a$ ⟩ ⟨kind  $a = Q:r \hookrightarrow pfs$ ⟩ ⟨targetnode  $a = n$ ⟩
      have CFG-node  $n \xrightarrow{cd} \text{CFG-node}(\text{sourcenode } ax')$ 
        by(fastforce intro!:SDG-proc-entry-exit-cdep[of - - - - -  $ai'$ ])
      with ⟨valid-node  $n$ ⟩
      have CFG-node  $n \text{ cd-} [] @ [ \text{CFG-node } n ] \rightarrow_d^* \text{CFG-node}(\text{sourcenode } ax')$ 
      by(fastforce intro:cdSp-Append-cdep cdSp-Nil)
      with ⟨CFG-node  $(\text{sourcenode } ax') \xrightarrow{cd} \text{CFG-node } n'$ ⟩
      have CFG-node  $n \text{ cd-} [ \text{CFG-node } n ] @ [ \text{CFG-node}(\text{sourcenode } ax') ] \rightarrow_d^*$ 
      CFG-node  $n'$ 
      by(fastforce intro:cdSp-Append-cdep)
      moreover
      from ⟨sourcenode  $ax' \in \text{set}(\text{sourcenodes } as)$ ⟩ ⟨ $n = \text{sourcenode } ax$ ⟩
        ⟨ $as = (ax \# asx) @ zs$ ⟩
      have  $\forall n'' \in \text{set}([ \text{CFG-node } n ] @ [ \text{CFG-node}(\text{sourcenode } ax') ])$ .
        parent-node  $n'' \in \text{set}(\text{sourcenodes } as)$ 
        by(fastforce simp:sourcenodes-def)
      ultimately show ?thesis by fastforce
    qed
  qed
  qed
  qed
  next
    case False
    then obtain  $a'$  where valid-edge  $a'$  and sourcenode  $a' = n'$ 
      and  $a' \in \text{get-return-edges } a$  by auto
    with ⟨valid-edge  $a$ ⟩ ⟨kind  $a = Q:r \hookrightarrow pfs$ ⟩ ⟨targetnode  $a = n$ ⟩
    have CFG-node  $n \xrightarrow{cd} \text{CFG-node } n'$  by(fastforce intro:SDG-proc-entry-exit-cdep)
    with ⟨valid-edge  $a$ ⟩ ⟨targetnode  $a = n$ ⟩ [THEN sym]
    have CFG-node  $n \text{ cd-} [] @ [ \text{CFG-node } n ] \rightarrow_d^* \text{CFG-node } n'$ 
      by(fastforce intro:cdep-SDG-path.intros)
    from ⟨ $n - as \rightarrow_{\iota}^* n'$ ⟩ ⟨ $n \neq n'$ ⟩ have  $as \neq []$ 
      by(fastforce elim:path.cases simp:intra-path-def)
    with ⟨ $n - as \rightarrow_{\iota}^* n'$ ⟩ have  $hd(\text{sourcenodes } as) = n$ 
      by(fastforce intro:path-sourcenode simp:intra-path-def)
    with ⟨ $as \neq []$ ⟩ have  $n \in \text{set}(\text{sourcenodes } as)$ 
      by(fastforce intro:hd-in-set simp:sourcenodes-def)
    with ⟨CFG-node  $n \text{ cd-} [] @ [ \text{CFG-node } n ] \rightarrow_d^* \text{CFG-node } n'$ ⟩
    show ?thesis by auto

```

qed
qed

1.8.7 Paths consisting of calls and control dependences

inductive *call-cdep-SDG-path* ::

'node *SDG-node* \Rightarrow 'node *SDG-node list* \Rightarrow 'node *SDG-node* \Rightarrow bool
(- *cc* \rightarrow_d^* - [51,0,0] 80)

where *ccSp-Nil*:

valid-SDG-node *n* \Longrightarrow *n cc* \rightarrow_d^* *n*

| *ccSp-Append-cdep*:

$\llbracket n \text{ cc-ns} \rightarrow_d^* n''; n'' \rightarrow_{cd} n' \rrbracket \Longrightarrow n \text{ cc-ns} @ [n''] \rightarrow_d^* n'$

| *ccSp-Append-call*:

$\llbracket n \text{ cc-ns} \rightarrow_d^* n''; n'' -p \rightarrow_{call} n' \rrbracket \Longrightarrow n \text{ cc-ns} @ [n''] \rightarrow_d^* n'$

lemma *cc-SDG-path-Append*:

$\llbracket n'' \text{ cc-ns}' \rightarrow_d^* n'; n \text{ cc-ns} \rightarrow_d^* n'' \rrbracket \Longrightarrow n \text{ cc-ns} @ \text{ns}' \rightarrow_d^* n'$

by (*induct rule:call-cdep-SDG-path.induct*,

auto intro:call-cdep-SDG-path.intros simp:append-assoc [THEN sym]
simp del:append-assoc)

lemma *cdep-SDG-path-cc-SDG-path*:

n cd-ns \rightarrow_d^* *n'* \Longrightarrow *n cc-ns* \rightarrow_d^* *n'*

by (*induct rule:cdep-SDG-path.induct, auto intro:call-cdep-SDG-path.intros*)

lemma *Entry-cc-SDG-path-to-inner-node*:

assumes *valid-SDG-node* *n* **and** *parent-node* *n* \neq (-*Exit*-)

obtains *ns* **where** *CFG-node* (-*Entry*-) *cc-ns* \rightarrow_d^* *n*

proof (*atomize-elim*)

obtain *m* **where** *m* = *parent-node* *n* **by** *simp*

from $\langle \text{valid-SDG-node } n \rangle$ **have** *valid-node* (*parent-node* *n*)

by (*rule valid-SDG-CFG-node*)

thus $\exists \text{ns. CFG-node } (-\text{Entry-}) \text{ cc-ns} \rightarrow_d^* n$

proof (*cases parent-node n rule:valid-node-cases*)

case *Entry*

with $\langle \text{valid-SDG-node } n \rangle$ **have** *n* = *CFG-node* (-*Entry*-)

by (*rule valid-SDG-node-parent-Entry*)

with $\langle \text{valid-SDG-node } n \rangle$ **show** *?thesis* **by** (*fastforce intro:ccSp-Nil*)

next

case *Exit*

with $\langle \text{parent-node } n \neq (-\text{Exit-}) \rangle$ **have** *False* **by** *simp*

thus *?thesis* **by** *simp*

next

case *inner*

with $\langle m = \text{parent-node } n \rangle$ **obtain** asx **where** $\langle (-\text{Entry-}) -asx \rightarrow_{\sqrt{*}} m \rangle$
by $\langle \text{fastforce } \text{dest:Entry-path } \text{inner-is-valid} \rangle$
then obtain as **where** $\langle (-\text{Entry-}) -as \rightarrow_{\sqrt{*}} m \rangle$
and $\forall a' \in \text{set } as. \text{intra-kind}(\text{kind } a') \vee (\exists Q \ r \ p \ fs. \text{kind } a' = Q:r \hookrightarrow_p fs)$
by $\langle \text{erule } \text{valid-Entry-path-ascending-path, fastforce} \rangle$
from $\langle \text{inner-node } (\text{parent-node } n) \rangle \langle m = \text{parent-node } n \rangle$
have $\text{inner-node } m$ **by** simp
with $\langle (-\text{Entry-}) -as \rightarrow_{\sqrt{*}} m \rangle \langle m = \text{parent-node } n \rangle \langle \text{valid-SDG-node } n \rangle$
 $\langle \forall a' \in \text{set } as. \text{intra-kind}(\text{kind } a') \vee (\exists Q \ r \ p \ fs. \text{kind } a' = Q:r \hookrightarrow_p fs) \rangle$
show $?thesis$
proof $\langle \text{induct } as \ \text{arbitrary:m } n \ \text{rule:length-induct} \rangle$
fix $as \ m \ n$
assume $IH:\forall as'. \text{length } as' < \text{length } as \longrightarrow$
 $\langle \forall m'. \langle (-\text{Entry-}) -as' \rightarrow_{\sqrt{*}} m' \rangle \longrightarrow$
 $\langle \forall n'. m' = \text{parent-node } n' \longrightarrow \text{valid-SDG-node } n' \longrightarrow$
 $\langle \forall a' \in \text{set } as'. \text{intra-kind}(\text{kind } a') \vee (\exists Q \ r \ p \ fs. \text{kind } a' = Q:r \hookrightarrow_p fs) \rangle \longrightarrow$
 $\text{inner-node } m' \longrightarrow \langle \exists ns. \text{CFG-node } (-\text{Entry-}) \ \text{cc-ns} \rightarrow_{d^*} n' \rangle \rangle$
and $\langle (-\text{Entry-}) -as \rightarrow_{\sqrt{*}} m \rangle$
and $m = \text{parent-node } n$ **and** $\text{valid-SDG-node } n$ **and** $\text{inner-node } m$
and $\forall a' \in \text{set } as. \text{intra-kind}(\text{kind } a') \vee (\exists Q \ r \ p \ fs. \text{kind } a' = Q:r \hookrightarrow_p fs)$
show $\exists ns. \text{CFG-node } (-\text{Entry-}) \ \text{cc-ns} \rightarrow_{d^*} n$
proof $\langle \text{cases } \forall a' \in \text{set } as. \text{intra-kind}(\text{kind } a') \rangle$
case True
with $\langle (-\text{Entry-}) -as \rightarrow_{\sqrt{*}} m \rangle$ **have** $\langle (-\text{Entry-}) -as \rightarrow_{\iota^*} m \rangle$
by $\langle \text{fastforce } \text{simp:intra-path-def } \text{vp-def} \rangle$
have $\neg \text{method-exit } m$
proof
assume $\text{method-exit } m$
thus False
proof $\langle \text{rule } \text{method-exit-cases} \rangle$
assume $m = (-\text{Exit-})$
with $\langle \text{inner-node } m \rangle$ **show** False **by** $\langle \text{simp } \text{add:inner-node-def} \rangle$
next
fix $a \ Q \ f \ p$ **assume** $m = \text{sourcenode } a$ **and** $\text{valid-edge } a$
and $\text{kind } a = Q \leftrightarrow_p f$
from $\langle (-\text{Entry-}) -as \rightarrow_{\iota^*} m \rangle$ **have** $\text{get-proc } m = \text{Main}$
by $\langle \text{fastforce } \text{dest:intra-path-get-procs } \text{simp:get-proc-Entry} \rangle$
from $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q \leftrightarrow_p f \rangle$
have $\text{get-proc } (\text{sourcenode } a) = p$ **by** $\langle \text{rule } \text{get-proc-return} \rangle$
with $\langle \text{get-proc } m = \text{Main} \rangle \langle m = \text{sourcenode } a \rangle$ **have** $p = \text{Main}$ **by** simp
with $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q \leftrightarrow_p f \rangle$ **show** False
by $\langle \text{fastforce } \text{intro:Main-no-return-source} \rangle$
qed
qed
with $\langle \text{inner-node } m \rangle \langle (-\text{Entry-}) -as \rightarrow_{\iota^*} m \rangle$
obtain ns **where** $\text{CFG-node } (-\text{Entry-}) \ \text{cd-ns} \rightarrow_{d^*} \text{CFG-node } m$
and $ns \neq []$ **and** $\forall n'' \in \text{set } ns. \text{parent-node } n'' \in \text{set}(\text{sourcenodes } as)$
by $\langle \text{erule } \text{Entry-cdep-SDG-path} \rangle$
then obtain n' **where** $n' \longrightarrow_{cd} \text{CFG-node } m$

and $\text{parent-node } n' \in \text{set}(\text{sourcenodes } as)$
by $-(\text{erule } \text{cdep-SDG-path.cases,auto})$
from $\langle \text{parent-node } n' \in \text{set}(\text{sourcenodes } as) \rangle$ **obtain** $ms \ ms'$
where $\text{sourcenodes } as = ms @ (\text{parent-node } n') \# ms'$
by $(\text{fastforce } \text{dest:split-list } \text{simp:sourcenodes-def})$
then obtain $as' \ a \ as''$ **where** $ms = \text{sourcenodes } as'$
and $ms' = \text{sourcenodes } as''$ **and** $as = as' @ a \# as''$
and $\text{parent-node } n' = \text{sourcenode } a$
by $(\text{fastforce } \text{elim:map-append-append-maps } \text{simp:sourcenodes-def})$
with $\langle (-\text{Entry-}) -as \rightarrow_i^* m \rangle$ **have** $\langle (-\text{Entry-}) -as' \rightarrow_i^* \text{parent-node } n' \rangle$
by $(\text{fastforce } \text{intro:path-split } \text{simp:intra-path-def})$
from $\langle n' \rightarrow_{cd} \text{CFG-node } m \rangle$ **have** $\text{valid-SDG-node } n'$
by $(\text{rule } \text{SDG-edge-valid-SDG-node})$
hence n' -cases:
 $n' = \text{CFG-node } (\text{parent-node } n') \vee \text{CFG-node } (\text{parent-node } n') \rightarrow_{cd} n'$
by $(\text{rule } \text{valid-SDG-node-cases})$
show $?thesis$
proof $(\text{cases } as' = [])$
case True
with $\langle (-\text{Entry-}) -as' \rightarrow_i^* \text{parent-node } n' \rangle$ **have** $\text{parent-node } n' = (-\text{Entry-})$
by $(\text{fastforce } \text{simp:intra-path-def})$
from n' -cases **have** $\exists ns. \text{CFG-node } (-\text{Entry-}) \text{ cd-} ns \rightarrow_d^* \text{CFG-node } m$
proof
assume $n' = \text{CFG-node } (\text{parent-node } n')$
with $\langle n' \rightarrow_{cd} \text{CFG-node } m \rangle$ $\langle \text{parent-node } n' = (-\text{Entry-}) \rangle$
have $\text{CFG-node } (-\text{Entry-}) \text{ cd-} [] @ [\text{CFG-node } (-\text{Entry-})] \rightarrow_d^* \text{CFG-node } m$
by $-(\text{rule } \text{cdSp-Append-cdep,rule } \text{cdSp-Nil,auto})$
thus $?thesis$ **by** fastforce
next
assume $\text{CFG-node } (\text{parent-node } n') \rightarrow_{cd} n'$
with $\langle \text{parent-node } n' = (-\text{Entry-}) \rangle$
have $\text{CFG-node } (-\text{Entry-}) \text{ cd-} [] @ [\text{CFG-node } (-\text{Entry-})] \rightarrow_d^* n'$
by $-(\text{rule } \text{cdSp-Append-cdep,rule } \text{cdSp-Nil,auto})$
with $\langle n' \rightarrow_{cd} \text{CFG-node } m \rangle$
have $\text{CFG-node } (-\text{Entry-}) \text{ cd-} [\text{CFG-node } (-\text{Entry-})] @ [n'] \rightarrow_d^* \text{CFG-node } m$
by $(\text{fastforce } \text{intro:cdSp-Append-cdep})$
thus $?thesis$ **by** fastforce
qed
then obtain ns **where** $\text{CFG-node } (-\text{Entry-}) \text{ cc-} ns \rightarrow_d^* \text{CFG-node } m$
by $(\text{fastforce } \text{intro:cdep-SDG-path-cc-SDG-path})$
show $?thesis$
proof $(\text{cases } n = \text{CFG-node } m)$
case True
with $\langle \text{CFG-node } (-\text{Entry-}) \text{ cc-} ns \rightarrow_d^* \text{CFG-node } m \rangle$
show $?thesis$ **by** fastforce
next
case False

```

with ⟨inner-node m⟩ ⟨valid-SDG-node n⟩ ⟨m = parent-node n⟩
have CFG-node m  $\longrightarrow_{cd}$  n
  by (fastforce intro:SDG-parent-cdep-edge inner-is-valid)
with ⟨CFG-node (-Entry-) cc-ns  $\rightarrow_d^*$  CFG-node m⟩
have CFG-node (-Entry-) cc-ns@[CFG-node m] $\rightarrow_d^*$  n
  by (fastforce intro:ccSp-Append-cdep)
thus ?thesis by fastforce
qed
next
  case False
    with ⟨as = as'@a#as''⟩ have length as' < length as by simp
  from ⟨(-Entry-) -as' $\rightarrow_{i^*}$  parent-node n'⟩ have valid-node (parent-node n')
    by (fastforce intro:path-valid-node simp:intra-path-def)
  hence inner-node (parent-node n')
  proof (cases parent-node n' rule:valid-node-cases)
    case Entry
      with ⟨(-Entry-) -as' $\rightarrow_{i^*}$  (parent-node n')⟩
      have (-Entry-) -as' $\rightarrow_*$  (-Entry-) by (fastforce simp:intra-path-def)
      with False have False by fastforce
      thus ?thesis by simp
    next
      case Exit
        with ⟨n'  $\longrightarrow_{cd}$  CFG-node m⟩ have n' = CFG-node (-Exit-)
        by -(rule valid-SDG-node-parent-Exit, erule SDG-edge-valid-SDG-node, simp)
        with ⟨n'  $\longrightarrow_{cd}$  CFG-node m⟩ Exit have False
          by simp (erule Exit-no-SDG-edge-source)
        thus ?thesis by simp
      next
        case inner
          thus ?thesis by simp
    qed
  from ⟨valid-node (parent-node n')⟩
  have valid-SDG-node (CFG-node (parent-node n')) by simp
  from ⟨(-Entry-) -as' $\rightarrow_{i^*}$  (parent-node n')⟩
  have (-Entry-) -as' $\rightarrow_{\sqrt{*}}$  (parent-node n')
    by (rule intra-path-vp)
  from ⟨ $\forall a' \in \text{set } as. \text{intra-kind}(\text{kind } a') \vee (\exists Q r p fs. \text{kind } a' = Q:r \hookrightarrow_p fs)$ ⟩
    ⟨as = as'@a#as''⟩
  have  $\forall a' \in \text{set } as'. \text{intra-kind}(\text{kind } a') \vee (\exists Q r p fs. \text{kind } a' = Q:r \hookrightarrow_p fs)$ 
    by auto
  with IH ⟨length as' < length as⟩ ⟨(-Entry-) -as' $\rightarrow_{\sqrt{*}}$  (parent-node n')⟩
    ⟨valid-SDG-node (CFG-node (parent-node n'))⟩ ⟨inner-node (parent-node
n')⟩
  obtain ns where CFG-node (-Entry-) cc-ns  $\rightarrow_d^*$  CFG-node (parent-node
n')
    apply (erule-tac x=as' in allE) apply clarsimp
    apply (erule-tac x=(parent-node n') in allE) apply clarsimp
    apply (erule-tac x=CFG-node (parent-node n') in allE) by clarsimp
  from n'-cases have  $\exists ns. \text{CFG-node} (-\text{Entry}-) \text{cc-ns} \rightarrow_d^* n'$ 

```

```

proof
  assume  $n' = \text{CFG-node } (\text{parent-node } n')$ 
  with  $\langle \text{CFG-node } (-\text{Entry-}) \text{ cc-ns} \rightarrow_d^* \text{ CFG-node } (\text{parent-node } n') \rangle$ 
  show  $?thesis$  by fastforce
next
  assume  $\text{CFG-node } (\text{parent-node } n') \rightarrow_{cd} n'$ 
  with  $\langle \text{CFG-node } (-\text{Entry-}) \text{ cc-ns} \rightarrow_d^* \text{ CFG-node } (\text{parent-node } n') \rangle$ 
  have  $\text{CFG-node } (-\text{Entry-}) \text{ cc-ns} @ [\text{CFG-node } (\text{parent-node } n')] \rightarrow_d^* n'$ 
    by (fastforce intro:ccSp-Append-cdep)
  thus  $?thesis$  by fastforce
qed
then obtain  $ns'$  where  $\text{CFG-node } (-\text{Entry-}) \text{ cc-ns}' \rightarrow_d^* n'$  by blast
with  $\langle n' \rightarrow_{cd} \text{CFG-node } m \rangle$ 
have  $\text{CFG-node } (-\text{Entry-}) \text{ cc-ns}' @ [n'] \rightarrow_d^* \text{CFG-node } m$ 
  by (fastforce intro:ccSp-Append-cdep)
show  $?thesis$ 
proof (cases  $n = \text{CFG-node } m$ )
  case True
    with  $\langle \text{CFG-node } (-\text{Entry-}) \text{ cc-ns}' @ [n'] \rightarrow_d^* \text{CFG-node } m \rangle$ 
    show  $?thesis$  by fastforce
  next
    case False
      with  $\langle \text{inner-node } m \rangle \langle \text{valid-SDG-node } n \rangle \langle m = \text{parent-node } n \rangle$ 
      have  $\text{CFG-node } m \rightarrow_{cd} n$ 
        by (fastforce intro:SDG-parent-cdep-edge inner-is-valid)
      with  $\langle \text{CFG-node } (-\text{Entry-}) \text{ cc-ns}' @ [n'] \rightarrow_d^* \text{CFG-node } m \rangle$ 
      have  $\text{CFG-node } (-\text{Entry-}) \text{ cc-}(ns' @ [n']) @ [\text{CFG-node } m] \rightarrow_d^* n$ 
        by (fastforce intro:ccSp-Append-cdep)
      thus  $?thesis$  by fastforce
    qed
  qed
next
  case False
    hence  $\exists a' \in \text{set } as. \neg \text{intra-kind } (\text{kind } a')$  by fastforce
    then obtain  $a \text{ as}' \text{ as}''$  where  $as = as' @ a \# as''$  and  $\neg \text{intra-kind } (\text{kind } a)$ 
      and  $\forall a' \in \text{set } as''. \text{intra-kind } (\text{kind } a')$ 
      by (fastforce elim!:split-list-last-propE)
    from  $\langle \forall a' \in \text{set } as. \text{intra-kind } (\text{kind } a') \vee (\exists Q \ r \ p \ fs. \text{kind } a' = Q:r \hookrightarrow_p fs) \rangle$ 
       $\langle as = as' @ a \# as'' \rangle \langle \neg \text{intra-kind } (\text{kind } a) \rangle$ 
    obtain  $Q \ r \ p \ fs$  where  $\text{kind } a = Q:r \hookrightarrow_p fs$ 
      and  $\forall a' \in \text{set } as'. \text{intra-kind } (\text{kind } a') \vee (\exists Q \ r \ p \ fs. \text{kind } a' = Q:r \hookrightarrow_p fs)$ 
      by auto
    from  $\langle as = as' @ a \# as'' \rangle$  have  $\text{length } as' < \text{length } as$  by fastforce
    from  $\langle (-\text{Entry-}) -as \rightarrow_{\sqrt{*}} m \rangle \langle as = as' @ a \# as'' \rangle$ 
    have  $(-\text{Entry-}) -as' \rightarrow_{\sqrt{*}} \text{sourcenode } a$  and valid-edge  $a$ 
      and  $\text{targetnode } a -as'' \rightarrow_{\sqrt{*}} m$ 
      by (auto intro:vp-split)
    hence valid-SDG-node  $(\text{CFG-node } (\text{sourcenode } a))$  by simp
    have  $\exists ns'. \text{CFG-node } (-\text{Entry-}) \text{ cc-ns}' \rightarrow_d^* \text{CFG-node } m$ 

```



```

proof(cases targetnode a = m)
  case True
  with ⟨valid-edge a⟩ ⟨kind a = Q:r↦pfs⟩
  have CFG-node (sourcenode a) -p→call CFG-node m
    by(fastforce intro:SDG-call-edge)
  have ∃ ns. CFG-node (-Entry-) cc-ns→d* CFG-node (sourcenode a)
  proof(cases as' = [])
    case True
    with ⟨(-Entry-) -as'→√* sourcenode a⟩ have (-Entry-) = sourcenode a
      by(fastforce simp:vp-def)
    with ⟨CFG-node (sourcenode a) -p→call CFG-node m⟩
    have CFG-node (-Entry-) cc-[]→d* CFG-node (sourcenode a)
      by(fastforce intro:ccSp-Nil SDG-edge-valid-SDG-node)
    thus ?thesis by fastforce
  next
  case False
  from ⟨valid-edge a⟩ have valid-node (sourcenode a) by simp
  hence inner-node (sourcenode a)
  proof(cases sourcenode a rule:valid-node-cases)
    case Entry
    with ⟨(-Entry-) -as'→√* sourcenode a⟩
    have (-Entry-) -as'→* (-Entry-) by(fastforce simp:vp-def)
    with False have False by fastforce
    thus ?thesis by simp
  next
  case Exit
  with ⟨valid-edge a⟩ have False by -(erule Exit-source)
  thus ?thesis by simp
  next
  case inner
  thus ?thesis by simp
  qed
  with IH ⟨length as' < length as⟩ ⟨(-Entry-) -as'→√* sourcenode a⟩
  ⟨valid-SDG-node (CFG-node (sourcenode a))⟩
  ⟨∀ a' ∈ set as'. intra-kind(kind a') ∨ (∃ Q r p fs. kind a' = Q:r↦pfs)⟩
  obtain ns where CFG-node (-Entry-) cc-ns→d* CFG-node (sourcenode
a)

  apply(erule-tac x=as' in alle) apply clarsimp
  apply(erule-tac x=sourcenode a in alle) apply clarsimp
  apply(erule-tac x=CFG-node (sourcenode a) in alle) by clarsimp
  thus ?thesis by fastforce
qed
  then obtain ns where CFG-node (-Entry-) cc-ns→d* CFG-node
(sourcenode a)
  by blast
  with ⟨CFG-node (sourcenode a) -p→call CFG-node m⟩
  show ?thesis by(fastforce intro:ccSp-Append-call)
next
  case False

```

```

from ⟨targetnode a -as''→√* m⟩ ⟨∀ a' ∈ set as''. intra-kind (kind a')⟩
have targetnode a -as''→i* m by(fastforce simp:vp-def intra-path-def)
hence get-proc (targetnode a) = get-proc m by(rule intra-path-get-procs)
from ⟨valid-edge a⟩ ⟨kind a = Q:r↔pfs⟩ have get-proc (targetnode a) = p
  by(rule get-proc-call)
from ⟨inner-node m⟩ ⟨valid-edge a⟩ ⟨targetnode a -as''→i* m⟩
  ⟨kind a = Q:r↔pfs⟩ ⟨targetnode a ≠ m⟩
obtain ns where CFG-node (targetnode a) cd-ns→d* CFG-node m
  and ns ≠ []
  and ∀ n'' ∈ set ns. parent-node n'' ∈ set(sourcenodes as'')
  by(fastforce elim!:in-proc-cdep-SDG-path)
then obtain n' where n' →cd CFG-node m
  and parent-node n' ∈ set(sourcenodes as'')
  by -(erule cdep-SDG-path.cases,auto)
from ⟨(parent-node n') ∈ set(sourcenodes as'')⟩ obtain ms ms'
  where sourcenodes as'' = ms@(parent-node n')#ms'
  by(fastforce dest:split-list simp:sourcenodes-def)
then obtain xs a' ys where ms = sourcenodes xs
  and ms' = sourcenodes ys and as'' = xs@a'#ys
  and parent-node n' = sourcenode a'
  by(fastforce elim:map-append-append-maps simp:sourcenodes-def)
from ⟨(-Entry-) -as→√* m⟩ ⟨as = as'@a#as''⟩ ⟨as'' = xs@a'#ys⟩
have (-Entry-) -(as'@a#xs)@a'#ys→√* m by simp
hence (-Entry-) -as'@a#xs→√* sourcenode a'
  and valid-edge a' by(auto intro:vp-split)
from ⟨as = as'@a#as''⟩ ⟨as'' = xs@a'#ys⟩
have length (as'@a#xs) < length as by simp
from ⟨valid-edge a'⟩ have valid-node (sourcenode a') by simp
hence inner-node (sourcenode a')
proof(cases sourcenode a' rule:valid-node-cases)
  case Entry
    with ⟨(-Entry-) -as'@a#xs→√* sourcenode a'⟩
    have (-Entry-) -as'@a#xs→* (-Entry-) by(fastforce simp:vp-def)
    hence False by fastforce
    thus ?thesis by simp
  next
    case Exit
      with ⟨valid-edge a'⟩ have False by -(erule Exit-source)
      thus ?thesis by simp
  next
    case inner
      thus ?thesis by simp
qed
from ⟨valid-edge a'⟩ have valid-SDG-node (CFG-node (sourcenode a'))
  by simp
from ⟨∀ a' ∈ set as. intra-kind(kind a') ∨ (∃ Q r p fs. kind a' = Q:r↔pfs)⟩
  ⟨as = as'@a#as''⟩ ⟨as'' = xs@a'#ys⟩
have ∀ a' ∈ set (as'@a#xs).
  intra-kind(kind a') ∨ (∃ Q r p fs. kind a' = Q:r↔pfs)

```

```

    by auto
  with IH ⟨length (as'@a#xs) < length as⟩
    ⟨(-Entry-) -as'@a#xs→√* sourcenode a'⟩
    ⟨valid-SDG-node (CFG-node (sourcenode a'))⟩
    ⟨inner-node (sourcenode a')⟩ ⟨parent-node n' = sourcenode a'⟩
  obtain ns where CFG-node (-Entry-) cc-ns→d* CFG-node (parent-node
n')
    apply(erule-tac x=as'@a#xs in allE) apply clarsimp
    apply(erule-tac x=sourcenode a' in allE) apply clarsimp
    apply(erule-tac x=CFG-node (sourcenode a') in allE) by clarsimp
  from ⟨n' →cd CFG-node m⟩ have valid-SDG-node n'
    by(rule SDG-edge-valid-SDG-node)
  hence n' = CFG-node (parent-node n') ∨ CFG-node (parent-node n')
→cd n'
    by(rule valid-SDG-node-cases)
  thus ?thesis
  proof
    assume n' = CFG-node (parent-node n')
    with ⟨CFG-node (-Entry-) cc-ns→d* CFG-node (parent-node n')⟩
      ⟨n' →cd CFG-node m⟩ show ?thesis
      by(fastforce intro:ccSp-Append-cdep)
  next
    assume CFG-node (parent-node n') →cd n'
    with ⟨CFG-node (-Entry-) cc-ns→d* CFG-node (parent-node n')⟩
      have CFG-node (-Entry-) cc-ns@[CFG-node (parent-node n')]→d* n'
      by(fastforce intro:ccSp-Append-cdep)
    with ⟨n' →cd CFG-node m⟩ show ?thesis
      by(fastforce intro:ccSp-Append-cdep)
  qed
  qed
  then obtain ns where CFG-node (-Entry-) cc-ns→d* CFG-node m by
blast
  show ?thesis
  proof(cases n = CFG-node m)
    case True
      with ⟨CFG-node (-Entry-) cc-ns→d* CFG-node m⟩ show ?thesis by
fastforce
    next
      case False
        with ⟨inner-node m⟩ ⟨valid-SDG-node n⟩ ⟨m = parent-node n⟩
          have CFG-node m →cd n
          by(fastforce intro:SDG-parent-cdep-edge inner-is-valid)
        with ⟨CFG-node (-Entry-) cc-ns→d* CFG-node m⟩ show ?thesis
          by(fastforce dest:ccSp-Append-cdep)
  qed
  qed
  qed
  qed
  qed
  qed

```

1.8.8 Same level paths in the SDG

inductive *matched* :: 'node SDG-node \Rightarrow 'node SDG-node list \Rightarrow 'node SDG-node \Rightarrow bool

where *matched-Nil*:

valid-SDG-node $n \implies \text{matched } n \ [] \ n$

| *matched-Append-intra-SDG-path*:

$\llbracket \text{matched } n \ ns \ n'; \ n' \ i - ns' \rightarrow_d^* \ n' \rrbracket \implies \text{matched } n \ (ns @ ns') \ n'$

| *matched-bracket-call*:

$\llbracket \text{matched } n_0 \ ns \ n_1; \ n_1 \ -p \rightarrow_{call} \ n_2; \ \text{matched } n_2 \ ns' \ n_3;$
 $(n_3 \ -p \rightarrow_{ret} \ n_4 \ \vee \ n_3 \ -p:V \rightarrow_{out} \ n_4); \ \text{valid-edge } a; \ a' \in \text{get-return-edges } a;$
sourcenode $a = \text{parent-node } n_1; \ \text{targetnode } a = \text{parent-node } n_2;$
sourcenode $a' = \text{parent-node } n_3; \ \text{targetnode } a' = \text{parent-node } n_4 \rrbracket$
 $\implies \text{matched } n_0 \ (ns @ n_1 \# ns' @ [n_3]) \ n_4$

| *matched-bracket-param*:

$\llbracket \text{matched } n_0 \ ns \ n_1; \ n_1 \ -p:V \rightarrow_{in} \ n_2; \ \text{matched } n_2 \ ns' \ n_3;$
 $n_3 \ -p:V' \rightarrow_{out} \ n_4; \ \text{valid-edge } a; \ a' \in \text{get-return-edges } a;$
sourcenode $a = \text{parent-node } n_1; \ \text{targetnode } a = \text{parent-node } n_2;$
sourcenode $a' = \text{parent-node } n_3; \ \text{targetnode } a' = \text{parent-node } n_4 \rrbracket$
 $\implies \text{matched } n_0 \ (ns @ n_1 \# ns' @ [n_3]) \ n_4$

lemma *matched-Append*:

$\llbracket \text{matched } n'' \ ns' \ n'; \ \text{matched } n \ ns \ n' \rrbracket \implies \text{matched } n \ (ns @ ns') \ n'$

by (*induct rule:matched.induct*,

auto intro:matched.intros simp:append-assoc [THEN sym] simp del:append-assoc)

lemma *intra-SDG-path-matched*:

assumes $n \ i - ns \rightarrow_d^* \ n'$ **shows** *matched* $n \ ns \ n'$

proof –

from $\langle n \ i - ns \rightarrow_d^* \ n' \rangle$ **have** *valid-SDG-node* n

by (*rule intra-SDG-path-valid-SDG-node*)

hence *matched* $n \ [] \ n$ **by** (*rule matched-Nil*)

with $\langle n \ i - ns \rightarrow_d^* \ n' \rangle$ **have** *matched* $n \ ([] @ ns) \ n'$

by – (*rule matched-Append-intra-SDG-path*)

thus *?thesis* **by** *simp*

qed

lemma *intra-proc-matched*:

assumes *valid-edge* a **and** *kind* $a = Q:r \rightarrow_p fs$ **and** $a' \in \text{get-return-edges } a$

shows *matched* (*CFG-node* (*targetnode* a)) [*CFG-node* (*targetnode* a)]

(*CFG-node* (*sourcenode* a'))

proof –

from *assms* **have** *CFG-node* (*targetnode* a) \longrightarrow_{cd} *CFG-node* (*sourcenode* a')

by (*fastforce intro:SDG-proc-entry-exit-cdep*)

with $\langle \text{valid-edge } a \rangle$

```

have CFG-node (targetnode a)  $i - []@ [CFG-node\ (targetnode\ a)] \rightarrow a^*$ 
  CFG-node (sourcenode a')
  by (fastforce intro:intra-SDG-path.intros)
with  $\langle valid-edge\ a \rangle$ 
have matched (CFG-node (targetnode a)) ( $[]@ [CFG-node\ (targetnode\ a)]$ )
  (CFG-node (sourcenode a'))
  by (fastforce intro:matched.intros)
thus ?thesis by simp
qed

```

lemma *matched-intra-CFG-path*:

```

assumes matched n ns n'
obtains as where parent-node n - as  $\rightarrow_l^*$  parent-node n'
proof (atomize-elim)
from  $\langle matched\ n\ ns\ n' \rangle$  show  $\exists as.\ parent-node\ n - as \rightarrow_l^* parent-node\ n'$ 
proof (induct rule:matched.induct)
  case matched-Nil thus ?case
    by (fastforce dest:empty-path valid-SDG-CFG-node simp:intra-path-def)
  next
    case (matched-Append-intra-SDG-path n ns n'' ns' n')
    from  $\langle \exists as.\ parent-node\ n - as \rightarrow_l^* parent-node\ n'' \rangle$  obtain as
      where parent-node n - as  $\rightarrow_l^*$  parent-node n'' by blast
    from  $\langle n''\ i - ns' \rightarrow_d^* n' \rangle$  obtain as' where parent-node n'' - as'  $\rightarrow_l^*$  parent-node
      n'
      by (fastforce elim:intra-SDG-path-intra-CFG-path)
    with  $\langle parent-node\ n - as \rightarrow_l^* parent-node\ n'' \rangle$ 
    have parent-node n - as @ as'  $\rightarrow_l^*$  parent-node n'
      by (rule intra-path-Append)
    thus ?case by fastforce
  next
    case (matched-bracket-call n0 ns n1 p n2 ns' n3 n4 V a a')
    from  $\langle valid-edge\ a \rangle \langle a' \in get-return-edges\ a \rangle \langle sourcenode\ a = parent-node\ n_1 \rangle$ 
       $\langle targetnode\ a' = parent-node\ n_4 \rangle$ 
    obtain a'' where valid-edge a'' and sourcenode a'' = parent-node n1
      and targetnode a'' = parent-node n4 and kind a'' = ( $\lambda cf.\ False$ ) $\checkmark$ 
      by (fastforce dest:call-return-node-edge)
    hence parent-node n1 - [a']  $\rightarrow^*$  parent-node n4 by (fastforce dest:path-edge)
    moreover
    from  $\langle kind\ a'' = (\lambda cf.\ False)\checkmark \rangle$  have  $\forall a \in set\ [a'].\ intra-kind(kind\ a)$ 
      by (fastforce simp:intra-kind-def)
    ultimately have parent-node n1 - [a']  $\rightarrow_l^*$  parent-node n4
      by (auto simp:intra-path-def)
    with  $\langle \exists as.\ parent-node\ n_0 - as \rightarrow_l^* parent-node\ n_1 \rangle$  show ?case
      by (fastforce intro:intra-path-Append)
  next
    case (matched-bracket-param n0 ns n1 p V n2 ns' n3 V' n4 a a')
    from  $\langle valid-edge\ a \rangle \langle a' \in get-return-edges\ a \rangle \langle sourcenode\ a = parent-node\ n_1 \rangle$ 
       $\langle targetnode\ a' = parent-node\ n_4 \rangle$ 

```

obtain a'' **where** *valid-edge* a'' **and** *sourcenode* $a'' = \text{parent-node } n_1$
and *targetnode* $a'' = \text{parent-node } n_4$ **and** *kind* $a'' = (\lambda cf. \text{False})_{\surd}$
by(*fastforce dest:call-return-node-edge*)
hence *parent-node* $n_1 - [a''] \rightarrow_* \text{parent-node } n_4$ **by**(*fastforce dest:path-edge*)
moreover
from $\langle \text{kind } a'' = (\lambda cf. \text{False})_{\surd} \rangle$ **have** $\forall a \in \text{set } [a'']$. *intra-kind*(*kind* a)
by(*fastforce simp:intra-kind-def*)
ultimately **have** *parent-node* $n_1 - [a''] \rightarrow_{\iota} * \text{parent-node } n_4$
by(*auto simp:intra-path-def*)
with $\langle \exists as. \text{parent-node } n_0 - as \rightarrow_{\iota} * \text{parent-node } n_1 \rangle$ **show** *?case*
by(*fastforce intro:intra-path-Append*)
qed
qed

lemma *matched-same-level-CFG-path*:

assumes *matched* $n \ ns \ n'$
obtains *as* **where** *parent-node* $n - as \rightarrow_{sl} * \text{parent-node } n'$
proof(*atomize-elim*)
from $\langle \text{matched } n \ ns \ n' \rangle$
show $\exists as. \text{parent-node } n - as \rightarrow_{sl} * \text{parent-node } n'$
proof(*induct rule:matched.induct*)
case *matched-Nil* **thus** *?case*
by(*fastforce dest:empty-path valid-SDG-CFG-node simp:slp-def same-level-path-def*)
next
case (*matched-Append-intra-SDG-path* $n \ ns \ n'' \ ns' \ n'$)
from $\langle \exists as. \text{parent-node } n - as \rightarrow_{sl} * \text{parent-node } n'' \rangle$
obtain *as* **where** *parent-node* $n - as \rightarrow_{sl} * \text{parent-node } n''$ **by** *blast*
from $\langle n'' \ i - ns' \rightarrow_{d} * n' \rangle$ **obtain** *as'* **where** *parent-node* $n'' - as' \rightarrow_{\iota} * \text{parent-node } n'$
by(*erule intra-SDG-path-intra-CFG-path*)
from $\langle \text{parent-node } n'' - as' \rightarrow_{\iota} * \text{parent-node } n' \rangle$
have *parent-node* $n'' - as' \rightarrow_{sl} * \text{parent-node } n'$ **by**(*rule intra-path-slp*)
with $\langle \text{parent-node } n - as \rightarrow_{sl} * \text{parent-node } n'' \rangle$
have *parent-node* $n - as @ as' \rightarrow_{sl} * \text{parent-node } n'$
by(*rule slp-Append*)
thus *?case* **by** *fastforce*
next
case (*matched-bracket-call* $n_0 \ ns \ n_1 \ p \ n_2 \ ns' \ n_3 \ n_4 \ V \ a \ a'$)
from $\langle \text{valid-edge } a \rangle \langle a' \in \text{get-return-edges } a \rangle$
obtain $Q \ r \ p' \ fs$ **where** *kind* $a = Q:r \leftrightarrow_p fs$
by(*fastforce dest!:only-call-get-return-edges*)
from $\langle \exists as. \text{parent-node } n_0 - as \rightarrow_{sl} * \text{parent-node } n_1 \rangle$
obtain *as* **where** *parent-node* $n_0 - as \rightarrow_{sl} * \text{parent-node } n_1$ **by** *blast*
from $\langle \exists as. \text{parent-node } n_2 - as \rightarrow_{sl} * \text{parent-node } n_3 \rangle$
obtain *as'* **where** *parent-node* $n_2 - as' \rightarrow_{sl} * \text{parent-node } n_3$ **by** *blast*
from $\langle \text{valid-edge } a \rangle \langle a' \in \text{get-return-edges } a \rangle \langle \text{kind } a = Q:r \leftrightarrow_p fs \rangle$
obtain $Q' \ f'$ **where** *kind* $a' = Q' \ f' \leftrightarrow_p$ **by**(*fastforce dest!:call-return-edges*)
from $\langle \text{valid-edge } a \rangle \langle a' \in \text{get-return-edges } a \rangle$ **have** *valid-edge* a'

by(rule *get-return-edges-valid*)
from $\langle \text{parent-node } n_2 - as' \rightarrow_{sl^*} \text{parent-node } n_3 \rangle$ **have** *same-level-path as'*
by(simp *add:slp-def*)
hence *same-level-path-aux* ($\llbracket @ [a] \rrbracket$ *as'*)
by(fastforce *intro:same-level-path-aux-callstack-Append simp:same-level-path-def*)
from $\langle \text{same-level-path } as' \rangle$ **have** *upd-cs* ($\llbracket @ [a] \rrbracket$ *as'*) = ($\llbracket @ [a] \rrbracket$)
by(fastforce *intro:same-level-path-upd-cs-callstack-Append simp:same-level-path-def*)
with $\langle \text{same-level-path-aux } (\llbracket @ [a] \rrbracket) \text{ as}' \rangle$ $\langle a' \in \text{get-return-edges } a \rangle$
 $\langle \text{kind } a = Q:r \hookrightarrow_p fs \rangle$ $\langle \text{kind } a' = Q' \hookrightarrow_p f' \rangle$
have *same-level-path* ($a \# as' @ [a']$)
by(fastforce *intro:same-level-path-aux-Append upd-cs-Append simp:same-level-path-def*)
from $\langle \text{valid-edge } a' \rangle$ $\langle \text{sourcnode } a' = \text{parent-node } n_3 \rangle$
 $\langle \text{targetnode } a' = \text{parent-node } n_4 \rangle$
have *parent-node* $n_3 - [a'] \rightarrow^* \text{parent-node } n_4$ **by**(fastforce *dest:path-edge*)
with $\langle \text{parent-node } n_2 - as' \rightarrow_{sl^*} \text{parent-node } n_3 \rangle$
have *parent-node* $n_2 - as' @ [a'] \rightarrow^* \text{parent-node } n_4$
by(fastforce *intro:path-Append simp:slp-def*)
with $\langle \text{valid-edge } a \rangle$ $\langle \text{sourcnode } a = \text{parent-node } n_1 \rangle$
 $\langle \text{targetnode } a = \text{parent-node } n_2 \rangle$
have *parent-node* $n_1 - a \# as' @ [a'] \rightarrow^* \text{parent-node } n_4$ **by** $-(\text{rule } \textit{Cons-path})$
with $\langle \text{same-level-path } (a \# as' @ [a']) \rangle$
have *parent-node* $n_1 - a \# as' @ [a'] \rightarrow_{sl^*} \text{parent-node } n_4$ **by**(simp *add:slp-def*)
with $\langle \text{parent-node } n_0 - as \rightarrow_{sl^*} \text{parent-node } n_1 \rangle$
have *parent-node* $n_0 - as @ a \# as' @ [a'] \rightarrow_{sl^*} \text{parent-node } n_4$ **by**(rule *slp-Append*)
with $\langle \text{sourcnode } a = \text{parent-node } n_1 \rangle$ $\langle \text{sourcnode } a' = \text{parent-node } n_3 \rangle$
show ?case **by** fastforce
next
case (*matched-bracket-param* $n_0 \ ns \ n_1 \ p \ V \ n_2 \ ns' \ n_3 \ V' \ n_4 \ a \ a'$)
from $\langle \text{valid-edge } a \rangle$ $\langle a' \in \text{get-return-edges } a \rangle$
obtain $Q \ r \ p' \ fs$ **where** $\text{kind } a = Q:r \hookrightarrow_p fs$
by(fastforce *dest!:only-call-get-return-edges*)
from $\langle \exists as. \text{parent-node } n_0 - as \rightarrow_{sl^*} \text{parent-node } n_1 \rangle$
obtain *as* **where** $\text{parent-node } n_0 - as \rightarrow_{sl^*} \text{parent-node } n_1$ **by** blast
from $\langle \exists as. \text{parent-node } n_2 - as \rightarrow_{sl^*} \text{parent-node } n_3 \rangle$
obtain *as'* **where** $\text{parent-node } n_2 - as' \rightarrow_{sl^*} \text{parent-node } n_3$ **by** blast
from $\langle \text{valid-edge } a \rangle$ $\langle a' \in \text{get-return-edges } a \rangle$ $\langle \text{kind } a = Q:r \hookrightarrow_p fs \rangle$
obtain $Q' \ f'$ **where** $\text{kind } a' = Q' \hookrightarrow_p f'$ **by**(fastforce *dest!:call-return-edges*)
from $\langle \text{valid-edge } a \rangle$ $\langle a' \in \text{get-return-edges } a \rangle$ **have** *valid-edge a'*
by(rule *get-return-edges-valid*)
from $\langle \text{parent-node } n_2 - as' \rightarrow_{sl^*} \text{parent-node } n_3 \rangle$ **have** *same-level-path as'*
by(simp *add:slp-def*)
hence *same-level-path-aux* ($\llbracket @ [a] \rrbracket$ *as'*)
by(fastforce *intro:same-level-path-aux-callstack-Append simp:same-level-path-def*)
from $\langle \text{same-level-path } as' \rangle$ **have** *upd-cs* ($\llbracket @ [a] \rrbracket$ *as'*) = ($\llbracket @ [a] \rrbracket$)
by(fastforce *intro:same-level-path-upd-cs-callstack-Append simp:same-level-path-def*)
with $\langle \text{same-level-path-aux } (\llbracket @ [a] \rrbracket) \text{ as}' \rangle$ $\langle a' \in \text{get-return-edges } a \rangle$

```

  ⟨kind a = Q:r↦p'fs⟩ ⟨kind a' = Q'↦p'f'⟩
have same-level-path (a#as'@[a'])
  by(fastforce intro:same-level-path-aux-Append upd-cs-Append
      simp:same-level-path-def)
from ⟨valid-edge a'⟩ ⟨sourcnode a' = parent-node n3⟩
  ⟨targetnode a' = parent-node n4⟩
have parent-node n3 -[a']→* parent-node n4 by(fastforce dest:path-edge)
with ⟨parent-node n2 -as'→sl* parent-node n3⟩
have parent-node n2 -as'@[a']→* parent-node n4
  by(fastforce intro:path-Append simp:slp-def)
with ⟨valid-edge a⟩ ⟨sourcnode a = parent-node n1⟩
  ⟨targetnode a = parent-node n2⟩
have parent-node n1 -a#as'@[a']→* parent-node n4 by -(rule Cons-path)
with ⟨same-level-path (a#as'@[a'])⟩
have parent-node n1 -a#as'@[a']→sl* parent-node n4 by(simp add:slp-def)
with ⟨parent-node n0 -as→sl* parent-node n1⟩
have parent-node n0 -as@a#as'@[a']→sl* parent-node n4 by(rule slp-Append)
with ⟨sourcnode a = parent-node n1⟩ ⟨sourcnode a' = parent-node n3⟩
show ?case by fastforce
qed
qed

```

1.8.9 Realizable paths in the SDG

inductive *realizable* ::

```

  'node SDG-node ⇒ 'node SDG-node list ⇒ 'node SDG-node ⇒ bool
where realizable-matched:matched n ns n' ⇒⇒ realizable n ns n'
  | realizable-call:
  [realizable n0 ns n1; n1 -p→call n2 ∨ n1 -p:V→in n2; matched n2 ns' n3]
  ⇒⇒ realizable n0 (ns@n1#ns') n3

```

lemma *realizable-Append-matched*:

```

  [realizable n ns n''; matched n'' ns' n'] ⇒⇒ realizable n (ns@ns') n'

```

proof(*induct rule:realizable.induct*)

case (*realizable-matched n ns n''*)

from ⟨*matched n'' ns' n'*⟩ ⟨*matched n ns n''*⟩ **have** *matched n (ns@ns') n'*

by(*rule matched-Append*)

thus ?case **by**(*rule realizable.realizable-matched*)

next

case (*realizable-call n₀ ns n₁ p n₂ V ns'' n₃*)

from ⟨*matched n₃ ns' n'*⟩ ⟨*matched n₂ ns'' n₃*⟩ **have** *matched n₂ (ns''@ns') n'*

by(*rule matched-Append*)

with ⟨*realizable n₀ ns n₁*⟩ ⟨*n₁ -p→_{call} n₂ ∨ n₁ -p:V→_{in} n₂*⟩

have *realizable n₀ (ns@n₁#(ns''@ns')) n'*

by(*rule realizable.realizable-call*)

thus ?case **by** *simp*

qed

lemma *realizable-valid-CFG-path*:
assumes *realizable n ns n'*
obtains as where *parent-node n -as $\rightarrow_{\sqrt{*}}$ parent-node n'*
proof(*atomize-elim*)
from \langle *realizable n ns n'* \rangle
show \exists *as. parent-node n -as $\rightarrow_{\sqrt{*}}$ parent-node n'*
proof(*induct rule:realizable.induct*)
case (*realizable-matched n ns n'*)
from \langle *matched n ns n'* \rangle **obtain as where** *parent-node n -as $\rightarrow_{sl{*}}$ parent-node n'*
by(*erule matched-same-level-CFG-path*)
thus *?case by*(*fastforce intro:slp-vp*)
next
case (*realizable-call n₀ ns n₁ p n₂ V ns' n₃*)
from \langle \exists *as. parent-node n₀ -as $\rightarrow_{\sqrt{*}}$ parent-node n₁* \rangle
obtain as where *parent-node n₀ -as $\rightarrow_{\sqrt{*}}$ parent-node n₁* **by** *blast*
from \langle *matched n₂ ns' n₃* \rangle **obtain as' where** *parent-node n₂ -as' $\rightarrow_{sl{*}}$ parent-node n₃*
by(*erule matched-same-level-CFG-path*)
from \langle *n₁ -p \rightarrow_{call} n₂ \vee n₁ -p:V \rightarrow_{in} n₂* \rangle
obtain a Q r fs where *valid-edge a*
and *sourcenode a = parent-node n₁ and targetnode a = parent-node n₂*
and *kind a = Q:r \hookrightarrow_{pfs}* **by**(*fastforce elim:SDG-edge.cases*)
hence *parent-node n₁ -[a] \rightarrow_{*} parent-node n₂*
by(*fastforce dest:path-edge*)
from \langle *parent-node n₀ -as $\rightarrow_{\sqrt{*}}$ parent-node n₁* \rangle
have *parent-node n₀ -as \rightarrow_{*} parent-node n₁* **and** *valid-path as*
by(*simp-all add:vp-def*)
with \langle *kind a = Q:r \hookrightarrow_{pfs}* \rangle **have** *valid-path (as@[a])*
by(*fastforce elim:valid-path-aux-Append simp:valid-path-def*)
moreover
from \langle *parent-node n₀ -as \rightarrow_{*} parent-node n₁* \rangle \langle *parent-node n₁ -[a] \rightarrow_{*} parent-node n₂* \rangle
have *parent-node n₀ -as@[a] \rightarrow_{*} parent-node n₂* **by**(*rule path-Append*)
ultimately have *parent-node n₀ -as@[a] $\rightarrow_{\sqrt{*}}$ parent-node n₂* **by**(*simp add:vp-def*)
with \langle *parent-node n₂ -as' $\rightarrow_{sl{*}}$ parent-node n₃* \rangle
have *parent-node n₀ -(as@[a])@as' $\rightarrow_{\sqrt{*}}$ parent-node n₃* **by** \langle *rule vp-slp-Append* \rangle
with \langle *sourcenode a = parent-node n₁* \rangle **show** *?case by fastforce*
qed
qed

lemma *cdep-SDG-path-realizable*:
 n *cc-ns $\rightarrow_{d{*}}$ n'* \implies *realizable n ns n'*
proof(*induct rule:call-cdep-SDG-path.induct*)
case (*ccSp-Nil n*)
from \langle *valid-SDG-node n* \rangle **show** *?case*
by(*fastforce intro:realizable-matched matched-Nil*)

```

next
  case (ccSp-Append-cdep n ns n'' n')
  from ⟨n'' →cd n'⟩ have valid-SDG-node n'' by(rule SDG-edge-valid-SDG-node)
  hence matched n'' [] n'' by(rule matched-Nil)
  from ⟨n'' →cd n'⟩ ⟨valid-SDG-node n''⟩
  have n'' i-[]@[n'']→d* n'
    by(fastforce intro:iSp-Append-cdep iSp-Nil)
  with ⟨matched n'' [] n''⟩ have matched n'' ([]@[n'']) n'
    by(fastforce intro:matched-Append-intra-SDG-path)
  with ⟨realizable n ns n''⟩ show ?case
    by(fastforce intro:realizable-Append-matched)
next
  case (ccSp-Append-call n ns n'' p n')
  from ⟨n'' -p→call n'⟩ have valid-SDG-node n' by(rule SDG-edge-valid-SDG-node)
  hence matched n' [] n' by(rule matched-Nil)
  with ⟨realizable n ns n''⟩ ⟨n'' -p→call n'⟩
  show ?case by(fastforce intro:realizable-call)
qed

```

1.8.10 SDG with summary edges

```

inductive sum-cdep-edge :: 'node SDG-node ⇒ 'node SDG-node ⇒ bool
  (- s →cd - [51,0] 80)
and sum-ddep-edge :: 'node SDG-node ⇒ 'var ⇒ 'node SDG-node ⇒ bool
  (- s -->dd - [51,0,0] 80)
and sum-call-edge :: 'node SDG-node ⇒ 'pname ⇒ 'node SDG-node ⇒ bool
  (- s -->call - [51,0,0] 80)
and sum-return-edge :: 'node SDG-node ⇒ 'pname ⇒ 'node SDG-node ⇒ bool
  (- s -->ret - [51,0,0] 80)
and sum-param-in-edge :: 'node SDG-node ⇒ 'pname ⇒ 'var ⇒ 'node SDG-node
⇒ bool
  (- s -:->in - [51,0,0,0] 80)
and sum-param-out-edge :: 'node SDG-node ⇒ 'pname ⇒ 'var ⇒ 'node SDG-node
⇒ bool
  (- s -:->out - [51,0,0,0] 80)
and sum-summary-edge :: 'node SDG-node ⇒ 'pname ⇒ 'node SDG-node ⇒
bool
  (- s -->sum - [51,0] 80)
and sum-SDG-edge :: 'node SDG-node ⇒ 'var option ⇒
('pname × bool) option ⇒ bool ⇒ 'node SDG-node ⇒ bool

```

where

```

n s →cd n' == sum-SDG-edge n None None False n'
| n s -V→dd n' == sum-SDG-edge n (Some V) None False n'
| n s -p→call n' == sum-SDG-edge n None (Some(p,True)) False n'
| n s -p→ret n' == sum-SDG-edge n None (Some(p,False)) False n'
| n s -p:V→in n' == sum-SDG-edge n (Some V) (Some(p,True)) False n'
| n s -p:V→out n' == sum-SDG-edge n (Some V) (Some(p,False)) False n'

```

| $n \text{ s-p} \rightarrow_{\text{sum}} n' \text{ == sum-SDG-edge } n \text{ None (Some(p,True)) True } n'$

| *sum-SDG-cdep-edge:*
 $\llbracket n = \text{CFG-node } m; n' = \text{CFG-node } m'; m \text{ controls } m' \rrbracket \implies n \text{ s} \rightarrow_{\text{cd}} n'$

| *sum-SDG-proc-entry-exit-cdep:*
 $\llbracket \text{valid-edge } a; \text{kind } a = Q:r \hookrightarrow_{\text{pfs}}; n = \text{CFG-node (targetnode } a);$
 $a' \in \text{get-return-edges } a; n' = \text{CFG-node (sourcenode } a') \rrbracket \implies n \text{ s} \rightarrow_{\text{cd}} n'$

| *sum-SDG-parent-cdep-edge:*
 $\llbracket \text{valid-SDG-node } n'; m = \text{parent-node } n'; n = \text{CFG-node } m; n \neq n' \rrbracket$
 $\implies n \text{ s} \rightarrow_{\text{cd}} n'$

| *sum-SDG-ddep-edge:* $n \text{ influences } V \text{ in } n' \implies n \text{ s-V} \rightarrow_{\text{dd}} n'$

| *sum-SDG-call-edge:*
 $\llbracket \text{valid-edge } a; \text{kind } a = Q:r \hookrightarrow_{\text{pfs}}; n = \text{CFG-node (sourcenode } a);$
 $n' = \text{CFG-node (targetnode } a) \rrbracket \implies n \text{ s-p} \rightarrow_{\text{call}} n'$

| *sum-SDG-return-edge:*
 $\llbracket \text{valid-edge } a; \text{kind } a = Q \leftarrow_{\text{pfs}}; n = \text{CFG-node (sourcenode } a);$
 $n' = \text{CFG-node (targetnode } a) \rrbracket \implies n \text{ s-p} \rightarrow_{\text{ret}} n'$

| *sum-SDG-param-in-edge:*
 $\llbracket \text{valid-edge } a; \text{kind } a = Q:r \hookrightarrow_{\text{pfs}}; (p, \text{ins}, \text{outs}) \in \text{set procs}; V = \text{ins!}x;$
 $x < \text{length ins}; n = \text{Actual-in (sourcenode } a, x); n' = \text{Formal-in (targetnode } a, x) \rrbracket$
 $\implies n \text{ s-p:V} \rightarrow_{\text{in}} n'$

| *sum-SDG-param-out-edge:*
 $\llbracket \text{valid-edge } a; \text{kind } a = Q \leftarrow_{\text{pf}}; (p, \text{ins}, \text{outs}) \in \text{set procs}; V = \text{outs!}x;$
 $x < \text{length outs}; n = \text{Formal-out (sourcenode } a, x);$
 $n' = \text{Actual-out (targetnode } a, x) \rrbracket$
 $\implies n \text{ s-p:V} \rightarrow_{\text{out}} n'$

| *sum-SDG-call-summary-edge:*
 $\llbracket \text{valid-edge } a; \text{kind } a = Q:r \hookrightarrow_{\text{pfs}}; a' \in \text{get-return-edges } a;$
 $n = \text{CFG-node (sourcenode } a); n' = \text{CFG-node (targetnode } a') \rrbracket$
 $\implies n \text{ s-p} \rightarrow_{\text{sum}} n'$

| *sum-SDG-param-summary-edge:*
 $\llbracket \text{valid-edge } a; \text{kind } a = Q:r \hookrightarrow_{\text{pfs}}; a' \in \text{get-return-edges } a;$
 $\text{matched (Formal-in (targetnode } a, x)) \text{ ns (Formal-out (sourcenode } a', x'))};$
 $n = \text{Actual-in (sourcenode } a, x); n' = \text{Actual-out (targetnode } a', x');$
 $(p, \text{ins}, \text{outs}) \in \text{set procs}; x < \text{length ins}; x' < \text{length outs} \rrbracket$
 $\implies n \text{ s-p} \rightarrow_{\text{sum}} n'$

lemma *sum-edge-cases:*

$\llbracket n \text{ s-p} \rightarrow_{\text{sum}} n';$
 $\bigwedge a \ Q \ r \ fs \ a'. \llbracket \text{valid-edge } a; \text{kind } a = Q:r \hookrightarrow_{\text{pfs}}; a' \in \text{get-return-edges } a;$
 $n = \text{CFG-node (sourcenode } a); n' = \text{CFG-node (targetnode } a') \rrbracket \implies$

$P;$

$\bigwedge a \ Q \ p \ r \ fs \ a' \ ns \ x \ x' \ ins \ outs.$
 $\llbracket \text{valid-edge } a; \text{kind } a = Q:r \hookrightarrow_{\text{pfs}}; a' \in \text{get-return-edges } a;$
 $\text{matched (Formal-in (targetnode } a, x)) \text{ ns (Formal-out (sourcenode } a', x'))};$

$n = \text{Actual-in } (\text{sourcenode } a, x); n' = \text{Actual-out } (\text{targetnode } a', x')$;
 $(p, \text{ins}, \text{outs}) \in \text{set procs}; x < \text{length ins}; x' < \text{length outs} \implies P$
 $\implies P$
by $-(\text{erule sum-SDG-edge.cases, auto, fastforce+})$

lemma *SDG-edge-sum-SDG-edge*:
 $\text{SDG-edge } n \text{ Vopt } \text{popt } n' \implies \text{sum-SDG-edge } n \text{ Vopt } \text{popt } \text{False } n'$
by $(\text{induct rule:SDG-edge.induct, auto intro:sum-SDG-edge.intros})$

lemma *sum-SDG-edge-SDG-edge*:
 $\text{sum-SDG-edge } n \text{ Vopt } \text{popt } \text{False } n' \implies \text{SDG-edge } n \text{ Vopt } \text{popt } n'$
by $(\text{induct } n \text{ Vopt } \text{popt } x \equiv \text{False } n' \text{ rule:sum-SDG-edge.induct, auto intro:SDG-edge.intros})$

lemma *sum-SDG-edge-valid-SDG-node*:
assumes $\text{sum-SDG-edge } n \text{ Vopt } \text{popt } b \ n'$
shows $\text{valid-SDG-node } n$ **and** $\text{valid-SDG-node } n'$
proof –
have $\text{valid-SDG-node } n \wedge \text{valid-SDG-node } n'$
proof $(\text{cases } b)$
case *True*
with $\langle \text{sum-SDG-edge } n \text{ Vopt } \text{popt } b \ n' \rangle$ **show** $?thesis$
proof $(\text{induct rule:sum-SDG-edge.induct})$
case $(\text{sum-SDG-call-summary-edge } a \ Q \ r \ p \ f \ a' \ n \ n')$
from $\langle \text{valid-edge } a \rangle \langle n = \text{CFG-node } (\text{sourcenode } a) \rangle$
have $\text{valid-SDG-node } n$ **by** *fastforce*
from $\langle \text{valid-edge } a \rangle \langle a' \in \text{get-return-edges } a \rangle$ **have** $\text{valid-edge } a'$
by $(\text{rule get-return-edges-valid})$
with $\langle n' = \text{CFG-node } (\text{targetnode } a') \rangle$ **have** $\text{valid-SDG-node } n'$ **by** *fastforce*
with $\langle \text{valid-SDG-node } n \rangle$ **show** $?case$ **by** *simp*
next
case $(\text{sum-SDG-param-summary-edge } a \ Q \ r \ p \ fs \ a' \ x \ ns \ x' \ n \ n' \ \text{ins} \ \text{outs})$
from $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q:r \hookrightarrow pfs \rangle \langle n = \text{Actual-in } (\text{sourcenode } a, x) \rangle$
 $\langle (p, \text{ins}, \text{outs}) \in \text{set procs} \rangle \langle x < \text{length ins} \rangle$
have $\text{valid-SDG-node } n$ **by** *fastforce*
from $\langle \text{valid-edge } a \rangle \langle a' \in \text{get-return-edges } a \rangle$ **have** $\text{valid-edge } a'$
by $(\text{rule get-return-edges-valid})$
from $\langle \text{valid-edge } a \rangle \langle a' \in \text{get-return-edges } a \rangle \langle \text{kind } a = Q:r \hookrightarrow pfs \rangle$
obtain $Q' \ f'$ **where** $\text{kind } a' = Q' \hookleftarrow p' f'$ **by** $(\text{fastforce dest!:call-return-edges})$
with $\langle \text{valid-edge } a' \rangle \langle n' = \text{Actual-out } (\text{targetnode } a', x') \rangle$
 $\langle (p, \text{ins}, \text{outs}) \in \text{set procs} \rangle \langle x' < \text{length outs} \rangle$
have $\text{valid-SDG-node } n'$ **by** *fastforce*
with $\langle \text{valid-SDG-node } n \rangle$ **show** $?case$ **by** *simp*
qed *simp-all*
next

```

case False
with  $\langle \text{sum-SDG-edge } n \text{ Vopt popt } b \text{ } n' \rangle$  have SDG-edge  $n \text{ Vopt popt } n'$ 
  by(fastforce intro:sum-SDG-edge-SDG-edge)
thus ?thesis by(fastforce intro:SDG-edge-valid-SDG-node)
qed
thus valid-SDG-node  $n$  and valid-SDG-node  $n'$  by simp-all
qed

```

lemma *Exit-no-sum-SDG-edge-source:*

```

assumes sum-SDG-edge  $(\text{CFG-node } (-\text{Exit-})) \text{ Vopt popt } b \text{ } n'$  shows False
proof(cases  $b$ )

```

```

case True

```

```

with  $\langle \text{sum-SDG-edge } (\text{CFG-node } (-\text{Exit-})) \text{ Vopt popt } b \text{ } n' \rangle$  show ?thesis

```

```

proof(induct CFG-node  $(-\text{Exit-}) \text{ Vopt popt } b \text{ } n'$  rule:sum-SDG-edge.induct)

```

```

case  $(\text{sum-SDG-call-summary-edge } a \text{ } Q \text{ } r \text{ } p \text{ } f \text{ } a' \text{ } n')$ 

```

```

from  $\langle \text{CFG-node } (-\text{Exit-}) = \text{CFG-node } (\text{sourcenode } a) \rangle$ 

```

```

have sourcenode  $a = (-\text{Exit-})$  by simp

```

```

with  $\langle \text{valid-edge } a \rangle$  show ?case by(rule Exit-source)

```

```

next

```

```

case  $(\text{sum-SDG-param-summary-edge } a \text{ } Q \text{ } r \text{ } p \text{ } f \text{ } a' \text{ } x \text{ } ns \text{ } x' \text{ } n' \text{ } \text{ins } \text{outs})$ 

```

```

thus ?case by simp

```

```

qed simp-all

```

```

next

```

```

case False

```

```

with  $\langle \text{sum-SDG-edge } (\text{CFG-node } (-\text{Exit-})) \text{ Vopt popt } b \text{ } n' \rangle$ 

```

```

have SDG-edge  $(\text{CFG-node } (-\text{Exit-})) \text{ Vopt popt } n'$ 

```

```

by(fastforce intro:sum-SDG-edge-SDG-edge)

```

```

thus ?thesis by(fastforce intro:Exit-no-SDG-edge-source)

```

```

qed

```

lemma *Exit-no-sum-SDG-edge-target:*

```

sum-SDG-edge  $n \text{ Vopt popt } b \text{ } (\text{CFG-node } (-\text{Exit-})) \implies \text{False}$ 

```

```

proof(induct CFG-node  $(-\text{Exit-})$  rule:sum-SDG-edge.induct)

```

```

case  $(\text{sum-SDG-cdep-edge } n \text{ } m \text{ } m')$ 

```

```

from  $\langle m \text{ controls } m' \rangle \langle \text{CFG-node } (-\text{Exit-}) = \text{CFG-node } m' \rangle$ 

```

```

have  $m \text{ controls } (-\text{Exit-})$  by simp

```

```

hence False by(fastforce dest:Exit-not-control-dependent)

```

```

thus ?case by simp

```

```

next

```

```

case  $(\text{sum-SDG-proc-entry-exit-cdep } a \text{ } Q \text{ } r \text{ } p \text{ } f \text{ } n \text{ } a')$ 

```

```

from  $\langle \text{valid-edge } a \rangle \langle a' \in \text{get-return-edges } a \rangle$  have valid-edge  $a'$ 

```

```

by(rule get-return-edges-valid)

```

```

moreover

```

```

from  $\langle \text{CFG-node } (-\text{Exit-}) = \text{CFG-node } (\text{sourcenode } a') \rangle$ 

```

```

have sourcenode  $a' = (-\text{Exit-})$  by simp

```

```

ultimately have False by(rule Exit-source)

```

```

thus ?case by simp

```

```

next
  case (sum-SDG-ddep-edge n V) thus ?case
    by(fastforce elim:SDG-Use.cases simp:data-dependence-def)
next
  case (sum-SDG-call-edge a Q r p fs n)
  from ⟨CFG-node (-Exit-) = CFG-node (targetnode a)⟩
  have targetnode a = (-Exit-) by simp
  with ⟨valid-edge a⟩ ⟨kind a = Q:r↔pfs⟩ have get-proc (-Exit-) = p
    by(fastforce intro:get-proc-call)
  hence p = Main by(simp add:get-proc-Exit)
  with ⟨valid-edge a⟩ ⟨kind a = Q:r↔pfs⟩ have False
    by(fastforce intro:Main-no-call-target)
  thus ?case by simp
next
  case (sum-SDG-return-edge a Q p f n)
  from ⟨CFG-node (-Exit-) = CFG-node (targetnode a)⟩
  have targetnode a = (-Exit-) by simp
  with ⟨valid-edge a⟩ ⟨kind a = Q↔pf⟩ have False by(rule Exit-no-return-target)
  thus ?case by simp
next
  case (sum-SDG-call-summary-edge a Q r p fs a' n)
  from ⟨valid-edge a⟩ ⟨a' ∈ get-return-edges a⟩ have valid-edge a'
    by(rule get-return-edges-valid)
  from ⟨valid-edge a⟩ ⟨kind a = Q:r↔pfs⟩ ⟨a' ∈ get-return-edges a⟩
  obtain Q' f' where kind a' = Q'↔pf' by(fastforce dest!:call-return-edges)
  from ⟨CFG-node (-Exit-) = CFG-node (targetnode a')⟩
  have targetnode a' = (-Exit-) by simp
  with ⟨valid-edge a'⟩ ⟨kind a' = Q'↔pf'⟩ have False by(rule Exit-no-return-target)
  thus ?case by simp
qed simp+

```

lemma *sum-SDG-summary-edge-matched*:

```

  assumes n s-p→sum n'
  obtains ns where matched n ns n' and n ∈ set ns
  and get-proc (parent-node(last ns)) = p
proof(atomize-elim)
  from ⟨n s-p→sum n'⟩
  show ∃ ns. matched n ns n' ∧ n ∈ set ns ∧ get-proc (parent-node(last ns)) = p
  proof(induct n None::'var option Some(p,True) True n'
    rule:sum-SDG-edge.induct)
  case (sum-SDG-call-summary-edge a Q r fs a' n n')
  from ⟨valid-edge a⟩ ⟨kind a = Q:r↔pfs⟩ ⟨n = CFG-node (sourcenode a)⟩
  have n -p→call CFG-node (targetnode a) by(fastforce intro:SDG-call-edge)
  hence valid-SDG-node n by(rule SDG-edge-valid-SDG-node)
  hence matched n [] n by(rule matched-Nil)
  from ⟨valid-edge a⟩ ⟨a' ∈ get-return-edges a⟩ have valid-edge a'
    by(rule get-return-edges-valid)

```

```

from ⟨valid-edge a⟩ ⟨kind a = Q:r↔pfs⟩ ⟨a' ∈ get-return-edges a⟩
have matched:matched (CFG-node (targetnode a)) [CFG-node (targetnode a)]
  (CFG-node (sourcenode a')) by(rule intra-proc-matched)
from ⟨valid-edge a⟩ ⟨a' ∈ get-return-edges a⟩ ⟨kind a = Q:r↔pfs⟩
obtain Q' f' where kind a' = Q'↔pf' by(fastforce dest!:call-return-edges)
with ⟨valid-edge a'⟩ have get-proc (sourcenode a') = p by(rule get-proc-return)
from ⟨valid-edge a'⟩ ⟨kind a' = Q'↔pf'⟩ ⟨n' = CFG-node (targetnode a')⟩
have CFG-node (sourcenode a') -p→ret n' by(fastforce intro:SDG-return-edge)
from ⟨matched n [] n⟩ ⟨n -p→call CFG-node (targetnode a)⟩ matched
  ⟨CFG-node (sourcenode a') -p→ret n'⟩ ⟨a' ∈ get-return-edges a⟩
  ⟨n = CFG-node (sourcenode a)⟩ ⟨n' = CFG-node (targetnode a')⟩ ⟨valid-edge
a)
  have matched n ([@n#[CFG-node (targetnode a)]@[CFG-node (sourcenode
a')]) n'
    by(fastforce intro:matched-bracket-call)
  with ⟨get-proc (sourcenode a') = p⟩ show ?case by auto
next
case (sum-SDG-param-summary-edge a Q r fs a' x ns x' n n' ins outs)
from ⟨valid-edge a⟩ ⟨kind a = Q:r↔pfs⟩ ⟨(p,ins,outs) ∈ set procs⟩
  ⟨x < length ins⟩ ⟨n = Actual-in (sourcenode a,x)⟩
have n -p:ins!x→in Formal-in (targetnode a,x)
  by(fastforce intro:SDG-param-in-edge)
hence valid-SDG-node n by(rule SDG-edge-valid-SDG-node)
hence matched n [] n by(rule matched-Nil)
from ⟨valid-edge a⟩ ⟨a' ∈ get-return-edges a⟩ have valid-edge a'
  by(rule get-return-edges-valid)
from ⟨valid-edge a⟩ ⟨a' ∈ get-return-edges a⟩ ⟨kind a = Q:r↔pfs⟩
obtain Q' f' where kind a' = Q'↔pf' by(fastforce dest!:call-return-edges)
with ⟨valid-edge a'⟩ have get-proc (sourcenode a') = p by(rule get-proc-return)
from ⟨valid-edge a'⟩ ⟨kind a' = Q'↔pf'⟩ ⟨(p,ins,outs) ∈ set procs⟩
  ⟨x' < length outs⟩ ⟨n' = Actual-out (targetnode a',x')⟩
have Formal-out (sourcenode a',x') -p:outs!x'→out n'
  by(fastforce intro:SDG-param-out-edge)
from ⟨matched n [] n⟩ ⟨n -p:ins!x→in Formal-in (targetnode a,x)⟩
  ⟨matched (Formal-in (targetnode a,x)) ns (Formal-out (sourcenode a',x'))⟩
  ⟨Formal-out (sourcenode a',x') -p:outs!x'→out n'⟩
  ⟨a' ∈ get-return-edges a⟩ ⟨n = Actual-in (sourcenode a,x)⟩
  ⟨n' = Actual-out (targetnode a',x')⟩ ⟨valid-edge a⟩
have matched n ([@n#ns@[Formal-out (sourcenode a',x')]) n'
  by(fastforce intro:matched-bracket-param)
  with ⟨get-proc (sourcenode a') = p⟩ show ?case by auto
qed simp-all
qed

```

lemma return-edge-determines-call-and-sum-edge:

assumes valid-edge a **and** kind a = Q↔pf
obtains a' Q' r' fs' **where** a ∈ get-return-edges a' **and** valid-edge a'
and kind a' = Q':r'↔pfs'

and $CFG\text{-node } (sourcenode\ a')\ s\text{-}p\text{-}\rightarrow_{sum}\ CFG\text{-node } (targetnode\ a)$
proof(*atomize-elim*)
from $\langle valid\text{-edge } a \rangle\ \langle kind\ a = Q \leftrightarrow pf \rangle$
have $CFG\text{-node } (sourcenode\ a)\ s\text{-}p\text{-}\rightarrow_{ret}\ CFG\text{-node } (targetnode\ a)$
by(*fastforce intro:sum-SDG-return-edge*)
from $\langle valid\text{-edge } a \rangle\ \langle kind\ a = Q \leftrightarrow pf \rangle$
obtain $a'\ Q'\ r'\ fs'$ **where** $valid\text{-edge } a'$ **and** $kind\ a' = Q':r'\hookrightarrow_pfs'$
and $a \in get\text{-return-edges } a'$ **by**(*blast dest:return-needs-call*)
hence $CFG\text{-node } (sourcenode\ a')\ s\text{-}p\text{-}\rightarrow_{call}\ CFG\text{-node } (targetnode\ a')$
by(*fastforce intro:sum-SDG-call-edge*)
from $\langle valid\text{-edge } a' \rangle\ \langle kind\ a' = Q':r'\hookrightarrow_pfs' \rangle\ \langle valid\text{-edge } a \rangle\ \langle a \in get\text{-return-edges } a' \rangle$
have $CFG\text{-node } (targetnode\ a') \longrightarrow_{cd}\ CFG\text{-node } (sourcenode\ a)$
by(*fastforce intro!:SDG-proc-entry-exit-cdep*)
hence $valid\text{-SDG-node } (CFG\text{-node } (targetnode\ a'))$
by(*rule SDG-edge-valid-SDG-node*)
with $\langle CFG\text{-node } (targetnode\ a') \longrightarrow_{cd}\ CFG\text{-node } (sourcenode\ a) \rangle$
have $CFG\text{-node } (targetnode\ a')\ i\text{-}\llbracket\ @\ [CFG\text{-node } (targetnode\ a')] \rightarrow_d^*\$
 $CFG\text{-node } (sourcenode\ a)$
by(*fastforce intro:iSp-Append-cdep iSp-Nil*)
from $\langle valid\text{-SDG-node } (CFG\text{-node } (targetnode\ a')) \rangle$
have $matched\ (CFG\text{-node } (targetnode\ a'))\ \llbracket\ (CFG\text{-node } (targetnode\ a')) \rrbracket$
by(*rule matched-Nil*)
with $\langle CFG\text{-node } (targetnode\ a')\ i\text{-}\llbracket\ @\ [CFG\text{-node } (targetnode\ a')] \rightarrow_d^*\$
 $CFG\text{-node } (sourcenode\ a) \rangle$
have $matched\ (CFG\text{-node } (targetnode\ a'))\ (\llbracket\ @\ [CFG\text{-node } (targetnode\ a')] \rrbracket)$
 $(CFG\text{-node } (sourcenode\ a))$
by(*fastforce intro:matched-Append-intra-SDG-path*)
with $\langle valid\text{-edge } a' \rangle\ \langle kind\ a' = Q':r'\hookrightarrow_pfs' \rangle\ \langle valid\text{-edge } a \rangle\ \langle kind\ a = Q \leftrightarrow pf \rangle$
 $\langle a \in get\text{-return-edges } a' \rangle$
have $CFG\text{-node } (sourcenode\ a')\ s\text{-}p\text{-}\rightarrow_{sum}\ CFG\text{-node } (targetnode\ a)$
by(*fastforce intro!:sum-SDG-call-summary-edge*)
with $\langle a \in get\text{-return-edges } a' \rangle\ \langle valid\text{-edge } a' \rangle\ \langle kind\ a' = Q':r'\hookrightarrow_pfs' \rangle$
show $\exists a'\ Q'\ r'\ fs'.\ a \in get\text{-return-edges } a' \wedge valid\text{-edge } a' \wedge$
 $kind\ a' = Q':r'\hookrightarrow_pfs' \wedge CFG\text{-node } (sourcenode\ a')\ s\text{-}p\text{-}\rightarrow_{sum}\ CFG\text{-node}$
 $(targetnode\ a)$
by *fastforce*
qed

1.8.11 Paths consisting of intraprocedural and summary edges in the SDG

inductive *intra-sum-SDG-path* ::

$'node\ SDG\text{-node} \Rightarrow 'node\ SDG\text{-node}\ list \Rightarrow 'node\ SDG\text{-node} \Rightarrow bool$
 $(- is\text{-}\rightarrow_d^* - [51,0,0] 80)$

where *isSp-Nil*:

$valid\text{-SDG-node } n \Longrightarrow n\ is\text{-}\llbracket\ \rightarrow_d^*\ n$

| *isSp-Append-cdep*:

$\llbracket n \text{ is-ns} \rightarrow_d^* n''; n'' s \rightarrow_{cd} n' \rrbracket \Longrightarrow n \text{ is-ns} @ [n''] \rightarrow_d^* n'$

| *isSp-Append-ddep*:

$\llbracket n \text{ is-ns} \rightarrow_d^* n''; n'' s - V \rightarrow_{dd} n'; n'' \neq n' \rrbracket \Longrightarrow n \text{ is-ns} @ [n''] \rightarrow_d^* n'$

| *isSp-Append-sum*:

$\llbracket n \text{ is-ns} \rightarrow_d^* n''; n'' s - p \rightarrow_{sum} n' \rrbracket \Longrightarrow n \text{ is-ns} @ [n''] \rightarrow_d^* n'$

lemma *is-SDG-path-Append*:

$\llbracket n'' \text{ is-ns}' \rightarrow_d^* n'; n \text{ is-ns} \rightarrow_d^* n'' \rrbracket \Longrightarrow n \text{ is-ns} @ ns' \rightarrow_d^* n'$

by(*induct rule:intra-sum-SDG-path.induct*,

auto intro:intra-sum-SDG-path.intros simp:append-assoc[THEN sym]
simp del:append-assoc)

lemma *is-SDG-path-valid-SDG-node*:

assumes $n \text{ is-ns} \rightarrow_d^* n'$ **shows** *valid-SDG-node n and valid-SDG-node n'*

using $\langle n \text{ is-ns} \rightarrow_d^* n' \rangle$

by(*induct rule:intra-sum-SDG-path.induct*,

auto intro:sum-SDG-edge-valid-SDG-node valid-SDG-CFG-node)

lemma *intra-SDG-path-is-SDG-path*:

$n \text{ i-ns} \rightarrow_d^* n' \Longrightarrow n \text{ is-ns} \rightarrow_d^* n'$

by(*induct rule:intra-SDG-path.induct*,

auto intro:intra-sum-SDG-path.intros SDG-edge-sum-SDG-edge)

lemma *is-SDG-path-hd*: $\llbracket n \text{ is-ns} \rightarrow_d^* n'; ns \neq [] \rrbracket \Longrightarrow \text{hd } ns = n$

apply(*induct rule:intra-sum-SDG-path.induct*) **apply** *clarsimp*

by(*case-tac ns, auto elim:intra-sum-SDG-path.cases*)**+**

lemma *intra-sum-SDG-path-rev-induct* [*consumes 1, case-names isSp-Nil*

isSp-Cons-cdep isSp-Cons-ddep isSp-Cons-sum]:

assumes $n \text{ is-ns} \rightarrow_d^* n'$

and *refl*: $\bigwedge n. \text{ valid-SDG-node } n \Longrightarrow P \ n \ [] \ n$

and *step-cdep*: $\bigwedge n \ ns \ n' \ n''. \llbracket n \ s \rightarrow_{cd} n''; n'' \text{ is-ns} \rightarrow_d^* n'; P \ n'' \ ns \ n' \rrbracket$
 $\Longrightarrow P \ n \ (n \# ns) \ n'$

and *step-ddep*: $\bigwedge n \ ns \ n' \ V \ n''. \llbracket n \ s - V \rightarrow_{dd} n''; n \neq n''; n'' \text{ is-ns} \rightarrow_d^* n';$
 $P \ n'' \ ns \ n' \rrbracket \Longrightarrow P \ n \ (n \# ns) \ n'$

and *step-sum*: $\bigwedge n \ ns \ n' \ p \ n''. \llbracket n \ s - p \rightarrow_{sum} n''; n'' \text{ is-ns} \rightarrow_d^* n'; P \ n'' \ ns \ n' \rrbracket$
 $\Longrightarrow P \ n \ (n \# ns) \ n'$

shows $P \ n \ ns \ n'$

using $\langle n \text{ is-ns} \rightarrow_d^* n' \rangle$

proof(*induct ns arbitrary:n*)

case Nil thus ?*case by*(*fastforce elim:intra-sum-SDG-path.cases intro:refl*)

next

case ($\text{Cons } nx \ nsx$)
note $IH = \langle \bigwedge n. n \text{ is-} nsx \rightarrow_{d^*} n' \implies P \ n \ nsx \ n' \rangle$
from $\langle n \text{ is-} nx \# nsx \rightarrow_{d^*} n' \rangle$ **have** $[simp]: n = nx$
by ($\text{fastforce dest:is-SDG-path-hd}$)
from $\langle n \text{ is-} nx \# nsx \rightarrow_{d^*} n' \rangle$ **have** $((\exists n''. n \ s \rightarrow_{cd} n'' \wedge n'' \text{ is-} nsx \rightarrow_{d^*} n') \vee$
 $(\exists n'' \ V. n \ s - V \rightarrow_{dd} n'' \wedge n \neq n'' \wedge n'' \text{ is-} nsx \rightarrow_{d^*} n')) \vee$
 $(\exists n'' \ p. n \ s - p \rightarrow_{sum} n'' \wedge n'' \text{ is-} nsx \rightarrow_{d^*} n')$
proof ($\text{induct } nsx \text{ arbitrary:} n' \text{ rule:rev-induct}$)
case Nil
from $\langle n \text{ is-} [nx] \rightarrow_{d^*} n' \rangle$ **have** $n \text{ is-} [] \rightarrow_{d^*} nx$
and $\text{disj:} nx \ s \rightarrow_{cd} n' \vee (\exists V. nx \ s - V \rightarrow_{dd} n' \wedge nx \neq n') \vee (\exists p. nx \ s - p \rightarrow_{sum}$
 $n')$
by ($\text{induct } n \ ns \equiv [nx] \ n' \text{ rule:intra-sum-SDG-path.induct, auto}$)
from $\langle n \text{ is-} [] \rightarrow_{d^*} nx \rangle$ **have** $[simp]: n = nx$
by ($\text{fastforce elim:intra-sum-SDG-path.cases}$)
from disj **have** $\text{valid-SDG-node } n'$ **by** ($\text{fastforce intro:sum-SDG-edge-valid-SDG-node}$)
hence $n' \text{ is-} [] \rightarrow_{d^*} n'$ **by** (rule isSp-Nil)
with disj **show** $?case$ **by** fastforce
next
case ($\text{snoc } x \ xs$)
note $\langle \bigwedge n'. n \text{ is-} nx \ # \ xs \rightarrow_{d^*} n' \implies$
 $((\exists n''. n \ s \rightarrow_{cd} n'' \wedge n'' \text{ is-} xs \rightarrow_{d^*} n') \vee$
 $(\exists n'' \ V. n \ s - V \rightarrow_{dd} n'' \wedge n \neq n'' \wedge n'' \text{ is-} xs \rightarrow_{d^*} n')) \vee$
 $(\exists n'' \ p. n \ s - p \rightarrow_{sum} n'' \wedge n'' \text{ is-} xs \rightarrow_{d^*} n') \rangle$
with $\langle n \text{ is-} nx \ # xs @ [x] \rightarrow_{d^*} n' \rangle$ **show** $?case$
proof ($\text{induct } n \ nx \ # xs @ [x] \ n' \text{ rule:intra-sum-SDG-path.induct}$)
case ($\text{isSp-Append-cdep } m \ ms \ m'' \ n'$)
note $IH = \langle \bigwedge n'. m \text{ is-} nx \ # \ xs \rightarrow_{d^*} n' \implies$
 $((\exists n''. m \ s \rightarrow_{cd} n'' \wedge n'' \text{ is-} xs \rightarrow_{d^*} n') \vee$
 $(\exists n'' \ V. m \ s - V \rightarrow_{dd} n'' \wedge m \neq n'' \wedge n'' \text{ is-} xs \rightarrow_{d^*} n')) \vee$
 $(\exists n'' \ p. m \ s - p \rightarrow_{sum} n'' \wedge n'' \text{ is-} xs \rightarrow_{d^*} n') \rangle$
from $\langle ms \ @ \ [m''] = nx \ # xs @ [x] \rangle$ **have** $[simp]: ms = nx \ # xs$
and $[simp]: m'' = x$ **by** simp-all
from $\langle m \text{ is-} ms \rightarrow_{d^*} m'' \rangle$ **have** $m \text{ is-} nx \ # xs \rightarrow_{d^*} m''$ **by** simp
from $IH[\text{OF this}]$ **obtain** n'' **where** $n'' \text{ is-} xs \rightarrow_{d^*} m''$
and $(m \ s \rightarrow_{cd} n'' \vee (\exists V. m \ s - V \rightarrow_{dd} n'' \wedge m \neq n'')) \vee (\exists p. m \ s - p \rightarrow_{sum}$
 $n'')$
by fastforce
from $\langle n'' \text{ is-} xs \rightarrow_{d^*} m'' \rangle$ $\langle m'' \ s \rightarrow_{cd} n' \rangle$
have $n'' \text{ is-} xs @ [m''] \rightarrow_{d^*} n'$ **by** ($\text{rule intra-sum-SDG-path.intros}$)
with $\langle (m \ s \rightarrow_{cd} n'' \vee (\exists V. m \ s - V \rightarrow_{dd} n'' \wedge m \neq n'')) \vee (\exists p. m \ s - p \rightarrow_{sum}$
 $n'') \rangle$
show $?case$ **by** fastforce
next
case ($\text{isSp-Append-ddep } m \ ms \ m'' \ V \ n'$)
note $IH = \langle \bigwedge n'. m \text{ is-} nx \ # \ xs \rightarrow_{d^*} n' \implies$
 $((\exists n''. m \ s \rightarrow_{cd} n'' \wedge n'' \text{ is-} xs \rightarrow_{d^*} n') \vee$
 $(\exists n'' \ V. m \ s - V \rightarrow_{dd} n'' \wedge m \neq n'' \wedge n'' \text{ is-} xs \rightarrow_{d^*} n')) \vee$
 $(\exists n'' \ p. m \ s - p \rightarrow_{sum} n'' \wedge n'' \text{ is-} xs \rightarrow_{d^*} n') \rangle$

from $\langle ms @ [m'] = nx \# xs @ [x] \rangle$ **have** $[simp]: ms = nx \# xs$
and $[simp]: m'' = x$ **by** *simp-all*
from $\langle m \text{ is-}ms \rightarrow_{d^*} m'' \rangle$ **have** $m \text{ is-}nx \# xs \rightarrow_{d^*} m''$ **by** *simp*
from $IH[OF \text{ this}]$ **obtain** n'' **where** $n'' \text{ is-}xs \rightarrow_{d^*} m''$
and $\langle m s \rightarrow_{cd} n'' \vee (\exists V. m s - V \rightarrow_{dd} n'' \wedge m \neq n'') \rangle \vee (\exists p. m s - p \rightarrow_{sum} n'')$
by *fastforce*
from $\langle n'' \text{ is-}xs \rightarrow_{d^*} m'' \rangle \langle m'' s - V \rightarrow_{dd} n' \rangle \langle m'' \neq n' \rangle$
have $n'' \text{ is-}xs @ [m'] \rightarrow_{d^*} n'$ **by** $(rule \text{ intra-sum-SDG-path.intros})$
with $\langle (m s \rightarrow_{cd} n'' \vee (\exists V. m s - V \rightarrow_{dd} n'' \wedge m \neq n'')) \rangle \vee (\exists p. m s - p \rightarrow_{sum} n'')$
show *?case* **by** *fastforce*
next
case $(isSp-Append-sum \ m \ ms \ m'' \ p \ n')$
note $IH = \langle \bigwedge n'. m \text{ is-}nx \# xs \rightarrow_{d^*} n' \implies$
 $(\exists n''. m s \rightarrow_{cd} n'' \wedge n'' \text{ is-}xs \rightarrow_{d^*} n') \vee$
 $(\exists n'' V. m s - V \rightarrow_{dd} n'' \wedge m \neq n'' \wedge n'' \text{ is-}xs \rightarrow_{d^*} n') \rangle \vee$
 $(\exists n'' p. m s - p \rightarrow_{sum} n'' \wedge n'' \text{ is-}xs \rightarrow_{d^*} n') \rangle$
from $\langle ms @ [m'] = nx \# xs @ [x] \rangle$ **have** $[simp]: ms = nx \# xs$
and $[simp]: m'' = x$ **by** *simp-all*
from $\langle m \text{ is-}ms \rightarrow_{d^*} m'' \rangle$ **have** $m \text{ is-}nx \# xs \rightarrow_{d^*} m''$ **by** *simp*
from $IH[OF \text{ this}]$ **obtain** n'' **where** $n'' \text{ is-}xs \rightarrow_{d^*} m''$
and $\langle m s \rightarrow_{cd} n'' \vee (\exists V. m s - V \rightarrow_{dd} n'' \wedge m \neq n'') \rangle \vee (\exists p. m s - p \rightarrow_{sum} n'')$
by *fastforce*
from $\langle n'' \text{ is-}xs \rightarrow_{d^*} m'' \rangle \langle m'' s - p \rightarrow_{sum} n' \rangle$
have $n'' \text{ is-}xs @ [m'] \rightarrow_{d^*} n'$ **by** $(rule \text{ intra-sum-SDG-path.intros})$
with $\langle (m s \rightarrow_{cd} n'' \vee (\exists V. m s - V \rightarrow_{dd} n'' \wedge m \neq n'')) \rangle \vee (\exists p. m s - p \rightarrow_{sum} n'')$
show *?case* **by** *fastforce*
qed
qed
thus *?case* **apply** $-$
proof $(erule \text{ disjE})+$
assume $\exists n''. n s \rightarrow_{cd} n'' \wedge n'' \text{ is-}nsx \rightarrow_{d^*} n'$
then obtain n'' **where** $n s \rightarrow_{cd} n''$ **and** $n'' \text{ is-}nsx \rightarrow_{d^*} n'$ **by** *blast*
from $IH[OF \langle n'' \text{ is-}nsx \rightarrow_{d^*} n' \rangle]$ **have** $P \ n'' \ nsx \ n'$.
from $step-cdep[OF \langle n s \rightarrow_{cd} n'' \rangle \langle n'' \text{ is-}nsx \rightarrow_{d^*} n' \rangle \text{ this}]$ **show** *?thesis* **by** *simp*
next
assume $\exists n'' V. n s - V \rightarrow_{dd} n'' \wedge n \neq n'' \wedge n'' \text{ is-}nsx \rightarrow_{d^*} n'$
then obtain $n'' V$ **where** $n s - V \rightarrow_{dd} n''$ **and** $n \neq n''$ **and** $n'' \text{ is-}nsx \rightarrow_{d^*} n'$
by *blast*
from $IH[OF \langle n'' \text{ is-}nsx \rightarrow_{d^*} n' \rangle]$ **have** $P \ n'' \ nsx \ n'$.
from $step-ddep[OF \langle n s - V \rightarrow_{dd} n'' \rangle \langle n \neq n'' \rangle \langle n'' \text{ is-}nsx \rightarrow_{d^*} n' \rangle \text{ this}]$
show *?thesis* **by** *simp*
next
assume $\exists n'' p. n s - p \rightarrow_{sum} n'' \wedge n'' \text{ is-}nsx \rightarrow_{d^*} n'$

then obtain $n'' p$ **where** $n s \rightarrow_{sum} n''$ **and** $n'' is \rightarrow_{d^*} n'$ **by** *blast*
from $IH[OF \langle n'' is \rightarrow_{d^*} n' \rangle]$ **have** $P n'' nsx n'$.
from $step\text{-}sum[OF \langle n s \rightarrow_{sum} n'' \rangle \langle n'' is \rightarrow_{d^*} n' \rangle \textit{this}]$ **show** *?thesis*
by *simp*
qed
qed

lemma *is-SDG-path-CFG-path*:

assumes $n is \rightarrow_{d^*} n'$

obtains *as* **where** $parent\text{-}node\ n \rightarrow_{i^*} parent\text{-}node\ n'$

proof(*atomize-elim*)

from $\langle n is \rightarrow_{d^*} n' \rangle$

show $\exists as. parent\text{-}node\ n \rightarrow_{i^*} parent\text{-}node\ n'$

proof(*induct rule:intra-sum-SDG-path.induct*)

case (*isSp-Nil* n)

from $\langle valid\text{-}SDG\text{-}node\ n \rangle$ **have** $valid\text{-}node\ (parent\text{-}node\ n)$

by(*rule valid-SDG-CFG-node*)

hence $parent\text{-}node\ n \rightarrow_{i^*} parent\text{-}node\ n$ **by**(*rule empty-path*)

thus *?case* **by**(*auto simp:intra-path-def*)

next

case (*isSp-Append-cdep* $n\ ns\ n''\ n'$)

from $\langle \exists as. parent\text{-}node\ n \rightarrow_{i^*} parent\text{-}node\ n'' \rangle$

obtain *as* **where** $parent\text{-}node\ n \rightarrow_{i^*} parent\text{-}node\ n''$ **by** *blast*

from $\langle n'' s \rightarrow_{cd} n' \rangle$ **have** $n'' \rightarrow_{cd} n'$ **by**(*rule sum-SDG-edge-SDG-edge*)

thus *?case*

proof(*rule cdep-edge-cases*)

assume $parent\text{-}node\ n''$ *controls* $parent\text{-}node\ n'$

then obtain as' **where** $parent\text{-}node\ n'' \rightarrow_{i^*} parent\text{-}node\ n'$ **and** $as' \neq$

□

by(*erule control-dependence-path*)

with $\langle parent\text{-}node\ n \rightarrow_{i^*} parent\text{-}node\ n'' \rangle$

have $parent\text{-}node\ n \rightarrow_{as@as'} \rightarrow_{i^*} parent\text{-}node\ n'$ **by** \rightarrow (*rule intra-path-Append*)

thus *?thesis* **by** *blast*

next

fix $a\ Q\ r\ p\ fs\ a'$

assume $valid\text{-}edge\ a$ **and** $kind\ a = Q:r \hookrightarrow pfs$ **and** $a' \in get\text{-}return\text{-}edges\ a$

and $parent\text{-}node\ n'' = targetnode\ a$ **and** $parent\text{-}node\ n' = sourcenode\ a'$

then obtain a'' **where** $valid\text{-}edge\ a''$ **and** $sourcenode\ a'' = targetnode\ a$

and $targetnode\ a'' = sourcenode\ a'$ **and** $kind\ a'' = (\lambda cf. False)_{\checkmark}$

by(*auto dest:intra-proc-additional-edge*)

hence $targetnode\ a \rightarrow_{[a'']} \rightarrow_{i^*} sourcenode\ a'$

by(*fastforce dest:path-edge simp:intra-path-def intra-kind-def*)

with $\langle parent\text{-}node\ n'' = targetnode\ a \rangle$ $\langle parent\text{-}node\ n' = sourcenode\ a' \rangle$

have $\exists as'. parent\text{-}node\ n'' \rightarrow_{as'} \rightarrow_{i^*} parent\text{-}node\ n' \wedge as' \neq \square$ **by** *fastforce*

then obtain as' **where** $parent\text{-}node\ n'' \rightarrow_{as'} \rightarrow_{i^*} parent\text{-}node\ n'$ **and** $as' \neq$

□

by *blast*

with $\langle parent\text{-}node\ n \rightarrow_{i^*} parent\text{-}node\ n'' \rangle$

```

have parent-node  $n - as @ as' \rightarrow_i^*$  parent-node  $n'$  by  $-(rule\ intra-path-Append)$ 
thus ?thesis by blast
next
fix  $m$  assume  $n'' = CFG-node\ m$  and  $m = parent-node\ n'$ 
with  $\langle parent-node\ n - as \rightarrow_i^* parent-node\ n'' \rangle$  show ?thesis by fastforce
qed
next
case (isSp-Append-ddep  $n\ ns\ n''\ V\ n'$ )
from  $\langle \exists as. parent-node\ n - as \rightarrow_i^* parent-node\ n'' \rangle$ 
obtain  $as$  where parent-node  $n - as \rightarrow_i^* parent-node\ n''$  by blast
from  $\langle n''\ s - V \rightarrow_{dd}\ n' \rangle$  have  $n''$  influences  $V$  in  $n'$ 
by (fastforce elim:sum-SDG-edge.cases)
then obtain  $as'$  where parent-node  $n'' - as' \rightarrow_i^* parent-node\ n'$ 
by (auto simp:data-dependence-def)
with  $\langle parent-node\ n - as \rightarrow_i^* parent-node\ n'' \rangle$ 
have parent-node  $n - as @ as' \rightarrow_i^* parent-node\ n'$  by  $-(rule\ intra-path-Append)$ 
thus ?case by blast
next
case (isSp-Append-sum  $n\ ns\ n''\ p\ n'$ )
from  $\langle \exists as. parent-node\ n - as \rightarrow_i^* parent-node\ n'' \rangle$ 
obtain  $as$  where parent-node  $n - as \rightarrow_i^* parent-node\ n''$  by blast
from  $\langle n''\ s - p \rightarrow_{sum}\ n' \rangle$  have  $\exists as'. parent-node\ n'' - as' \rightarrow_i^* parent-node\ n'$ 
proof (rule sum-edge-cases)
fix  $a\ Q\ fs\ a'$ 
assume valid-edge  $a$  and  $a' \in get-return-edges\ a$ 
and  $n'' = CFG-node\ (sourcenode\ a)$  and  $n' = CFG-node\ (targetnode\ a')$ 
from  $\langle valid-edge\ a \rangle \langle a' \in get-return-edges\ a \rangle$ 
obtain  $a''$  where sourcenode  $a - [a''] \rightarrow_i^* targetnode\ a'$ 
apply  $-\ apply$  (drule call-return-node-edge)
apply (auto simp:intra-path-def) apply (drule path-edge)
by (auto simp:intra-kind-def)
with  $\langle n'' = CFG-node\ (sourcenode\ a) \rangle \langle n' = CFG-node\ (targetnode\ a') \rangle$ 
show ?thesis by simp blast
next
fix  $a\ p\ fs\ a'\ ns\ x\ x'\ ins\ outs$ 
assume valid-edge  $a$  and  $a' \in get-return-edges\ a$ 
and  $n'' = Actual-in\ (sourcenode\ a,\ x)$ 
and  $n' = Actual-out\ (targetnode\ a',\ x')$ 
from  $\langle valid-edge\ a \rangle \langle a' \in get-return-edges\ a \rangle$ 
obtain  $a''$  where sourcenode  $a - [a''] \rightarrow_i^* targetnode\ a'$ 
apply  $-\ apply$  (drule call-return-node-edge)
apply (auto simp:intra-path-def) apply (drule path-edge)
by (auto simp:intra-kind-def)
with  $\langle n'' = Actual-in\ (sourcenode\ a,\ x) \rangle \langle n' = Actual-out\ (targetnode\ a',\ x') \rangle$ 
show ?thesis by simp blast
qed
then obtain  $as'$  where parent-node  $n'' - as' \rightarrow_i^* parent-node\ n'$  by blast
with  $\langle parent-node\ n - as \rightarrow_i^* parent-node\ n'' \rangle$ 
have parent-node  $n - as @ as' \rightarrow_i^* parent-node\ n'$  by  $-(rule\ intra-path-Append)$ 

```

thus ?case by blast
 qed
 qed

lemma *matched-is-SDG-path*:

assumes *matched* n ns n' obtains ns' where n *is-ns'* \rightarrow_d^* n'
 proof (atomize-elim)
 from \langle *matched* n ns n' \rangle show $\exists ns'. n$ *is-ns'* \rightarrow_d^* n'
 proof (induct rule:*matched.induct*)
 case *matched-Nil* thus ?case by (fastforce intro:*isSp-Nil*)
 next
 case *matched-Append-intra-SDG-path* thus ?case
 by (fastforce intro:*is-SDG-path-Append intra-SDG-path-is-SDG-path*)
 next
 case \langle *matched-bracket-call* n_0 ns n_1 p n_2 ns' n_3 n_4 V a a' \rangle
 from $\langle \exists ns'. n_0$ *is-ns'* \rightarrow_d^* $n_1 \rangle$ obtain nsx where n_0 *is-nsx* \rightarrow_d^* n_1 by blast
 from $\langle n_1 -p\rightarrow_{call}$ $n_2 \rangle$ \langle *sourcenode* $a =$ *parent-node* $n_1 \rangle$ \langle *targetnode* $a =$ *parent-node* $n_2 \rangle$
 have $n_1 =$ *CFG-node* (*sourcenode* a) and $n_2 =$ *CFG-node* (*targetnode* a)
 by (auto elim:*SDG-edge.cases*)
 from \langle *valid-edge* $a \rangle$ $\langle a' \in$ *get-return-edges* $a \rangle$
 obtain Q r p' fs where *kind* $a = Q:r\hookrightarrow_p fs$
 by (fastforce dest!:*only-call-get-return-edges*)
 with $\langle n_1 -p\rightarrow_{call}$ $n_2 \rangle$ \langle *valid-edge* $a \rangle$
 $\langle n_1 =$ *CFG-node* (*sourcenode* a) \rangle $\langle n_2 =$ *CFG-node* (*targetnode* a) \rangle
 have [simp]: $p' = p$ by $-($ *erule* *SDG-edge.cases*, (fastforce dest:*edge-det*) $)+$
 from \langle *valid-edge* $a \rangle$ $\langle a' \in$ *get-return-edges* $a \rangle$ have *valid-edge* a'
 by (rule *get-return-edges-valid*)
 from $\langle n_3 -p\rightarrow_{ret}$ $n_4 \vee n_3 -p:V\rightarrow_{out}$ $n_4 \rangle$ show ?case
 proof
 assume $n_3 -p\rightarrow_{ret}$ n_4
 then obtain ax Q' f' where *valid-edge* ax and *kind* $ax = Q'\hookrightarrow_p f'$
 and $n_3 =$ *CFG-node* (*sourcenode* ax) and $n_4 =$ *CFG-node* (*targetnode* ax)
 by (fastforce elim:*SDG-edge.cases*)
 with \langle *sourcenode* $a' =$ *parent-node* $n_3 \rangle$ \langle *targetnode* $a' =$ *parent-node* $n_4 \rangle$
 \langle *valid-edge* $a' \rangle$ have [simp]: $ax = a'$ by (fastforce dest:*edge-det*)
 from \langle *valid-edge* $a \rangle$ \langle *kind* $a = Q:r\hookrightarrow_p fs \rangle$ \langle *valid-edge* $ax \rangle$ \langle *kind* $ax = Q'\hookrightarrow_p f' \rangle$
 $\langle a' \in$ *get-return-edges* $a \rangle$ \langle *matched* n_2 ns' $n_3 \rangle$
 $\langle n_1 =$ *CFG-node* (*sourcenode* a) \rangle $\langle n_2 =$ *CFG-node* (*targetnode* a) \rangle
 $\langle n_3 =$ *CFG-node* (*sourcenode* ax) \rangle $\langle n_4 =$ *CFG-node* (*targetnode* ax) \rangle
 have n_1 *s-p* \rightarrow_{sum} n_4
 by (fastforce intro!:*sum-SDG-call-summary-edge*[of a - - - ax])
 with $\langle n_0$ *is-nsx* \rightarrow_d^* $n_1 \rangle$ have n_0 *is-nsx* \rightarrow_d^* n_4 by (rule *isSp-Append-sum*)
 thus ?case by blast
 next
 assume $n_3 -p:V\rightarrow_{out}$ n_4
 then obtain ax Q' f' x where *valid-edge* ax and *kind* $ax = Q'\hookrightarrow_p f'$
 and $n_3 =$ *Formal-out* (*sourcenode* ax, x)

```

    and  $n_4 = \text{Actual-out } (\text{targetnode } ax, x)$ 
    by(fastforce elim:SDG-edge.cases)
  with  $\langle \text{sourcenode } a' = \text{parent-node } n_3 \rangle \langle \text{targetnode } a' = \text{parent-node } n_4 \rangle$ 
     $\langle \text{valid-edge } a' \rangle$  have [simp]: $ax = a'$  by(fastforce dest:edge-det)
from  $\langle \text{valid-edge } ax \rangle \langle \text{kind } ax = Q' \leftarrow_p f' \rangle \langle n_3 = \text{Formal-out } (\text{sourcenode } ax, x) \rangle$ 
   $\langle n_4 = \text{Actual-out } (\text{targetnode } ax, x) \rangle$ 
have CFG-node ( $\text{sourcenode } a'$ )  $-p \rightarrow_{ret}$  CFG-node ( $\text{targetnode } a'$ )
  by(fastforce intro:SDG-return-edge)
from  $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q:r \leftarrow_p fs \rangle \langle \text{valid-edge } a' \rangle$ 
   $\langle a' \in \text{get-return-edges } a \rangle \langle n_4 = \text{Actual-out } (\text{targetnode } ax, x) \rangle$ 
have CFG-node ( $\text{targetnode } a$ )  $\rightarrow_{cd}$  CFG-node ( $\text{sourcenode } a'$ )
  by(fastforce intro!:SDG-proc-entry-exit-cdep)
with  $n_2 = \text{CFG-node } (\text{targetnode } a)$ 
have matched  $n_2$  ( $\llbracket @(\llbracket @ [n_2] \rrbracket) \rrbracket$ ) (CFG-node ( $\text{sourcenode } a'$ ))
  by(fastforce intro:matched.intros intra-SDG-path.intros
    SDG-edge-valid-SDG-node)
with  $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q:r \leftarrow_p fs \rangle \langle \text{valid-edge } a' \rangle \langle \text{kind } ax = Q' \leftarrow_p f' \rangle$ 
   $\langle a' \in \text{get-return-edges } a \rangle \langle n_1 = \text{CFG-node } (\text{sourcenode } a) \rangle$ 
   $\langle n_2 = \text{CFG-node } (\text{targetnode } a) \rangle \langle n_4 = \text{Actual-out } (\text{targetnode } ax, x) \rangle$ 
have  $n_1$   $s-p \rightarrow_{sum}$  CFG-node ( $\text{targetnode } a'$ )
  by(fastforce intro!:sum-SDG-call-summary-edge[of a - - - a'])
with  $\langle n_0$  is-nsx $\rightarrow_{d^*}$   $n_1 \rangle$  have  $n_0$  is-nsx $\@[n_1] \rightarrow_{d^*}$  CFG-node ( $\text{targetnode}$ 
 $a'$ )
  by(rule isSp-Append-sum)
from  $\langle n_4 = \text{Actual-out } (\text{targetnode } ax, x) \rangle \langle n_3 -p:V \rightarrow_{out} n_4 \rangle$ 
have CFG-node ( $\text{targetnode } a'$ )  $s \rightarrow_{cd}$   $n_4$ 
  by(fastforce intro:sum-SDG-parent-cdep-edge SDG-edge-valid-SDG-node)
with  $\langle n_0$  is-nsx $\@[n_1] \rightarrow_{d^*}$  CFG-node ( $\text{targetnode } a'$ )
have  $n_0$  is-nsx $\@[n_1] \@[CFG-node (\text{targetnode } a')] \rightarrow_{d^*}$   $n_4$ 
  by(rule isSp-Append-cdep)
thus ?case by blast
qed
next
case (matched-bracket-param  $n_0$  ns  $n_1$   $p$   $V$   $n_2$  ns'  $n_3$   $V'$   $n_4$   $a$   $a'$ )
from  $\langle \exists ns'. n_0$  is-ns' $\rightarrow_{d^*}$   $n_1 \rangle$  obtain nsx where  $n_0$  is-nsx $\rightarrow_{d^*}$   $n_1$  by blast
from  $\langle n_1 -p:V \rightarrow_{in} n_2 \rangle \langle \text{sourcenode } a = \text{parent-node } n_1 \rangle$ 
   $\langle \text{targetnode } a = \text{parent-node } n_2 \rangle$  obtain  $x$  ins outs
where  $n_1 = \text{Actual-in } (\text{sourcenode } a, x)$  and  $n_2 = \text{Formal-in } (\text{targetnode } a, x)$ 
and  $(p, ins, outs) \in \text{set procs}$  and  $V = ins!x$  and  $x < \text{length } ins$ 
  by(fastforce elim:SDG-edge.cases)
from  $\langle \text{valid-edge } a \rangle \langle a' \in \text{get-return-edges } a \rangle$ 
obtain  $Q$   $r$   $p'$   $fs$  where  $\text{kind } a = Q:r \leftarrow_p fs$ 
  by(fastforce dest!:only-call-get-return-edges)
with  $\langle n_1 -p:V \rightarrow_{in} n_2 \rangle \langle \text{valid-edge } a \rangle$ 
   $\langle n_1 = \text{Actual-in } (\text{sourcenode } a, x) \rangle \langle n_2 = \text{Formal-in } (\text{targetnode } a, x) \rangle$ 
have [simp]: $p' = p$  by  $-(\text{erule } \text{SDG-edge.cases}, (\text{fastforce } \text{dest:edge-det}))+$ 
from  $\langle \text{valid-edge } a \rangle \langle a' \in \text{get-return-edges } a \rangle$  have valid-edge  $a'$ 
  by(rule get-return-edges-valid)
from  $\langle n_3 -p:V' \rightarrow_{out} n_4 \rangle$  obtain  $ax$   $Q'$   $f'$   $x'$  ins' outs' where valid-edge  $ax$ 

```

```

and  $kind\ ax = Q' \leftrightarrow_{pf} f'$  and  $n_3 = Formal-out\ (sourcnode\ ax, x')$ 
and  $n_4 = Actual-out\ (targetnode\ ax, x')$  and  $(p, ins', outs') \in set\ procs$ 
and  $V' = outs' \setminus x'$  and  $x' < length\ outs'$ 
by(fastforce elim:SDG-edge.cases)
with  $\langle sourcnode\ a' = parent-node\ n_3 \rangle \langle targetnode\ a' = parent-node\ n_4 \rangle$ 
 $\langle valid-edge\ a' \rangle$  have [simp]:  $ax = a'$  by(fastforce dest:edge-det)
from unique-callers  $\langle (p, ins, outs) \in set\ procs \rangle \langle (p, ins', outs') \in set\ procs \rangle$ 
have [simp]:  $ins = ins'\ outs = outs'$ 
by(auto dest:distinct-fst-isin-same-fst)
from  $\langle valid-edge\ a \rangle \langle kind\ a = Q:r \rightarrow_p fs \rangle \langle valid-edge\ a' \rangle \langle kind\ ax = Q' \leftrightarrow_{pf} f' \rangle$ 
 $\langle a' \in get-return-edges\ a \rangle \langle matched\ n_2\ ns'\ n_3 \rangle \langle n_1 = Actual-in\ (sourcnode\ a, x) \rangle$ 
 $\langle n_2 = Formal-in\ (targetnode\ a, x) \rangle \langle n_3 = Formal-out\ (sourcnode\ ax, x') \rangle$ 
 $\langle n_4 = Actual-out\ (targetnode\ ax, x') \rangle \langle (p, ins, outs) \in set\ procs \rangle$ 
 $\langle x < length\ ins \rangle \langle x' < length\ outs' \rangle \langle V = ins \setminus x \rangle \langle V' = outs' \setminus x' \rangle$ 
have  $n_1\ s-p \rightarrow_{sum}\ n_4$ 
by(fastforce intro!:sum-SDG-param-summary-edge[of a - - - a'])
with  $\langle n_0\ is-nsx \rightarrow_{d^*}\ n_1 \rangle$  have  $n_0\ is-nsx @ [n_1] \rightarrow_{d^*}\ n_4$  by(rule isSp-Append-sum)
thus ?case by blast
qed
qed

```

lemma *is-SDG-path-matched*:

assumes $n\ is-ns \rightarrow_{d^*}\ n'$ **obtains** ns' **where** $matched\ n\ ns'\ n'$ **and** $set\ ns \subseteq set\ ns'$

proof(*atomize-elim*)

from $\langle n\ is-ns \rightarrow_{d^*}\ n' \rangle$ **show** $\exists ns'.\ matched\ n\ ns'\ n' \wedge set\ ns \subseteq set\ ns'$

proof(*induct rule:intra-sum-SDG-path.induct*)

case (*isSp-Nil* n)

from $\langle valid-SDG-node\ n \rangle$ **have** $matched\ n\ []\ n$ **by**(*rule matched-Nil*)

thus ?*case* **by** *fastforce*

next

case (*isSp-Append-cdep* $n\ ns\ n''\ n'$)

from $\langle \exists ns'.\ matched\ n\ ns'\ n'' \wedge set\ ns \subseteq set\ ns' \rangle$

obtain ns' **where** $matched\ n\ ns'\ n''$ **and** $set\ ns \subseteq set\ ns'$ **by** *blast*

from $\langle n''\ s \rightarrow_{cd}\ n' \rangle$ **have** $n''\ i-[] @ [n''] \rightarrow_{d^*}\ n'$

by(*fastforce intro:intra-SDG-path.intros sum-SDG-edge-valid-SDG-node sum-SDG-edge-SDG-edge*)

with $\langle matched\ n\ ns'\ n'' \rangle$ **have** $matched\ n\ (ns' @ [n''])\ n'$

by(*fastforce intro!:matched-Append-intra-SDG-path*)

with $\langle set\ ns \subseteq set\ ns' \rangle$ **show** ?*case* **by** *fastforce*

next

case (*isSp-Append-ddep* $n\ ns\ n''\ V\ n'$)

from $\langle \exists ns'.\ matched\ n\ ns'\ n'' \wedge set\ ns \subseteq set\ ns' \rangle$

obtain ns' **where** $matched\ n\ ns'\ n''$ **and** $set\ ns \subseteq set\ ns'$ **by** *blast*

from $\langle n''\ s - V \rightarrow_{dd}\ n' \rangle \langle n'' \neq n' \rangle$ **have** $n''\ i-[] @ [n''] \rightarrow_{d^*}\ n'$

by(*fastforce intro:intra-SDG-path.intros sum-SDG-edge-valid-SDG-node sum-SDG-edge-SDG-edge*)

with $\langle \text{matched } n \text{ } ns' \text{ } n'' \rangle$ **have** $\text{matched } n \text{ } (ns'@[n'']) \text{ } n'$
by $(\text{fastforce } \text{intro!}:\text{matched-Append-intra-SDG-path})$
with $\langle \text{set } ns \subseteq \text{set } ns' \rangle$ **show** $?case$ **by** fastforce
next
case $(\text{isSp-Append-sum } n \text{ } ns \text{ } n'' \text{ } p \text{ } n')$
from $\langle \exists ns'. \text{matched } n \text{ } ns' \text{ } n'' \wedge \text{set } ns \subseteq \text{set } ns' \rangle$
obtain ns' **where** $\text{matched } n \text{ } ns' \text{ } n''$ **and** $\text{set } ns \subseteq \text{set } ns'$ **by** blast
from $\langle n'' \text{ } s \text{ } \rightarrow_{\text{sum}} \text{ } n' \rangle$ **obtain** ns'' **where** $\text{matched } n'' \text{ } ns'' \text{ } n'$ **and** $n'' \in \text{set } ns''$
by $-(\text{erule } \text{sum-SDG-summary-edge-matched})$
with $\langle \text{matched } n \text{ } ns' \text{ } n'' \rangle$ **have** $\text{matched } n \text{ } (ns'@[ns'']) \text{ } n'$ **by** $-(\text{rule } \text{matched-Append})$
with $\langle \text{set } ns \subseteq \text{set } ns' \rangle$ $\langle n'' \in \text{set } ns'' \rangle$ **show** $?case$ **by** fastforce
qed
qed

lemma $\text{is-SDG-path-intra-CFG-path}$:

assumes $n \text{ } \text{is-ns} \rightarrow_d^* \text{ } n'$
obtains as **where** $\text{parent-node } n \text{ } \text{--} as \rightarrow_{\iota}^* \text{ } \text{parent-node } n'$
proof (atomize-elim)
from $\langle n \text{ } \text{is-ns} \rightarrow_d^* \text{ } n' \rangle$
show $\exists as. \text{parent-node } n \text{ } \text{--} as \rightarrow_{\iota}^* \text{ } \text{parent-node } n'$
proof $(\text{induct } \text{rule}:\text{intra-sum-SDG-path.induct})$
case $(\text{isSp-Nil } n)$
from $\langle \text{valid-SDG-node } n \rangle$ **have** $\text{parent-node } n \text{ } \text{--} [] \rightarrow^* \text{ } \text{parent-node } n$
by $(\text{fastforce } \text{intro}:\text{empty-path } \text{valid-SDG-CFG-node})$
thus $?case$ **by** $(\text{auto } \text{simp}:\text{intra-path-def})$
next
case $(\text{isSp-Append-cdep } n \text{ } ns \text{ } n'' \text{ } n')$
from $\langle \exists as. \text{parent-node } n \text{ } \text{--} as \rightarrow_{\iota}^* \text{ } \text{parent-node } n'' \rangle$
obtain as **where** $\text{parent-node } n \text{ } \text{--} as \rightarrow_{\iota}^* \text{ } \text{parent-node } n''$ **by** blast
from $\langle n'' \text{ } s \text{ } \rightarrow_{cd} \text{ } n' \rangle$ **have** $n'' \text{ } \rightarrow_{cd} \text{ } n'$ **by** $(\text{rule } \text{sum-SDG-edge-SDG-edge})$
thus $?case$
proof $(\text{rule } \text{cdep-edge-cases})$
assume $\text{parent-node } n'' \text{ } \text{controls } \text{parent-node } n'$
then obtain as' **where** $\text{parent-node } n'' \text{ } \text{--} as' \rightarrow_{\iota}^* \text{ } \text{parent-node } n'$ **and** $as' \neq$
 as
by $(\text{erule } \text{control-dependence-path})$
with $\langle \text{parent-node } n \text{ } \text{--} as \rightarrow_{\iota}^* \text{ } \text{parent-node } n'' \rangle$
have $\text{parent-node } n \text{ } \text{--} as @ as' \rightarrow_{\iota}^* \text{ } \text{parent-node } n'$ **by** $-(\text{rule } \text{intra-path-Append})$
thus $?thesis$ **by** blast
next
fix $a \text{ } Q \text{ } r \text{ } p \text{ } fs \text{ } a'$
assume $\text{valid-edge } a$ **and** $\text{kind } a = Q:r \hookrightarrow_p fs \text{ } a' \in \text{get-return-edges } a$
and $\text{parent-node } n'' = \text{targetnode } a$ **and** $\text{parent-node } n' = \text{sourcenode } a'$
then obtain a'' **where** $\text{valid-edge } a''$ **and** $\text{sourcenode } a'' = \text{targetnode } a$
and $\text{targetnode } a'' = \text{sourcenode } a'$ **and** $\text{kind } a'' = (\lambda cf. \text{False}) \checkmark$
by $(\text{auto } \text{dest}:\text{intra-proc-additional-edge})$
hence $\text{targetnode } a \text{ } \text{--} [a''] \rightarrow_{\iota}^* \text{ } \text{sourcenode } a'$

```

    by(fastforce dest:path-edge simp:intra-path-def intra-kind-def)
  with ⟨parent-node n'' = targetnode a⟩ ⟨parent-node n' = sourcenode a'⟩
  have ∃ as'. parent-node n'' -as'→i* parent-node n' ∧ as' ≠ [] by fastforce
  then obtain as' where parent-node n'' -as'→i* parent-node n' and as' ≠
[]
    by blast
  with ⟨parent-node n -as→i* parent-node n''⟩
  have parent-node n -as@as'→i* parent-node n' by -(rule intra-path-Append)
  thus ?thesis by blast
next
  fix m assume n'' = CFG-node m and m = parent-node n'
  with ⟨parent-node n -as→i* parent-node n''⟩ show ?thesis by fastforce
qed
next
  case (isSp-Append-ddep n ns n'' V n')
  from ⟨∃ as. parent-node n -as→i* parent-node n''⟩
  obtain as where parent-node n -as→i* parent-node n'' by blast
  from ⟨n'' s -V→dd n'⟩ have n'' influences V in n'
    by(fastforce elim:sum-SDG-edge.cases)
  then obtain as' where parent-node n'' -as'→i* parent-node n'
    by(auto simp:data-dependence-def)
  with ⟨parent-node n -as→i* parent-node n''⟩
  have parent-node n -as@as'→i* parent-node n' by -(rule intra-path-Append)
  thus ?case by blast
next
  case (isSp-Append-sum n ns n'' p n')
  from ⟨∃ as. parent-node n -as→i* parent-node n''⟩
  obtain as where parent-node n -as→i* parent-node n'' by blast
  from ⟨n'' s -p→sum n'⟩ obtain ns' where matched n'' ns' n'
    by -(erule sum-SDG-summary-edge-matched)
  then obtain as' where parent-node n'' -as'→i* parent-node n'
    by(erule matched-intra-CFG-path)
  with ⟨parent-node n -as→i* parent-node n''⟩
  have parent-node n -as@as'→i* parent-node n'
    by(fastforce intro:path-Append simp:intra-path-def)
  thus ?case by blast
qed
qed

```

SDG paths without return edges

inductive *intra-call-sum-SDG-path* ::

'node SDG-node ⇒ *'node SDG-node list* ⇒ *'node SDG-node* ⇒ *bool*

(- *ics* ->_d* - [51,0,0] 80)

where *icsSp-Nil*:

valid-SDG-node n ⇒ *n ics* - [] →_d* *n*

| *icsSp-Append-cdep*:

$\llbracket n \text{ ics} - ns \rightarrow_d^* n''; n'' s \rightarrow_{cd} n' \rrbracket \implies n \text{ ics} - ns @ [n''] \rightarrow_d^* n'$

| *icsSp-Append-ddep*:
 $\llbracket n \text{ ics-ns} \rightarrow_{d^*} n''; n'' \text{ s-V} \rightarrow_{dd} n'; n'' \neq n' \rrbracket \Longrightarrow n \text{ ics-ns} @ [n'] \rightarrow_{d^*} n'$

| *icsSp-Append-sum*:
 $\llbracket n \text{ ics-ns} \rightarrow_{d^*} n''; n'' \text{ s-p} \rightarrow_{sum} n' \rrbracket \Longrightarrow n \text{ ics-ns} @ [n'] \rightarrow_{d^*} n'$

| *icsSp-Append-call*:
 $\llbracket n \text{ ics-ns} \rightarrow_{d^*} n''; n'' \text{ s-p} \rightarrow_{call} n' \rrbracket \Longrightarrow n \text{ ics-ns} @ [n'] \rightarrow_{d^*} n'$

| *icsSp-Append-param-in*:
 $\llbracket n \text{ ics-ns} \rightarrow_{d^*} n''; n'' \text{ s-p: V} \rightarrow_{in} n' \rrbracket \Longrightarrow n \text{ ics-ns} @ [n'] \rightarrow_{d^*} n'$

lemma *ics-SDG-path-valid-SDG-node*:

assumes $n \text{ ics-ns} \rightarrow_{d^*} n'$ **shows** *valid-SDG-node* n **and** *valid-SDG-node* n'
using $\langle n \text{ ics-ns} \rightarrow_{d^*} n' \rangle$
by (*induct rule:intra-call-sum-SDG-path.induct*,
auto intro:sum-SDG-edge-valid-SDG-node valid-SDG-CFG-node)

lemma *ics-SDG-path-Append*:

$\llbracket n'' \text{ ics-ns}' \rightarrow_{d^*} n'; n \text{ ics-ns} \rightarrow_{d^*} n'' \rrbracket \Longrightarrow n \text{ ics-ns} @ ns' \rightarrow_{d^*} n'$
by (*induct rule:intra-call-sum-SDG-path.induct*,
auto intro:intra-call-sum-SDG-path.intros simp:append-assoc [THEN sym]
simp del:append-assoc)

lemma *is-SDG-path-ics-SDG-path*:

$n \text{ is-ns} \rightarrow_{d^*} n' \Longrightarrow n \text{ ics-ns} \rightarrow_{d^*} n'$
by (*induct rule:intra-sum-SDG-path.induct, auto intro:intra-call-sum-SDG-path.intros*)

lemma *cc-SDG-path-ics-SDG-path*:

$n \text{ cc-ns} \rightarrow_{d^*} n' \Longrightarrow n \text{ ics-ns} \rightarrow_{d^*} n'$
by (*induct rule:call-cdep-SDG-path.induct*,
auto intro:intra-call-sum-SDG-path.intros SDG-edge-sum-SDG-edge)

lemma *ics-SDG-path-split*:

assumes $n \text{ ics-ns} \rightarrow_{d^*} n'$ **and** $n'' \in \text{set } ns$
obtains $ns' ns''$ **where** $ns = ns' @ ns''$ **and** $n \text{ ics-ns}' \rightarrow_{d^*} n''$
and $n'' \text{ ics-ns}'' \rightarrow_{d^*} n'$
proof (*atomize-elim*)
from $\langle n \text{ ics-ns} \rightarrow_{d^*} n' \rangle \langle n'' \in \text{set } ns \rangle$
show $\exists ns' ns''. ns = ns' @ ns'' \wedge n \text{ ics-ns}' \rightarrow_{d^*} n'' \wedge n'' \text{ ics-ns}'' \rightarrow_{d^*} n'$
proof (*induct rule:intra-call-sum-SDG-path.induct*)
case *icsSp-Nil* **thus** ?case **by** *simp*
next
case (*icsSp-Append-cdep* $n \ ns \ nx \ n'$)

note $IH = \langle n'' \in \text{set } ns \implies$
 $\exists ns' ns''. ns = ns' @ ns'' \wedge n \text{ ics-} ns' \rightarrow_d^* n'' \wedge n'' \text{ ics-} ns'' \rightarrow_d^* nx \rangle$
from $\langle n'' \in \text{set } (ns@[nx]) \rangle$ **have** $n'' \in \text{set } ns \vee n'' = nx$ **by** *fastforce*
thus *?case*
proof
assume $n'' \in \text{set } ns$
from $IH[OF \text{ this}]$ **obtain** $ns' ns''$ **where** $ns = ns' @ ns''$
and $n \text{ ics-} ns' \rightarrow_d^* n''$ **and** $n'' \text{ ics-} ns'' \rightarrow_d^* nx$ **by** *blast*
from $\langle n'' \text{ ics-} ns'' \rightarrow_d^* nx \rangle \langle nx s \rightarrow_{cd} n' \rangle$
have $n'' \text{ ics-} ns''@[nx] \rightarrow_d^* n'$
by $(\text{rule intra-call-sum-SDG-path.icsSp-Append-cdep})$
with $\langle ns = ns'@ns'' \rangle \langle n \text{ ics-} ns' \rightarrow_d^* n'' \rangle$ **show** *?thesis* **by** *fastforce*
next
assume $n'' = nx$
from $\langle nx s \rightarrow_{cd} n' \rangle$ **have** $nx \text{ ics-} [] \rightarrow_d^* nx$
by $(\text{fastforce intro:icsSp-Nil SDG-edge-valid-SDG-node sum-SDG-edge-SDG-edge})$
with $\langle nx s \rightarrow_{cd} n' \rangle$ **have** $nx \text{ ics-} []@[nx] \rightarrow_d^* n'$
by $(\text{rule intra-call-sum-SDG-path.icsSp-Append-cdep})$
with $\langle n \text{ ics-} ns \rightarrow_d^* nx \rangle \langle n'' = nx \rangle$ **show** *?thesis* **by** *fastforce*
qed
next
case $(\text{icsSp-Append-ddep } n \ ns \ nx \ V \ n')$
note $IH = \langle n'' \in \text{set } ns \implies$
 $\exists ns' ns''. ns = ns' @ ns'' \wedge n \text{ ics-} ns' \rightarrow_d^* n'' \wedge n'' \text{ ics-} ns'' \rightarrow_d^* nx \rangle$
from $\langle n'' \in \text{set } (ns@[nx]) \rangle$ **have** $n'' \in \text{set } ns \vee n'' = nx$ **by** *fastforce*
thus *?case*
proof
assume $n'' \in \text{set } ns$
from $IH[OF \text{ this}]$ **obtain** $ns' ns''$ **where** $ns = ns' @ ns''$
and $n \text{ ics-} ns' \rightarrow_d^* n''$ **and** $n'' \text{ ics-} ns'' \rightarrow_d^* nx$ **by** *blast*
from $\langle n'' \text{ ics-} ns'' \rightarrow_d^* nx \rangle \langle nx s - V \rightarrow_{dd} n' \rangle \langle nx \neq n' \rangle$
have $n'' \text{ ics-} ns''@[nx] \rightarrow_d^* n'$
by $(\text{rule intra-call-sum-SDG-path.icsSp-Append-ddep})$
with $\langle ns = ns'@ns'' \rangle \langle n \text{ ics-} ns' \rightarrow_d^* n'' \rangle$ **show** *?thesis* **by** *fastforce*
next
assume $n'' = nx$
from $\langle nx s - V \rightarrow_{dd} n' \rangle$ **have** $nx \text{ ics-} [] \rightarrow_d^* nx$
by $(\text{fastforce intro:icsSp-Nil SDG-edge-valid-SDG-node sum-SDG-edge-SDG-edge})$
with $\langle nx s - V \rightarrow_{dd} n' \rangle \langle nx \neq n' \rangle$ **have** $nx \text{ ics-} []@[nx] \rightarrow_d^* n'$
by $(\text{rule intra-call-sum-SDG-path.icsSp-Append-ddep})$
with $\langle n \text{ ics-} ns \rightarrow_d^* nx \rangle \langle n'' = nx \rangle$ **show** *?thesis* **by** *fastforce*
qed
next
case $(\text{icsSp-Append-sum } n \ ns \ nx \ p \ n')$
note $IH = \langle n'' \in \text{set } ns \implies$
 $\exists ns' ns''. ns = ns' @ ns'' \wedge n \text{ ics-} ns' \rightarrow_d^* n'' \wedge n'' \text{ ics-} ns'' \rightarrow_d^* nx \rangle$
from $\langle n'' \in \text{set } (ns@[nx]) \rangle$ **have** $n'' \in \text{set } ns \vee n'' = nx$ **by** *fastforce*
thus *?case*
proof

assume $n'' \in \text{set } ns$
from $IH[OF \text{ this}]$ **obtain** $ns' ns''$ **where** $ns = ns' @ ns''$
and $n \text{ ics-ns}' \rightarrow_{d^*} n''$ **and** $n'' \text{ ics-ns}'' \rightarrow_{d^*} nx$ **by** *blast*
from $\langle n'' \text{ ics-ns}'' \rightarrow_{d^*} nx \rangle \langle nx \text{ s-p} \rightarrow_{\text{sum}} n' \rangle$
have $n'' \text{ ics-ns}'' @ [nx] \rightarrow_{d^*} n'$
by $(\text{rule intra-call-sum-SDG-path.icsSp-Append-sum})$
with $\langle ns = ns' @ ns'' \rangle \langle n \text{ ics-ns}' \rightarrow_{d^*} n'' \rangle$ **show** *?thesis* **by** *fastforce*
next
assume $n'' = nx$
from $\langle nx \text{ s-p} \rightarrow_{\text{sum}} n' \rangle$ **have** *valid-SDG-node nx*
by $(\text{fastforce elim:sum-SDG-edge.cases})$
hence $nx \text{ ics-} [] \rightarrow_{d^*} nx$ **by** $(\text{fastforce intro:icsSp-Nil})$
with $\langle nx \text{ s-p} \rightarrow_{\text{sum}} n' \rangle$ **have** $nx \text{ ics-} [] @ [nx] \rightarrow_{d^*} n'$
by $-(\text{rule intra-call-sum-SDG-path.icsSp-Append-sum})$
with $\langle n \text{ ics-ns} \rightarrow_{d^*} nx \rangle \langle n'' = nx \rangle$ **show** *?thesis* **by** *fastforce*
qed
next
case $(\text{icsSp-Append-call } n \text{ ns } nx \text{ p } n')$
note $IH = \langle n'' \in \text{set } ns \implies$
 $\exists ns' ns''. ns = ns' @ ns'' \wedge n \text{ ics-ns}' \rightarrow_{d^*} n'' \wedge n'' \text{ ics-ns}'' \rightarrow_{d^*} nx \rangle$
from $\langle n'' \in \text{set } (ns @ [nx]) \rangle$ **have** $n'' \in \text{set } ns \vee n'' = nx$ **by** *fastforce*
thus *?case*
proof
assume $n'' \in \text{set } ns$
from $IH[OF \text{ this}]$ **obtain** $ns' ns''$ **where** $ns = ns' @ ns''$
and $n \text{ ics-ns}' \rightarrow_{d^*} n''$ **and** $n'' \text{ ics-ns}'' \rightarrow_{d^*} nx$ **by** *blast*
from $\langle n'' \text{ ics-ns}'' \rightarrow_{d^*} nx \rangle \langle nx \text{ s-p} \rightarrow_{\text{call}} n' \rangle$
have $n'' \text{ ics-ns}'' @ [nx] \rightarrow_{d^*} n'$
by $(\text{rule intra-call-sum-SDG-path.icsSp-Append-call})$
with $\langle ns = ns' @ ns'' \rangle \langle n \text{ ics-ns}' \rightarrow_{d^*} n'' \rangle$ **show** *?thesis* **by** *fastforce*
next
assume $n'' = nx$
from $\langle nx \text{ s-p} \rightarrow_{\text{call}} n' \rangle$ **have** $nx \text{ ics-} [] \rightarrow_{d^*} nx$
by $(\text{fastforce intro:icsSp-Nil SDG-edge-valid-SDG-node sum-SDG-edge-SDG-edge})$
with $\langle nx \text{ s-p} \rightarrow_{\text{call}} n' \rangle$ **have** $nx \text{ ics-} [] @ [nx] \rightarrow_{d^*} n'$
by $-(\text{rule intra-call-sum-SDG-path.icsSp-Append-call})$
with $\langle n \text{ ics-ns} \rightarrow_{d^*} nx \rangle \langle n'' = nx \rangle$ **show** *?thesis* **by** *fastforce*
qed
next
case $(\text{icsSp-Append-param-in } n \text{ ns } nx \text{ p } V \text{ n}')$
note $IH = \langle n'' \in \text{set } ns \implies$
 $\exists ns' ns''. ns = ns' @ ns'' \wedge n \text{ ics-ns}' \rightarrow_{d^*} n'' \wedge n'' \text{ ics-ns}'' \rightarrow_{d^*} nx \rangle$
from $\langle n'' \in \text{set } (ns @ [nx]) \rangle$ **have** $n'' \in \text{set } ns \vee n'' = nx$ **by** *fastforce*
thus *?case*
proof
assume $n'' \in \text{set } ns$
from $IH[OF \text{ this}]$ **obtain** $ns' ns''$ **where** $ns = ns' @ ns''$
and $n \text{ ics-ns}' \rightarrow_{d^*} n''$ **and** $n'' \text{ ics-ns}'' \rightarrow_{d^*} nx$ **by** *blast*
from $\langle n'' \text{ ics-ns}'' \rightarrow_{d^*} nx \rangle \langle nx \text{ s-p}: V \rightarrow_{in} n' \rangle$

```

have  $n'' \text{ ics-ns}''@[nx] \rightarrow_d^* n'$ 
  by(rule intra-call-sum-SDG-path.icsSp-Append-param-in)
with  $\langle ns = ns'@ns'' \rangle \langle n \text{ ics-ns}' \rightarrow_d^* n'' \rangle$  show ?thesis by fastforce
next
assume  $n'' = nx$ 
from  $\langle nx \text{ s-p: } V \rightarrow_{in} n' \rangle$  have  $n \text{ ics-} [] \rightarrow_d^* nx$ 
by(fastforce intro:icsSp-Nil SDG-edge-valid-SDG-node sum-SDG-edge-SDG-edge)
with  $\langle nx \text{ s-p: } V \rightarrow_{in} n' \rangle$  have  $n \text{ ics-} []@[nx] \rightarrow_d^* n'$ 
  by -(rule intra-call-sum-SDG-path.icsSp-Append-param-in)
with  $\langle n \text{ ics-ns} \rightarrow_d^* nx \rangle \langle n'' = nx \rangle$  show ?thesis by fastforce
qed
qed
qed

```

lemma *realizable-ics-SDG-path*:

```

assumes realizable  $n \ ns \ n'$  obtains  $ns'$  where  $n \text{ ics-ns}' \rightarrow_d^* n'$ 
proof(atomize-elim)
from  $\langle \text{realizable } n \ ns \ n' \rangle$  show  $\exists ns'. n \text{ ics-ns}' \rightarrow_d^* n'$ 
proof(induct rule:realizable.induct)
  case (realizable-matched  $n \ ns \ n'$ )
    from  $\langle \text{matched } n \ ns \ n' \rangle$  obtain  $ns'$  where  $n \text{ is-ns}' \rightarrow_d^* n'$ 
      by(erule matched-is-SDG-path)
    thus ?case by(fastforce intro:is-SDG-path-ics-SDG-path)
  next
    case (realizable-call  $n_0 \ ns \ n_1 \ p \ n_2 \ V \ ns' \ n_3$ )
      from  $\langle \exists ns'. n_0 \ \text{ics-ns}' \rightarrow_d^* n_1 \rangle$  obtain  $nsx$  where  $n_0 \ \text{ics-nsx} \rightarrow_d^* n_1$  by
        blast
      with  $\langle n_1 \ -p \rightarrow_{call} n_2 \vee n_1 \ -p: V \rightarrow_{in} n_2 \rangle$  have  $n_0 \ \text{ics-nsx}@[n_1] \rightarrow_d^* n_2$ 
      by(fastforce intro:SDG-edge-sum-SDG-edge icsSp-Append-call icsSp-Append-param-in)
      from  $\langle \text{matched } n_2 \ ns' \ n_3 \rangle$  obtain  $nsx'$  where  $n_2 \ \text{is-nsx}' \rightarrow_d^* n_3$ 
      by(erule matched-is-SDG-path)
      hence  $n_2 \ \text{ics-nsx}' \rightarrow_d^* n_3$  by(rule is-SDG-path-ics-SDG-path)
      from  $\langle n_2 \ \text{ics-nsx}' \rightarrow_d^* n_3 \rangle \langle n_0 \ \text{ics-nsx}@[n_1] \rightarrow_d^* n_2 \rangle$ 
      have  $n_0 \ \text{ics-}(nsx@[n_1])@nsx' \rightarrow_d^* n_3$  by(rule ics-SDG-path-Append)
      thus ?case by blast
    qed
  qed

```

lemma *ics-SDG-path-realizable*:

```

assumes  $n \ \text{ics-ns} \rightarrow_d^* n'$ 
obtains  $ns'$  where realizable  $n \ ns' \ n'$  and  $\text{set } ns \subseteq \text{set } ns'$ 
proof(atomize-elim)
from  $\langle n \ \text{ics-ns} \rightarrow_d^* n' \rangle$  show  $\exists ns'. \text{realizable } n \ ns' \ n' \wedge \text{set } ns \subseteq \text{set } ns'$ 
proof(induct rule:intra-call-sum-SDG-path.induct)
  case (icsSp-Nil  $n$ )
    hence matched  $n \ [] \ n$  by(rule matched-Nil)
    thus ?case by(fastforce intro:realizable-matched)

```

next
case (*icsSp-Append-cdep* n ns n'' n')
from $\langle \exists ns'. \text{realizable } n \ ns' \ n'' \wedge \text{set } ns \subseteq \text{set } ns' \rangle$
obtain ns' **where** $\text{realizable } n \ ns' \ n''$ **and** $\text{set } ns \subseteq \text{set } ns'$ **by** *blast*
from $\langle n'' \ s \rightarrow_{cd} n' \rangle$ **have** *valid-SDG-node* n'' **by**(*rule sum-SDG-edge-valid-SDG-node*)
hence $n'' \ i - \square \rightarrow_{d*} n''$ **by**(*rule iSp-Nil*)
with $\langle n'' \ s \rightarrow_{cd} n' \rangle$ **have** $n'' \ i - \square @ [n''] \rightarrow_{d*} n'$
by(*fastforce elim:iSp-Append-cdep sum-SDG-edge-SDG-edge*)
hence *matched* $n'' [n''] n'$ **by**(*fastforce intro:intra-SDG-path-matched*)
with $\langle \text{realizable } n \ ns' \ n'' \rangle$ **have** $\text{realizable } n \ (ns' @ [n'']) \ n'$
by(*rule realizable-Append-matched*)
with $\langle \text{set } ns \subseteq \text{set } ns' \rangle$ **show** *?case* **by** *fastforce*

next
case (*icsSp-Append-ddep* n ns n'' V n')
from $\langle \exists ns'. \text{realizable } n \ ns' \ n'' \wedge \text{set } ns \subseteq \text{set } ns' \rangle$
obtain ns' **where** $\text{realizable } n \ ns' \ n''$ **and** $\text{set } ns \subseteq \text{set } ns'$ **by** *blast*
from $\langle n'' \ s - V \rightarrow_{dd} n' \rangle$ **have** *valid-SDG-node* n''
by(*rule sum-SDG-edge-valid-SDG-node*)
hence $n'' \ i - \square \rightarrow_{d*} n''$ **by**(*rule iSp-Nil*)
with $\langle n'' \ s - V \rightarrow_{dd} n' \rangle$ $\langle n'' \neq n' \rangle$ **have** $n'' \ i - \square @ [n''] \rightarrow_{d*} n'$
by(*fastforce elim:iSp-Append-ddep sum-SDG-edge-SDG-edge*)
hence *matched* $n'' [n''] n'$ **by**(*fastforce intro:intra-SDG-path-matched*)
with $\langle \text{realizable } n \ ns' \ n'' \rangle$ **have** $\text{realizable } n \ (ns' @ [n'']) \ n'$
by(*fastforce intro:realizable-Append-matched*)
with $\langle \text{set } ns \subseteq \text{set } ns' \rangle$ **show** *?case* **by** *fastforce*

next
case (*icsSp-Append-sum* n ns n'' p n')
from $\langle \exists ns'. \text{realizable } n \ ns' \ n'' \wedge \text{set } ns \subseteq \text{set } ns' \rangle$
obtain ns' **where** $\text{realizable } n \ ns' \ n''$ **and** $\text{set } ns \subseteq \text{set } ns'$ **by** *blast*
from $\langle n'' \ s - p \rightarrow_{sum} n' \rangle$ **show** *?case*
proof(*rule sum-edge-cases*)
fix $a \ Q \ r \ fs \ a'$
assume *valid-edge* a **and** $\text{kind } a = Q:r \leftrightarrow_p fs$ **and** $a' \in \text{get-return-edges } a$
and $n'' = \text{CFG-node (sourcenode } a)$ **and** $n' = \text{CFG-node (targetnode } a')$
from $\langle \text{valid-edge } a \rangle$ $\langle \text{kind } a = Q:r \leftrightarrow_p fs \rangle$ $\langle a' \in \text{get-return-edges } a \rangle$
have *match':matched* ($\text{CFG-node (targetnode } a)$) [$\text{CFG-node (targetnode } a)$]
($\text{CFG-node (sourcenode } a')$)
by(*rule intra-proc-matched*)
from $\langle \text{valid-edge } a \rangle$ $\langle \text{kind } a = Q:r \leftrightarrow_p fs \rangle$ $\langle n'' = \text{CFG-node (sourcenode } a) \rangle$
have $n'' \ -p \rightarrow_{call} \text{CFG-node (targetnode } a)$
by(*fastforce intro:SDG-call-edge*)
hence *matched* $n'' \ \square \ n''$
by(*fastforce intro:matched-Nil SDG-edge-valid-SDG-node*)
from $\langle \text{valid-edge } a \rangle$ $\langle a' \in \text{get-return-edges } a \rangle$ **have** *valid-edge* a'
by(*rule get-return-edges-valid*)
from $\langle \text{valid-edge } a \rangle$ $\langle \text{kind } a = Q:r \leftrightarrow_p fs \rangle$ $\langle a' \in \text{get-return-edges } a \rangle$
obtain $Q' \ f'$ **where** $\text{kind } a' = Q' \leftrightarrow_{p'} f'$ **by**(*fastforce dest!:call-return-edges*)
from $\langle \text{valid-edge } a' \rangle$ $\langle \text{kind } a' = Q' \leftrightarrow_{p'} f' \rangle$ $\langle n' = \text{CFG-node (targetnode } a') \rangle$
have $\text{CFG-node (sourcenode } a') \ -p \rightarrow_{ret} n'$

```

    by(fastforce intro:SDG-return-edge)
  from ⟨matched n'' [] n'⟩ ⟨n'' -p→call CFG-node (targetnode a)⟩
    match' ⟨CFG-node (sourcenode a') -p→ret n'⟩ ⟨valid-edge a⟩
    ⟨a' ∈ get-return-edges a⟩ ⟨n' = CFG-node (targetnode a')⟩
    ⟨n'' = CFG-node (sourcenode a)⟩
  have matched n'' ([@n''#[CFG-node (targetnode a)]@[CFG-node (sourcenode
a')]])
    n'
    by(fastforce intro:matched-bracket-call)
  with ⟨realizable n ns' n'⟩
  have realizable n
    (ns'@[n''#[CFG-node (targetnode a),CFG-node (sourcenode a')]]) n'
    by(fastforce intro:realizable-Append-matched)
  with ⟨set ns ⊆ set ns'⟩ show ?thesis by fastforce
next
fix a Q r p fs a' ns'' x x' ins outs
assume valid-edge a and kind a = Q:r↔pfs and a' ∈ get-return-edges a
  and match':matched (Formal-in (targetnode a,x)) ns''
    (Formal-out (sourcenode a',x'))
  and n'' = Actual-in (sourcenode a,x)
  and n' = Actual-out (targetnode a',x') and (p,ins,outs) ∈ set procs
  and x < length ins and x' < length outs
from ⟨valid-edge a⟩ ⟨kind a = Q:r↔pfs⟩ ⟨n'' = Actual-in (sourcenode a,x)⟩
  ⟨(p,ins,outs) ∈ set procs⟩ ⟨x < length ins⟩
have n'' -p:ins!x→in Formal-in (targetnode a,x)
  by(fastforce intro!:SDG-param-in-edge)
hence matched n'' [] n''
  by(fastforce intro:matched-Nil SDG-edge-valid-SDG-node)
from ⟨valid-edge a⟩ ⟨a' ∈ get-return-edges a⟩ have valid-edge a'
  by(rule get-return-edges-valid)
from ⟨valid-edge a⟩ ⟨kind a = Q:r↔pfs⟩ ⟨a' ∈ get-return-edges a⟩
obtain Q' f' where kind a' = Q'↔pf f' by(fastforce dest!:call-return-edges)
from ⟨valid-edge a'⟩ ⟨kind a' = Q'↔pf f'⟩ ⟨n' = Actual-out (targetnode a',x')⟩
  ⟨(p,ins,outs) ∈ set procs⟩ ⟨x' < length outs⟩
have Formal-out (sourcenode a',x') -p:outs!x'→out n'
  by(fastforce intro:SDG-param-out-edge)
from ⟨matched n'' [] n'⟩ ⟨n'' -p:ins!x→in Formal-in (targetnode a,x)⟩
  match' ⟨Formal-out (sourcenode a',x') -p:outs!x'→out n'⟩ ⟨valid-edge a⟩
  ⟨a' ∈ get-return-edges a⟩ ⟨n' = Actual-out (targetnode a',x')⟩
  ⟨n'' = Actual-in (sourcenode a,x)⟩
have matched n'' ([@n''#ns''@[Formal-out (sourcenode a',x')]]) n'
  by(fastforce intro:matched-bracket-param)
with ⟨realizable n ns' n'⟩
have realizable n (ns'@[n''#ns''@[Formal-out (sourcenode a',x')]]) n'
  by(fastforce intro:realizable-Append-matched)
with ⟨set ns ⊆ set ns'⟩ show ?thesis by fastforce
qed
next
case (icsSp-Append-call n ns n'' p n')

```


from $\langle \exists ns'. \text{realizable } n \ ns' \ n'' \wedge \text{set } ns \subseteq \text{set } ns' \rangle$
obtain ns' **where** $\text{realizable } n \ ns' \ n''$ **and** $\text{set } ns \subseteq \text{set } ns'$ **by** *blast*
from $\langle n'' \ s-p \rightarrow_{\text{call}} n' \rangle$ **have** *valid-SDG-node* n'
by(*rule sum-SDG-edge-valid-SDG-node*)
hence *matched* $n' \ [] \ n'$ **by**(*rule matched-Nil*)
with $\langle \text{realizable } n \ ns' \ n'' \rangle \langle n'' \ s-p \rightarrow_{\text{call}} n' \rangle$
have *realizable* $n \ (ns'@n''\#[])$ n'
by(*fastforce intro:realizable-call sum-SDG-edge-SDG-edge*)
with $\langle \text{set } ns \subseteq \text{set } ns' \rangle$ **show** *?case* **by** *fastforce*
next
case (*icsSp-Append-param-in* $n \ ns \ n'' \ p \ V \ n'$)
from $\langle \exists ns'. \text{realizable } n \ ns' \ n'' \wedge \text{set } ns \subseteq \text{set } ns' \rangle$
obtain ns' **where** $\text{realizable } n \ ns' \ n''$ **and** $\text{set } ns \subseteq \text{set } ns'$ **by** *blast*
from $\langle n'' \ s-p:V \rightarrow_{\text{in}} n' \rangle$ **have** *valid-SDG-node* n'
by(*rule sum-SDG-edge-valid-SDG-node*)
hence *matched* $n' \ [] \ n'$ **by**(*rule matched-Nil*)
with $\langle \text{realizable } n \ ns' \ n'' \rangle \langle n'' \ s-p:V \rightarrow_{\text{in}} n' \rangle$
have *realizable* $n \ (ns'@n''\#[])$ n'
by(*fastforce intro:realizable-call sum-SDG-edge-SDG-edge*)
with $\langle \text{set } ns \subseteq \text{set } ns' \rangle$ **show** *?case* **by** *fastforce*
qed
qed

lemma *realizable-Append-ics-SDG-path*:
assumes *realizable* $n \ ns \ n''$ **and** $n'' \ \text{ics-}ns' \rightarrow_{d^*} n'$
obtains ns'' **where** *realizable* $n \ (ns@ns'')$ n'
proof(*atomize-elim*)
from $\langle n'' \ \text{ics-}ns' \rightarrow_{d^*} n' \rangle \langle \text{realizable } n \ ns \ n'' \rangle$
show $\exists ns''. \text{realizable } n \ (ns@ns'')$ n'
proof(*induct rule:intra-call-sum-SDG-path.induct*)
case (*icsSp-Nil* n'') **thus** *?case* **by**(*rule-tac* $x=[]$ **in** exI) *fastforce*
next
case (*icsSp-Append-cdep* $n'' \ ns' \ nx \ n'$)
then obtain ns'' **where** *realizable* $n \ (ns@ns'')$ nx **by** *fastforce*
from $\langle nx \ s \rightarrow_{\text{cd}} n' \rangle$ **have** *valid-SDG-node* nx **by**(*rule sum-SDG-edge-valid-SDG-node*)
hence *matched* $nx \ [] \ nx$ **by**(*rule matched-Nil*)
from $\langle nx \ s \rightarrow_{\text{cd}} n' \rangle \langle \text{valid-SDG-node } nx \rangle$
have $nx \ i-[]@[nx] \rightarrow_{d^*} n'$
by(*fastforce intro:iSp-Append-cdep iSp-Nil sum-SDG-edge-SDG-edge*)
with $\langle \text{matched } nx \ [] \ nx \rangle$ **have** *matched* $nx \ ([]@[nx]) \ n'$
by(*fastforce intro:matched-Append-intra-SDG-path*)
with $\langle \text{realizable } n \ (ns@ns'') \ nx \rangle$ **have** *realizable* $n \ ((ns@ns'')@[nx]) \ n'$
by(*fastforce intro:realizable-Append-matched*)
thus *?case* **by** *fastforce*
next
case (*icsSp-Append-ddep* $n'' \ ns' \ nx \ V \ n'$)
then obtain ns'' **where** *realizable* $n \ (ns@ns'')$ nx **by** *fastforce*

```

from ⟨ $nx\ s - V \rightarrow_{dd}\ n'$ ⟩ have valid-SDG-node  $nx$  by(rule sum-SDG-edge-valid-SDG-node)
hence matched  $nx$  []  $nx$  by(rule matched-Nil)
from ⟨ $nx\ s - V \rightarrow_{dd}\ n'$ ⟩ ⟨ $nx \neq n'$ ⟩ ⟨valid-SDG-node  $nx$ ⟩
have  $nx\ i - []@[nx] \rightarrow_{d^*}\ n'$ 
  by(fastforce intro:iSp-Append-ddep iSp-Nil sum-SDG-edge-SDG-edge)
with ⟨matched  $nx$  []  $nx$ ⟩ have matched  $nx$  ([]@[ $nx$ ])  $n'$ 
  by(fastforce intro:matched-Append-intra-SDG-path)
with ⟨realizable  $n$  ( $ns@ns''$ )  $nx$ ⟩ have realizable  $n$  (( $ns@ns''$ )@[ $nx$ ])  $n'$ 
  by(fastforce intro:realizable-Append-matched)
thus ?case by fastforce
next
  case (icsSp-Append-sum  $n''\ ns'\ nx\ p\ n'$ )
  then obtain  $ns''$  where realizable  $n$  ( $ns@ns''$ )  $nx$  by fastforce
  from ⟨ $nx\ s - p \rightarrow_{sum}\ n'$ ⟩ obtain  $nsx$  where matched  $nx\ nsx\ n'$ 
    by -(erule sum-SDG-summary-edge-matched)
  with ⟨realizable  $n$  ( $ns@ns''$ )  $nx$ ⟩ have realizable  $n$  (( $ns@ns''$ )@[ $nsx$ ])  $n'$ 
    by(rule realizable-Append-matched)
  thus ?case by fastforce
next
  case (icsSp-Append-call  $n''\ ns'\ nx\ p\ n'$ )
  then obtain  $ns''$  where realizable  $n$  ( $ns@ns''$ )  $nx$  by fastforce
from ⟨ $nx\ s - p \rightarrow_{call}\ n'$ ⟩ have valid-SDG-node  $n'$  by(rule sum-SDG-edge-valid-SDG-node)
hence matched  $n'$  []  $n'$  by(rule matched-Nil)
with ⟨realizable  $n$  ( $ns@ns''$ )  $nx$ ⟩ ⟨ $nx\ s - p \rightarrow_{call}\ n'$ ⟩
have realizable  $n$  (( $ns@ns''$ )@[ $nx$ ])  $n'$ 
  by(fastforce intro:realizable-call sum-SDG-edge-SDG-edge)
thus ?case by fastforce
next
  case (icsSp-Append-param-in  $n''\ ns'\ nx\ p\ V\ n'$ )
  then obtain  $ns''$  where realizable  $n$  ( $ns@ns''$ )  $nx$  by fastforce
from ⟨ $nx\ s - p : V \rightarrow_{in}\ n'$ ⟩ have valid-SDG-node  $n'$ 
  by(rule sum-SDG-edge-valid-SDG-node)
hence matched  $n'$  []  $n'$  by(rule matched-Nil)
with ⟨realizable  $n$  ( $ns@ns''$ )  $nx$ ⟩ ⟨ $nx\ s - p : V \rightarrow_{in}\ n'$ ⟩
have realizable  $n$  (( $ns@ns''$ )@[ $nx$ ])  $n'$ 
  by(fastforce intro:realizable-call sum-SDG-edge-SDG-edge)
thus ?case by fastforce
qed
qed

```

1.8.12 SDG paths without call edges

```

inductive intra-return-sum-SDG-path ::
  'node SDG-node  $\Rightarrow$  'node SDG-node list  $\Rightarrow$  'node SDG-node  $\Rightarrow$  bool
(- irs  $\rightarrow_{d^*}$  - [ $51, 0, 0$ ]  $80$ )
where irsSp-Nil:
  valid-SDG-node  $n \Longrightarrow n\ irs - [] \rightarrow_{d^*}\ n$ 

| irsSp-Cons-cdep:

```

$\llbracket n'' \text{ irs-ns} \rightarrow_{d^*} n'; n \text{ s} \rightarrow_{cd} n'' \rrbracket \Longrightarrow n \text{ irs-n\#ns} \rightarrow_{d^*} n'$
| *irsSp-Cons-ddep*:
 $\llbracket n'' \text{ irs-ns} \rightarrow_{d^*} n'; n \text{ s-V} \rightarrow_{dd} n''; n \neq n'' \rrbracket \Longrightarrow n \text{ irs-n\#ns} \rightarrow_{d^*} n'$
| *irsSp-Cons-sum*:
 $\llbracket n'' \text{ irs-ns} \rightarrow_{d^*} n'; n \text{ s-p} \rightarrow_{sum} n'' \rrbracket \Longrightarrow n \text{ irs-n\#ns} \rightarrow_{d^*} n'$
| *irsSp-Cons-return*:
 $\llbracket n'' \text{ irs-ns} \rightarrow_{d^*} n'; n \text{ s-p} \rightarrow_{ret} n'' \rrbracket \Longrightarrow n \text{ irs-n\#ns} \rightarrow_{d^*} n'$
| *irsSp-Cons-param-out*:
 $\llbracket n'' \text{ irs-ns} \rightarrow_{d^*} n'; n \text{ s-p:V} \rightarrow_{out} n'' \rrbracket \Longrightarrow n \text{ irs-n\#ns} \rightarrow_{d^*} n'$

lemma *irs-SDG-path-Append*:

$\llbracket n \text{ irs-ns} \rightarrow_{d^*} n''; n'' \text{ irs-ns}' \rightarrow_{d^*} n' \rrbracket \Longrightarrow n \text{ irs-ns}@ns' \rightarrow_{d^*} n'$
by(*induct rule:intra-return-sum-SDG-path.induct*,
auto intro:intra-return-sum-SDG-path.intros)

lemma *is-SDG-path-irs-SDG-path*:

$n \text{ is-ns} \rightarrow_{d^*} n' \Longrightarrow n \text{ irs-ns} \rightarrow_{d^*} n'$
proof(*induct rule:intra-sum-SDG-path.induct*)
 case (*isSp-Nil n*)
 from (*valid-SDG-node n*) **show** ?*case* **by**(*rule irsSp-Nil*)
next
 case (*isSp-Append-cdep n ns n'' n'*)
 from ($n'' \text{ s} \rightarrow_{cd} n'$) **have** $n'' \text{ irs-}[n''] \rightarrow_{d^*} n'$
 by(*fastforce intro:irsSp-Cons-cdep irsSp-Nil sum-SDG-edge-valid-SDG-node*)
 with ($n \text{ irs-ns} \rightarrow_{d^*} n''$) **show** ?*case* **by**(*rule irs-SDG-path-Append*)
next
 case (*isSp-Append-ddep n ns n'' V n'*)
 from ($n'' \text{ s-V} \rightarrow_{dd} n'$) ($n'' \neq n'$) **have** $n'' \text{ irs-}[n''] \rightarrow_{d^*} n'$
 by(*fastforce intro:irsSp-Cons-ddep irsSp-Nil sum-SDG-edge-valid-SDG-node*)
 with ($n \text{ irs-ns} \rightarrow_{d^*} n''$) **show** ?*case* **by**(*rule irs-SDG-path-Append*)
next
 case (*isSp-Append-sum n ns n'' p n'*)
 from ($n'' \text{ s-p} \rightarrow_{sum} n'$) **have** $n'' \text{ irs-}[n''] \rightarrow_{d^*} n'$
 by(*fastforce intro:irsSp-Cons-sum irsSp-Nil sum-SDG-edge-valid-SDG-node*)
 with ($n \text{ irs-ns} \rightarrow_{d^*} n''$) **show** ?*case* **by**(*rule irs-SDG-path-Append*)
qed

lemma *irs-SDG-path-split*:

assumes $n \text{ irs-ns} \rightarrow_{d^*} n'$
obtains $n \text{ is-ns} \rightarrow_{d^*} n'$
| $nsx \text{ nsx}' \text{ nx} \text{ nx}' \text{ p}$ **where** $ns = nsx @ nx \# nsx'$ **and** $n \text{ irs-nsx} \rightarrow_{d^*} nx$

and $nx\ s-p \rightarrow_{ret} nx' \vee (\exists V. nx\ s-p: V \rightarrow_{out} nx')$ **and** $nx' is-nsx' \rightarrow_{d^*} n'$
proof(*atomize-elim*)
from $\langle n\ irs-ns \rightarrow_{d^*} n' \rangle$ **show** $n\ is-ns \rightarrow_{d^*} n' \vee$
 $(\exists nsx\ nx\ nsx'\ p\ nx'. ns = nsx@nx\ \#nsx' \wedge n\ irs-nsx \rightarrow_{d^*} nx \wedge$
 $(nx\ s-p \rightarrow_{ret} nx' \vee (\exists V. nx\ s-p: V \rightarrow_{out} nx')) \wedge nx' is-nsx' \rightarrow_{d^*}$
 $n')$
proof(*induct rule:intra-return-sum-SDG-path.induct*)
case (*irsSp-Nil* n)
from $\langle valid-SDG-node\ n \rangle$ **have** $n\ is-[] \rightarrow_{d^*} n$ **by**(*rule isSp-Nil*)
thus $?case$ **by** *simp*
next
case (*irsSp-Cons-cdep* $n''\ ns\ n'$)
from $\langle n''\ is-ns \rightarrow_{d^*} n' \rangle$
 $(\exists nsx\ nx\ nsx'\ p\ nx'. ns = nsx@nx\ \#nsx' \wedge n''\ irs-nsx \rightarrow_{d^*} nx \wedge$
 $(nx\ s-p \rightarrow_{ret} nx' \vee (\exists V. nx\ s-p: V \rightarrow_{out} nx')) \wedge nx' is-nsx' \rightarrow_{d^*}$
 $n')$
show $?case$
proof
assume $n''\ is-ns \rightarrow_{d^*} n'$
from $\langle n\ s \rightarrow_{cd} n'' \rangle$ **have** $n\ is-[]@[n] \rightarrow_{d^*} n''$
by(*fastforce intro:isSp-Append-cdep isSp-Nil sum-SDG-edge-valid-SDG-node*)
with $\langle n''\ is-ns \rightarrow_{d^*} n' \rangle$ **have** $n\ is-[n]@ns \rightarrow_{d^*} n'$
by(*fastforce intro:is-SDG-path-Append*)
thus $?case$ **by** *simp*
next
assume $\exists nsx\ nx\ nsx'\ p\ nx'. ns = nsx@nx\ \#nsx' \wedge n''\ irs-nsx \rightarrow_{d^*} nx \wedge$
 $(nx\ s-p \rightarrow_{ret} nx' \vee (\exists V. nx\ s-p: V \rightarrow_{out} nx')) \wedge nx' is-nsx' \rightarrow_{d^*}$
 n'
then obtain $nsx\ nsx'\ nx\ nx'\ p$ **where** $ns = nsx@nx\ \#nsx'$ **and** $n''\ irs-nsx \rightarrow_{d^*}$
 nx
and $nx\ s-p \rightarrow_{ret} nx' \vee (\exists V. nx\ s-p: V \rightarrow_{out} nx')$ **and** $nx' is-nsx' \rightarrow_{d^*} n'$
by *blast*
from $\langle n''\ irs-nsx \rightarrow_{d^*} nx \rangle$ $\langle n\ s \rightarrow_{cd} n'' \rangle$ **have** $n\ irs-n\ \#nsx \rightarrow_{d^*} nx$
by(*rule intra-return-sum-SDG-path.irsSp-Cons-cdep*)
with $\langle ns = nsx@nx\ \#nsx' \rangle$ $\langle nx\ s-p \rightarrow_{ret} nx' \vee (\exists V. nx\ s-p: V \rightarrow_{out} nx') \rangle$
 $\langle nx' is-nsx' \rightarrow_{d^*} n' \rangle$
show $?case$ **by** *fastforce*
qed
next
case (*irsSp-Cons-ddep* $n''\ ns\ n'\ n\ V$)
from $\langle n''\ is-ns \rightarrow_{d^*} n' \rangle$
 $(\exists nsx\ nx\ nsx'\ p\ nx'. ns = nsx@nx\ \#nsx' \wedge n''\ irs-nsx \rightarrow_{d^*} nx \wedge$
 $(nx\ s-p \rightarrow_{ret} nx' \vee (\exists V. nx\ s-p: V \rightarrow_{out} nx')) \wedge nx' is-nsx' \rightarrow_{d^*}$
 $n')$
show $?case$
proof
assume $n''\ is-ns \rightarrow_{d^*} n'$
from $\langle n\ s - V \rightarrow_{dd} n'' \rangle$ $\langle n \neq n'' \rangle$ **have** $n\ is-[]@[n] \rightarrow_{d^*} n''$
by(*fastforce intro:isSp-Append-ddep isSp-Nil sum-SDG-edge-valid-SDG-node*)

with $\langle n'' \text{ is-ns} \rightarrow_d^* n' \rangle$ **have** $n \text{ is-}[n]@ns \rightarrow_d^* n'$
by (*fastforce intro:is-SDG-path-Append*)
thus *?case by simp*
next
assume $\exists nsx \ nx \ nsx' \ p \ nx'. \ ns = nsx@nx\#nsx' \wedge n'' \text{ irs-nsx} \rightarrow_d^* nx \wedge$
 $(nx \ s-p \rightarrow_{ret} nx' \vee (\exists V. \ nx \ s-p: V \rightarrow_{out} nx')) \wedge nx' \text{ is-nsx}' \rightarrow_d^*$
 n'
then obtain $nsx \ nsx' \ nx \ nx' \ p$ **where** $ns = nsx@nx\#nsx'$ **and** $n'' \text{ irs-nsx} \rightarrow_d^*$
 nx
and $nx \ s-p \rightarrow_{ret} nx' \vee (\exists V. \ nx \ s-p: V \rightarrow_{out} nx')$ **and** $nx' \text{ is-nsx}' \rightarrow_d^* n'$
by *blast*
from $\langle n'' \text{ irs-nsx} \rightarrow_d^* nx \rangle \langle n \ s-p \rightarrow_{dd} n'' \rangle \langle n \neq n'' \rangle$ **have** $n \text{ irs-n}\#nsx \rightarrow_d^*$
 nx
by (*rule intra-return-sum-SDG-path.irsSp-Cons-ddep*)
with $\langle ns = nsx@nx\#nsx' \rangle \langle nx \ s-p \rightarrow_{ret} nx' \vee (\exists V. \ nx \ s-p: V \rightarrow_{out} nx') \rangle$
 $\langle nx' \text{ is-nsx}' \rightarrow_d^* n' \rangle$
show *?case by fastforce*
qed
next
case (*irsSp-Cons-sum* $n'' \ ns \ n' \ n \ p$)
from $\langle n'' \text{ is-ns} \rightarrow_d^* n' \vee$
 $(\exists nsx \ nx \ nsx' \ p \ nx'. \ ns = nsx@nx\#nsx' \wedge n'' \text{ irs-nsx} \rightarrow_d^* nx \wedge$
 $(nx \ s-p \rightarrow_{ret} nx' \vee (\exists V. \ nx \ s-p: V \rightarrow_{out} nx')) \wedge nx' \text{ is-nsx}' \rightarrow_d^*$
 $n') \rangle$
show *?case*
proof
assume $n'' \text{ is-ns} \rightarrow_d^* n'$
from $\langle n \ s-p \rightarrow_{sum} n'' \rangle$ **have** $n \text{ is-}[]@n \rightarrow_d^* n''$
by (*fastforce intro:isSp-Append-sum isSp-Nil sum-SDG-edge-valid-SDG-node*)
with $\langle n'' \text{ is-ns} \rightarrow_d^* n' \rangle$ **have** $n \text{ is-}[n]@ns \rightarrow_d^* n'$
by (*fastforce intro:is-SDG-path-Append*)
thus *?case by simp*
next
assume $\exists nsx \ nx \ nsx' \ p \ nx'. \ ns = nsx@nx\#nsx' \wedge n'' \text{ irs-nsx} \rightarrow_d^* nx \wedge$
 $(nx \ s-p \rightarrow_{ret} nx' \vee (\exists V. \ nx \ s-p: V \rightarrow_{out} nx')) \wedge nx' \text{ is-nsx}' \rightarrow_d^*$
 n'
then obtain $nsx \ nsx' \ nx \ nx' \ p'$ **where** $ns = nsx@nx\#nsx'$ **and** $n'' \text{ irs-nsx} \rightarrow_d^*$
 nx
and $nx \ s-p' \rightarrow_{ret} nx' \vee (\exists V. \ nx \ s-p': V \rightarrow_{out} nx')$
and $nx' \text{ is-nsx}' \rightarrow_d^* n'$ **by** *blast*
from $\langle n'' \text{ irs-nsx} \rightarrow_d^* nx \rangle \langle n \ s-p \rightarrow_{sum} n'' \rangle$ **have** $n \text{ irs-n}\#nsx \rightarrow_d^* nx$
by (*rule intra-return-sum-SDG-path.irsSp-Cons-sum*)
with $\langle ns = nsx@nx\#nsx' \rangle \langle nx \ s-p' \rightarrow_{ret} nx' \vee (\exists V. \ nx \ s-p': V \rightarrow_{out} nx') \rangle$
 $\langle nx' \text{ is-nsx}' \rightarrow_d^* n' \rangle$
show *?case by fastforce*
qed
next
case (*irsSp-Cons-return* $n'' \ ns \ n' \ n \ p$)
from $\langle n'' \text{ is-ns} \rightarrow_d^* n' \vee$

$(\exists nsx\ nx\ nsx'\ p\ nx'.\ ns = nsx@nx\#nsx' \wedge n''\ irs-nsx \rightarrow_{d^*}\ nx \wedge$
 $(nx\ s-p \rightarrow_{ret}\ nx' \vee (\exists V.\ nx\ s-p: V \rightarrow_{out}\ nx')) \wedge nx'\ is-nsx' \rightarrow_{d^*}$
 $n')\rangle$
show ?case
proof
assume $n''\ is-ns \rightarrow_{d^*}\ n'$
from $\langle n\ s-p \rightarrow_{ret}\ n'' \rangle$ **have** *valid-SDG-node* n **by**(rule *sum-SDG-edge-valid-SDG-node*)
hence $n\ irs-[] \rightarrow_{d^*}\ n$ **by**(rule *irsSp-Nil*)
with $\langle n\ s-p \rightarrow_{ret}\ n'' \rangle\ \langle n''\ is-ns \rightarrow_{d^*}\ n' \rangle$ **show** ?thesis **by** *fastforce*
next
assume $\exists nsx\ nx\ nsx'\ p\ nx'.\ ns = nsx@nx\#nsx' \wedge n''\ irs-nsx \rightarrow_{d^*}\ nx \wedge$
 $(nx\ s-p \rightarrow_{ret}\ nx' \vee (\exists V.\ nx\ s-p: V \rightarrow_{out}\ nx')) \wedge nx'\ is-nsx' \rightarrow_{d^*}$
 n'
then obtain $nsx\ nsx'\ nx\ nx'\ p'$ **where** $ns = nsx@nx\#nsx'$ **and** $n''\ irs-nsx \rightarrow_{d^*}$
 nx
and $nx\ s-p' \rightarrow_{ret}\ nx' \vee (\exists V.\ nx\ s-p': V \rightarrow_{out}\ nx')$
and $nx'\ is-nsx' \rightarrow_{d^*}\ n'$ **by** *blast*
from $\langle n''\ irs-nsx \rightarrow_{d^*}\ nx \rangle\ \langle n\ s-p \rightarrow_{ret}\ n'' \rangle$ **have** $n\ irs-n\#nsx \rightarrow_{d^*}\ nx$
by(rule *intra-return-sum-SDG-path.irsSp-Cons-return*)
with $\langle ns = nsx@nx\#nsx' \rangle\ \langle nx\ s-p' \rightarrow_{ret}\ nx' \vee (\exists V.\ nx\ s-p': V \rightarrow_{out}\ nx') \rangle$
 $\langle nx'\ is-nsx' \rightarrow_{d^*}\ n' \rangle$
show ?thesis **by** *fastforce*
qed
next
case (*irsSp-Cons-param-out* $n''\ ns\ n'\ n\ p\ V$)
from $\langle n''\ is-ns \rightarrow_{d^*}\ n' \rangle\ \vee$
 $(\exists nsx\ nx\ nsx'\ p\ nx'.\ ns = nsx@nx\#nsx' \wedge n''\ irs-nsx \rightarrow_{d^*}\ nx \wedge$
 $(nx\ s-p \rightarrow_{ret}\ nx' \vee (\exists V.\ nx\ s-p: V \rightarrow_{out}\ nx')) \wedge nx'\ is-nsx' \rightarrow_{d^*}$
 $n')\rangle$
show ?case
proof
assume $n''\ is-ns \rightarrow_{d^*}\ n'$
from $\langle n\ s-p: V \rightarrow_{out}\ n'' \rangle$ **have** *valid-SDG-node* n
by(rule *sum-SDG-edge-valid-SDG-node*)
hence $n\ irs-[] \rightarrow_{d^*}\ n$ **by**(rule *irsSp-Nil*)
with $\langle n\ s-p: V \rightarrow_{out}\ n'' \rangle\ \langle n''\ is-ns \rightarrow_{d^*}\ n' \rangle$ **show** ?thesis **by** *fastforce*
next
assume $\exists nsx\ nx\ nsx'\ p\ nx'.\ ns = nsx@nx\#nsx' \wedge n''\ irs-nsx \rightarrow_{d^*}\ nx \wedge$
 $(nx\ s-p \rightarrow_{ret}\ nx' \vee (\exists V.\ nx\ s-p: V \rightarrow_{out}\ nx')) \wedge nx'\ is-nsx' \rightarrow_{d^*}$
 n'
then obtain $nsx\ nsx'\ nx\ nx'\ p'$ **where** $ns = nsx@nx\#nsx'$ **and** $n''\ irs-nsx \rightarrow_{d^*}$
 nx
and $nx\ s-p' \rightarrow_{ret}\ nx' \vee (\exists V.\ nx\ s-p': V \rightarrow_{out}\ nx')$
and $nx'\ is-nsx' \rightarrow_{d^*}\ n'$ **by** *blast*
from $\langle n''\ irs-nsx \rightarrow_{d^*}\ nx \rangle\ \langle n\ s-p: V \rightarrow_{out}\ n'' \rangle$ **have** $n\ irs-n\#nsx \rightarrow_{d^*}\ nx$
by(rule *intra-return-sum-SDG-path.irsSp-Cons-param-out*)
with $\langle ns = nsx@nx\#nsx' \rangle\ \langle nx\ s-p' \rightarrow_{ret}\ nx' \vee (\exists V.\ nx\ s-p': V \rightarrow_{out}\ nx') \rangle$
 $\langle nx'\ is-nsx' \rightarrow_{d^*}\ n' \rangle$
show ?thesis **by** *fastforce*

qed
 qed
 qed

lemma *irs-SDG-path-matched*:

assumes $n \text{ irs-ns} \rightarrow_{d^*} n''$ **and** $n'' \text{ s-p} \rightarrow_{\text{ret}} n' \vee n'' \text{ s-p} : V \rightarrow_{\text{out}} n'$
obtains $nx \text{ nsx}$ **where** $\text{matched } nx \text{ nsx } n'$ **and** $n \in \text{set } nsx$
and $nx \text{ s-p} \rightarrow_{\text{sum}} \text{CFG-node (parent-node } n')$

proof(*atomize-elim*)

from *assms*

show $\exists nx \text{ nsx. matched } nx \text{ nsx } n' \wedge n \in \text{set } nsx \wedge$

$nx \text{ s-p} \rightarrow_{\text{sum}} \text{CFG-node (parent-node } n')$

proof(*induct ns arbitrary:n'' n' p V rule:length-induct*)

fix $ns \ n'' \ n' \ p \ V$

assume $IH:\forall ns'. \text{length } ns' < \text{length } ns \rightarrow$

$(\forall n''. n \text{ irs-ns}' \rightarrow_{d^*} n'' \rightarrow$

$(\forall nx' \ p' \ V'. (n'' \text{ s-p}' \rightarrow_{\text{ret}} nx' \vee n'' \text{ s-p}' : V' \rightarrow_{\text{out}} nx') \rightarrow$

$(\exists nx \text{ nsx. matched } nx \text{ nsx } nx' \wedge n \in \text{set } nsx \wedge$

$nx \text{ s-p}' \rightarrow_{\text{sum}} \text{CFG-node (parent-node } nx'))$)

and $n \text{ irs-ns} \rightarrow_{d^*} n''$ **and** $n'' \text{ s-p} \rightarrow_{\text{ret}} n' \vee n'' \text{ s-p} : V \rightarrow_{\text{out}} n'$

from $\langle n'' \text{ s-p} \rightarrow_{\text{ret}} n' \vee n'' \text{ s-p} : V \rightarrow_{\text{out}} n' \rangle$ **have** *valid-SDG-node* n''

by(*fastforce intro:sum-SDG-edge-valid-SDG-node*)

from $\langle n'' \text{ s-p} \rightarrow_{\text{ret}} n' \vee n'' \text{ s-p} : V \rightarrow_{\text{out}} n' \rangle$

have $n'' \text{ -p} \rightarrow_{\text{ret}} n' \vee n'' \text{ -p} : V \rightarrow_{\text{out}} n'$

by(*fastforce intro:sum-SDG-edge-SDG-edge SDG-edge-sum-SDG-edge*)

from $\langle n'' \text{ s-p} \rightarrow_{\text{ret}} n' \vee n'' \text{ s-p} : V \rightarrow_{\text{out}} n' \rangle$

have *CFG-node (parent-node* n'') *s-p* \rightarrow_{ret} *CFG-node (parent-node* n')

by(*fastforce elim:sum-SDG-edge.cases intro:sum-SDG-return-edge*)

then obtain $a \ Q \ f$ **where** *valid-edge* a **and** *kind* $a = Q \leftrightarrow_{pf}$

and *parent-node* $n'' = \text{sourcenode } a$ **and** *parent-node* $n' = \text{targetnode } a$

by(*fastforce elim:sum-SDG-edge.cases*)

from $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q \leftrightarrow_{pf} \rangle$ **obtain** $a' \ Q' \ r' \ fs'$

where $a \in \text{get-return-edges } a'$ **and** *valid-edge* a' **and** *kind* $a' = Q' : r' \hookrightarrow_{pfs}$

and *CFG-node (sourcenode* a') *s-p* \rightarrow_{sum} *CFG-node (targetnode* $a')$

by(*erule return-edge-determines-call-and-sum-edge*)

from $\langle \text{valid-edge } a' \rangle \langle \text{kind } a' = Q' : r' \hookrightarrow_{pfs} \rangle$

have *CFG-node (sourcenode* a') *s-p* $\rightarrow_{\text{call}}$ *CFG-node (targetnode* $a')$

by(*fastforce intro:sum-SDG-call-edge*)

from $\langle \text{CFG-node (parent-node } n'') \text{ s-p} \rightarrow_{\text{ret}} \text{CFG-node (parent-node } n') \rangle$

have *get-proc (parent-node* $n'') = p$

by(*auto elim!:sum-SDG-edge.cases intro:get-proc-return*)

from $\langle n \text{ irs-ns} \rightarrow_{d^*} n'' \rangle$

show $\exists nx \text{ nsx. matched } nx \text{ nsx } n' \wedge n \in \text{set } nsx \wedge$

$nx \text{ s-p} \rightarrow_{\text{sum}} \text{CFG-node (parent-node } n')$

proof(*rule irs-SDG-path-split*)

assume $n \text{ is-ns} \rightarrow_{d^*} n''$

hence *valid-SDG-node* n **by**(*rule is-SDG-path-valid-SDG-node*)

then obtain asx **where** $(\text{-Entry-}) \text{ -asx} \rightarrow_{\sqrt{*}} \text{parent-node } n$

by(*fastforce dest:valid-SDG-CFG-node Entry-path*)
then obtain asx' **where** $(-Entry-) -asx' \rightarrow_{\sqrt{*}} parent\text{-}node\ n$
and $\forall a' \in set\ asx'.\ intra\text{-}kind(kind\ a') \vee (\exists Q\ r\ p\ fs.\ kind\ a' = Q:r \hookrightarrow_p fs)$
by $(erule\ valid\text{-}Entry\text{-}path\text{-}ascending\text{-}path)$
from $\langle n\ is\text{-}ns \rightarrow_d^* n'' \rangle$ **obtain** as **where** $parent\text{-}node\ n -as \rightarrow_{\iota^*} parent\text{-}node\ n''$
 by(*erule is-SDG-path-CFG-path*)
hence $get\text{-}proc\ (parent\text{-}node\ n) = get\text{-}proc\ (parent\text{-}node\ n'')$
by(*rule intra-path-get-procs*)
from $\langle valid\text{-}SDG\text{-}node\ n \rangle$ **have** $valid\text{-}node\ (parent\text{-}node\ n)$
by(*rule valid-SDG-CFG-node*)
hence $valid\text{-}SDG\text{-}node\ (CFG\text{-}node\ (parent\text{-}node\ n))$ **by** *simp*
have $\exists a\ as.\ valid\text{-}edge\ a \wedge (\exists Q\ p\ r\ fs.\ kind\ a = Q:r \hookrightarrow_p fs) \wedge$
 $targetnode\ a -as \rightarrow_{\iota^*} parent\text{-}node\ n$
proof(*cases* $\forall a' \in set\ asx'.\ intra\text{-}kind(kind\ a')$)
case *True*
with $\langle (-Entry-) -asx' \rightarrow_{\sqrt{*}} parent\text{-}node\ n \rangle$
have $\langle (-Entry-) -asx' \rightarrow_{\iota^*} parent\text{-}node\ n \rangle$
by(*fastforce simp:intra-path-def vp-def*)
hence $get\text{-}proc\ (-Entry-) = get\text{-}proc\ (parent\text{-}node\ n)$
by(*rule intra-path-get-procs*)
with $get\text{-}proc\text{-}Entry$ **have** $get\text{-}proc\ (parent\text{-}node\ n) = Main$ **by** *simp*
from $\langle get\text{-}proc\ (parent\text{-}node\ n) = get\text{-}proc\ (parent\text{-}node\ n'') \rangle$
 $\langle get\text{-}proc\ (parent\text{-}node\ n) = Main \rangle$
have $get\text{-}proc\ (parent\text{-}node\ n'') = Main$ **by** *simp*
from $\langle valid\text{-}edge\ a \rangle \langle kind\ a = Q \hookrightarrow_p fs \rangle$ **have** $get\text{-}proc\ (sourcnode\ a) = p$
by(*rule get-proc-return*)
with $\langle parent\text{-}node\ n'' = sourcnode\ a \rangle \langle get\text{-}proc\ (parent\text{-}node\ n'') = Main \rangle$
have $p = Main$ **by** *simp*
with $\langle kind\ a = Q \hookrightarrow_p fs \rangle$ **have** $kind\ a = Q \hookrightarrow_{Main} f$ **by** *simp*
with $\langle valid\text{-}edge\ a \rangle$ **have** *False* **by**(*rule Main-no-return-source*)
thus *?thesis* **by** *simp*
next
assume $\neg (\forall a' \in set\ asx'.\ intra\text{-}kind\ (kind\ a'))$
with $\langle \forall a' \in set\ asx'.\ intra\text{-}kind(kind\ a') \vee (\exists Q\ r\ p\ fs.\ kind\ a' = Q:r \hookrightarrow_p fs) \rangle$
have $\exists a' \in set\ asx'.\ \exists Q\ r\ p\ fs.\ kind\ a' = Q:r \hookrightarrow_p fs$
by(*fastforce simp:intra-kind-def*)
then obtain $as\ a'\ as'$ **where** $asx' = as@a'\#as'$
and $\exists Q\ r\ p\ fs.\ kind\ a' = Q:r \hookrightarrow_p fs$
and $\forall a' \in set\ as'.\ \neg (\exists Q\ r\ p\ fs.\ kind\ a' = Q:r \hookrightarrow_p fs)$
by(*erule split-list-last-propE*)
with $\langle \forall a' \in set\ asx'.\ intra\text{-}kind(kind\ a') \vee (\exists Q\ r\ p\ fs.\ kind\ a' = Q:r \hookrightarrow_p fs) \rangle$
have $\forall a' \in set\ as'.\ intra\text{-}kind\ (kind\ a')$ **by**(*auto simp:intra-kind-def*)
from $\langle (-Entry-) -asx' \rightarrow_{\sqrt{*}} parent\text{-}node\ n \rangle \langle asx' = as@a'\#as' \rangle$
have $valid\text{-}edge\ a'$ **and** $targetnode\ a' -as' \rightarrow^* parent\text{-}node\ n$
by(*auto dest:path-split simp:vp-def*)
with $\langle \forall a' \in set\ as'.\ intra\text{-}kind\ (kind\ a') \rangle \langle \exists Q\ r\ p\ fs.\ kind\ a' = Q:r \hookrightarrow_p fs \rangle$
show *?thesis* **by**(*fastforce simp:intra-path-def*)
qed


```

then obtain  $ax\ asx\ Qx\ rx\ fsx\ px$  where  $valid-edge\ ax$ 
  and  $kind\ ax = Qx:rx \hookrightarrow_{px} fsx$  and  $targetnode\ ax -asx \rightarrow_{\iota} * parent-node\ n$ 
  by  $blast$ 
from  $\langle valid-edge\ ax \rangle \langle kind\ ax = Qx:rx \hookrightarrow_{px} fsx \rangle$ 
have  $get-proc\ (targetnode\ ax) = px$ 
  by  $(rule\ get-proc-call)$ 
from  $\langle targetnode\ ax -asx \rightarrow_{\iota} * parent-node\ n \rangle$ 
have  $get-proc\ (targetnode\ ax) = get-proc\ (parent-node\ n)$ 
  by  $(rule\ intra-path-get-procs)$ 
with  $\langle get-proc\ (parent-node\ n) = get-proc\ (parent-node\ n'') \rangle$ 
   $\langle get-proc\ (targetnode\ ax) = px \rangle$ 
have  $get-proc\ (parent-node\ n'') = px$  by  $simp$ 
with  $\langle get-proc\ (parent-node\ n'') = p \rangle$  have  $[simp]: px = p$  by  $simp$ 
from  $\langle valid-edge\ a' \rangle \langle valid-edge\ ax \rangle \langle kind\ a' = Q':r' \hookrightarrow_{pfs'} \rangle$ 
   $\langle kind\ ax = Qx:rx \hookrightarrow_{px} fsx \rangle$ 
have  $targetnode\ a' = targetnode\ ax$  by  $simp(rule\ same-proc-call-unique-target)$ 
have  $parent-node\ n \neq (-Exit-)$ 
proof
  assume  $parent-node\ n = (-Exit-)$ 
from  $\langle n\ is -ns \rightarrow_d * n'' \rangle$  obtain  $as$  where  $parent-node\ n -as \rightarrow_{\iota} * parent-node$ 
 $n''$ 
  by  $(erule\ is-SDG-path-CFG-path)$ 
with  $\langle parent-node\ n = (-Exit-) \rangle$ 
have  $(-Exit-) -as \rightarrow * parent-node\ n''$  by  $(simp\ add:intra-path-def)$ 
hence  $parent-node\ n'' = (-Exit-)$  by  $(fastforce\ dest:path-Exit-source)$ 
from  $\langle get-proc\ (parent-node\ n'') = p \rangle \langle parent-node\ n'' = (-Exit-) \rangle$ 
   $\langle parent-node\ n'' = sourcenode\ a \rangle$   $get-proc-Exit$ 
have  $p = Main$  by  $simp$ 
with  $\langle kind\ a = Q \hookrightarrow_{pf} \rangle$  have  $kind\ a = Q \hookrightarrow_{Main} f$  by  $simp$ 
with  $\langle valid-edge\ a \rangle$  show  $False$  by  $(rule\ Main-no-return-source)$ 
qed
have  $\exists nsx. CFG-node\ (targetnode\ a')\ cd - nsx \rightarrow_d * CFG-node\ (parent-node$ 
 $n)$ 
proof  $(cases\ targetnode\ a' = parent-node\ n)$ 
  case  $True$ 
  with  $\langle valid-SDG-node\ (CFG-node\ (parent-node\ n)) \rangle$ 
  have  $CFG-node\ (targetnode\ a')\ cd - [] \rightarrow_d * CFG-node\ (parent-node\ n)$ 
  by  $(fastforce\ intro:cdSp-Nil)$ 
  thus  $?thesis$  by  $blast$ 
next
  case  $False$ 
  with  $\langle targetnode\ ax -asx \rightarrow_{\iota} * parent-node\ n \rangle \langle parent-node\ n \neq (-Exit-) \rangle$ 
   $\langle valid-edge\ ax \rangle \langle kind\ ax = Qx:rx \hookrightarrow_{px} fsx \rangle \langle targetnode\ a' = targetnode\ ax \rangle$ 
obtain  $nsx$ 
  where  $CFG-node\ (targetnode\ a')\ cd - nsx \rightarrow_d * CFG-node\ (parent-node\ n)$ 
  by  $(fastforce\ elim!:in-proc-cdep-SDG-path)$ 
  thus  $?thesis$  by  $blast$ 
qed
then obtain  $nsx$ 

```

where $CFG\text{-node}$ ($targetnode\ a'$) $cd\text{-}nsx\rightarrow_d^*$ $CFG\text{-node}$ ($parent\text{-}node\ n$)
by *blast*
hence $CFG\text{-node}$ ($targetnode\ a'$) $i\text{-}nsx\rightarrow_d^*$ $CFG\text{-node}$ ($parent\text{-}node\ n$)
by(*rule cdep-SDG-path-intra-SDG-path*)
show *?thesis*
proof(*cases ns*)
case *Nil*
with $\langle n\ is\text{-}ns\rightarrow_d^*\ n'' \rangle$ **have** $n = n''$
by(*fastforce elim:intra-sum-SDG-path.cases*)
from $\langle valid\text{-}edge\ a' \rangle$ $\langle kind\ a' = Q':r'\hookrightarrow_pfs' \rangle$ $\langle a \in get\text{-}return\text{-}edges\ a' \rangle$
have *matched* ($CFG\text{-node}$ ($targetnode\ a'$)) [$CFG\text{-node}$ ($targetnode\ a'$)]
($CFG\text{-node}$ ($sourcenode\ a$)) **by**(*rule intra-proc-matched*)
from $\langle valid\text{-}SDG\text{-}node\ n'' \rangle$
have $n'' = CFG\text{-node}$ ($parent\text{-}node\ n''$) \vee $CFG\text{-node}$ ($parent\text{-}node\ n''$) \longrightarrow_{cd}
 n''
by(*rule valid-SDG-node-cases*)
hence $\exists\ nsx.$ $CFG\text{-node}$ ($parent\text{-}node\ n''$) $i\text{-}nsx\rightarrow_d^*$ n''
proof
assume $n'' = CFG\text{-node}$ ($parent\text{-}node\ n''$)
with $\langle valid\text{-}SDG\text{-}node\ n'' \rangle$ **have** $CFG\text{-node}$ ($parent\text{-}node\ n''$) $i\text{-}\square\rightarrow_d^*$ n''
by(*fastforce intro:iSp-Nil*)
thus *?thesis* **by** *blast*
next
assume $CFG\text{-node}$ ($parent\text{-}node\ n''$) \longrightarrow_{cd} n''
from $\langle valid\text{-}SDG\text{-}node\ n'' \rangle$ **have** *valid-node* ($parent\text{-}node\ n''$)
by(*rule valid-SDG-CFG-node*)
hence $valid\text{-}SDG\text{-}node$ ($CFG\text{-node}$ ($parent\text{-}node\ n''$)) **by** *simp*
hence $CFG\text{-node}$ ($parent\text{-}node\ n''$) $i\text{-}\square\rightarrow_d^*$ $CFG\text{-node}$ ($parent\text{-}node\ n''$)
by(*rule iSp-Nil*)
with $\langle CFG\text{-node}$ ($parent\text{-}node\ n''$) \longrightarrow_{cd} $n'' \rangle$
have $CFG\text{-node}$ ($parent\text{-}node\ n''$) $i\text{-}\square\text{@}[CFG\text{-node}$ ($parent\text{-}node\ n''$) \rightarrow_d^*
 n''
by(*fastforce intro:iSp-Append-cdep sum-SDG-edge-SDG-edge*)
thus *?thesis* **by** *blast*
qed
with $\langle parent\text{-}node\ n'' = sourcenode\ a \rangle$
obtain *nsx* **where** $CFG\text{-node}$ ($sourcenode\ a$) $i\text{-}nsx\rightarrow_d^*$ n'' **by** *fastforce*
with $\langle matched$ ($CFG\text{-node}$ ($targetnode\ a'$)) [$CFG\text{-node}$ ($targetnode\ a'$)]
($CFG\text{-node}$ ($sourcenode\ a$)) \rangle
have *matched* ($CFG\text{-node}$ ($targetnode\ a'$)) ($[CFG\text{-node}$ ($targetnode\ a'$)] $\text{@}nsx$)
 n''
by(*fastforce intro:matched-Append intra-SDG-path-matched*)
moreover
from $\langle valid\text{-}edge\ a' \rangle$ $\langle kind\ a' = Q':r'\hookrightarrow_pfs' \rangle$
have $CFG\text{-node}$ ($sourcenode\ a'$) $\text{-}p\rightarrow_{call}$ $CFG\text{-node}$ ($targetnode\ a'$)
by(*fastforce intro:SDG-call-edge*)
moreover
from $\langle valid\text{-}edge\ a' \rangle$ **have** $valid\text{-}SDG\text{-}node$ ($CFG\text{-node}$ ($sourcenode\ a'$))
by *simp*

hence $\text{matched} (\text{CFG-node} (\text{sourcenode } a')) \sqcap (\text{CFG-node} (\text{sourcenode } a'))$
by $(\text{rule matched-Nil})$
ultimately have $\text{matched} (\text{CFG-node} (\text{sourcenode } a'))$
 $(\sqcap @ (\text{CFG-node} (\text{sourcenode } a')) \# ([\text{CFG-node} (\text{targetnode } a')] @ \text{nsx}) @ [n'])$
 n'
using $\langle n'' s-p \rightarrow_{\text{ret}} n' \vee n'' s-p: V \rightarrow_{\text{out}} n' \rangle \langle \text{parent-node } n' = \text{targetnode } a \rangle$
 $a)$
 $\langle \text{parent-node } n'' = \text{sourcenode } a \rangle \langle \text{valid-edge } a' \rangle \langle a \in \text{get-return-edges } a' \rangle$
by $(\text{fastforce intro:matched-bracket-call dest:sum-SDG-edge-SDG-edge})$
with $\langle n = n'' \rangle \langle \text{CFG-node} (\text{sourcenode } a') s-p \rightarrow_{\text{sum}} \text{CFG-node} (\text{targetnode } a) \rangle$
 $a)$
 $\langle \text{parent-node } n' = \text{targetnode } a \rangle$
show $?thesis$ **by** fastforce
next
case Cons
with $\langle n \text{ is-ns} \rightarrow_{d^*} n'' \rangle$ **have** $n \in \text{set } ns$
by $(\text{induct rule:intra-sum-SDG-path-rev-induct})$ auto
from $\langle n \text{ is-ns} \rightarrow_{d^*} n'' \rangle$ **obtain** ns' **where** $\text{matched } n \ ns' \ n''$
and $\text{set } ns \subseteq \text{set } ns'$ **by** $(\text{erule is-SDG-path-matched})$
with $\langle n \in \text{set } ns \rangle$ **have** $n \in \text{set } ns'$ **by** fastforce
from $\langle \text{valid-SDG-node } n \rangle$
have $n = \text{CFG-node} (\text{parent-node } n) \vee \text{CFG-node} (\text{parent-node } n) \rightarrow_{cd} n$
by $(\text{rule valid-SDG-node-cases})$
hence $\exists \text{nsx. CFG-node} (\text{parent-node } n) i-\text{nsx} \rightarrow_{d^*} n$
proof
assume $n = \text{CFG-node} (\text{parent-node } n)$
with $\langle \text{valid-SDG-node } n \rangle$ **have** $\text{CFG-node} (\text{parent-node } n) i-\square \rightarrow_{d^*} n$
by $(\text{fastforce intro:iSp-Nil})$
thus $?thesis$ **by** blast
next
assume $\text{CFG-node} (\text{parent-node } n) \rightarrow_{cd} n$
from $\langle \text{valid-SDG-node} (\text{CFG-node} (\text{parent-node } n)) \rangle$
have $\text{CFG-node} (\text{parent-node } n) i-\square \rightarrow_{d^*} \text{CFG-node} (\text{parent-node } n)$
by (rule iSp-Nil)
with $\langle \text{CFG-node} (\text{parent-node } n) \rightarrow_{cd} n \rangle$
have $\text{CFG-node} (\text{parent-node } n) i-\square @ [\text{CFG-node} (\text{parent-node } n)] \rightarrow_{d^*} n$
by $(\text{fastforce intro:iSp-Append-cdep sum-SDG-edge-SDG-edge})$
thus $?thesis$ **by** blast
qed
then obtain nsx' **where** $\text{CFG-node} (\text{parent-node } n) i-\text{nsx}' \rightarrow_{d^*} n$ **by** blast
with $\langle \text{CFG-node} (\text{targetnode } a') i-\text{nsx} \rightarrow_{d^*} \text{CFG-node} (\text{parent-node } n) \rangle$
have $\text{CFG-node} (\text{targetnode } a') i-\text{nsx} @ \text{nsx}' \rightarrow_{d^*} n$
by $-(\text{rule intra-SDG-path-Append})$
hence $\text{matched} (\text{CFG-node} (\text{targetnode } a')) (\text{nsx} @ \text{nsx}') \ n$
by $(\text{rule intra-SDG-path-matched})$
with $\langle \text{matched } n \ ns' \ n'' \rangle$
have $\text{matched} (\text{CFG-node} (\text{targetnode } a')) ((\text{nsx} @ \text{nsx}') @ \text{ns}') \ n''$
by $(\text{rule matched-Append})$
moreover

```

from ⟨valid-edge a'⟩ ⟨kind a' = Q':r'↦pfs'⟩
have CFG-node (sourcenode a') -p→call CFG-node (targetnode a')
  by(fastforce intro:SDG-call-edge)
moreover
from ⟨valid-edge a'⟩ have valid-SDG-node (CFG-node (sourcenode a'))
  by simp
hence matched (CFG-node (sourcenode a')) [] (CFG-node (sourcenode a'))
  by(rule matched-Nil)
ultimately have matched (CFG-node (sourcenode a'))
  ([]@(CFG-node (sourcenode a'))#((nsx@nsx')@ns')@[n']) n'
using ⟨n'' s-p→ret n' ∨ n'' s-p:V→out n'⟩ ⟨parent-node n' = targetnode
a)
  ⟨parent-node n'' = sourcenode a⟩ ⟨valid-edge a'⟩ ⟨a ∈ get-return-edges a'⟩
  by(fastforce intro:matched-bracket-call dest:sum-SDG-edge-SDG-edge)
with ⟨CFG-node (sourcenode a') s-p→sum CFG-node (targetnode a)⟩
  ⟨parent-node n' = targetnode a⟩ ⟨n ∈ set ns'⟩
show ?thesis by fastforce
qed
next
fix ms ms' m m' px
assume ns = ms@m#ms' and n irs-ms→d* m
  and m s-px→ret m' ∨ (∃ V. m s-px:V→out m') and m' is-ms'→d* n''
from ⟨ns = ms@m#ms'⟩ have length ms < length ns by simp
with IH ⟨n irs-ms→d* m⟩ ⟨m s-px→ret m' ∨ (∃ V. m s-px:V→out m')⟩
obtain mx msx
  where matched mx msx m' and n ∈ set msx
  and mx s-px→sum CFG-node (parent-node m') by fastforce
from ⟨m' is-ms'→d* n''⟩ obtain msx' where matched m' msx' n''
  by -(erule is-SDG-path-matched)
with ⟨matched mx msx m'⟩ have matched mx (msx@msx') n''
  by -(rule matched-Append)
from ⟨m s-px→ret m' ∨ (∃ V. m s-px:V→out m')⟩
have m -px→ret m' ∨ (∃ V. m -px:V→out m')
  by(auto intro:sum-SDG-edge-SDG-edge SDG-edge-sum-SDG-edge)
from ⟨m s-px→ret m' ∨ (∃ V. m s-px:V→out m')⟩
have CFG-node (parent-node m) s-px→ret CFG-node (parent-node m')
  by(fastforce elim:sum-SDG-edge.cases intro:sum-SDG-return-edge)
then obtain ax Qx fx where valid-edge ax and kind ax = Qx↦pxfx
and parent-node m = sourcenode ax and parent-node m' = targetnode ax
  by(fastforce elim:sum-SDG-edge.cases)
from ⟨valid-edge ax⟩ ⟨kind ax = Qx↦pxfx⟩ obtain ax' Qx' rx' fsx'
  where ax ∈ get-return-edges ax' and valid-edge ax'
  and kind ax' = Qx':rx'↦pxfsx'
  and CFG-node (sourcenode ax') s-px→sum CFG-node (targetnode ax)
  by(erule return-edge-determines-call-and-sum-edge)
from ⟨valid-edge ax'⟩ ⟨kind ax' = Qx':rx'↦pxfsx'⟩
have CFG-node (sourcenode ax') s-px→call CFG-node (targetnode ax')
  by(fastforce intro:sum-SDG-call-edge)
from ⟨mx s-px→sum CFG-node (parent-node m')⟩

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have valid-SDG-node mx by(rule sum-SDG-edge-valid-SDG-node)
have  $\exists \text{msx}''$ . CFG-node (targetnode a') cd-msx'' $\rightarrow_d^*$  mx
proof(cases targetnode a' = parent-node mx)
  case True
    from (valid-SDG-node mx)
      have  $mx = \text{CFG-node}$  (parent-node mx)  $\vee \text{CFG-node}$  (parent-node mx)
 $\rightarrow_{cd}$  mx
      by(rule valid-SDG-node-cases)
    thus ?thesis
  proof
    assume  $mx = \text{CFG-node}$  (parent-node mx)
    with (valid-SDG-node mx) True
    have  $\text{CFG-node}$  (targetnode a') cd-[] $\rightarrow_d^*$  mx by(fastforce intro:cdSp-Nil)
    thus ?thesis by blast
  next
    assume  $\text{CFG-node}$  (parent-node mx)  $\rightarrow_{cd}$  mx
    with (valid-edge a') True[THEN sym]
    have  $\text{CFG-node}$  (targetnode a') cd-[]@[ $\text{CFG-node}$  (targetnode a')] $\rightarrow_d^*$  mx
      by(fastforce intro:cdep-SDG-path.intros)
    thus ?thesis by blast
  qed
next
case False
show ?thesis
proof(cases  $\forall ai$ . valid-edge ai  $\wedge$  sourcenode ai = parent-node mx
 $\rightarrow ai \notin \text{get-return-edges } a'$ )
  case True
    { assume parent-node mx = (-Exit-)
      with ( $mx \text{ s-px} \rightarrow_{sum} \text{CFG-node}$  (parent-node m'))
      obtain ai where valid-edge ai and sourcenode ai = (-Exit-)
        by  $-(erule \text{sum-SDG-edge.cases, auto})$ 
      hence False by(rule Exit-source) }
    hence parent-node mx  $\neq (-Exit-)$  by fastforce
    from (valid-SDG-node mx) have valid-node (parent-node mx)
      by(rule valid-SDG-CFG-node)
    then obtain asx where (-Entry-)  $-asx \rightarrow_{\sqrt{*}}$  parent-node mx
      by(fastforce intro:Entry-path)
    then obtain asx' where (-Entry-)  $-asx' \rightarrow_{\sqrt{*}}$  parent-node mx
      and  $\forall a' \in \text{set } asx'$ . intra-kind(kind a')  $\vee (\exists Q \text{ r p fs. kind } a' = Q:r \hookrightarrow_p fs)$ 
      by  $-(erule \text{valid-Entry-path-ascending-path})$ 
    from ( $mx \text{ s-px} \rightarrow_{sum} \text{CFG-node}$  (parent-node m'))
    obtain nsi where matched mx nsi ( $\text{CFG-node}$  (parent-node m'))
      by  $-(erule \text{sum-SDG-summary-edge-matched})$ 
    then obtain asi where parent-node mx  $-asi \rightarrow_{sl^*}$  parent-node m'
      by(fastforce elim:matched-same-level-CFG-path)
    hence get-proc (parent-node mx) = get-proc (parent-node m')
      by(rule slp-get-proc)
    from ( $m' \text{ is-ms}' \rightarrow_d^* n''$ ) obtain nsi' where matched m' nsi' n''
      by  $-(erule \text{is-SDG-path-matched})$ 

```

then obtain asi' **where** $parent\text{-}node\ m' - asi' \rightarrow_{sl}^* parent\text{-}node\ n''$
by $-(erule\ matched\text{-}same\text{-}level\text{-}CFG\text{-}path)$
hence $get\text{-}proc\ (parent\text{-}node\ m') = get\text{-}proc\ (parent\text{-}node\ n'')$
by $(rule\ slp\text{-}get\text{-}proc)$
with $\langle get\text{-}proc\ (parent\text{-}node\ mx) = get\text{-}proc\ (parent\text{-}node\ m') \rangle$
have $get\text{-}proc\ (parent\text{-}node\ mx) = get\text{-}proc\ (parent\text{-}node\ n'')$ **by** $simp$
from $\langle get\text{-}proc\ (parent\text{-}node\ n'') = p \rangle$
 $\langle get\text{-}proc\ (parent\text{-}node\ mx) = get\text{-}proc\ (parent\text{-}node\ n'') \rangle$
have $get\text{-}proc\ (parent\text{-}node\ mx) = p$ **by** $simp$
have $\exists\ asx.\ targetnode\ a' - asx \rightarrow_{l^*} parent\text{-}node\ mx$
proof $(cases\ \forall\ a' \in\ set\ asx'.\ intra\text{-}kind(kind\ a'))$
case $True$
with $\langle (-Entry-) - asx' \rightarrow_{\sqrt{}}^* parent\text{-}node\ mx \rangle$
have $(-Entry-) - asx' \rightarrow_{l^*} parent\text{-}node\ mx$
by $(simp\ add:vp\text{-}def\ intra\text{-}path\text{-}def)$
hence $get\text{-}proc\ (-Entry-) = get\text{-}proc\ (parent\text{-}node\ mx)$
by $(rule\ intra\text{-}path\text{-}get\text{-}procs)$
with $\langle get\text{-}proc\ (parent\text{-}node\ mx) = p \rangle$ **have** $get\text{-}proc\ (-Entry-) = p$
by $simp$
with $\langle CFG\text{-}node\ (parent\text{-}node\ n'')\ s - p \rightarrow_{ret} CFG\text{-}node\ (parent\text{-}node\ n'') \rangle$
have $False$
by $-(erule\ sum\text{-}SDG\text{-}edge.cases,$
 $\quad auto\ intro:Main\text{-}no\text{-}return\text{-}source\ simp:get\text{-}proc\text{-}Entry)$
thus $?thesis$ **by** $simp$
next
case $False$
hence $\exists\ a' \in\ set\ asx'.\ \neg\ intra\text{-}kind\ (kind\ a')$ **by** $fastforce$
then obtain $ai\ as'\ as''$ **where** $asx' = as' @ ai \# as''$
and $\neg\ intra\text{-}kind\ (kind\ ai)$ **and** $\forall\ a' \in\ set\ as''.\ intra\text{-}kind\ (kind\ a')$
by $(fastforce\ elim!:split\text{-}list\text{-}last\text{-}propE)$
from $\langle asx' = as' @ ai \# as'' \rangle\ \langle \neg\ intra\text{-}kind\ (kind\ ai) \rangle$
 $\langle \forall\ a' \in\ set\ asx'.\ intra\text{-}kind(kind\ a') \vee (\exists\ Q\ r\ p\ fs.\ kind\ a' = Q:r \hookrightarrow_p fs) \rangle$
obtain $Qi\ ri\ pi\ fsi$ **where** $kind\ ai = Qi:ri \hookrightarrow_{pi} fsi$
and $\forall\ a' \in\ set\ as'.\ intra\text{-}kind(kind\ a') \vee$
 $(\exists\ Q\ r\ p\ fs.\ kind\ a' = Q:r \hookrightarrow_p fs)$
by $auto$
from $\langle (-Entry-) - asx' \rightarrow_{\sqrt{}}^* parent\text{-}node\ mx \rangle\ \langle asx' = as' @ ai \# as'' \rangle$
 $\langle \forall\ a' \in\ set\ as''.\ intra\text{-}kind\ (kind\ a') \rangle$
have $valid\text{-}edge\ ai$ **and** $targetnode\ ai - as'' \rightarrow_{l^*} parent\text{-}node\ mx$
by $(auto\ intro:path\text{-}split\ simp:vp\text{-}def\ intra\text{-}path\text{-}def)$
hence $get\text{-}proc\ (targetnode\ ai) = get\text{-}proc\ (parent\text{-}node\ mx)$
by $-(rule\ intra\text{-}path\text{-}get\text{-}procs)$
with $\langle get\text{-}proc\ (parent\text{-}node\ mx) = p \rangle\ \langle valid\text{-}edge\ ai \rangle$
 $\langle kind\ ai = Qi:ri \hookrightarrow_{pi} fsi \rangle$
have $[simp]:pi = p$ **by** $(fastforce\ dest:get\text{-}proc\text{-}call)$
from $\langle valid\text{-}edge\ ai \rangle\ \langle valid\text{-}edge\ a' \rangle$
 $\langle kind\ ai = Qi:ri \hookrightarrow_{pi} fsi \rangle\ \langle kind\ a' = Q':r' \hookrightarrow_{pfs'} \rangle$
have $targetnode\ ai = targetnode\ a'$
by $(fastforce\ intro:same\text{-}proc\text{-}call\text{-}unique\text{-}target)$

```

    with ⟨targetnode ai -as''→l* parent-node mx⟩
    show ?thesis by fastforce
  qed
  then obtain asx where targetnode a' -asx→l* parent-node mx by blast
  from this ⟨valid-edge a'⟩ ⟨kind a' = Q':r'↪pfs'⟩
    ⟨parent-node mx ≠ (-Exit-)⟩ ⟨targetnode a' ≠ parent-node mx⟩ True
  obtain msi
  where CFG-node(targetnode a') cd-msi→d* CFG-node(parent-node mx)
    by(fastforce elim!:in-proc-cdep-SDG-path)
  from ⟨valid-SDG-node mx⟩
  have mx = CFG-node (parent-node mx) ∨ CFG-node (parent-node mx)
  →cd mx
    by(rule valid-SDG-node-cases)
  thus ?thesis
  proof
    assume mx = CFG-node (parent-node mx)
    with ⟨CFG-node(targetnode a') cd-msi→d* CFG-node(parent-node mx)⟩
    show ?thesis by fastforce
  next
    assume CFG-node (parent-node mx) →cd mx
    with ⟨CFG-node(targetnode a') cd-msi→d* CFG-node(parent-node mx)⟩
  have CFG-node(targetnode a') cd-msi@[CFG-node(parent-node mx)]→d*
  mx
    by(fastforce intro:cdSp-Append-cdep)
    thus ?thesis by fastforce
  qed
  next
  case False
  then obtain ai where valid-edge ai and sourcenode ai = parent-node mx
    and ai ∈ get-return-edges a' by blast
  with ⟨valid-edge a'⟩ ⟨kind a' = Q':r'↪pfs'⟩
  have CFG-node (targetnode a') →cd CFG-node (parent-node mx)
    by(auto intro:SDG-proc-entry-exit-cdep)
  with ⟨valid-edge a'⟩
  have cd-path:CFG-node (targetnode a') cd-[]@[CFG-node (targetnode
  a')]→d*
    CFG-node (parent-node mx)
    by(fastforce intro:cdSp-Append-cdep cdSp-Nil)
  from ⟨valid-SDG-node mx⟩
  have mx = CFG-node (parent-node mx) ∨ CFG-node (parent-node mx)
  →cd mx
    by(rule valid-SDG-node-cases)
  thus ?thesis
  proof
    assume mx = CFG-node (parent-node mx)
    with cd-path show ?thesis by fastforce
  next
    assume CFG-node (parent-node mx) →cd mx
    with cd-path have CFG-node (targetnode a')

```

```

      cd-[CFG-node (targetnode a')]@[CFG-node (parent-node mx)]→d* mx
    by(fastforce intro:cdSp-Append-cdep)
  thus ?thesis by fastforce
qed
qed
qed
then obtain msx''
  where CFG-node (targetnode a') cd-msx''→d* mx by blast
  hence CFG-node (targetnode a') i-msx''→d* mx
  by(rule cdep-SDG-path-intra-SDG-path)
  with ⟨valid-edge a'⟩
  have matched (CFG-node (targetnode a')) ([]@msx'') mx
  by(fastforce intro:matched-Append-intra-SDG-path matched-Nil)
  with ⟨matched mx (msx@msx') n''⟩
  have matched (CFG-node (targetnode a')) (msx''@(msx@msx')) n''
  by(fastforce intro:matched-Append)
  with ⟨valid-edge a'⟩ ⟨CFG-node (sourcenode a') s-p→call CFG-node (targetnode
a')⟩
    ⟨n'' -p→ret n' ∨ n'' -p:V→out n'⟩ ⟨a ∈ get-return-edges a'⟩
    ⟨parent-node n'' = sourcenode a'⟩ ⟨parent-node n' = targetnode a'⟩
  have matched (CFG-node (sourcenode a'))
    ([]@CFG-node (sourcenode a')#(msx''@(msx@msx'))@[n'']) n'
  by(fastforce intro:matched-bracket-call matched-Nil sum-SDG-edge-SDG-edge)
  with ⟨n ∈ set msx⟩ ⟨CFG-node (sourcenode a') s-p→sum CFG-node (targetnode
a')⟩
    ⟨parent-node n' = targetnode a'⟩
  show ?thesis by fastforce
qed
qed
qed

```

lemma *irs-SDG-path-realizable*:

```

  assumes n irs-ns→d* n' and n ≠ n'
  obtains ns' where realizable (CFG-node (-Entry-) ns' n') and n ∈ set ns'
proof(atomize-elim)
  from ⟨n irs-ns→d* n'⟩
  have n = n' ∨ (∃ ns'. realizable (CFG-node (-Entry-) ns' n') ∧ n ∈ set ns')
  proof(rule irs-SDG-path-split)
    assume n is-ns→d* n'
    show ?thesis
  proof(cases ns = [])
    case True
    with ⟨n is-ns→d* n'⟩ have n = n' by(fastforce elim:intra-sum-SDG-path.cases)
    thus ?thesis by simp
  next
    case False
    with ⟨n is-ns→d* n'⟩ have n ∈ set ns by(fastforce dest:is-SDG-path-hd)
    from ⟨n is-ns→d* n'⟩ have valid-SDG-node n and valid-SDG-node n'

```


by(rule is-SDG-path-valid-SDG-node)+
 hence valid-node (parent-node n) by -(rule valid-SDG-CFG-node)
 from ⟨n is-ns→_d* n'⟩ obtain ns' where matched n ns' n' and set ns ⊆ set
 ns'
 by(erule is-SDG-path-matched)
 with ⟨n ∈ set ns⟩ have n ∈ set ns' by fastforce
 from ⟨valid-node (parent-node n)⟩
 show ?thesis
 proof(cases parent-node n = (-Exit-))
 case True
 with ⟨valid-SDG-node n⟩ have n = CFG-node (-Exit-)
 by(rule valid-SDG-node-parent-Exit)
 from ⟨n is-ns→_d* n'⟩ obtain as where parent-node n -as→_t* parent-node
 n'
 by -(erule is-SDG-path-intra-CFG-path)
 with ⟨n = CFG-node (-Exit-)⟩ have parent-node n' = (-Exit-)
 by(fastforce dest:path-Exit-source simp:intra-path-def)
 with ⟨valid-SDG-node n'⟩ have n' = CFG-node (-Exit-)
 by(rule valid-SDG-node-parent-Exit)
 with ⟨n = CFG-node (-Exit-)⟩ show ?thesis by simp
 next
 case False
 with ⟨valid-SDG-node n⟩
 obtain nsx where CFG-node (-Entry-) cc-nsx→_d* n
 by(erule Entry-cc-SDG-path-to-inner-node)
 hence realizable (CFG-node (-Entry-)) nsx n
 by(rule cdep-SDG-path-realizable)
 with ⟨matched n ns' n'⟩
 have realizable (CFG-node (-Entry-)) (nsx@ns') n'
 by -(rule realizable-Append-matched)
 with ⟨n ∈ set ns'⟩ show ?thesis by fastforce
 qed
 qed
 next
 fix nsx nsx' nx nx' p
 assume ns = nsx@nx#nsx' and n irs-nsx→_d* nx
 and nx s-p→_{ret} nx' ∨ (∃ V. nx s-p:V→_{out} nx') and nx' is-nsx'→_d* n'
 from ⟨nx s-p→_{ret} nx' ∨ (∃ V. nx s-p:V→_{out} nx')⟩
 have CFG-node (parent-node nx) s-p→_{ret} CFG-node (parent-node nx')
 by(fastforce elim:sum-SDG-edge.cases intro:sum-SDG-return-edge)
 then obtain a Q f where valid-edge a and kind a = Q↔_{pf}
 and parent-node nx = sourcenode a and parent-node nx' = targetnode a
 by(fastforce elim:sum-SDG-edge.cases)
 from ⟨valid-edge a⟩ ⟨kind a = Q↔_{pf}⟩ obtain a' Q' r' fs'
 where a ∈ get-return-edges a' and valid-edge a' and kind a' = Q':r'↔_{pf}s'
 and CFG-node (sourcenode a') s-p→_{sum} CFG-node (targetnode a)
 by(erule return-edge-determines-call-and-sum-edge)
 from ⟨valid-edge a'⟩ ⟨kind a' = Q':r'↔_{pf}s'⟩
 have CFG-node (sourcenode a') s-p→_{call} CFG-node (targetnode a')

```

    by(fastforce intro:sum-SDG-call-edge)
  from  $\langle n \text{ irs-nsx} \rightarrow_d^* nx \rangle \langle nx \text{ s-p} \rightarrow_{ret} nx' \vee (\exists V. nx \text{ s-p}:V \rightarrow_{out} nx') \rangle$ 
  obtain  $m \text{ ms}$  where  $\text{matched } m \text{ ms } nx'$  and  $n \in \text{set } ms$ 
    and  $m \text{ s-p} \rightarrow_{sum} \text{CFG-node } (\text{parent-node } nx')$ 
    by(fastforce elim:irs-SDG-path-matched)
  from  $\langle nx' \text{ is-nsx}' \rightarrow_d^* n' \rangle$  obtain  $ms'$  where  $\text{matched } nx' \text{ ms}' n'$ 
    and  $\text{set } nsx' \subseteq \text{set } ms'$  by (erule is-SDG-path-matched)
  with  $\langle \text{matched } m \text{ ms } nx' \rangle$  have  $\text{matched } m (ms@ms') n'$  by  $-(\text{rule matched-Append})$ 
  from  $\langle m \text{ s-p} \rightarrow_{sum} \text{CFG-node } (\text{parent-node } nx') \rangle$  have  $\text{valid-SDG-node } m$ 
    by(rule sum-SDG-edge-valid-SDG-node)
  hence  $\text{valid-node } (\text{parent-node } m)$  by (rule valid-SDG-CFG-node)
  thus ?thesis
  proof(cases parent-node m = (-Exit-))
    case True
      from  $\langle m \text{ s-p} \rightarrow_{sum} \text{CFG-node } (\text{parent-node } nx') \rangle$  obtain  $a$  where  $\text{valid-edge}$ 
    a
      and  $\text{sourcenode } a = \text{parent-node } m$ 
      by(fastforce elim:sum-SDG-edge.cases)
      with True have False by  $-(\text{rule Exit-source,simp-all})$ 
      thus ?thesis by simp
    next
      case False
      with  $\langle \text{valid-SDG-node } m \rangle$ 
      obtain  $ms''$  where  $\text{CFG-node } (-\text{Entry-}) \text{ cc-}ms'' \rightarrow_d^* m$ 
        by(erule Entry-cc-SDG-path-to-inner-node)
      hence  $\text{realizable } (\text{CFG-node } (-\text{Entry-})) \text{ ms}'' m$ 
        by(rule cdep-SDG-path-realizable)
      with  $\langle \text{matched } m (ms@ms') n' \rangle$ 
      have  $\text{realizable } (\text{CFG-node } (-\text{Entry-})) (ms''@(ms@ms')) n'$ 
        by  $-(\text{rule realizable-Append-matched})$ 
      with  $\langle n \in \text{set } ms \rangle$  show ?thesis by fastforce
    qed
  qed
  with  $\langle n \neq n' \rangle$  show  $\exists ns'. \text{realizable } (\text{CFG-node } (-\text{Entry-})) ns' n' \wedge n \in \text{set } ns'$ 
    by simp
  qed
end
end
end

```

1.9 Horwitz-Reps-Binkley Slice

theory *HRBSlice* imports *SDG* begin

context *SDG* begin

1.9.1 Set describing phase 1 of the two-phase slicer

inductive-set *sum-SDG-slice1* :: 'node SDG-node \Rightarrow 'node SDG-node set
for *n*::'node SDG-node
where *refl-slice1*:*valid-SDG-node* *n* \Longrightarrow *n* \in *sum-SDG-slice1* *n*
| *cdep-slice1*:
 $\llbracket n'' s \rightarrow_{cd} n'; n' \in \text{sum-SDG-slice1 } n \rrbracket \Longrightarrow n'' \in \text{sum-SDG-slice1 } n$
| *ddep-slice1*:
 $\llbracket n'' s - V \rightarrow_{dd} n'; n' \in \text{sum-SDG-slice1 } n \rrbracket \Longrightarrow n'' \in \text{sum-SDG-slice1 } n$
| *call-slice1*:
 $\llbracket n'' s - p \rightarrow_{call} n'; n' \in \text{sum-SDG-slice1 } n \rrbracket \Longrightarrow n'' \in \text{sum-SDG-slice1 } n$
| *param-in-slice1*:
 $\llbracket n'' s - p : V \rightarrow_{in} n'; n' \in \text{sum-SDG-slice1 } n \rrbracket \Longrightarrow n'' \in \text{sum-SDG-slice1 } n$
| *sum-slice1*:
 $\llbracket n'' s - p \rightarrow_{sum} n'; n' \in \text{sum-SDG-slice1 } n \rrbracket \Longrightarrow n'' \in \text{sum-SDG-slice1 } n$

lemma *slice1-cdep-slice1*:

$\llbracket nx \in \text{sum-SDG-slice1 } n; n s \rightarrow_{cd} n' \rrbracket \Longrightarrow nx \in \text{sum-SDG-slice1 } n'$
by(*induct rule*:*sum-SDG-slice1.induct*,
auto intro:*sum-SDG-slice1.intros sum-SDG-edge-valid-SDG-node*)

lemma *slice1-ddep-slice1*:

$\llbracket nx \in \text{sum-SDG-slice1 } n; n s - V \rightarrow_{dd} n' \rrbracket \Longrightarrow nx \in \text{sum-SDG-slice1 } n'$
by(*induct rule*:*sum-SDG-slice1.induct*,
auto intro:*sum-SDG-slice1.intros sum-SDG-edge-valid-SDG-node*)

lemma *slice1-sum-slice1*:

$\llbracket nx \in \text{sum-SDG-slice1 } n; n s - p \rightarrow_{sum} n' \rrbracket \Longrightarrow nx \in \text{sum-SDG-slice1 } n'$
by(*induct rule*:*sum-SDG-slice1.induct*,
auto intro:*sum-SDG-slice1.intros sum-SDG-edge-valid-SDG-node*)

lemma *slice1-call-slice1*:

$\llbracket nx \in \text{sum-SDG-slice1 } n; n s - p \rightarrow_{call} n' \rrbracket \Longrightarrow nx \in \text{sum-SDG-slice1 } n'$
by(*induct rule*:*sum-SDG-slice1.induct*,
auto intro:*sum-SDG-slice1.intros sum-SDG-edge-valid-SDG-node*)

lemma *slice1-param-in-slice1*:

$\llbracket nx \in \text{sum-SDG-slice1 } n; n s - p : V \rightarrow_{in} n' \rrbracket \Longrightarrow nx \in \text{sum-SDG-slice1 } n'$
by(*induct rule*:*sum-SDG-slice1.induct*,
auto intro:*sum-SDG-slice1.intros sum-SDG-edge-valid-SDG-node*)

lemma *is-SDG-path-slice1*:

$\llbracket n \text{ is-ns} \rightarrow_{d^*} n'; n' \in \text{sum-SDG-slice1 } n'' \rrbracket \Longrightarrow n \in \text{sum-SDG-slice1 } n''$
proof(*induct rule*:*intra-sum-SDG-path.induct*)
case *isSp-Nil* **thus** ?*case* **by** *simp*
next
case (*isSp-Append-cdep* *n ns nx n'*)
note *IH* = (*nx* \in *sum-SDG-slice1* *n''* \Longrightarrow *n* \in *sum-SDG-slice1* *n''*)

```

from ⟨ $nx \ s \rightarrow_{cd} \ n'$ ⟩ ⟨ $n' \in \text{sum-SDG-slice1 } n'$ ⟩
have  $nx \in \text{sum-SDG-slice1 } n''$  by(rule cdep-slice1)
from IH[OF this] show ?case .
next
case (isSp-Append-ddep n ns nx V n')
note IH = ⟨ $nx \in \text{sum-SDG-slice1 } n'' \implies n \in \text{sum-SDG-slice1 } n''$ ⟩
from ⟨ $nx \ s - V \rightarrow_{dd} \ n'$ ⟩ ⟨ $n' \in \text{sum-SDG-slice1 } n''$ ⟩
have  $nx \in \text{sum-SDG-slice1 } n''$  by(rule ddep-slice1)
from IH[OF this] show ?case .
next
case (isSp-Append-sum n ns nx p n')
note IH = ⟨ $nx \in \text{sum-SDG-slice1 } n'' \implies n \in \text{sum-SDG-slice1 } n''$ ⟩
from ⟨ $nx \ s - p \rightarrow_{sum} \ n'$ ⟩ ⟨ $n' \in \text{sum-SDG-slice1 } n''$ ⟩
have  $nx \in \text{sum-SDG-slice1 } n''$  by(rule sum-slice1)
from IH[OF this] show ?case .
qed

```

1.9.2 Set describing phase 2 of the two-phase slicer

```

inductive-set sum-SDG-slice2 :: 'node SDG-node  $\Rightarrow$  'node SDG-node set
for n::'node SDG-node
where refl-slice2:valid-SDG-node n  $\implies n \in \text{sum-SDG-slice2 } n$ 
| cdep-slice2:
  [⟨ $n'' \ s \rightarrow_{cd} \ n'$ ⟩;  $n' \in \text{sum-SDG-slice2 } n$ ]  $\implies n'' \in \text{sum-SDG-slice2 } n$ 
| ddep-slice2:
  [⟨ $n'' \ s - V \rightarrow_{dd} \ n'$ ⟩;  $n' \in \text{sum-SDG-slice2 } n$ ]  $\implies n'' \in \text{sum-SDG-slice2 } n$ 
| return-slice2:
  [⟨ $n'' \ s - p \rightarrow_{ret} \ n'$ ⟩;  $n' \in \text{sum-SDG-slice2 } n$ ]  $\implies n'' \in \text{sum-SDG-slice2 } n$ 
| param-out-slice2:
  [⟨ $n'' \ s - p: V \rightarrow_{out} \ n'$ ⟩;  $n' \in \text{sum-SDG-slice2 } n$ ]  $\implies n'' \in \text{sum-SDG-slice2 } n$ 
| sum-slice2:
  [⟨ $n'' \ s - p \rightarrow_{sum} \ n'$ ⟩;  $n' \in \text{sum-SDG-slice2 } n$ ]  $\implies n'' \in \text{sum-SDG-slice2 } n$ 

```

lemma slice2-cdep-slice2:

```

[⟨ $nx \in \text{sum-SDG-slice2 } n$ ;  $n \ s \rightarrow_{cd} \ n'$ ⟩]  $\implies nx \in \text{sum-SDG-slice2 } n'$ 
by(induct rule:sum-SDG-slice2.induct,
  auto intro:sum-SDG-slice2.intros sum-SDG-edge-valid-SDG-node)

```

lemma slice2-ddep-slice2:

```

[⟨ $nx \in \text{sum-SDG-slice2 } n$ ;  $n \ s - V \rightarrow_{dd} \ n'$ ⟩]  $\implies nx \in \text{sum-SDG-slice2 } n'$ 
by(induct rule:sum-SDG-slice2.induct,
  auto intro:sum-SDG-slice2.intros sum-SDG-edge-valid-SDG-node)

```

lemma slice2-sum-slice2:

```

[⟨ $nx \in \text{sum-SDG-slice2 } n$ ;  $n \ s - p \rightarrow_{sum} \ n'$ ⟩]  $\implies nx \in \text{sum-SDG-slice2 } n'$ 
by(induct rule:sum-SDG-slice2.induct,
  auto intro:sum-SDG-slice2.intros sum-SDG-edge-valid-SDG-node)

```

lemma *slice2-ret-slice2*:

$\llbracket nx \in \text{sum-SDG-slice2 } n; n \text{ s-p} \rightarrow_{\text{ret}} n' \rrbracket \implies nx \in \text{sum-SDG-slice2 } n'$
by(*induct rule:sum-SDG-slice2.induct*,
auto intro:sum-SDG-slice2.intros sum-SDG-edge-valid-SDG-node)

lemma *slice2-param-out-slice2*:

$\llbracket nx \in \text{sum-SDG-slice2 } n; n \text{ s-p: } V \rightarrow_{\text{out}} n' \rrbracket \implies nx \in \text{sum-SDG-slice2 } n'$
by(*induct rule:sum-SDG-slice2.induct*,
auto intro:sum-SDG-slice2.intros sum-SDG-edge-valid-SDG-node)

lemma *is-SDG-path-slice2*:

$\llbracket n \text{ is-ns} \rightarrow_{d^*} n'; n' \in \text{sum-SDG-slice2 } n'' \rrbracket \implies n \in \text{sum-SDG-slice2 } n''$
proof(*induct rule:intra-sum-SDG-path.induct*)

case *isSp-Nil* **thus** *?case* **by** *simp*

next

case (*isSp-Append-cdep* *n ns nx n'*)

note $IH = \langle nx \in \text{sum-SDG-slice2 } n'' \implies n \in \text{sum-SDG-slice2 } n'' \rangle$

from $\langle nx \text{ s} \rightarrow_{cd} n' \rangle \langle n' \in \text{sum-SDG-slice2 } n'' \rangle$

have $nx \in \text{sum-SDG-slice2 } n''$ **by**(*rule cdep-slice2*)

from IH [*OF this*] **show** *?case* .

next

case (*isSp-Append-ddep* *n ns nx V n'*)

note $IH = \langle nx \in \text{sum-SDG-slice2 } n'' \implies n \in \text{sum-SDG-slice2 } n'' \rangle$

from $\langle nx \text{ s-} V \rightarrow_{dd} n' \rangle \langle n' \in \text{sum-SDG-slice2 } n'' \rangle$

have $nx \in \text{sum-SDG-slice2 } n''$ **by**(*rule ddep-slice2*)

from IH [*OF this*] **show** *?case* .

next

case (*isSp-Append-sum* *n ns nx p n'*)

note $IH = \langle nx \in \text{sum-SDG-slice2 } n'' \implies n \in \text{sum-SDG-slice2 } n'' \rangle$

from $\langle nx \text{ s-p} \rightarrow_{\text{sum}} n' \rangle \langle n' \in \text{sum-SDG-slice2 } n'' \rangle$

have $nx \in \text{sum-SDG-slice2 } n''$ **by**(*rule sum-slice2*)

from IH [*OF this*] **show** *?case* .

qed

lemma *slice2-is-SDG-path-slice2*:

$\llbracket n \text{ is-ns} \rightarrow_{d^*} n'; n'' \in \text{sum-SDG-slice2 } n \rrbracket \implies n'' \in \text{sum-SDG-slice2 } n'$
proof(*induct rule:intra-sum-SDG-path.induct*)

case *isSp-Nil* **thus** *?case* **by** *simp*

next

case (*isSp-Append-cdep* *n ns nx n'*)

from $\langle n'' \in \text{sum-SDG-slice2 } n \implies n'' \in \text{sum-SDG-slice2 } nx \rangle \langle n'' \in \text{sum-SDG-slice2 } n \rangle$

have $n'' \in \text{sum-SDG-slice2 } nx$.

with $\langle nx \text{ s} \rightarrow_{cd} n' \rangle$ **show** *?case* **by** $\text{--}(rule \text{ slice2-cdep-slice2})$

next

case (*isSp-Append-ddep* *n ns nx V n'*)

```

from ⟨ $n'' \in \text{sum-SDG-slice2 } n \implies n'' \in \text{sum-SDG-slice2 } nx$ ⟩ ⟨ $n'' \in \text{sum-SDG-slice2 } n$ ⟩
have  $n'' \in \text{sum-SDG-slice2 } nx$  .
with ⟨ $nx \text{ s} - V \rightarrow_{dd} n'$ ⟩ show ?case by  $-(\text{rule slice2-ddep-slice2})$ 
next
case (isSp-Append-sum  $n \ ns \ nx \ p \ n'$ )
from ⟨ $n'' \in \text{sum-SDG-slice2 } n \implies n'' \in \text{sum-SDG-slice2 } nx$ ⟩ ⟨ $n'' \in \text{sum-SDG-slice2 } n$ ⟩
have  $n'' \in \text{sum-SDG-slice2 } nx$  .
with ⟨ $nx \text{ s} - p \rightarrow_{sum} n'$ ⟩ show ?case by  $-(\text{rule slice2-sum-slice2})$ 
qed

```

1.9.3 The backward slice using the Horwitz-Reps-Binkley slicer

Note: our slicing criterion is a set of nodes, not a unique node.

```

inductive-set combine-SDG-slices :: 'node SDG-node set  $\Rightarrow$  'node SDG-node set
for  $S :: 'node \text{ SDG-node set}$ 
where combSlice-refl:  $n \in S \implies n \in \text{combine-SDG-slices } S$ 
| combSlice-Return-parent-node:
 $\llbracket n' \in S; n'' \text{ s} - p \rightarrow_{ret} \text{CFG-node } (\text{parent-node } n'); n \in \text{sum-SDG-slice2 } n' \rrbracket$ 
 $\implies n \in \text{combine-SDG-slices } S$ 

```

```

definition HRB-slice :: 'node SDG-node set  $\Rightarrow$  'node SDG-node set
where HRB-slice  $S \equiv \{n'. \exists n \in S. n' \in \text{combine-SDG-slices } (\text{sum-SDG-slice1 } n)\}$ 

```

```

lemma HRB-slice-cases[consumes 1, case-names phase1 phase2]:
 $\llbracket x \in \text{HRB-slice } S; \bigwedge n \ nx. \llbracket n \in \text{sum-SDG-slice1 } nx; nx \in S \rrbracket \implies P \ n;$ 
 $\bigwedge nx \ n' \ n'' \ p \ n. \llbracket n' \in \text{sum-SDG-slice1 } nx; n'' \text{ s} - p \rightarrow_{ret} \text{CFG-node } (\text{parent-node } n');$ 
 $n \in \text{sum-SDG-slice2 } n'; nx \in S \rrbracket \implies P \ n \rrbracket$ 
 $\implies P \ x$ 
by(fastforce elim:combine-SDG-slices.cases simp:HRB-slice-def)

```

```

lemma HRB-slice-refl:
assumes valid-node  $m$  and CFG-node  $m \in S$  shows CFG-node  $m \in \text{HRB-slice } S$ 
proof -
from ⟨valid-node  $m$ ⟩ have CFG-node  $m \in \text{sum-SDG-slice1 } (\text{CFG-node } m)$ 
by(fastforce intro:refl-slice1)
with ⟨CFG-node  $m \in S$ ⟩ show ?thesis
by(simp add:HRB-slice-def)(blast intro:combSlice-refl)
qed

```

lemma *HRB-slice-valid-node*: $n \in \text{HRB-slice } S \implies \text{valid-SDG-node } n$
proof(*induct rule:HRB-slice-cases*)
 case (*phase1 n nx*) **thus** ?*case*
 by(*induct rule:sum-SDG-slice1.induct,auto intro:sum-SDG-edge-valid-SDG-node*)
next
 case (*phase2 nx n' n'' p n*)
 from $\langle n \in \text{sum-SDG-slice2 } n' \rangle$
 show ?*case*
 by(*induct rule:sum-SDG-slice2.induct,auto intro:sum-SDG-edge-valid-SDG-node*)
qed

lemma *valid-SDG-node-in-slice-parent-node-in-slice*:
 assumes $n \in \text{HRB-slice } S$ **shows** $\text{CFG-node } (\text{parent-node } n) \in \text{HRB-slice } S$
proof –
 from $\langle n \in \text{HRB-slice } S \rangle$ **have** $\text{valid-SDG-node } n$ **by**(*rule HRB-slice-valid-node*)
 hence $n = \text{CFG-node } (\text{parent-node } n) \vee \text{CFG-node } (\text{parent-node } n) \longrightarrow_{\text{cd}} n$
 by(*rule valid-SDG-node-cases*)
 thus ?*thesis*
proof
 assume $n = \text{CFG-node } (\text{parent-node } n)$
 with $\langle n \in \text{HRB-slice } S \rangle$ **show** ?*thesis* **by** *simp*
next
 assume $\text{CFG-node } (\text{parent-node } n) \longrightarrow_{\text{cd}} n$
 hence $\text{CFG-node } (\text{parent-node } n) s \longrightarrow_{\text{cd}} n$ **by**(*rule SDG-edge-sum-SDG-edge*)
 with $\langle n \in \text{HRB-slice } S \rangle$ **show** ?*thesis*
 by(*fastforce elim:combine-SDG-slices.cases*
 intro:combine-SDG-slices.intros cdep-slice1 cdep-slice2
 simp:HRB-slice-def)
qed
qed

lemma *HRB-slice-is-SDG-path-HRB-slice*:
 $\llbracket n \text{ is-ns} \rightarrow_d^* n'; n'' \in \text{HRB-slice } \{n\}; n' \in S \rrbracket \implies n'' \in \text{HRB-slice } S$
proof(*induct arbitrary:S rule:intra-sum-SDG-path.induct*)
 case (*isSp-Nil n*) **thus** ?*case* **by**(*fastforce simp:HRB-slice-def*)
next
 case (*isSp-Append-cdep n ns nx n'*)
 note $IH = \langle \bigwedge S. \llbracket n'' \in \text{HRB-slice } \{n\}; nx \in S \rrbracket \implies n'' \in \text{HRB-slice } S \rangle$
 from $IH[OF \langle n'' \in \text{HRB-slice } \{n\} \rangle]$ **have** $n'' \in \text{HRB-slice } \{nx\}$ **by** *simp*
 thus ?*case*
proof(*induct rule:HRB-slice-cases*)
 case (*phase1 n nx'*)
 from $\langle nx' \in \{nx\} \rangle$ **have** $nx' = nx$ **by** *simp*
 with $\langle n \in \text{sum-SDG-slice1 } nx' \rangle \langle nx s \longrightarrow_{\text{cd}} n' \rangle$ **have** $n \in \text{sum-SDG-slice1 } n'$
 by(*fastforce intro:slice1-cdep-slice1*)
 with $\langle n' \in S \rangle$ **show** ?*case*

by(*fastforce intro:combine-SDG-slices.combSlice-refl simp:HRB-slice-def*)
 next
 case (*phase2 nx'' nx' n'' p n*)
 from $\langle nx'' \in \{nx\} \rangle$ have $nx'' = nx$ by *simp*
 with $\langle nx' \in \text{sum-SDG-slice1 } nx' \rangle \langle nx \xrightarrow{cd} n' \rangle$ have $nx' \in \text{sum-SDG-slice1 } n'$
 by(*fastforce intro:slice1-cdep-slice1*)
 with $\langle n'' \xrightarrow{s-p} \text{ret CFG-node (parent-node } nx') \rangle \langle n \in \text{sum-SDG-slice2 } nx' \rangle \langle n' \in S \rangle$
 show ?case by(*fastforce intro:combine-SDG-slices.combSlice-Return-parent-node simp:HRB-slice-def*)
 qed
 next
 case (*isSp-Append-ddep n ns nx V n'*)
 note $IH = \langle \bigwedge S. \llbracket n'' \in \text{HRB-slice } \{n\}; nx \in S \rrbracket \implies n'' \in \text{HRB-slice } S \rangle$
 from $IH[OF \langle n'' \in \text{HRB-slice } \{n\} \rangle]$ have $n'' \in \text{HRB-slice } \{nx\}$ by *simp*
 thus ?case
 proof(*induct rule:HRB-slice-cases*)
 case (*phase1 n nx'*)
 from $\langle nx' \in \{nx\} \rangle$ have $nx' = nx$ by *simp*
 with $\langle n \in \text{sum-SDG-slice1 } nx' \rangle \langle nx \xrightarrow{s-V} dd \ n' \rangle$ have $n \in \text{sum-SDG-slice1 } n'$
 by(*fastforce intro:slice1-ddep-slice1*)
 with $\langle n' \in S \rangle$ show ?case
 by(*fastforce intro:combine-SDG-slices.combSlice-refl simp:HRB-slice-def*)
 next
 case (*phase2 nx'' nx' n'' p n*)
 from $\langle nx'' \in \{nx\} \rangle$ have $nx'' = nx$ by *simp*
 with $\langle nx' \in \text{sum-SDG-slice1 } nx' \rangle \langle nx \xrightarrow{s-V} dd \ n' \rangle$ have $nx' \in \text{sum-SDG-slice1 } n'$
 by(*fastforce intro:slice1-ddep-slice1*)
 with $\langle n'' \xrightarrow{s-p} \text{ret CFG-node (parent-node } nx') \rangle \langle n \in \text{sum-SDG-slice2 } nx' \rangle \langle n' \in S \rangle$
 show ?case by(*fastforce intro:combine-SDG-slices.combSlice-Return-parent-node simp:HRB-slice-def*)
 qed
 next
 case (*isSp-Append-sum n ns nx p n'*)
 note $IH = \langle \bigwedge S. \llbracket n'' \in \text{HRB-slice } \{n\}; nx \in S \rrbracket \implies n'' \in \text{HRB-slice } S \rangle$
 from $IH[OF \langle n'' \in \text{HRB-slice } \{n\} \rangle]$ have $n'' \in \text{HRB-slice } \{nx\}$ by *simp*
 thus ?case
 proof(*induct rule:HRB-slice-cases*)
 case (*phase1 n nx'*)
 from $\langle nx' \in \{nx\} \rangle$ have $nx' = nx$ by *simp*
 with $\langle n \in \text{sum-SDG-slice1 } nx' \rangle \langle nx \xrightarrow{s-p} \text{sum } n' \rangle$ have $n \in \text{sum-SDG-slice1 } n'$
 by(*fastforce intro:slice1-sum-slice1*)
 with $\langle n' \in S \rangle$ show ?case

by(*fastforce* *intro:combine-SDG-slices.combSlice-refl simp:HRB-slice-def*)
 next
 case (*phase2* $\langle nx'' \ nx' \ n'' \ p' \ n \rangle$)
 from $\langle nx'' \in \{nx\} \rangle$ have $nx'' = nx$ by *simp*
 with $\langle nx' \in \text{sum-SDG-slice1 } nx'' \rangle \langle nx \ s-p \rightarrow_{\text{sum}} n' \rangle$ have $nx' \in \text{sum-SDG-slice1}$
 n'
 by(*fastforce* *intro:slice1-sum-slice1*)
 with $\langle n'' \ s-p' \rightarrow_{\text{ret}} \text{CFG-node } (\text{parent-node } nx') \rangle \langle n \in \text{sum-SDG-slice2 } nx' \rangle \langle n' \in S \rangle$
 show ?case by(*fastforce* *intro:combine-SDG-slices.combSlice-Return-parent-node simp:HRB-slice-def*)
 qed
 qed

lemma *call-return-nodes-in-slice*:

assumes *valid-edge* a and *kind* $a = Q \leftrightarrow_{pf}$
 and *valid-edge* a' and *kind* $a' = Q' : r' \hookrightarrow_{pfs'} s'$ and $a \in \text{get-return-edges } a'$
 and *CFG-node* (*targetnode* a) $\in \text{HRB-slice } S$
 shows *CFG-node* (*sourcenode* a) $\in \text{HRB-slice } S$
 and *CFG-node* (*sourcenode* a') $\in \text{HRB-slice } S$
 and *CFG-node* (*targetnode* a') $\in \text{HRB-slice } S$
proof –
 from $\langle \text{valid-edge } a' \rangle \langle \text{kind } a' = Q' : r' \hookrightarrow_{pfs'} s' \rangle \langle a \in \text{get-return-edges } a' \rangle$
 have *CFG-node* (*sourcenode* a') $s-p \rightarrow_{\text{sum}} \text{CFG-node } (\text{targetnode } a)$
 by(*fastforce* *intro:sum-SDG-call-summary-edge*)
 with $\langle \text{CFG-node } (\text{targetnode } a) \in \text{HRB-slice } S \rangle$
 show *CFG-node* (*sourcenode* a') $\in \text{HRB-slice } S$
 by(*fastforce* *elim!:combine-SDG-slices.cases*
 intro:combine-SDG-slices.intros sum-slice1 sum-slice2
 simp:HRB-slice-def)
 from $\langle \text{CFG-node } (\text{targetnode } a) \in \text{HRB-slice } S \rangle$
obtain n_c **where** *CFG-node* (*targetnode* a) $\in \text{combine-SDG-slices } (\text{sum-SDG-slice1 } n_c)$
 and $n_c \in S$
 by(*simp* *add:HRB-slice-def*) *blast*
thus *CFG-node* (*sourcenode* a) $\in \text{HRB-slice } S$
proof(*induct* *CFG-node* (*targetnode* a) *rule:combine-SDG-slices.induct*)
 case *combSlice-refl*
 from $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q \leftrightarrow_{pf} \rangle$
 have *CFG-node* (*sourcenode* a) $s-p \rightarrow_{\text{ret}} \text{CFG-node } (\text{targetnode } a)$
 by(*fastforce* *intro:sum-SDG-return-edge*)
 with $\langle \text{valid-edge } a \rangle$
 have *CFG-node* (*sourcenode* a) $\in \text{sum-SDG-slice2 } (\text{CFG-node } (\text{targetnode } a))$
 by(*fastforce* *intro:sum-SDG-slice2.intros*)
 with $\langle \text{CFG-node } (\text{targetnode } a) \in \text{sum-SDG-slice1 } n_c \rangle \langle n_c \in S \rangle$
 $\langle \text{CFG-node } (\text{sourcenode } a) \ s-p \rightarrow_{\text{ret}} \text{CFG-node } (\text{targetnode } a) \rangle$
 show ?case by(*fastforce* *intro:combSlice-Return-parent-node simp:HRB-slice-def*)

```

next
  case (combSlice-Return-parent-node n' n'' p')
  from (valid-edge a) (kind a = Q↔pf)
  have CFG-node (sourcenode a) s-p→ret CFG-node (targetnode a)
    by(fastforce intro:sum-SDG-return-edge)
  with (CFG-node (targetnode a) ∈ sum-SDG-slice2 n')
  have CFG-node (sourcenode a) ∈ sum-SDG-slice2 n'
    by(fastforce intro:sum-SDG-slice2.intros)
  with (n' ∈ sum-SDG-slice1 n_c) (n'' s-p'→ret CFG-node (parent-node n')) (n_c
  ∈ S)
  show ?case by(fastforce intro:combine-SDG-slices.combSlice-Return-parent-node

      simp:HRB-slice-def)

qed
from (valid-edge a') (kind a' = Q':r'↔pfs') (a ∈ get-return-edges a')
have CFG-node (targetnode a') s→cd CFG-node (sourcenode a)
  by(fastforce intro:sum-SDG-proc-entry-exit-cdep)
with (CFG-node (sourcenode a) ∈ HRB-slice S) (n_c ∈ S)
show CFG-node (targetnode a') ∈ HRB-slice S
  by(fastforce elim!:combine-SDG-slices.cases
      intro:combine-SDG-slices.intros cdep-slice1 cdep-slice2
      simp:HRB-slice-def)

qed

```

1.9.4 Proof of Precision

lemma *in-intra-SDG-path-in-slice2*:

$\llbracket n \text{ i-ns} \rightarrow_{a^*} n'; n'' \in \text{set ns} \rrbracket \implies n'' \in \text{sum-SDG-slice2 } n'$

proof(*induct rule:intra-SDG-path.induct*)

case *iSp-Nil* thus ?case by *simp*

next

case (*iSp-Append-cdep* n ns nx n')

note $IH = (n'' \in \text{set ns} \implies n'' \in \text{sum-SDG-slice2 } nx)$

from (n'' ∈ set (ns@[nx])) **have** $n'' \in \text{set ns} \vee n'' = nx$ **by** *auto*

thus ?case

proof

assume $n'' \in \text{set ns}$

from $IH[OF \text{ this}]$ **have** $n'' \in \text{sum-SDG-slice2 } nx$ **by** *simp*

with (nx →_{cd} n') **show** ?thesis

by(fastforce intro:slice2-cdep-slice2 SDG-edge-sum-SDG-edge)

next

assume $n'' = nx$

from (nx →_{cd} n') **have** *valid-SDG-node* n' **by**(*rule* SDG-edge-valid-SDG-node)

hence $n' \in \text{sum-SDG-slice2 } n'$ **by**(*rule* refl-slice2)

with (nx →_{cd} n') **have** $nx \in \text{sum-SDG-slice2 } n'$

by(fastforce intro:cdep-slice2 SDG-edge-sum-SDG-edge)

with (n'' = nx) **show** ?thesis **by** *simp*

qed

next

```

case (iSp-Append-ddep n ns nx V n')
note IH = ⟨n'' ∈ set ns ⇒ n'' ∈ sum-SDG-slice2 nx⟩
from ⟨n'' ∈ set (ns@[nx])⟩ have n'' ∈ set ns ∨ n'' = nx by auto
thus ?case
proof
  assume n'' ∈ set ns
  from IH[OF this] have n'' ∈ sum-SDG-slice2 nx by simp
  with ⟨nx - V →dd n'⟩ show ?thesis
  by(fastforce intro:slice2-ddep-slice2 SDG-edge-sum-SDG-edge)
next
  assume n'' = nx
  from ⟨nx - V →dd n'⟩ have valid-SDG-node n' by(rule SDG-edge-valid-SDG-node)
  hence n' ∈ sum-SDG-slice2 n' by(rule refl-slice2)
  with ⟨nx - V →dd n'⟩ have nx ∈ sum-SDG-slice2 n'
  by(fastforce intro:ddep-slice2 SDG-edge-sum-SDG-edge)
  with ⟨n'' = nx⟩ show ?thesis by simp
qed
qed

```

lemma in-intra-SDG-path-in-HRB-slice:

```

[[n i-ns→d* n'; n'' ∈ set ns; n' ∈ S]] ⇒ n'' ∈ HRB-slice S
proof(induct arbitrary:S rule:intra-SDG-path.induct)
  case iSp-Nil thus ?case by simp
next
  case (iSp-Append-cdep n ns nx n')
  note IH = ⟨∧S. [[n'' ∈ set ns; nx ∈ S]] ⇒ n'' ∈ HRB-slice S⟩
  from ⟨n'' ∈ set (ns@[nx])⟩ have n'' ∈ set ns ∨ n'' = nx by auto
  thus ?case
  proof
    assume n'' ∈ set ns
    from IH[OF ⟨n'' ∈ set ns⟩] have n'' ∈ HRB-slice {nx} by simp
    from this ⟨nx →cd n'⟩ ⟨n' ∈ S⟩ show ?case
    by(fastforce elim:HRB-slice-cases slice1-cdep-slice1
      intro:beXI[where x=n'] combine-SDG-slices.intros SDG-edge-sum-SDG-edge

      simp:HRB-slice-def)
  next
    assume n'' = nx
    from ⟨nx →cd n'⟩ have valid-SDG-node n' by(rule SDG-edge-valid-SDG-node)
    hence n' ∈ sum-SDG-slice1 n' by(rule refl-slice1)
    with ⟨nx →cd n'⟩ have nx ∈ sum-SDG-slice1 n'
    by(fastforce intro:cdep-slice1 SDG-edge-sum-SDG-edge)
    with ⟨n'' = nx⟩ ⟨n' ∈ S⟩ show ?case
    by(fastforce intro:combSlice-refl simp:HRB-slice-def)
  qed
qed
next
  case (iSp-Append-ddep n ns nx V n')
  note IH = ⟨∧S. [[n'' ∈ set ns; nx ∈ S]] ⇒ n'' ∈ HRB-slice S⟩

```

```

from ⟨ $n'' \in \text{set } (ns@[nx])$ ⟩ have  $n'' \in \text{set } ns \vee n'' = nx$  by auto
thus ?case
proof
  assume  $n'' \in \text{set } ns$ 
  from  $IH[OF \langle n'' \in \text{set } ns \rangle]$  have  $n'' \in \text{HRB-slice } \{nx\}$  by simp
  from  $\text{this } \langle nx - V \rightarrow_{dd} n' \rangle \langle n' \in S \rangle$  show ?case
    by(fastforce elim:HRB-slice-cases slice1-ddep-slice1
      intro:beXI[where x=n'] combine-SDG-slices.intros SDG-edge-sum-SDG-edge

      simp:HRB-slice-def)
next
  assume  $n'' = nx$ 
  from  $\langle nx - V \rightarrow_{dd} n' \rangle$  have valid-SDG-node  $n'$  by(rule SDG-edge-valid-SDG-node)
  hence  $n' \in \text{sum-SDG-slice1 } n'$  by(rule refl-slice1)
  with  $\langle nx - V \rightarrow_{dd} n' \rangle$  have  $nx \in \text{sum-SDG-slice1 } n'$ 
    by(fastforce intro:ddep-slice1 SDG-edge-sum-SDG-edge)
  with  $\langle n'' = nx \rangle \langle n' \in S \rangle$  show ?case
    by(fastforce intro:combSlice-refl simp:HRB-slice-def)
qed
qed

lemma in-matched-in-slice2:
   $[[\text{matched } n \ ns \ n'; n'' \in \text{set } ns]] \implies n'' \in \text{sum-SDG-slice2 } n'$ 
proof(induct rule:matched.induct)
  case matched-Nil thus ?case by simp
next
  case (matched-Append-intra-SDG-path  $n \ ns \ nx \ ns' \ n'$ )
  note  $IH = \langle n'' \in \text{set } ns \implies n'' \in \text{sum-SDG-slice2 } nx \rangle$ 
  from  $\langle n'' \in \text{set } (ns@ns') \rangle$  have  $n'' \in \text{set } ns \vee n'' \in \text{set } ns'$  by simp
  thus ?case
  proof
    assume  $n'' \in \text{set } ns$ 
    from  $IH[OF \text{this}]$  have  $n'' \in \text{sum-SDG-slice2 } nx$  .
    with  $\langle nx \ i - ns' \rightarrow_{d^*} n' \rangle$  show ?thesis
      by(fastforce intro:slice2-is-SDG-path-slice2
        intra-SDG-path-is-SDG-path)

  next
    assume  $n'' \in \text{set } ns'$ 
    with  $\langle nx \ i - ns' \rightarrow_{d^*} n' \rangle$  show ?case by(rule in-intra-SDG-path-in-slice2)
  qed
next
  case (matched-bracket-call  $n_0 \ ns \ n_1 \ p \ n_2 \ ns' \ n_3 \ n_4 \ V \ a \ a'$ )
  note  $IH1 = \langle n'' \in \text{set } ns \implies n'' \in \text{sum-SDG-slice2 } n_1 \rangle$ 
  note  $IH2 = \langle n'' \in \text{set } ns' \implies n'' \in \text{sum-SDG-slice2 } n_3 \rangle$ 
  from  $\langle n_1 - p \rightarrow_{call} n_2 \rangle \langle \text{matched } n_2 \ ns' \ n_3 \rangle \langle n_3 - p \rightarrow_{ret} n_4 \vee n_3 - p : V \rightarrow_{out} n_4 \rangle$ 
     $\langle a' \in \text{get-return-edges } a \rangle \langle \text{valid-edge } a \rangle$ 
     $\langle \text{sourcenode } a = \text{parent-node } n_1 \rangle \langle \text{targetnode } a = \text{parent-node } n_2 \rangle$ 
     $\langle \text{sourcenode } a' = \text{parent-node } n_3 \rangle \langle \text{targetnode } a' = \text{parent-node } n_4 \rangle$ 

```

```

have matched  $n_1$  ( $[]@n_1\#ns'\@[n_3]$ )  $n_4$ 
  by(fastforce intro:matched.matched-bracket-call matched-Nil
    elim:SDG-edge-valid-SDG-node)
then obtain nsx where  $n_1$  is-nsx $\rightarrow_d^*$   $n_4$  by(erule matched-is-SDG-path)
from  $\langle n'' \in \text{set } (ns@n_1\#ns'\@[n_3]) \rangle$ 
have  $((n'' \in \text{set } ns \vee n'' = n_1) \vee n'' \in \text{set } ns')$   $\vee n'' = n_3$  by auto
thus ?case apply -
proof(erule disjE)+
  assume  $n'' \in \text{set } ns$ 
  from IH1[OF this] have  $n'' \in \text{sum-SDG-slice2 } n_1$  .
  with  $\langle n_1 \text{ is-nsx} \rightarrow_d^* n_4 \rangle$  show ?thesis
    by -(rule slice2-is-SDG-path-slice2)
next
  assume  $n'' = n_1$ 
  from  $\langle n_1 \text{ is-nsx} \rightarrow_d^* n_4 \rangle$  have  $n_1 \in \text{sum-SDG-slice2 } n_4$ 
    by(fastforce intro:is-SDG-path-slice2 refl-slice2 is-SDG-path-valid-SDG-node)
  with  $\langle n'' = n_1 \rangle$  show ?thesis by(fastforce intro:combSlice-refl simp:HRB-slice-def)
next
  assume  $n'' \in \text{set } ns'$ 
  from IH2[OF this] have  $n'' \in \text{sum-SDG-slice2 } n_3$  .
  with  $\langle n_3 -p \rightarrow_{\text{ret}} n_4 \vee n_3 -p:V \rightarrow_{\text{out}} n_4 \rangle$  show ?thesis
    by(fastforce intro:slice2-ret-slice2 slice2-param-out-slice2
      SDG-edge-sum-SDG-edge)
next
  assume  $n'' = n_3$ 
  from  $\langle n_3 -p \rightarrow_{\text{ret}} n_4 \vee n_3 -p:V \rightarrow_{\text{out}} n_4 \rangle$  have  $n_3 -p \rightarrow_{\text{ret}} n_4 \vee n_3 -p:V \rightarrow_{\text{out}}$ 
 $n_4$ 
    by(fastforce intro:SDG-edge-sum-SDG-edge)
  hence  $n_3 \in \text{sum-SDG-slice2 } n_4$ 
    by(fastforce intro:return-slice2 param-out-slice2 refl-slice2
      sum-SDG-edge-valid-SDG-node)
  with  $\langle n'' = n_3 \rangle$  show ?thesis by simp
qed
next
case (matched-bracket-param  $n_0$  ns  $n_1$  p V  $n_2$  ns'  $n_3$  V'  $n_4$  a a')
note IH1 =  $\langle n'' \in \text{set } ns \implies n'' \in \text{sum-SDG-slice2 } n_1 \rangle$ 
note IH2 =  $\langle n'' \in \text{set } ns' \implies n'' \in \text{sum-SDG-slice2 } n_3 \rangle$ 
from  $\langle n_1 -p:V \rightarrow_{\text{in}} n_2 \rangle$   $\langle \text{matched } n_2 \text{ ns}' n_3 \rangle$   $\langle n_3 -p:V' \rightarrow_{\text{out}} n_4 \rangle$ 
   $\langle a' \in \text{get-return-edges } a \rangle$   $\langle \text{valid-edge } a \rangle$ 
   $\langle \text{sourcenode } a = \text{parent-node } n_1 \rangle$   $\langle \text{targetnode } a = \text{parent-node } n_2 \rangle$ 
   $\langle \text{sourcenode } a' = \text{parent-node } n_3 \rangle$   $\langle \text{targetnode } a' = \text{parent-node } n_4 \rangle$ 
have matched  $n_1$  ( $[]@n_1\#ns'\@[n_3]$ )  $n_4$ 
  by(fastforce intro:matched.matched-bracket-param matched-Nil
    elim:SDG-edge-valid-SDG-node)
then obtain nsx where  $n_1$  is-nsx $\rightarrow_d^*$   $n_4$  by(erule matched-is-SDG-path)
from  $\langle n'' \in \text{set } (ns@n_1\#ns'\@[n_3]) \rangle$ 
have  $((n'' \in \text{set } ns \vee n'' = n_1) \vee n'' \in \text{set } ns')$   $\vee n'' = n_3$  by auto
thus ?case apply -
proof(erule disjE)+

```

```

assume  $n'' \in \text{set } ns$ 
from  $IH1[OF \text{ this}]$  have  $n'' \in \text{sum-SDG-slice2 } n_1$  .
with  $\langle n_1 \text{ is-nsx} \rightarrow_d^* n_4 \rangle$  show  $?thesis$ 
  by  $-(\text{rule slice2-is-SDG-path-slice2})$ 
next
assume  $n'' = n_1$ 
from  $\langle n_1 \text{ is-nsx} \rightarrow_d^* n_4 \rangle$  have  $n_1 \in \text{sum-SDG-slice2 } n_4$ 
  by  $(\text{fastforce intro:is-SDG-path-slice2 refl-slice2 is-SDG-path-valid-SDG-node})$ 
with  $\langle n'' = n_1 \rangle$  show  $?thesis$  by  $(\text{fastforce intro:combSlice-refl simp:HRB-slice-def})$ 
next
assume  $n'' \in \text{set } ns'$ 
from  $IH2[OF \text{ this}]$  have  $n'' \in \text{sum-SDG-slice2 } n_3$  .
with  $\langle n_3 -p: V' \rightarrow_{out} n_4 \rangle$  show  $?thesis$ 
  by  $(\text{fastforce intro:slice2-param-out-slice2 SDG-edge-sum-SDG-edge})$ 
next
assume  $n'' = n_3$ 
from  $\langle n_3 -p: V' \rightarrow_{out} n_4 \rangle$  have  $n_3 \text{ s-p: } V' \rightarrow_{out} n_4$  by  $(\text{rule SDG-edge-sum-SDG-edge})$ 
hence  $n_3 \in \text{sum-SDG-slice2 } n_4$ 
  by  $(\text{fastforce intro:param-out-slice2 refl-slice2 sum-SDG-edge-valid-SDG-node})$ 
with  $\langle n'' = n_3 \rangle$  show  $?thesis$  by  $\text{simp}$ 
qed
qed

```

lemma *in-matched-in-HRB-slice*:

```

 $\llbracket \text{matched } n \text{ ns } n'; n'' \in \text{set } ns; n' \in S \rrbracket \implies n'' \in \text{HRB-slice } S$ 
proof  $(\text{induct arbitrary:S rule:matched.induct})$ 
  case matched-Nil thus  $?case$  by  $\text{simp}$ 
next
case  $(\text{matched-Append-intra-SDG-path } n \text{ ns } nx \text{ ns}' n')$ 
note  $IH = \langle \bigwedge S. \llbracket n'' \in \text{set } ns; nx \in S \rrbracket \implies n'' \in \text{HRB-slice } S \rangle$ 
from  $\langle n'' \in \text{set } (ns @ ns') \rangle$  have  $n'' \in \text{set } ns \vee n'' \in \text{set } ns'$  by  $\text{simp}$ 
thus  $?case$ 
proof
assume  $n'' \in \text{set } ns$ 
from  $IH[OF \langle n'' \in \text{set } ns \rangle]$  have  $n'' \in \text{HRB-slice } \{nx\}$  by  $\text{simp}$ 
with  $\langle nx \text{ i-ns}' \rightarrow_d^* n' \rangle \langle n' \in S \rangle$  show  $?thesis$ 
  by  $(\text{fastforce intro:HRB-slice-is-SDG-path-HRB-slice intra-SDG-path-is-SDG-path})$ 
next
assume  $n'' \in \text{set } ns'$ 
with  $\langle nx \text{ i-ns}' \rightarrow_d^* n' \rangle \langle n' \in S \rangle$  show  $?case$ 
  by  $(\text{fastforce intro:in-intra-SDG-path-in-HRB-slice simp:HRB-slice-def})$ 
qed
next
case  $(\text{matched-bracket-call } n_0 \text{ ns } n_1 \text{ p } n_2 \text{ ns}' n_3 \text{ n}_4 \text{ V a a'})$ 
note  $IH1 = \langle \bigwedge S. \llbracket n'' \in \text{set } ns; n_1 \in S \rrbracket \implies n'' \in \text{HRB-slice } S \rangle$ 
note  $IH2 = \langle \bigwedge S. \llbracket n'' \in \text{set } ns'; n_3 \in S \rrbracket \implies n'' \in \text{HRB-slice } S \rangle$ 
from  $\langle n_1 -p \rightarrow_{call} n_2 \rangle \langle \text{matched } n_2 \text{ ns}' n_3 \rangle \langle n_3 -p \rightarrow_{ret} n_4 \vee n_3 -p: V \rightarrow_{out} n_4 \rangle$ 

```

```

    ⟨a' ∈ get-return-edges a⟩ ⟨valid-edge a⟩
    ⟨sourcnode a = parent-node n1⟩ ⟨targetnode a = parent-node n2⟩
    ⟨sourcnode a' = parent-node n3⟩ ⟨targetnode a' = parent-node n4⟩
have matched n1 ([]@n1#ns'[n3]) n4
  by(fastforce intro:matched.matched-bracket-call matched-Nil
    elim:SDG-edge-valid-SDG-node)
then obtain nsx where n1 is-nsx→d* n4 by(erule matched-is-SDG-path)
from ⟨n'' ∈ set (ns@n1#ns'[n3])⟩
have ((n'' ∈ set ns ∨ n'' = n1) ∨ n'' ∈ set ns') ∨ n'' = n3 by auto
thus ?case apply -
proof(erule disjE)+
  assume n'' ∈ set ns
  from IH1[OF this] have n'' ∈ HRB-slice {n1} by simp
  with ⟨n1 is-nsx→d* n4⟩ ⟨n4 ∈ S⟩ show ?thesis
  by -(rule HRB-slice-is-SDG-path-HRB-slice)
next
  assume n'' = n1
  from ⟨n1 is-nsx→d* n4⟩ have n1 ∈ sum-SDG-slice1 n4
  by(fastforce intro:is-SDG-path-slice1 refl-slice1 is-SDG-path-valid-SDG-node)
  with ⟨n'' = n1⟩ ⟨n4 ∈ S⟩ show ?thesis
  by(fastforce intro:combSlice-refl simp:HRB-slice-def)
next
  assume n'' ∈ set ns'
  with ⟨matched n2 ns' n3⟩ have n'' ∈ sum-SDG-slice2 n3
  by(rule in-matched-in-slice2)
  with ⟨n3 -p→ret n4 ∨ n3 -p:V→out n4⟩ have n'' ∈ sum-SDG-slice2 n4
  by(fastforce intro:slice2-ret-slice2 slice2-param-out-slice2
    SDG-edge-sum-SDG-edge)
  from ⟨n3 -p→ret n4 ∨ n3 -p:V→out n4⟩ have valid-SDG-node n4
  by(fastforce intro:SDG-edge-valid-SDG-node)
  hence n4 ∈ sum-SDG-slice1 n4 by(rule refl-slice1)
  from ⟨n3 -p→ret n4 ∨ n3 -p:V→out n4⟩
  have CFG-node (parent-node n3) -p→ret CFG-node (parent-node n4)
  by(fastforce elim:SDG-edge.cases intro:SDG-return-edge)
  with ⟨n'' ∈ sum-SDG-slice2 n4⟩ ⟨n4 ∈ sum-SDG-slice1 n4⟩ ⟨n4 ∈ S⟩
  show ?case by(fastforce intro:combSlice-Return-parent-node SDG-edge-sum-SDG-edge
    simp:HRB-slice-def)
next
  assume n'' = n3
  from ⟨n3 -p→ret n4 ∨ n3 -p:V→out n4⟩
  have CFG-node (parent-node n3) -p→ret CFG-node (parent-node n4)
  by(fastforce elim:SDG-edge.cases intro:SDG-return-edge)
  from ⟨n3 -p→ret n4 ∨ n3 -p:V→out n4⟩ have valid-SDG-node n4
  by(fastforce intro:SDG-edge-valid-SDG-node)
  hence n4 ∈ sum-SDG-slice1 n4 by(rule refl-slice1)
  from ⟨valid-SDG-node n4⟩ have n4 ∈ sum-SDG-slice2 n4 by(rule refl-slice2)
  with ⟨n3 -p→ret n4 ∨ n3 -p:V→out n4⟩ have n3 ∈ sum-SDG-slice2 n4
  by(fastforce intro:return-slice2 param-out-slice2 SDG-edge-sum-SDG-edge)

```

```

with ⟨ $n_4 \in \text{sum-SDG-slice1 } n_4$ ⟩
  ⟨ $\text{CFG-node (parent-node } n_3) -p \rightarrow_{\text{ret}} \text{CFG-node (parent-node } n_4)$ ⟩ ⟨ $n'' = n_3$ ⟩
⟨ $n_4 \in S$ ⟩
show ?case by(fastforce intro:combSlice-Return-parent-node SDG-edge-sum-SDG-edge
  simp:HRB-slice-def)

qed
next
case (matched-bracket-param  $n_0$   $ns$   $n_1$   $p$   $V$   $n_2$   $ns'$   $n_3$   $V'$   $n_4$   $a$   $a'$ )
note IH1 = ⟨ $\bigwedge S. \llbracket n'' \in \text{set } ns; n_1 \in S \rrbracket \implies n'' \in \text{HRB-slice } S$ ⟩
note IH2 = ⟨ $\bigwedge S. \llbracket n'' \in \text{set } ns'; n_3 \in S \rrbracket \implies n'' \in \text{HRB-slice } S$ ⟩
from ⟨ $n_1 -p: V \rightarrow_{\text{in}} n_2$ ⟩ ⟨matched  $n_2$   $ns'$   $n_3$ ⟩ ⟨ $n_3 -p: V' \rightarrow_{\text{out}} n_4$ ⟩
  ⟨ $a' \in \text{get-return-edges } a$ ⟩ ⟨valid-edge  $a$ ⟩
  ⟨sourcenode  $a = \text{parent-node } n_1$ ⟩ ⟨targetnode  $a = \text{parent-node } n_2$ ⟩
  ⟨sourcenode  $a' = \text{parent-node } n_3$ ⟩ ⟨targetnode  $a' = \text{parent-node } n_4$ ⟩
have matched  $n_1$  (⟨ $\text{[]@}n_1\#\text{ns}'@[n_3]$ ⟩)  $n_4$ 
  by(fastforce intro:matched.matched-bracket-param matched-Nil
    elim:SDG-edge-valid-SDG-node)
then obtain  $nsx$  where  $n_1$  is-nsx  $\rightarrow_{d^*}$   $n_4$  by(erule matched-is-SDG-path)
from ⟨ $n'' \in \text{set } (ns@n_1\#\text{ns}'@[n_3])$ ⟩
have (( $n'' \in \text{set } ns \vee n'' = n_1$ )  $\vee n'' \in \text{set } ns'$ )  $\vee n'' = n_3$  by auto
thus ?case apply -
proof(erule disjE)+
  assume  $n'' \in \text{set } ns$ 
  from IH1[OF this] have  $n'' \in \text{HRB-slice } \{n_1\}$  by simp
  with ⟨ $n_1$  is-nsx  $\rightarrow_{d^*}$   $n_4$ ⟩ ⟨ $n_4 \in S$ ⟩ show ?thesis
    by -(rule HRB-slice-is-SDG-path-HRB-slice)
next
  assume  $n'' = n_1$ 
  from ⟨ $n_1$  is-nsx  $\rightarrow_{d^*}$   $n_4$ ⟩ have  $n_1 \in \text{sum-SDG-slice1 } n_4$ 
    by(fastforce intro:is-SDG-path-slice1 refl-slice1 is-SDG-path-valid-SDG-node)
  with ⟨ $n'' = n_1$ ⟩ ⟨ $n_4 \in S$ ⟩ show ?thesis
    by(fastforce intro:combSlice-refl simp:HRB-slice-def)
next
  assume  $n'' \in \text{set } ns'$ 
  with ⟨matched  $n_2$   $ns'$   $n_3$ ⟩ have  $n'' \in \text{sum-SDG-slice2 } n_3$ 
    by(rule in-matched-in-slice2)
  with ⟨ $n_3 -p: V' \rightarrow_{\text{out}} n_4$ ⟩ have  $n'' \in \text{sum-SDG-slice2 } n_4$ 
    by(fastforce intro:slice2-param-out-slice2 SDG-edge-sum-SDG-edge)
  from ⟨ $n_3 -p: V' \rightarrow_{\text{out}} n_4$ ⟩ have valid-SDG-node  $n_4$  by(rule SDG-edge-valid-SDG-node)
  hence  $n_4 \in \text{sum-SDG-slice1 } n_4$  by(rule refl-slice1)
  from ⟨ $n_3 -p: V' \rightarrow_{\text{out}} n_4$ ⟩
  have CFG-node (parent-node }  $n_3$ ) -p \rightarrow_{\text{ret}} \text{CFG-node (parent-node }  $n_4$ )
    by(fastforce elim:SDG-edge.cases intro:SDG-return-edge)
  with ⟨ $n'' \in \text{sum-SDG-slice2 } n_4$ ⟩ ⟨ $n_4 \in \text{sum-SDG-slice1 } n_4$ ⟩ ⟨ $n_4 \in S$ ⟩
show ?case by(fastforce intro:combSlice-Return-parent-node SDG-edge-sum-SDG-edge
  simp:HRB-slice-def)

next
  assume  $n'' = n_3$ 

```



```

from  $\langle n_3 -p: V' \rightarrow_{out} n_4 \rangle$  have  $n_3 s-p: V' \rightarrow_{out} n_4$  by(rule SDG-edge-sum-SDG-edge)
from  $\langle n_3 -p: V' \rightarrow_{out} n_4 \rangle$  have valid-SDG-node  $n_4$  by(rule SDG-edge-valid-SDG-node)
hence  $n_4 \in \text{sum-SDG-slice1 } n_4$  by(rule refl-slice1)
from  $\langle \text{valid-SDG-node } n_4 \rangle$  have  $n_4 \in \text{sum-SDG-slice2 } n_4$  by(rule refl-slice2)
with  $\langle n_3 s-p: V' \rightarrow_{out} n_4 \rangle$  have  $n_3 \in \text{sum-SDG-slice2 } n_4$  by(rule param-out-slice2)
from  $\langle n_3 -p: V' \rightarrow_{out} n_4 \rangle$ 
have CFG-node (parent-node  $n_3$ )  $-p \rightarrow_{ret}$  CFG-node (parent-node  $n_4$ )
by(fastforce elim:SDG-edge.cases intro:SDG-return-edge)
with  $\langle n_3 \in \text{sum-SDG-slice2 } n_4 \rangle$   $\langle n_4 \in \text{sum-SDG-slice1 } n_4 \rangle$   $\langle n'' = n_3 \rangle$   $\langle n_4 \in S \rangle$ 
show ?case by(fastforce intro:combSlice-Return-parent-node SDG-edge-sum-SDG-edge
simp:HRB-slice-def)

```

qed
qed

theorem *in-realizable-in-HRB-slice*:

$\llbracket \text{realizable } n \text{ ns } n'; n'' \in \text{set ns}; n' \in S \rrbracket \implies n'' \in \text{HRB-slice } S$

proof(*induct arbitrary:S rule:realizable.induct*)

case (*realizable-matched* $n \text{ ns } n'$) **thus** ?*case* **by**(rule *in-matched-in-HRB-slice*)

next

case (*realizable-call* $n_0 \text{ ns } n_1 \text{ p } n_2 \text{ V } ns' \text{ } n_3$)

note $IH = \langle \bigwedge S. \llbracket n'' \in \text{set ns}; n_1 \in S \rrbracket \implies n'' \in \text{HRB-slice } S \rangle$

from $\langle n'' \in \text{set } (ns @ n_1 \# ns') \rangle$ **have** $\langle n'' \in \text{set ns} \vee n'' = n_1 \rangle \vee n'' \in \text{set } ns'$

by *auto*

thus ?*case* **apply** $-$

proof(*erule disjE*) $+$

assume $n'' \in \text{set ns}$

from $IH[OF \text{ this}]$ **have** $n'' \in \text{HRB-slice } \{n_1\}$ **by** *simp*

hence $n'' \in \text{HRB-slice } \{n_2\}$

proof(*induct rule:HRB-slice-cases*)

case (*phase1* $n \text{ nx}$)

from $\langle nx \in \{n_1\} \rangle$ **have** $nx = n_1$ **by** *simp*

with $\langle n \in \text{sum-SDG-slice1 } nx \rangle$ $\langle n_1 -p \rightarrow_{call} n_2 \vee n_1 -p: V \rightarrow_{in} n_2 \rangle$

have $n \in \text{sum-SDG-slice1 } n_2$

by(*fastforce intro:slice1-call-slice1 slice1-param-in-slice1*
SDG-edge-sum-SDG-edge)

thus ?*case*

by(*fastforce intro:combine-SDG-slices.combSlice-refl simp:HRB-slice-def*)

next

case (*phase2* $nx \text{ } n' \text{ } n'' \text{ } p' \text{ } n$)

from $\langle nx \in \{n_1\} \rangle$ **have** $nx = n_1$ **by** *simp*

with $\langle n' \in \text{sum-SDG-slice1 } nx \rangle$ $\langle n_1 -p \rightarrow_{call} n_2 \vee n_1 -p: V \rightarrow_{in} n_2 \rangle$

have $n' \in \text{sum-SDG-slice1 } n_2$

by(*fastforce intro:slice1-call-slice1 slice1-param-in-slice1*
SDG-edge-sum-SDG-edge)

with $\langle n'' s-p' \rightarrow_{ret} \text{CFG-node } (\text{parent-node } n') \rangle$ $\langle n \in \text{sum-SDG-slice2 } n' \rangle$

show ?*case*

by(*fastforce intro:combine-SDG-slices.combSlice-Return-parent-node*
simp:HRB-slice-def)

```

qed
from ⟨matched  $n_2$   $ns'$   $n_3$ ⟩ obtain  $nsx$  where  $n_2$  is- $nsx \rightarrow_{d^*}$   $n_3$ 
  by(erule matched-is-SDG-path)
with ⟨ $n'' \in HRB\text{-slice } \{n_2\}$ ⟩ ⟨ $n_3 \in S$ ⟩ show ?thesis
  by(fastforce intro:HRB-slice-is-SDG-path-HRB-slice)
next
assume  $n'' = n_1$ 
from ⟨matched  $n_2$   $ns'$   $n_3$ ⟩ obtain  $nsx$  where  $n_2$  is- $nsx \rightarrow_{d^*}$   $n_3$ 
  by(erule matched-is-SDG-path)
hence  $n_2 \in \text{sum-SDG-slice1 } n_2$ 
  by(fastforce intro:refl-slice1 is-SDG-path-valid-SDG-node)
with ⟨ $n_1 -p \rightarrow_{\text{call}} n_2 \vee n_1 -p:V \rightarrow_{\text{in}} n_2$ ⟩
have  $n_1 \in \text{sum-SDG-slice1 } n_2$ 
  by(fastforce intro:call-slice1 param-in-slice1 SDG-edge-sum-SDG-edge)
hence  $n_1 \in HRB\text{-slice } \{n_2\}$  by(fastforce intro:combSlice-refl simp:HRB-slice-def)
with ⟨ $n_2$  is- $nsx \rightarrow_{d^*}$   $n_3$ ⟩ ⟨ $n'' = n_1$ ⟩ ⟨ $n_3 \in S$ ⟩ show ?thesis
  by(fastforce intro:HRB-slice-is-SDG-path-HRB-slice)
next
assume  $n'' \in \text{set } ns'$ 
from ⟨matched  $n_2$   $ns'$   $n_3$ ⟩ this ⟨ $n_3 \in S$ ⟩ show ?thesis
  by(rule in-matched-in-HRB-slice)
qed
qed

```

lemma slice1-ics-SDG-path:

```

assumes  $n \in \text{sum-SDG-slice1 } n'$  and  $n \neq n'$ 
obtains  $ns$  where CFG-node (-Entry-) ics- $ns \rightarrow_{d^*}$   $n'$  and  $n \in \text{set } ns$ 
proof(atomize-elim)
from ⟨ $n \in \text{sum-SDG-slice1 } n'$ ⟩
have  $n = n' \vee (\exists ns. \text{CFG-node } (-\text{Entry-}) \text{ ics-} ns \rightarrow_{d^*} n' \wedge n \in \text{set } ns)$ 
proof(induct rule:sum-SDG-slice1.induct)
  case refl-slice1 thus ?case by simp
next
  case (cdep-slice1  $n''$   $n$ )
from ⟨ $n''$   $s \rightarrow_{\text{cd}} n$ ⟩ have valid-SDG-node  $n''$  by(rule sum-SDG-edge-valid-SDG-node)
hence  $n''$  ics- $\square \rightarrow_{d^*}$   $n''$  by(rule icsSp-Nil)
from ⟨valid-SDG-node  $n''$ ⟩ have valid-node (parent-node  $n''$ )
  by(rule valid-SDG-CFG-node)
thus ?case
proof(cases parent-node  $n'' = (-\text{Exit-})$ )
  case True
  with ⟨valid-SDG-node  $n''$ ⟩ have  $n'' = \text{CFG-node } (-\text{Exit-})$ 
    by(rule valid-SDG-node-parent-Exit)
  with ⟨ $n''$   $s \rightarrow_{\text{cd}} n$ ⟩ have False by(fastforce intro:Exit-no-sum-SDG-edge-source)
  thus ?thesis by simp
next
  case False
  from ⟨ $n''$   $s \rightarrow_{\text{cd}} n$ ⟩ have valid-SDG-node  $n''$ 

```

```

    by(rule sum-SDG-edge-valid-SDG-node)
  from this False obtain ns
    where CFG-node (-Entry-) cc-ns→d* n''
    by(erule Entry-cc-SDG-path-to-inner-node)
  with ⟨n'' s →cd n⟩ have CFG-node (-Entry-) cc-ns@[n'']→d* n
    by(fastforce intro:ccSp-Append-cdep sum-SDG-edge-SDG-edge)
  hence CFG-node (-Entry-) ics-ns@[n'']→d* n
    by(rule cc-SDG-path-ics-SDG-path)
  from ⟨n = n' ∨ (∃ ns. CFG-node (-Entry-) ics-ns→d* n' ∧ n ∈ set ns)⟩
  show ?thesis
  proof
    assume n = n'
    with ⟨CFG-node (-Entry-) ics-ns@[n'']→d* n⟩ show ?thesis by fastforce
  next
    assume ∃ ns. CFG-node (-Entry-) ics-ns→d* n' ∧ n ∈ set ns
    then obtain nsx where CFG-node (-Entry-) ics-nsx→d* n' and n ∈ set
      nsx
      by blast
    then obtain ns' ns'' where nsx = ns'@ns'' and n ics-ns''→d* n'
      by -(erule ics-SDG-path-split)
    with ⟨CFG-node (-Entry-) ics-ns@[n'']→d* n⟩
    show ?thesis by(fastforce intro:ics-SDG-path-Append)
  qed
  qed
next
  case (ddep-slice1 n'' V n)
  from ⟨n'' s - V →dd n⟩ have valid-SDG-node n'' by(rule sum-SDG-edge-valid-SDG-node)
  hence n'' ics-[]→d* n'' by(rule icsSp-Nil)
  from ⟨valid-SDG-node n''⟩ have valid-node (parent-node n'')
    by(rule valid-SDG-CFG-node)
  thus ?case
  proof(cases parent-node n'' = (-Exit-))
    case True
      with ⟨valid-SDG-node n''⟩ have n'' = CFG-node (-Exit-)
        by(rule valid-SDG-node-parent-Exit)
      with ⟨n'' s - V →dd n⟩ have False by(fastforce intro:Exit-no-sum-SDG-edge-source)
      thus ?thesis by simp
    next
      case False
        from ⟨n'' s - V →dd n⟩ have valid-SDG-node n''
          by(rule sum-SDG-edge-valid-SDG-node)
        from this False obtain ns
          where CFG-node (-Entry-) cc-ns→d* n''
          by(erule Entry-cc-SDG-path-to-inner-node)
        hence CFG-node (-Entry-) ics-ns→d* n''
          by(rule cc-SDG-path-ics-SDG-path)
        show ?thesis
        proof(cases n'' = n)
          case True

```

```

from  $\langle n = n' \vee (\exists ns. \text{CFG-node } (-\text{Entry-}) \text{ ics-ns} \rightarrow_d^* n' \wedge n \in \text{set } ns) \rangle$ 
show ?thesis
proof
  assume  $n = n'$ 
  with  $\langle n'' = n \rangle$  show ?thesis by simp
next
  assume  $\exists ns. \text{CFG-node } (-\text{Entry-}) \text{ ics-ns} \rightarrow_d^* n' \wedge n \in \text{set } ns$ 
  with  $\langle n'' = n \rangle$  show ?thesis by fastforce
qed
next
case False
with  $\langle n'' s - V \rightarrow_{dd} n \rangle$   $\langle \text{CFG-node } (-\text{Entry-}) \text{ ics-ns} \rightarrow_d^* n'' \rangle$ 
have  $\text{CFG-node } (-\text{Entry-}) \text{ ics-ns}@[n''] \rightarrow_d^* n$ 
  by  $-(\text{rule icsSp-Append-ddep})$ 
from  $\langle n = n' \vee (\exists ns. \text{CFG-node } (-\text{Entry-}) \text{ ics-ns} \rightarrow_d^* n' \wedge n \in \text{set } ns) \rangle$ 
show ?thesis
proof
  assume  $n = n'$ 
  with  $\langle \text{CFG-node } (-\text{Entry-}) \text{ ics-ns}@[n''] \rightarrow_d^* n \rangle$  show ?thesis by fastforce
next
  assume  $\exists ns. \text{CFG-node } (-\text{Entry-}) \text{ ics-ns} \rightarrow_d^* n' \wedge n \in \text{set } ns$ 
  then obtain  $nsx$  where  $\text{CFG-node } (-\text{Entry-}) \text{ ics-ns}x \rightarrow_d^* n'$  and  $n \in$ 
set  $nsx$ 
  by blast
  then obtain  $ns' ns''$  where  $nsx = ns'@ns''$  and  $n \text{ ics-ns}'' \rightarrow_d^* n'$ 
  by  $-(\text{erule ics-SDG-path-split})$ 
  with  $\langle \text{CFG-node } (-\text{Entry-}) \text{ ics-ns}@[n''] \rightarrow_d^* n \rangle$ 
  show ?thesis by (fastforce intro: ics-SDG-path-Append)
qed
qed
qed
next
case (call-slice1  $n'' p n$ )
from  $\langle n'' s - p \rightarrow_{\text{call}} n \rangle$  have valid-SDG-node  $n''$ 
  by (rule sum-SDG-edge-valid-SDG-node)
hence  $n'' \text{ ics-}[] \rightarrow_d^* n''$  by (rule icsSp-Nil)
from  $\langle \text{valid-SDG-node } n'' \rangle$  have valid-node (parent-node  $n''$ )
  by (rule valid-SDG-CFG-node)
thus ?case
proof (cases parent-node  $n'' = (-\text{Exit-})$ )
  case True
  with  $\langle \text{valid-SDG-node } n'' \rangle$  have  $n'' = \text{CFG-node } (-\text{Exit-})$ 
  by (rule valid-SDG-node-parent-Exit)
with  $\langle n'' s - p \rightarrow_{\text{call}} n \rangle$  have False by (fastforce intro: Exit-no-sum-SDG-edge-source)
  thus ?thesis by simp
next
case False
from  $\langle n'' s - p \rightarrow_{\text{call}} n \rangle$  have valid-SDG-node  $n''$ 
  by (rule sum-SDG-edge-valid-SDG-node)

```

```

from this False obtain ns
  where CFG-node (-Entry-) cc-ns→d* n''
  by(erule Entry-cc-SDG-path-to-inner-node)
with ⟨n'' s-p→call n⟩ have CFG-node (-Entry-) cc-ns@[n'']→d* n
  by(fastforce intro:ccSp-Append-call sum-SDG-edge-SDG-edge)
hence CFG-node (-Entry-) ics-ns@[n'']→d* n
  by(rule cc-SDG-path-ics-SDG-path)
from ⟨n = n' ∨ (∃ ns. CFG-node (-Entry-) ics-ns→d* n' ∧ n ∈ set ns)⟩
show ?thesis
proof
  assume n = n'
  with ⟨CFG-node (-Entry-) ics-ns@[n'']→d* n⟩ show ?thesis by fastforce
next
  assume ∃ ns. CFG-node (-Entry-) ics-ns→d* n' ∧ n ∈ set ns
  then obtain nsx where CFG-node (-Entry-) ics-nsx→d* n' and n ∈ set
nsx
    by blast
  then obtain ns' ns'' where nsx = ns'@ns'' and n ics-ns''→d* n'
    by -(erule ics-SDG-path-split)
  with ⟨CFG-node (-Entry-) ics-ns@[n'']→d* n⟩
  show ?thesis by(fastforce intro:ics-SDG-path-Append)
qed
qed
next
  case (param-in-slice1 n'' p V n)
  from ⟨n'' s-p:V→in n⟩ have valid-SDG-node n''
    by(rule sum-SDG-edge-valid-SDG-node)
  hence n'' ics-[]→d* n'' by(rule icsSp-Nil)
  from ⟨valid-SDG-node n''⟩ have valid-node (parent-node n'')
    by(rule valid-SDG-CFG-node)
  thus ?case
  proof(cases parent-node n'' = (-Exit-))
    case True
      with ⟨valid-SDG-node n''⟩ have n'' = CFG-node (-Exit-)
        by(rule valid-SDG-node-parent-Exit)
      with ⟨n'' s-p:V→in n⟩ have False by(fastforce intro:Exit-no-sum-SDG-edge-source)
      thus ?thesis by simp
    next
      case False
        from ⟨n'' s-p:V→in n⟩ have valid-SDG-node n''
          by(rule sum-SDG-edge-valid-SDG-node)
        from this False obtain ns
          where CFG-node (-Entry-) cc-ns→d* n''
          by(erule Entry-cc-SDG-path-to-inner-node)
        hence CFG-node (-Entry-) ics-ns→d* n''
          by(rule cc-SDG-path-ics-SDG-path)
        with ⟨n'' s-p:V→in n⟩ have CFG-node (-Entry-) ics-ns@[n'']→d* n
          by -(rule icsSp-Append-param-in)
        from ⟨n = n' ∨ (∃ ns. CFG-node (-Entry-) ics-ns→d* n' ∧ n ∈ set ns)⟩

```

```

show ?thesis
proof
  assume  $n = n'$ 
  with  $\langle \text{CFG-node } (-\text{Entry-}) \text{ ics-ns@[n']} \rightarrow_d^* n \rangle$  show ?thesis by fastforce
next
  assume  $\exists ns. \text{CFG-node } (-\text{Entry-}) \text{ ics-ns} \rightarrow_d^* n' \wedge n \in \text{set } ns$ 
  then obtain  $nsx$  where  $\text{CFG-node } (-\text{Entry-}) \text{ ics-nsx} \rightarrow_d^* n'$  and  $n \in \text{set } nsx$ 
    by blast
  then obtain  $ns' ns''$  where  $nsx = ns'@ns''$  and  $n \text{ ics-ns''} \rightarrow_d^* n'$ 
    by  $-(\text{erule ics-SDG-path-split})$ 
  with  $\langle \text{CFG-node } (-\text{Entry-}) \text{ ics-ns@[n']} \rightarrow_d^* n \rangle$ 
  show ?thesis by  $(\text{fastforce intro:ics-SDG-path-Append})$ 
qed
next
case  $(\text{sum-slice1 } n'' p n)$ 
from  $\langle n'' s-p \rightarrow_{\text{sum}} n \rangle$  have  $\text{valid-SDG-node } n''$ 
  by  $(\text{rule sum-SDG-edge-valid-SDG-node})$ 
hence  $n'' \text{ ics-} \square \rightarrow_d^* n''$  by  $(\text{rule icsSp-Nil})$ 
from  $\langle \text{valid-SDG-node } n'' \rangle$  have  $\text{valid-node } (\text{parent-node } n'')$ 
  by  $(\text{rule valid-SDG-CFG-node})$ 
thus ?case
proof  $(\text{cases parent-node } n'' = (-\text{Exit-}))$ 
  case True
  with  $\langle \text{valid-SDG-node } n'' \rangle$  have  $n'' = \text{CFG-node } (-\text{Exit-})$ 
    by  $(\text{rule valid-SDG-node-parent-Exit})$ 
  with  $\langle n'' s-p \rightarrow_{\text{sum}} n \rangle$  have False by  $(\text{fastforce intro:Exit-no-sum-SDG-edge-source})$ 
  thus ?thesis by simp
  case False
  from  $\langle n'' s-p \rightarrow_{\text{sum}} n \rangle$  have  $\text{valid-SDG-node } n''$ 
    by  $(\text{rule sum-SDG-edge-valid-SDG-node})$ 
  from this False obtain  $ns$ 
    where  $\text{CFG-node } (-\text{Entry-}) \text{ cc-ns} \rightarrow_d^* n''$ 
    by  $(\text{erule Entry-cc-SDG-path-to-inner-node})$ 
  hence  $\text{CFG-node } (-\text{Entry-}) \text{ ics-ns} \rightarrow_d^* n''$ 
    by  $(\text{rule cc-SDG-path-ics-SDG-path})$ 
  with  $\langle n'' s-p \rightarrow_{\text{sum}} n \rangle$  have  $\text{CFG-node } (-\text{Entry-}) \text{ ics-ns@[n']} \rightarrow_d^* n$ 
    by  $-(\text{rule icsSp-Append-sum})$ 
  from  $\langle n = n' \vee (\exists ns. \text{CFG-node } (-\text{Entry-}) \text{ ics-ns} \rightarrow_d^* n' \wedge n \in \text{set } ns) \rangle$ 
  show ?thesis
  proof
    assume  $n = n'$ 
    with  $\langle \text{CFG-node } (-\text{Entry-}) \text{ ics-ns@[n']} \rightarrow_d^* n \rangle$  show ?thesis by fastforce
  next
    assume  $\exists ns. \text{CFG-node } (-\text{Entry-}) \text{ ics-ns} \rightarrow_d^* n' \wedge n \in \text{set } ns$ 
    then obtain  $nsx$  where  $\text{CFG-node } (-\text{Entry-}) \text{ ics-nsx} \rightarrow_d^* n'$  and  $n \in \text{set } nsx$ 

```

```

    by blast
  then obtain ns' ns'' where nsx = ns'@ns'' and n ics-ns''→d* n'
    by -(erule ics-SDG-path-split)
  with ⟨CFG-node (-Entry-) ics-ns@[n']→d* n⟩
  show ?thesis by(fastforce intro:ics-SDG-path-Append)
qed
qed
qed
with ⟨n ≠ n'⟩ show ∃ ns. CFG-node (-Entry-) ics-ns→d* n' ∧ n ∈ set ns by
simp
qed

```

lemma *slice2-irs-SDG-path*:

```

  assumes n ∈ sum-SDG-slice2 n' and valid-SDG-node n'
  obtains ns where n irs-ns→d* n'
using assms
by(induct rule:sum-SDG-slice2.induct,auto intro:intra-return-sum-SDG-path.intros)

```

theorem *HRB-slice-realizable*:

```

  assumes n ∈ HRB-slice S and ∀ n' ∈ S. valid-SDG-node n' and n ∉ S
  obtains n' ns where n' ∈ S and realizable (CFG-node (-Entry-)) ns n'
  and n ∈ set ns
proof(atomize-elim)
  from ⟨n ∈ HRB-slice S⟩ ⟨n ∉ S⟩
  show ∃ n' ns. n' ∈ S ∧ realizable (CFG-node (-Entry-)) ns n' ∧ n ∈ set ns
proof(induct rule:HRB-slice-cases)
  case (phase1 n nx)
  with ⟨n ∉ S⟩ show ?case
  by(fastforce elim:slice1-ics-SDG-path ics-SDG-path-realizable)
next
  case (phase2 n' nx n'' p n)
  from ⟨∀ n' ∈ S. valid-SDG-node n'⟩ ⟨n' ∈ S⟩ have valid-SDG-node n' by simp
  with ⟨nx ∈ sum-SDG-slice1 n'⟩ have valid-SDG-node nx
  by(auto elim:slice1-ics-SDG-path ics-SDG-path-split
    intro:ics-SDG-path-valid-SDG-node)
  with ⟨n ∈ sum-SDG-slice2 nx⟩
  obtain nsx where n irs-nsx→d* nx by(erule slice2-irs-SDG-path)
  show ?case
proof(cases n = nx)
  case True
  show ?thesis
proof(cases nx = n')
  case True
  with ⟨n = nx⟩ ⟨n ∉ S⟩ ⟨n' ∈ S⟩ have False by simp
  thus ?thesis by simp
next
  case False

```

```

with ⟨nx ∈ sum-SDG-slice1 n'⟩ obtain ns
  where realizable (CFG-node (-Entry-)) ns n' and nx ∈ set ns
  by(fastforce elim:slice1-ics-SDG-path ics-SDG-path-realizable)
with ⟨n = nx⟩ ⟨n' ∈ S⟩ show ?thesis by blast
qed
next
case False
with ⟨n irs-nsx →d* nx⟩ obtain ns
  where realizable (CFG-node (-Entry-)) ns nx and n ∈ set ns
  by(erule irs-SDG-path-realizable)
show ?thesis
proof(cases nx = n')
  case True
  with ⟨realizable (CFG-node (-Entry-)) ns nx⟩ ⟨n ∈ set ns⟩ ⟨n' ∈ S⟩
  show ?thesis by blast
next
case False
with ⟨nx ∈ sum-SDG-slice1 n'⟩ obtain nsx'
  where CFG-node (-Entry-) ics-nsx' →d* n' and nx ∈ set nsx'
  by(erule slice1-ics-SDG-path)
then obtain ns' where nx ics-ns' →d* n' by -(erule ics-SDG-path-split)
with ⟨realizable (CFG-node (-Entry-)) ns nx⟩
obtain ns'' where realizable (CFG-node (-Entry-)) (ns@ns'') n'
  by(erule realizable-Append-ics-SDG-path)
with ⟨n ∈ set ns⟩ ⟨n' ∈ S⟩ show ?thesis by fastforce
qed
qed
qed
qed

```

theorem *HRB-slice-precise*:

$\llbracket \forall n' \in S. \text{valid-SDG-node } n'; n \notin S \rrbracket \implies$

$n \in \text{HRB-slice } S =$

$(\exists n' ns. n' \in S \wedge \text{realizable } (\text{CFG-node } (-\text{Entry-})) ns n' \wedge n \in \text{set } ns)$

by(*fastforce elim:HRB-slice-realizable intro:in-realizable-in-HRB-slice*)

end

end

1.10 Observable sets w.r.t. standard control dependence

theory *SCDObservable* **imports** *Observable HRBSlice* **begin**

context *SDG* **begin**

lemma *matched-bracket-assms-variant*:

assumes $n_1 -p \rightarrow_{call} n_2 \vee n_1 -p: V' \rightarrow_{in} n_2$ **and** *matched* $n_2 ns' n_3$
and $n_3 -p \rightarrow_{ret} n_4 \vee n_3 -p: V \rightarrow_{out} n_4$
and *call-of-return-node* (parent-node n_4) (parent-node n_1)
obtains $a a'$ **where** *valid-edge* a **and** $a' \in$ *get-return-edges* a
and *sourcenode* $a =$ *parent-node* n_1 **and** *targetnode* $a =$ *parent-node* n_2
and *sourcenode* $a' =$ *parent-node* n_3 **and** *targetnode* $a' =$ *parent-node* n_4
proof(*atomize-elim*)
from $\langle n_1 -p \rightarrow_{call} n_2 \vee n_1 -p: V' \rightarrow_{in} n_2 \rangle$ **obtain** $Q r fs$ **where** *valid-edge* a
and *kind* $a = Q:r \hookrightarrow_{pfs}$ **and** *parent-node* $n_1 =$ *sourcenode* a
and *parent-node* $n_2 =$ *targetnode* a
by(*fastforce elim:SDG-edge.cases*)
from $\langle n_3 -p \rightarrow_{ret} n_4 \vee n_3 -p: V \rightarrow_{out} n_4 \rangle$ **obtain** $a' Q' f'$
where *valid-edge* a' **and** *kind* $a' = Q' \hookleftarrow_{pf'}$
and *parent-node* $n_3 =$ *sourcenode* a' **and** *parent-node* $n_4 =$ *targetnode* a'
by(*fastforce elim:SDG-edge.cases*)
from \langle *valid-edge* $a' \rangle$ \langle *kind* $a' = Q' \hookleftarrow_{pf'}$ \rangle
obtain ax **where** *valid-edge* ax **and** $\exists Q r fs.$ *kind* $ax = Q:r \hookrightarrow_{pfs}$
and $a' \in$ *get-return-edges* ax
by $-($ *drule return-needs-call,fastforce+* $)$
from \langle *valid-edge* $a \rangle$ \langle *valid-edge* $ax \rangle$ \langle *kind* $a = Q:r \hookrightarrow_{pfs}$ \rangle $\langle \exists Q r fs.$ *kind* $ax =$
 $Q:r \hookrightarrow_{pfs}$ \rangle
have *targetnode* $a =$ *targetnode* ax **by**(*fastforce dest:same-proc-call-unique-target*)
from \langle *valid-edge* $a' \rangle$ $\langle a' \in$ *get-return-edges* $ax \rangle$ \langle *valid-edge* $ax \rangle$
have *call-of-return-node* (*targetnode* a') (*sourcenode* ax)
by(*fastforce simp:return-node-def call-of-return-node-def*)
with \langle *call-of-return-node* (*parent-node* n_4) (*parent-node* n_1) \rangle
 \langle *parent-node* $n_4 =$ *targetnode* $a' \rangle$
have *sourcenode* $ax =$ *parent-node* n_1 **by** *fastforce*
with \langle *valid-edge* $ax \rangle$ $\langle a' \in$ *get-return-edges* $ax \rangle$ \langle *targetnode* $a =$ *targetnode* $ax \rangle$
 \langle *parent-node* $n_2 =$ *targetnode* $a \rangle$ \langle *parent-node* $n_3 =$ *sourcenode* $a' \rangle$
 \langle *parent-node* $n_4 =$ *targetnode* $a' \rangle$
show $\exists a a'.$ *valid-edge* $a \wedge a' \in$ *get-return-edges* $a \wedge$
sourcenode $a =$ *parent-node* $n_1 \wedge$ *targetnode* $a =$ *parent-node* $n_2 \wedge$
sourcenode $a' =$ *parent-node* $n_3 \wedge$ *targetnode* $a' =$ *parent-node* n_4
by *fastforce*
qed

1.10.1 Observable set of standard control dependence is at most a singleton

definition *SDG-to-CFG-set* :: $'node$ *SDG-node set* \Rightarrow $'node$ *set* ($[_]_{CFG}$)
where $[S]_{CFG} \equiv \{m. \text{CFG-node } m \in S\}$

lemma [*intro*]: $\forall n \in S.$ *valid-SDG-node* $n \implies \forall n \in [S]_{CFG}.$ *valid-node* n
by(*fastforce simp:SDG-to-CFG-set-def*)

lemma *Exit-HRB-Slice*:
assumes $n \in \lfloor \text{HRB-slice } \{ \text{CFG-node } (-\text{Exit-}) \} \rfloor_{\text{CFG}}$ **shows** $n = (-\text{Exit-})$
proof –
from $\langle n \in \lfloor \text{HRB-slice } \{ \text{CFG-node } (-\text{Exit-}) \} \rfloor_{\text{CFG}} \rangle$
have $\text{CFG-node } n \in \text{HRB-slice } \{ \text{CFG-node } (-\text{Exit-}) \}$
by (*simp add:SDG-to-CFG-set-def*)
thus ?thesis
proof (*induct CFG-node n rule:HRB-slice-cases*)
case (*phase1 nx*)
from $\langle nx \in \{ \text{CFG-node } (-\text{Exit-}) \} \rangle$ **have** $nx = \text{CFG-node } (-\text{Exit-})$ **by** *simp*
with $\langle \text{CFG-node } n \in \text{sum-SDG-slice1 } nx \rangle$
have $\text{CFG-node } n = \text{CFG-node } (-\text{Exit-}) \vee$
 $(\exists n \text{ Vopt } \text{popt } b. \text{sum-SDG-edge } n \text{ Vopt } \text{popt } b (\text{CFG-node } (-\text{Exit-})))$
by (*induct rule:sum-SDG-slice1.induct*) *auto*
then show ?thesis **by** (*fastforce dest:Exit-no-sum-SDG-edge-target*)
next
case (*phase2 nx n' n'' p*)
from $\langle nx \in \{ \text{CFG-node } (-\text{Exit-}) \} \rangle$ **have** $nx = \text{CFG-node } (-\text{Exit-})$ **by** *simp*
with $\langle n' \in \text{sum-SDG-slice1 } nx \rangle$
have $n' = \text{CFG-node } (-\text{Exit-}) \vee$
 $(\exists n \text{ Vopt } \text{popt } b. \text{sum-SDG-edge } n \text{ Vopt } \text{popt } b (\text{CFG-node } (-\text{Exit-})))$
by (*induct rule:sum-SDG-slice1.induct*) *auto*
hence $n' = \text{CFG-node } (-\text{Exit-})$ **by** (*fastforce dest:Exit-no-sum-SDG-edge-target*)
with $\langle \text{CFG-node } n \in \text{sum-SDG-slice2 } n' \rangle$
have $\text{CFG-node } n = \text{CFG-node } (-\text{Exit-}) \vee$
 $(\exists n \text{ Vopt } \text{popt } b. \text{sum-SDG-edge } n \text{ Vopt } \text{popt } b (\text{CFG-node } (-\text{Exit-})))$
by (*induct rule:sum-SDG-slice2.induct*) *auto*
then show ?thesis **by** (*fastforce dest:Exit-no-sum-SDG-edge-target*)
qed
qed

lemma *Exit-in-obs-intra-slice-node*:
assumes $(-\text{Exit-}) \in \text{obs-intra } n' \lfloor \text{HRB-slice } S \rfloor_{\text{CFG}}$
shows $\text{CFG-node } (-\text{Exit-}) \in S$
proof –
let ?S' = $\lfloor \text{HRB-slice } S \rfloor_{\text{CFG}}$
from $\langle (-\text{Exit-}) \in \text{obs-intra } n' ?S' \rangle$ **obtain** *as* **where** $n' - \text{as} \rightarrow_{\iota^*} (-\text{Exit-})$
and $\forall nx \in \text{set}(\text{ourcenodes } \text{as}). nx \notin ?S' \text{ and } (-\text{Exit-}) \in ?S'$
by (*erule obs-intraE*)
from $\langle (-\text{Exit-}) \in ?S' \rangle$
have $\text{CFG-node } (-\text{Exit-}) \in \text{HRB-slice } S$ **by** (*simp add:SDG-to-CFG-set-def*)
thus ?thesis
proof (*induct CFG-node (-Exit-) rule:HRB-slice-cases*)
case (*phase1 nx*)
thus ?case
by (*induct CFG-node (-Exit-) rule:sum-SDG-slice1.induct,*
auto dest:Exit-no-sum-SDG-edge-source)
next

```

case (phase2 nx n' n'' p)
from ⟨CFG-node (-Exit-) ∈ sum-SDG-slice2 n'⟩ ⟨n' ∈ sum-SDG-slice1 nx⟩ ⟨nx
∈ S⟩
show ?case
  apply(induct n≡CFG-node (-Exit-) rule:sum-SDG-slice2.induct)
  apply(auto dest:Exit-no-sum-SDG-edge-source)
  apply(induct n≡CFG-node (-Exit-) rule:sum-SDG-slice1.induct)
  apply(auto dest:Exit-no-sum-SDG-edge-source)
done
qed
qed

```

lemma *obs-intra-postdominate*:

```

assumes n ∈ obs-intra n' [HRB-slice S]CFG and ¬ method-exit n
shows n postdominates n'
proof(rule ccontr)
  assume ¬ n postdominates n'
  from ⟨n ∈ obs-intra n' [HRB-slice S]CFG⟩ have valid-node n
    by(fastforce dest:in-obs-intra-valid)
  with ⟨n ∈ obs-intra n' [HRB-slice S]CFG⟩ ⟨¬ method-exit n⟩ have n postdomi-
nates n
    by(fastforce intro:postdominate-refl)
  from ⟨n ∈ obs-intra n' [HRB-slice S]CFG⟩ obtain as where n' -as→l* n
    and all-notinS:∀ n' ∈ set(sourcenodes as). n' ∉ [HRB-slice S]CFG
    and n ∈ [HRB-slice S]CFG by(erule obs-intraE)
  from ⟨n postdominates n⟩ ⟨¬ n postdominates n'⟩ ⟨n' -as→l* n⟩
obtain as' a as'' where [simp]:as = as'@a#as''
    and valid-edge a and ¬ n postdominates (sourcenode a)
    and n postdominates (targetnode a) and intra-kind (kind a)
    by(fastforce elim!:postdominate-path-branch simp:intra-path-def)
  from ⟨n' -as→l* n⟩ have sourcenode a -a#as''→l* n
    by(fastforce elim:path-split intro:Cons-path simp:intra-path-def)
  with ⟨¬ n postdominates (sourcenode a)⟩ ⟨valid-edge a⟩ ⟨valid-node n⟩
obtain asx pex where sourcenode a -asx→l* pex and method-exit pex
    and n ∉ set(sourcenodes asx) by(fastforce simp:postdominate-def)
have asx ≠ []
proof
  assume asx = []
  with ⟨sourcenode a -asx→l* pex⟩ have sourcenode a = pex
    by(fastforce simp:intra-path-def)
  from ⟨method-exit pex⟩ show False
proof(rule method-exit-cases)
  assume pex = (-Exit-)
  with ⟨sourcenode a = pex⟩ have sourcenode a = (-Exit-) by simp
  with ⟨valid-edge a⟩ show False by(rule Exit-source)
next
  fix a' Q f p
  assume pex = sourcenode a' and valid-edge a' and kind a' = Q↔pf

```

```

from ⟨valid-edge a'⟩ ⟨kind a' = Q↔pf⟩ ⟨valid-edge a⟩ ⟨intra-kind (kind a)⟩
  ⟨sourcenode a = pex⟩ ⟨pex = sourcenode a'⟩
show False by(fastforce dest:return-edges-only simp:intra-kind-def)
qed
qed
then obtain ax asx' where [simp]:asx = ax#asx' by(cases asx) auto
with ⟨sourcenode a -asx→i* pex⟩ have sourcenode a -ax#asx'→* pex
  by(simp add:intra-path-def)
hence valid-edge ax and [simp]:sourcenode a = sourcenode ax
  and targetnode ax -asx'→* pex by(auto elim:path-split-Cons)
with ⟨sourcenode a -asx→i* pex⟩ have targetnode ax -asx'→i* pex
  by(simp add:intra-path-def)
with ⟨valid-edge ax⟩ ⟨n ∉ set(sourcenodes asx)⟩ ⟨method-exit pex⟩
have ¬ n postdominates targetnode ax
  by(fastforce simp:postdominate-def sourcenodes-def)
from ⟨n ∈ obs-intra n' [HRB-slice S]CFG⟩ all-notinS
have n ∉ set (sourcenodes (a#as''))
  by(fastforce elim:obs-intra.cases simp:sourcenodes-def)
from ⟨sourcenode a -asx→i* pex⟩ have intra-kind (kind ax)
  by(simp add:intra-path-def)
with ⟨sourcenode a -a#as''→i* n⟩ ⟨n postdominates (targetnode a)⟩
  ⟨¬ n postdominates targetnode ax⟩ ⟨valid-edge ax⟩
  ⟨n ∉ set (sourcenodes (a#as''))⟩ ⟨intra-kind (kind a)⟩
have (sourcenode a) controls n
  by(fastforce simp:control-dependence-def)
hence CFG-node (sourcenode a) s→cd CFG-node n
  by(fastforce intro:sum-SDG-cdep-edge)
with ⟨n ∈ obs-intra n' [HRB-slice S]CFG⟩ have sourcenode a ∈ [HRB-slice
S]CFG
  by(auto elim!:obs-intraE combine-SDG-slices.cases
    intro:combine-SDG-slices.intros sum-SDG-slice1.intros
    sum-SDG-slice2.intros simp:HRB-slice-def SDG-to-CFG-set-def)
with all-notinS show False by(simp add:sourcenodes-def)
qed

```

lemma obs-intra-singleton-disj:

assumes valid-node n

shows (∃ m. obs-intra n [HRB-slice S]_{CFG} = {m}) ∨
obs-intra n [HRB-slice S]_{CFG} = {}

proof(rule ccontr)

assume ¬ ((∃ m. obs-intra n [HRB-slice S]_{CFG} = {m}) ∨
obs-intra n [HRB-slice S]_{CFG} = {})

hence ∃ nx nx'. nx ∈ obs-intra n [HRB-slice S]_{CFG} ∧
nx' ∈ obs-intra n [HRB-slice S]_{CFG} ∧ nx ≠ nx' **by** auto

then obtain nx nx' **where** nx ∈ obs-intra n [HRB-slice S]_{CFG}

and nx' ∈ obs-intra n [HRB-slice S]_{CFG} **and** nx ≠ nx' **by** auto

from ⟨nx ∈ obs-intra n [HRB-slice S]_{CFG}⟩ **obtain** as **where** n -as→_i* nx

and $all:\forall n' \in set(sourcenodes\ as). n' \notin [HRB\text{-}slice\ S]_{CFG}$
and $nx \in [HRB\text{-}slice\ S]_{CFG}$
by(erule obs-intraE)
from $\langle n - as \rightarrow_i^* nx \rangle$ **have** $n - as \rightarrow_i^* nx$ **and** $\forall a \in set\ as. intra\text{-}kind\ (kind\ a)$
by(simp-all add:intra-path-def)
hence valid-node nx **by**(fastforce dest:path-valid-node)
with $\langle nx \in [HRB\text{-}slice\ S]_{CFG} \rangle$ **have** obs-intra nx $[HRB\text{-}slice\ S]_{CFG} = \{nx\}$
by $\neg(rule\ n\text{-}in\text{-}obs\text{-}intra)$
with $\langle n - as \rightarrow_i^* nx \rangle$ $\langle nx \in obs\text{-}intra\ n\ [HRB\text{-}slice\ S]_{CFG} \rangle$
 $\langle nx' \in obs\text{-}intra\ n\ [HRB\text{-}slice\ S]_{CFG} \rangle$ $\langle nx \neq nx' \rangle$ **have** $as \neq []$
by(fastforce elim:path.cases simp:intra-path-def)
with $\langle n - as \rightarrow_i^* nx \rangle$ $\langle nx \in obs\text{-}intra\ n\ [HRB\text{-}slice\ S]_{CFG} \rangle$
 $\langle nx' \in obs\text{-}intra\ n\ [HRB\text{-}slice\ S]_{CFG} \rangle$ $\langle nx \neq nx' \rangle$
 $\langle obs\text{-}intra\ nx\ [HRB\text{-}slice\ S]_{CFG} = \{nx\} \rangle$ $\langle \forall a \in set\ as. intra\text{-}kind\ (kind\ a) \rangle$ **all**
have $\exists a\ as'\ as''. n - as' \rightarrow_i^* sourcenode\ a \wedge targetnode\ a - as'' \rightarrow_i^* nx \wedge$
 $valid\text{-}edge\ a \wedge as = as'@a\#as'' \wedge intra\text{-}kind\ (kind\ a) \wedge$
 $obs\text{-}intra\ (targetnode\ a)\ [HRB\text{-}slice\ S]_{CFG} = \{nx\} \wedge$
 $(\neg (\exists m. obs\text{-}intra\ (sourcenode\ a)\ [HRB\text{-}slice\ S]_{CFG} = \{m\} \vee$
 $obs\text{-}intra\ (sourcenode\ a)\ [HRB\text{-}slice\ S]_{CFG} = \{\}))$
proof(induct arbitrary: nx' rule:path.induct)
case (Cons-path n'' as n' a n)
note IH = $\langle \bigwedge nx'. [n' \in obs\text{-}intra\ n''\ [HRB\text{-}slice\ S]_{CFG};$
 $nx' \in obs\text{-}intra\ n''\ [HRB\text{-}slice\ S]_{CFG}; n' \neq nx';$
 $obs\text{-}intra\ n'\ [HRB\text{-}slice\ S]_{CFG} = \{n'\};$
 $\forall a \in set\ as. intra\text{-}kind\ (kind\ a);$
 $\forall n' \in set\ (sourcenodes\ as). n' \notin [HRB\text{-}slice\ S]_{CFG}; as \neq [] \rangle$
 $\implies \exists a\ as'\ as''. n'' - as' \rightarrow_i^* sourcenode\ a \wedge targetnode\ a - as'' \rightarrow_i^* n' \wedge$
 $valid\text{-}edge\ a \wedge as = as'@a\#as'' \wedge intra\text{-}kind\ (kind\ a) \wedge$
 $obs\text{-}intra\ (targetnode\ a)\ [HRB\text{-}slice\ S]_{CFG} = \{n'\} \wedge$
 $(\neg (\exists m. obs\text{-}intra\ (sourcenode\ a)\ [HRB\text{-}slice\ S]_{CFG} = \{m\} \vee$
 $obs\text{-}intra\ (sourcenode\ a)\ [HRB\text{-}slice\ S]_{CFG} = \{\}))$
note more-than-one = $\langle n' \in obs\text{-}intra\ n\ [HRB\text{-}slice\ S]_{CFG} \rangle$
 $\langle nx' \in obs\text{-}intra\ n\ [HRB\text{-}slice\ S]_{CFG} \rangle$ $\langle n' \neq nx' \rangle$
from $\langle \forall a \in set\ (a\#as). intra\text{-}kind\ (kind\ a) \rangle$
have $\forall a \in set\ as. intra\text{-}kind\ (kind\ a)$ **and** intra-kind (kind a) **by** simp-all
from $\langle \forall n' \in set\ (sourcenodes\ (a\#as)). n' \notin [HRB\text{-}slice\ S]_{CFG} \rangle$
have $all:\forall n' \in set\ (sourcenodes\ as). n' \notin [HRB\text{-}slice\ S]_{CFG}$
by(simp add:sourcenodes-def)
show ?case
proof(cases as = [])
case True
with $\langle n'' - as \rightarrow_i^* n' \rangle$ **have** [simp]: $n'' = n'$ **by**(fastforce elim:path.cases)
from more-than-one $\langle sourcenode\ a = n \rangle$
have $\neg (\exists m. obs\text{-}intra\ (sourcenode\ a)\ [HRB\text{-}slice\ S]_{CFG} = \{m\} \vee$
 $obs\text{-}intra\ (sourcenode\ a)\ [HRB\text{-}slice\ S]_{CFG} = \{\})$
by auto
with $\langle targetnode\ a = n' \rangle$ $\langle obs\text{-}intra\ n'\ [HRB\text{-}slice\ S]_{CFG} = \{n'\} \rangle$
 $\langle sourcenode\ a = n \rangle$ True $\langle valid\text{-}edge\ a \rangle$ $\langle intra\text{-}kind\ (kind\ a) \rangle$
show ?thesis

```

    apply(rule-tac x=a in exI)
    apply(rule-tac x=[] in exI)
    apply(rule-tac x=[] in exI)
    by(auto intro:empty-path simp:intra-path-def)
next
case False
from ⟨n'' - as →* n'⟩ ⟨∀ a ∈ set (a # as). intra-kind (kind a)⟩
have n'' - as →l* n' by(simp add:intra-path-def)
with all
have subset:obs-intra n' [HRB-slice S]CFG ⊆ obs-intra n'' [HRB-slice S]CFG
  by -(rule path-obs-intra-subset)
thus ?thesis
  proof(cases obs-intra n' [HRB-slice S]CFG = obs-intra n'' [HRB-slice
S]CFG)
  case True
  with ⟨n'' - as →l* n'⟩ ⟨valid-edge a⟩ ⟨sourcenode a = n⟩ ⟨targetnode a = n''⟩
  ⟨obs-intra n' [HRB-slice S]CFG = {n'}⟩ ⟨intra-kind (kind a)⟩ more-than-one
  show ?thesis
    apply(rule-tac x=a in exI)
    apply(rule-tac x=[] in exI)
    apply(rule-tac x=as in exI)
    by(fastforce intro:empty-path simp:intra-path-def)
  next
  case False
  with subset
  have obs-intra n' [HRB-slice S]CFG ⊂ obs-intra n'' [HRB-slice S]CFG by
simp
  with ⟨obs-intra n' [HRB-slice S]CFG = {n'}⟩
  obtain ni where n' ∈ obs-intra n'' [HRB-slice S]CFG
    and ni ∈ obs-intra n'' [HRB-slice S]CFG and n' ≠ ni by auto
  from IH[OF this ⟨obs-intra n' [HRB-slice S]CFG = {n'}⟩
    ⟨∀ a ∈ set as. intra-kind (kind a)⟩ all ⟨as ≠ []⟩] obtain a' as' as''
    where n'' - as' →l* sourcenode a'
    and hyps:targetnode a' - as'' →l* n' valid-edge a' as = as'@a'#as''
      intra-kind (kind a') obs-intra (targetnode a') [HRB-slice S]CFG = {n'}
      ¬ (∃ m. obs-intra (sourcenode a') [HRB-slice S]CFG = {m} ∨
        obs-intra (sourcenode a') [HRB-slice S]CFG = {})
    by blast
  from ⟨n'' - as' →l* sourcenode a'⟩ ⟨valid-edge a⟩ ⟨sourcenode a = n⟩
  ⟨targetnode a = n''⟩ ⟨intra-kind (kind a)⟩ ⟨intra-kind (kind a')⟩
  have n - a # as' →l* sourcenode a'
    by(fastforce intro:path.Cons-path simp:intra-path-def)
  with hyps show ?thesis
    apply(rule-tac x=a' in exI)
    apply(rule-tac x=a#as' in exI)
    apply(rule-tac x=as'' in exI)
    by fastforce
qed
qed

```

qed simp
then obtain a as' as'' **where** *valid-edge* a **and** *intra-kind* ($kind\ a$)
and *obs-intra* (*targetnode* a) $\lfloor HRB\text{-}slice\ S \rfloor_{CFG} = \{nx\}$
and *more-than-one*: $\neg (\exists m. \text{obs-intra}(\text{sourcenode } a) \lfloor HRB\text{-}slice\ S \rfloor_{CFG} = \{m\})$
 \vee
 $\text{obs-intra}(\text{sourcenode } a) \lfloor HRB\text{-}slice\ S \rfloor_{CFG} = \{\}$
by *blast*
have *sourcenode* $a \notin \lfloor HRB\text{-}slice\ S \rfloor_{CFG}$
proof(*rule ccontr*)
assume $\neg \text{sourcenode } a \notin \lfloor HRB\text{-}slice\ S \rfloor_{CFG}$
hence *sourcenode* $a \in \lfloor HRB\text{-}slice\ S \rfloor_{CFG}$ **by** *simp*
with $\langle \text{valid-edge } a \rangle$
have *obs-intra* (*sourcenode* a) $\lfloor HRB\text{-}slice\ S \rfloor_{CFG} = \{\text{sourcenode } a\}$
by(*fastforce intro!:n-in-obs-intra*)
with *more-than-one* **show** *False* **by** *simp*
qed
with $\langle \text{valid-edge } a \rangle$ $\langle \text{intra-kind } (kind\ a) \rangle$
have *obs-intra* (*targetnode* a) $\lfloor HRB\text{-}slice\ S \rfloor_{CFG} \subseteq$
 $\text{obs-intra}(\text{sourcenode } a) \lfloor HRB\text{-}slice\ S \rfloor_{CFG}$
by(*rule edge-obs-intra-subset*)
with $\langle \text{obs-intra}(\text{targetnode } a) \lfloor HRB\text{-}slice\ S \rfloor_{CFG} = \{nx\} \rangle$
have $nx \in \text{obs-intra}(\text{sourcenode } a) \lfloor HRB\text{-}slice\ S \rfloor_{CFG}$ **by** *simp*
with *more-than-one* **obtain** m
where $m \in \text{obs-intra}(\text{sourcenode } a) \lfloor HRB\text{-}slice\ S \rfloor_{CFG}$ **and** $nx \neq m$ **by** *auto*
from $\langle m \in \text{obs-intra}(\text{sourcenode } a) \lfloor HRB\text{-}slice\ S \rfloor_{CFG} \rangle$ **have** *valid-node* m
by(*fastforce dest:in-obs-intra-valid*)
from $\langle \text{obs-intra}(\text{targetnode } a) \lfloor HRB\text{-}slice\ S \rfloor_{CFG} = \{nx\} \rangle$ **have** *valid-node* nx
by(*fastforce dest:in-obs-intra-valid*)
show *False*
proof(*cases m postdominates (sourcenode a)*)
case *True*
with $\langle nx \in \text{obs-intra}(\text{sourcenode } a) \lfloor HRB\text{-}slice\ S \rfloor_{CFG} \rangle$
 $\langle m \in \text{obs-intra}(\text{sourcenode } a) \lfloor HRB\text{-}slice\ S \rfloor_{CFG} \rangle$
have m *postdominates* nx
by(*fastforce intro:postdominate-inner-path-targetnode elim:obs-intraE*)
with $\langle nx \neq m \rangle$ **have** $\neg nx$ *postdominates* m **by**(*fastforce dest:postdominate-antisym*)
with $\langle \text{valid-node } nx \rangle$ $\langle \text{valid-node } m \rangle$ **obtain** $asx\ pex$ **where** $m - asx \rightarrow_{\iota^*} pex$
and *method-exit* pex **and** $nx \notin \text{set}(\text{sourcenodes } asx)$
by(*fastforce simp:postdominate-def*)
have $\neg nx$ *postdominates* (*sourcenode* a)
proof
assume nx *postdominates* *sourcenode* a
from $\langle nx \in \text{obs-intra}(\text{sourcenode } a) \lfloor HRB\text{-}slice\ S \rfloor_{CFG} \rangle$
 $\langle m \in \text{obs-intra}(\text{sourcenode } a) \lfloor HRB\text{-}slice\ S \rfloor_{CFG} \rangle$
obtain asx' **where** *sourcenode* $a - asx' \rightarrow_{\iota^*} m$ **and** $nx \notin \text{set}(\text{sourcenodes } asx')$
by(*fastforce elim:obs-intraE*)
with $\langle m - asx' \rightarrow_{\iota^*} pex \rangle$ **have** *sourcenode* $a - asx' @ asx' \rightarrow_{\iota^*} pex$
by(*fastforce intro:path-Append simp:intra-path-def*)

```

with ⟨ $nx \notin \text{set}(\text{sourcenodes } asx)$ ⟩ ⟨ $nx \notin \text{set}(\text{sourcenodes } asx')$ ⟩
  ⟨ $nx \text{ postdominates sourcenode } a$ ⟩ ⟨ $\text{method-exit } pex$ ⟩ show False
  by(fastforce simp:sourcenodes-def postdominate-def)
qed
show False
proof(cases method-exit nx)
  case True
  from ⟨ $m \text{ postdominates } nx$ ⟩ obtain  $xs$  where  $nx \rightarrow_{i,*} m$ 
    by  $-(\text{erule postdominate-implies-inner-path})$ 
  with True have  $nx = m$ 
    by(fastforce dest!:method-exit-inner-path simp:intra-path-def)
  with ⟨ $nx \neq m$ ⟩ show False by simp
next
  case False
  with ⟨ $nx \in \text{obs-intra}(\text{sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG}$ ⟩
  have  $nx \text{ postdominates sourcenode } a$  by(rule obs-intra-postdominate)
  with ⟨ $\neg nx \text{ postdominates}(\text{sourcenode } a)$ ⟩ show False by simp
qed
next
  case False
  show False
proof(cases method-exit m)
  case True
  from ⟨ $m \in \text{obs-intra}(\text{sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG}$ ⟩
    ⟨ $nx \in \text{obs-intra}(\text{sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG}$ ⟩
  obtain  $xs$  where  $\text{sourcenode } a \rightarrow_{i,*} m$  and  $nx \notin \text{set}(\text{sourcenodes } xs)$ 
    by(fastforce elim:obs-intraE)
  obtain  $x' xs'$  where [simp]: $xs = x' \# xs'$ 
  proof(cases xs)
    case Nil
    with ⟨ $\text{sourcenode } a \rightarrow_{i,*} m$ ⟩ have [simp]: $\text{sourcenode } a = m$ 
      by(fastforce simp:intra-path-def)
    with ⟨ $m \in \text{obs-intra}(\text{sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG}$ ⟩
    have  $m \in \lfloor \text{HRB-slice } S \rfloor_{CFG}$  by(fastforce elim:obs-intraE)
    with ⟨ $\text{valid-node } m$ ⟩ have  $\text{obs-intra } m \lfloor \text{HRB-slice } S \rfloor_{CFG} = \{m\}$ 
      by(rule n-in-obs-intra)
    with ⟨ $nx \in \text{obs-intra}(\text{sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG}$ ⟩ ⟨ $nx \neq m$ ⟩ have
      False
      by fastforce
      thus ?thesis by simp
    qed blast
  from ⟨ $\text{sourcenode } a \rightarrow_{i,*} m$ ⟩ have  $\text{sourcenode } a = \text{sourcenode } x'$ 
    and  $\text{valid-edge } x'$  and  $\text{targetnode } x' \rightarrow_{i,*} m$ 
    and  $\text{intra-kind}(\text{kind } x')$ 
    by(auto elim:path-split-Cons simp:intra-path-def)
  from ⟨ $\text{targetnode } x' \rightarrow_{i,*} m$ ⟩ ⟨ $nx \notin \text{set}(\text{sourcenodes } xs)$ ⟩ ⟨ $\text{valid-edge } x'$ ⟩
    ⟨ $\text{valid-node } m$ ⟩ True
  have  $\neg nx \text{ postdominates}(\text{targetnode } x')$ 
    by(fastforce simp:postdominate-def sourcenodes-def)

```



```

show False
proof(cases method-exit nx)
  case True
  from ⟨m ∈ obs-intra (sourcenode a) [HRB-slice S]CFG⟩
    ⟨nx ∈ obs-intra (sourcenode a) [HRB-slice S]CFG⟩
  have get-proc m = get-proc nx
  by(fastforce elim:obs-intraE dest:intra-path-get-procs)
  with ⟨method-exit m⟩ ⟨method-exit nx⟩ have m = nx
  by(rule method-exit-unique)
  with ⟨nx ≠ m⟩ show False by simp
next
case False
with ⟨obs-intra (targetnode a) [HRB-slice S]CFG = {nx}⟩
have nx postdominates (targetnode a)
  by(fastforce intro:obs-intra-postdominate)
from ⟨obs-intra (targetnode a) [HRB-slice S]CFG = {nx}⟩
obtain ys where targetnode a -ys→l* nx
  and ∀ nx' ∈ set(sourcenodes ys). nx' ∉ [HRB-slice S]CFG
  and nx ∈ [HRB-slice S]CFG by(fastforce elim:obs-intraE)
hence nx ∉ set(sourcenodes ys) by fastforce
have sourcenode a ≠ nx
proof
  assume sourcenode a = nx
  from ⟨nx ∈ obs-intra (sourcenode a) [HRB-slice S]CFG⟩
  have nx ∈ [HRB-slice S]CFG by -(erule obs-intraE)
  with ⟨valid-node nx⟩
  have obs-intra nx [HRB-slice S]CFG = {nx} by -(erule n-in-obs-intra)
  with ⟨sourcenode a = nx⟩ ⟨m ∈ obs-intra (sourcenode a) [HRB-slice
S]CFG⟩
    ⟨nx ≠ m⟩ show False by fastforce
qed
with ⟨nx ∉ set(sourcenodes ys)⟩ have nx ∉ set(sourcenodes (a#ys))
  by(fastforce simp:sourcenodes-def)
from ⟨valid-edge a⟩ ⟨targetnode a -ys→l* nx⟩ ⟨intra-kind (kind a)⟩
have sourcenode a -a#ys→l* nx
  by(fastforce intro:Cons-path simp:intra-path-def)
from ⟨sourcenode a -a#ys→l* nx⟩ ⟨nx ∉ set(sourcenodes (a#ys))⟩
  ⟨intra-kind (kind a)⟩ ⟨nx postdominates (targetnode a)⟩
  ⟨valid-edge x'⟩ ⟨intra-kind (kind x')⟩ ⟨¬ nx postdominates (targetnode x')⟩
  ⟨sourcenode a = sourcenode x'⟩
have (sourcenode a) controls nx
  by(fastforce simp:control-dependence-def)
hence CFG-node (sourcenode a) →cd CFG-node nx
  by(fastforce intro:SDG-cdep-edge)
with ⟨nx ∈ [HRB-slice S]CFG⟩ have sourcenode a ∈ [HRB-slice S]CFG
  by(fastforce elim!:combine-SDG-slices.cases
    dest:SDG-edge-sum-SDG-edge cdep-slice1 cdep-slice2
    intro:combine-SDG-slices.intros
    simp:HRB-slice-def SDG-to-CFG-set-def)

```

```

with  $\langle \text{valid-edge } a \rangle$ 
have  $\text{obs-intra } (\text{sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG} = \{\text{sourcenode } a\}$ 
  by(fastforce intro!:n-in-obs-intra)
with  $\langle m \in \text{obs-intra } (\text{sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$ 
   $\langle nx \in \text{obs-intra } (\text{sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG} \langle nx \neq m \rangle$ 
show False by simp
qed
next
case False
with  $\langle m \in \text{obs-intra } (\text{sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$ 
have  $m \text{ postdominates } (\text{sourcenode } a)$  by(rule obs-intra-postdominate)
with  $\langle \neg m \text{ postdominates } (\text{sourcenode } a) \rangle$  show False by simp
qed
qed
qed

```

lemma *obs-intra-finite:valid-node* $n \implies \text{finite } (\text{obs-intra } n \lfloor \text{HRB-slice } S \rfloor_{CFG})$
by(*fastforce dest:obs-intra-singleton-disj[of - S]*)

lemma *obs-intra-singleton:valid-node* $n \implies \text{card } (\text{obs-intra } n \lfloor \text{HRB-slice } S \rfloor_{CFG}) \leq 1$
by(*fastforce dest:obs-intra-singleton-disj[of - S]*)

lemma *obs-intra-singleton-element*:
 $m \in \text{obs-intra } n \lfloor \text{HRB-slice } S \rfloor_{CFG} \implies \text{obs-intra } n \lfloor \text{HRB-slice } S \rfloor_{CFG} = \{m\}$
apply –
apply(*frule in-obs-intra-valid*)
apply(*drule obs-intra-singleton-disj*) **apply** *auto*
done

lemma *obs-intra-the-element*:
 $m \in \text{obs-intra } n \lfloor \text{HRB-slice } S \rfloor_{CFG} \implies (\text{THE } m. m \in \text{obs-intra } n \lfloor \text{HRB-slice } S \rfloor_{CFG}) = m$
by(*fastforce dest:obs-intra-singleton-element*)

lemma *obs-singleton-element*:
assumes $ms \in \text{obs } ns \lfloor \text{HRB-slice } S \rfloor_{CFG}$ **and** $\forall n \in \text{set } (tl \ ns). \text{return-node } n$
shows $\text{obs } ns \lfloor \text{HRB-slice } S \rfloor_{CFG} = \{ms\}$
proof –
from $\langle ms \in \text{obs } ns \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle \langle \forall n \in \text{set } (tl \ ns). \text{return-node } n \rangle$
obtain $nsx \ n \ nsx' \ n'$ **where** $ns = nsx @ n \# nsx'$ **and** $ms = n' \# nsx'$
and $\text{split}: n' \in \text{obs-intra } n \lfloor \text{HRB-slice } S \rfloor_{CFG}$
 $\forall nx \in \text{set } nsx'. \exists nx'. \text{call-of-return-node } nx \ nx' \wedge nx' \in \lfloor \text{HRB-slice } S \rfloor_{CFG}$
 $\forall xs \ x \ xs'. nsx = xs @ x \# xs' \wedge \text{obs-intra } x \lfloor \text{HRB-slice } S \rfloor_{CFG} \neq \{\}$

$\longrightarrow (\exists x'' \in \text{set } (xs'@[n]). \exists nx. \text{call-of-return-node } x'' \ nx \wedge$
 $nx \notin \llbracket \text{HRB-slice } S \rrbracket_{CFG}$)

by(erule obsE)

from $\langle n' \in \text{obs-intra } n \llbracket \text{HRB-slice } S \rrbracket_{CFG} \rangle$

have $\text{obs-intra } n \llbracket \text{HRB-slice } S \rrbracket_{CFG} = \{n'\}$

by(fastforce intro!:obs-intra-singleton-element)

{ fix } xs **assume $xs \neq ms$ **and** $xs \in \text{obs } ns \llbracket \text{HRB-slice } S \rrbracket_{CFG}$**

from $\langle xs \in \text{obs } ns \llbracket \text{HRB-slice } S \rrbracket_{CFG} \rangle \langle \forall n \in \text{set } (tl \ ns). \text{return-node } n \rangle$

obtain $zs \ z \ zs' \ z'$ **where** $ns = zs@z\#zs'$ **and** $xs = z'\#zs'$

and $z' \in \text{obs-intra } z \llbracket \text{HRB-slice } S \rrbracket_{CFG}$

and $\forall z' \in \text{set } zs'. \exists nx'. \text{call-of-return-node } z' \ nx' \wedge nx' \in \llbracket \text{HRB-slice } S \rrbracket_{CFG}$

and $\forall xs \ x \ xs'. zs = xs@x\#xs' \wedge \text{obs-intra } x \llbracket \text{HRB-slice } S \rrbracket_{CFG} \neq \{\}$

$\longrightarrow (\exists x'' \in \text{set } (xs'@[z]). \exists nx. \text{call-of-return-node } x'' \ nx \wedge$
 $nx \notin \llbracket \text{HRB-slice } S \rrbracket_{CFG}$)

by(erule obsE)

with $\langle ns = nsx@n\#nsx' \rangle \text{split}$

have $nsx = zs \wedge n = z \wedge nsx' = zs'$

by $-(\text{rule obs-split-det}[\text{of } \text{-----} \llbracket \text{HRB-slice } S \rrbracket_{CFG} \rrbracket, \text{fastforce}+])$

with $\langle \text{obs-intra } n \llbracket \text{HRB-slice } S \rrbracket_{CFG} = \{n'\} \rangle \langle z' \in \text{obs-intra } z \llbracket \text{HRB-slice}$
 $S \rrbracket_{CFG} \rangle$

have $z' = n'$ **by** simp

with $\langle xs \neq ms \rangle \langle ms = n'\#nsx' \rangle \langle xs = z'\#zs' \rangle \langle nsx = zs \wedge n = z \wedge nsx' = zs' \rangle$

have *False* **by** simp }

with $\langle ms \in \text{obs } ns \llbracket \text{HRB-slice } S \rrbracket_{CFG} \rangle$ **show** ?thesis **by** fastforce

qed

lemma obs-finite: $\forall n \in \text{set } (tl \ ns). \text{return-node } n$
 $\implies \text{finite } (\text{obs } ns \llbracket \text{HRB-slice } S \rrbracket_{CFG})$

by(cases obs ns $\llbracket \text{HRB-slice } S \rrbracket_{CFG} = \{\}$, auto dest:obs-singleton-element[of - - S])

lemma obs-singleton: $\forall n \in \text{set } (tl \ ns). \text{return-node } n$
 $\implies \text{card } (\text{obs } ns \llbracket \text{HRB-slice } S \rrbracket_{CFG}) \leq 1$

by(cases obs ns $\llbracket \text{HRB-slice } S \rrbracket_{CFG} = \{\}$, auto dest:obs-singleton-element[of - - S])

lemma obs-the-element:

$\llbracket ms \in \text{obs } ns \llbracket \text{HRB-slice } S \rrbracket_{CFG}; \forall n \in \text{set } (tl \ ns). \text{return-node } n \rrbracket$
 $\implies (\text{THE } ms. ms \in \text{obs } ns \llbracket \text{HRB-slice } S \rrbracket_{CFG}) = ms$

by(cases obs ns $\llbracket \text{HRB-slice } S \rrbracket_{CFG} = \{\}$, auto dest:obs-singleton-element[of - - S])

end

end

1.11 Distance of Paths

theory *Distance* **imports** CFG **begin**

context *CFG* **begin**

inductive *distance* :: 'node \Rightarrow 'node \Rightarrow nat \Rightarrow bool

where *distanceI*:

$\llbracket n - as \rightarrow_i^* n'; \text{length } as = x; \forall as'. n - as' \rightarrow_i^* n' \longrightarrow x \leq \text{length } as \rrbracket$
 $\Longrightarrow \text{distance } n \ n' \ x$

lemma *every-path-distance*:

assumes $n - as \rightarrow_i^* n'$

obtains x **where** $\text{distance } n \ n' \ x$ **and** $x \leq \text{length } as$

proof(*atomize-elim*)

show $\exists x. \text{distance } n \ n' \ x \wedge x \leq \text{length } as$

proof(*cases* $\exists as'. n - as' \rightarrow_i^* n' \wedge$

$(\forall asx. n - asx \rightarrow_i^* n' \longrightarrow \text{length } as' \leq \text{length } asx)$)

case *True*

then obtain as'

where $n - as' \rightarrow_i^* n' \wedge (\forall asx. n - asx \rightarrow_i^* n' \longrightarrow \text{length } as' \leq \text{length } asx)$

by *blast*

hence $n - as' \rightarrow_i^* n'$ **and** $all: \forall asx. n - asx \rightarrow_i^* n' \longrightarrow \text{length } as' \leq \text{length } asx$

by *simp-all*

hence $\text{distance } n \ n' \ (\text{length } as')$ **by**(*fastforce intro:distanceI*)

from $\langle n - as \rightarrow_i^* n' \rangle$ **all have** $\text{length } as' \leq \text{length } as$ **by** *fastforce*

with $\langle \text{distance } n \ n' \ (\text{length } as') \rangle$ **show** *?thesis* **by** *blast*

next

case *False*

hence $all: \forall as'. n - as' \rightarrow_i^* n' \longrightarrow (\exists asx. n - asx \rightarrow_i^* n' \wedge \text{length } as' > \text{length } asx)$

by *fastforce*

have $wf \ (\text{measure } \text{length})$ **by** *simp*

from $\langle n - as \rightarrow_i^* n' \rangle$ **have** $as \in \{as. n - as \rightarrow_i^* n'\}$ **by** *simp*

with $\langle wf \ (\text{measure } \text{length}) \rangle$ **obtain** as' **where** $as' \in \{as. n - as \rightarrow_i^* n'\}$

and $notin: \wedge as''. (as'', as') \in (\text{measure } \text{length}) \Longrightarrow as'' \notin \{as. n - as \rightarrow_i^* n'\}$

by(*erule wfE-min*)

from $\langle as' \in \{as. n - as \rightarrow_i^* n'\} \rangle$ **have** $n - as' \rightarrow_i^* n'$ **by** *simp*

with all **obtain** asx **where** $n - asx \rightarrow_i^* n'$

and $\text{length } as' > \text{length } asx$

by *blast*

with $notin$ **have** $asx \notin \{as. n - as \rightarrow_i^* n'\}$ **by** *simp*

hence $\neg n - asx \rightarrow_i^* n'$ **by** *simp*

with $\langle n - asx \rightarrow_i^* n' \rangle$ **have** *False* **by** *simp*

thus *?thesis* **by** *simp*

qed

qed

lemma *distance-det*:

$\llbracket \text{distance } n \ n' \ x; \text{distance } n \ n' \ x' \rrbracket \Longrightarrow x = x'$

apply(*erule distance.cases*)**+** **apply** *clarsimp*

apply(*erule-tac x=asa in allE*) **apply**(*erule-tac x=as in allE*)
by simp

lemma *only-one-SOME-dist-edge*:

assumes *valid-edge a* **and** *intra-kind(kind a)* **and** *distance (targetnode a) n' x*
shows $\exists! a'. \text{sourcenode } a = \text{sourcenode } a' \wedge \text{distance (targetnode } a') n' x \wedge$
valid-edge a' \wedge intra-kind(kind a') \wedge
 $\text{targetnode } a' = (\text{SOME } nx. \exists a'. \text{sourcenode } a = \text{sourcenode } a' \wedge$
 $\text{distance (targetnode } a') n' x \wedge$
 $\text{valid-edge } a' \wedge \text{intra-kind(kind } a') \wedge$
 $\text{targetnode } a' = nx)$

proof(*rule ex-ex1I*)

show $\exists a'. \text{sourcenode } a = \text{sourcenode } a' \wedge$
 $\text{distance (targetnode } a') n' x \wedge \text{valid-edge } a' \wedge \text{intra-kind(kind } a') \wedge$
 $\text{targetnode } a' = (\text{SOME } nx. \exists a'. \text{sourcenode } a = \text{sourcenode } a' \wedge$
 $\text{distance (targetnode } a') n' x \wedge$
 $\text{valid-edge } a' \wedge \text{intra-kind(kind } a') \wedge$
 $\text{targetnode } a' = nx)$

proof –

have $(\exists a'. \text{sourcenode } a = \text{sourcenode } a' \wedge$
 $\text{distance (targetnode } a') n' x \wedge \text{valid-edge } a' \wedge \text{intra-kind(kind } a') \wedge$
 $\text{targetnode } a' = (\text{SOME } nx. \exists a'. \text{sourcenode } a = \text{sourcenode } a' \wedge$
 $\text{distance (targetnode } a') n' x \wedge$
 $\text{valid-edge } a' \wedge \text{intra-kind(kind } a') \wedge$
 $\text{targetnode } a' = nx)) =$

$(\exists nx. \exists a'. \text{sourcenode } a = \text{sourcenode } a' \wedge \text{distance (targetnode } a') n' x \wedge$
 $\text{valid-edge } a' \wedge \text{intra-kind(kind } a') \wedge \text{targetnode } a' = nx)$

apply(*unfold some-eq-ex[of $\lambda nx. \exists a'. \text{sourcenode } a = \text{sourcenode } a' \wedge$
 $\text{distance (targetnode } a') n' x \wedge \text{valid-edge } a' \wedge \text{intra-kind(kind } a') \wedge$
 $\text{targetnode } a' = nx]$)*

by simp

also have ...

using $\langle \text{valid-edge } a \rangle \langle \text{intra-kind(kind } a) \rangle \langle \text{distance (targetnode } a) n' x \rangle$

by blast

finally show *?thesis* .

qed

next

fix $a' ax$

assume $\text{sourcenode } a = \text{sourcenode } a' \wedge$
 $\text{distance (targetnode } a') n' x \wedge \text{valid-edge } a' \wedge \text{intra-kind(kind } a') \wedge$
 $\text{targetnode } a' = (\text{SOME } nx. \exists a'. \text{sourcenode } a = \text{sourcenode } a' \wedge$
 $\text{distance (targetnode } a') n' x \wedge$
 $\text{valid-edge } a' \wedge \text{intra-kind(kind } a') \wedge$
 $\text{targetnode } a' = nx)$

and $\text{sourcenode } a = \text{sourcenode } ax \wedge$

$\text{distance (targetnode } ax) n' x \wedge \text{valid-edge } ax \wedge \text{intra-kind(kind } ax) \wedge$
 $\text{targetnode } ax = (\text{SOME } nx. \exists a'. \text{sourcenode } a = \text{sourcenode } a' \wedge$
 $\text{distance (targetnode } a') n' x \wedge$

$valid-edge\ a' \wedge intra-kind(kind\ a') \wedge$
 $targetnode\ a' = nx$

thus $a' = ax$ **by**(*fastforce intro!:edge-det*)
qed

lemma *distance-successor-distance*:
assumes $distance\ n\ n'\ x$ **and** $x \neq 0$
obtains a **where** $valid-edge\ a$ **and** $n = sourcenode\ a$ **and** $intra-kind(kind\ a)$
and $distance\ (targetnode\ a)\ n'\ (x - 1)$
and $targetnode\ a = (SOME\ nx.\ \exists a'.\ sourcenode\ a = sourcenode\ a' \wedge$
 $distance\ (targetnode\ a')\ n'\ (x - 1) \wedge$
 $valid-edge\ a' \wedge intra-kind(kind\ a') \wedge$
 $targetnode\ a' = nx)$

proof(*atomize-elim*)
show $\exists a.\ valid-edge\ a \wedge n = sourcenode\ a \wedge intra-kind(kind\ a) \wedge$
 $distance\ (targetnode\ a)\ n'\ (x - 1) \wedge$
 $targetnode\ a = (SOME\ nx.\ \exists a'.\ sourcenode\ a = sourcenode\ a' \wedge$
 $distance\ (targetnode\ a')\ n'\ (x - 1) \wedge$
 $valid-edge\ a' \wedge intra-kind(kind\ a') \wedge$
 $targetnode\ a' = nx)$

proof(*rule ccontr*)
assume $\neg (\exists a.\ valid-edge\ a \wedge n = sourcenode\ a \wedge intra-kind(kind\ a) \wedge$
 $distance\ (targetnode\ a)\ n'\ (x - 1) \wedge$
 $targetnode\ a = (SOME\ nx.\ \exists a'.\ sourcenode\ a = sourcenode\ a' \wedge$
 $distance\ (targetnode\ a')\ n'\ (x - 1) \wedge$
 $valid-edge\ a' \wedge intra-kind(kind\ a') \wedge$
 $targetnode\ a' = nx))$

hence $imp:\forall a.\ valid-edge\ a \wedge n = sourcenode\ a \wedge intra-kind(kind\ a) \wedge$
 $targetnode\ a = (SOME\ nx.\ \exists a'.\ sourcenode\ a = sourcenode\ a' \wedge$
 $distance\ (targetnode\ a')\ n'\ (x - 1) \wedge$
 $valid-edge\ a' \wedge intra-kind(kind\ a') \wedge$
 $targetnode\ a' = nx)$
 $\longrightarrow \neg distance\ (targetnode\ a)\ n'\ (x - 1)$ **by** *blast*

from $\langle distance\ n\ n'\ x \rangle$ **obtain** as **where** $n -as \rightarrow_i^* n'$ **and** $x = length\ as$
and $all:\forall as'.\ n -as' \rightarrow_i^* n' \longrightarrow x \leq length\ as'$
by(*auto elim:distance.cases*)

from $\langle n -as \rightarrow_i^* n' \rangle$ **have** $n -as \rightarrow^* n'$ **and** $\forall a \in set\ as.\ intra-kind(kind\ a)$
by(*simp-all add:intra-path-def*)

from $this\ \langle x = length\ as \rangle$ **all** imp **show** *False*

proof(*induct rule:path.induct*)
case (*empty-path n*)
from $\langle x = length\ [] \rangle\ \langle x \neq 0 \rangle$ **show** *False* **by** *simp*

next
case (*Cons-path n'' as n' a n*)
note $imp = \langle \forall a.\ valid-edge\ a \wedge n = sourcenode\ a \wedge intra-kind(kind\ a) \wedge$
 $targetnode\ a = (SOME\ nx.\ \exists a'.\ sourcenode\ a = sourcenode\ a' \wedge$
 $distance\ (targetnode\ a')\ n'\ (x - 1) \wedge$
 $valid-edge\ a' \wedge intra-kind(kind\ a') \wedge$

```

                                targetnode a' = nx)
                                → ¬ distance (targetnode a) n' (x - 1)
note all = ⟨∀ as'. n - as' →i* n' → x ≤ length as'⟩
from ⟨∀ a ∈ set (a # as). intra-kind (kind a)⟩
have intra-kind (kind a) and ∀ a ∈ set as. intra-kind (kind a)
  by simp-all
from ⟨n'' - as →* n'⟩ ⟨∀ a ∈ set as. intra-kind (kind a)⟩
have n'' - as →i* n' by (simp add: intra-path-def)
then obtain y where distance n'' n' y
  and y ≤ length as by (erule every-path-distance)
from ⟨distance n'' n' y⟩ obtain as' where n'' - as' →i* n'
  and y = length as' by (auto elim: distance.cases)
hence n'' - as' →* n' and ∀ a ∈ set as'. intra-kind (kind a)
  by (simp-all add: intra-path-def)
show False
proof (cases y < length as)
  case True
from ⟨valid-edge a⟩ ⟨sourcnode a = n⟩ ⟨targetnode a = n''⟩ ⟨n'' - as' →* n'⟩
have n - a # as' →* n' by -(rule path.Cons-path)
with ⟨∀ a ∈ set as'. intra-kind (kind a)⟩ ⟨intra-kind (kind a)⟩
have n - a # as' →i* n' by (simp add: intra-path-def)
with all have x ≤ length (a # as') by blast
with ⟨x = length (a # as)⟩ True ⟨y = length as'⟩ show False by simp
next
  case False
with ⟨y ≤ length as⟩ ⟨x = length (a # as)⟩ have y = x - 1 by simp
from ⟨targetnode a = n''⟩ ⟨distance n'' n' y⟩
have distance (targetnode a) n' y by simp
with ⟨valid-edge a⟩ ⟨intra-kind (kind a)⟩
obtain a' where sourcnode a = sourcnode a'
  and distance (targetnode a') n' y and valid-edge a'
  and intra-kind (kind a')
  and targetnode a' = (SOME nx. ∃ a'. sourcnode a = sourcnode a' ∧
    distance (targetnode a') n' y ∧
    valid-edge a' ∧ intra-kind (kind a') ∧
    targetnode a' = nx)
  by (auto dest: only-one-SOME-dist-edge)
with imp ⟨sourcnode a = n⟩ ⟨y = x - 1⟩ show False by fastforce
qed
qed
qed
end
end

```

1.12 Static backward slice

theory *Slice* **imports** *SCDObservable Distance* **begin**

context *SDG* **begin**

1.12.1 Preliminary definitions on the parameter nodes for defining sliced call and return edges

fun *csppa* :: 'node \Rightarrow 'node *SDG-node set* \Rightarrow nat \Rightarrow
 ((('var \rightarrow 'val) \Rightarrow 'val option) list) \Rightarrow ((('var \rightarrow 'val) \Rightarrow 'val option) list)
where *csppa* *m S x* [] = []
 | *csppa* *m S x* (f#fs) =
 (if *Formal-in*(*m,x*) \notin *S* then empty else f)#*csppa* *m S* (*Suc* x) fs

definition *cspp* :: 'node \Rightarrow 'node *SDG-node set* \Rightarrow
 ((('var \rightarrow 'val) \Rightarrow 'val option) list) \Rightarrow ((('var \rightarrow 'val) \Rightarrow 'val option) list)
where *cspp* *m S fs* \equiv *csppa* *m S* 0 fs

lemma [*simp*]: *length* (*csppa* *m S x fs*) = *length* fs
by(*induct* fs *arbitrary:x*)(*auto*)

lemma [*simp*]: *length* (*cspp* *m S fs*) = *length* fs
by(*simp* *add:cspp-def*)

lemma *csppa-Formal-in-notin-slice*:
 [$x < \text{length } fs; \text{Formal-in}(m, x + i) \notin S$]
 \implies (*csppa* *m S i fs*)!x = empty
by(*induct* fs *arbitrary:i x,auto simp:nth-Cons*)

lemma *csppa-Formal-in-in-slice*:
 [$x < \text{length } fs; \text{Formal-in}(m, x + i) \in S$]
 \implies (*csppa* *m S i fs*)!x = fs!x
by(*induct* fs *arbitrary:i x,auto simp:nth-Cons*)

definition *map-merge* :: ('var \rightarrow 'val) \Rightarrow ('var \rightarrow 'val) \Rightarrow (nat \Rightarrow bool) \Rightarrow
 'var list \Rightarrow ('var \rightarrow 'val)
where *map-merge* f g Q xs \equiv ($\lambda V. \text{if } (\exists i. i < \text{length } xs \wedge xs!i = V \wedge Q i)$ then
 g V
 else f V)

definition *rspp* :: 'node \Rightarrow 'node *SDG-node set* \Rightarrow 'var list \Rightarrow
 ('var \rightarrow 'val) \Rightarrow ('var \rightarrow 'val) \Rightarrow ('var \rightarrow 'val)
where *rspp* *m S xs f g* \equiv *map-merge* f (empty(*ParamDefs* *m* [:=] *map* g xs))
 ($\lambda i. \text{Actual-out}(m,i) \in S$) (*ParamDefs* *m*)

lemma *rspp-Actual-out-in-slice*:

assumes $x < \text{length } (\text{ParamDefs } (\text{targetnode } a))$ **and** *valid-edge* a
and $\text{length } (\text{ParamDefs } (\text{targetnode } a)) = \text{length } xs$
and $\text{Actual-out } (\text{targetnode } a, x) \in S$
shows $(\text{rspp } (\text{targetnode } a) S xs f g) ((\text{ParamDefs } (\text{targetnode } a))!x) = g(xs!x)$

proof –

from $\langle \text{valid-edge } a \rangle$ **have** $\text{distinct}(\text{ParamDefs } (\text{targetnode } a))$
by(*rule distinct-ParamDefs*)
from $\langle x < \text{length } (\text{ParamDefs } (\text{targetnode } a)) \rangle$
 $\langle \text{length } (\text{ParamDefs } (\text{targetnode } a)) = \text{length } xs \rangle$
 $\langle \text{distinct}(\text{ParamDefs } (\text{targetnode } a)) \rangle$
have $(\text{empty}(\text{ParamDefs } (\text{targetnode } a) [:=] \text{map } g xs))$
 $((\text{ParamDefs } (\text{targetnode } a))!x) = (\text{map } g xs)!x$
by(*fastforce intro:fun-upds-nth*)
with $\langle \text{Actual-out}(\text{targetnode } a, x) \in S \rangle$ $\langle x < \text{length } (\text{ParamDefs } (\text{targetnode } a)) \rangle$
 $\langle \text{length } (\text{ParamDefs } (\text{targetnode } a)) = \text{length } xs \rangle$ **show** *?thesis*
by(*fastforce simp:rspp-def map-merge-def*)

qed

lemma *rspp-Actual-out-notin-slice*:

assumes $x < \text{length } (\text{ParamDefs } (\text{targetnode } a))$ **and** *valid-edge* a
and $\text{length } (\text{ParamDefs } (\text{targetnode } a)) = \text{length } xs$
and $\text{Actual-out}((\text{targetnode } a), x) \notin S$
shows $(\text{rspp } (\text{targetnode } a) S xs f g) ((\text{ParamDefs } (\text{targetnode } a))!x) =$
 $f((\text{ParamDefs } (\text{targetnode } a))!x)$

proof –

from $\langle \text{valid-edge } a \rangle$ **have** $\text{distinct}(\text{ParamDefs } (\text{targetnode } a))$
by(*rule distinct-ParamDefs*)
from $\langle x < \text{length } (\text{ParamDefs } (\text{targetnode } a)) \rangle$
 $\langle \text{length } (\text{ParamDefs } (\text{targetnode } a)) = \text{length } xs \rangle$
 $\langle \text{distinct}(\text{ParamDefs } (\text{targetnode } a)) \rangle$
have $(\text{empty}(\text{ParamDefs } (\text{targetnode } a) [:=] \text{map } g xs))$
 $((\text{ParamDefs } (\text{targetnode } a))!x) = (\text{map } g xs)!x$
by(*fastforce intro:fun-upds-nth*)
with $\langle \text{Actual-out}((\text{targetnode } a), x) \notin S \rangle$ $\langle \text{distinct}(\text{ParamDefs } (\text{targetnode } a)) \rangle$
 $\langle x < \text{length } (\text{ParamDefs } (\text{targetnode } a)) \rangle$
show *?thesis* **by**(*fastforce simp:rspp-def map-merge-def nth-eq-iff-index-eq*)

qed

1.12.2 Defining the sliced edge kinds

primrec *slice-kind-aux* :: $'node \Rightarrow 'node \Rightarrow 'node \text{ SDG-node set} \Rightarrow$
 $('var, 'val, 'ret, 'pname) \text{ edge-kind} \Rightarrow ('var, 'val, 'ret, 'pname) \text{ edge-kind}$
where $\text{slice-kind-aux } m m' S \uparrow f = (\text{if } m \in \lfloor S \rfloor_{CFG} \text{ then } \uparrow f \text{ else } \uparrow id)$
 $\lfloor \text{slice-kind-aux } m m' S (Q)_{\surd} = (\text{if } m \in \lfloor S \rfloor_{CFG} \text{ then } (Q)_{\surd} \text{ else}$
 $(\text{if } \text{obs-intra } m \lfloor S \rfloor_{CFG} = \{\} \text{ then}$
 $(\text{let } mex = (\text{THE } mex. \text{method-exit } mex \wedge \text{get-proc } m = \text{get-proc } mex) \text{ in}$
 $(\text{if } (\exists x. \text{distance } m' mex x \wedge \text{distance } m mex (x + 1) \wedge$
 $(m' = (\text{SOME } mx'. \exists a'. m = \text{sourcenode } a' \wedge$

$$\begin{aligned}
& \text{distance } (\text{targetnode } a') \text{ mex } x \wedge \\
& \text{valid-edge } a' \wedge \text{intra-kind}(\text{kind } a') \wedge \\
& \text{targetnode } a' = \text{mx}') \\
& \text{then } (\lambda cf. \text{True})_{\surd} \text{ else } (\lambda cf. \text{False})_{\surd}) \\
& \text{else } (\text{let } \text{mx} = \text{THE } \text{mx}. \text{mx} \in \text{obs-intra } m \ [S]_{CFG} \text{ in} \\
& \quad (\text{if } (\exists x. \text{distance } m' \text{ mx } x \wedge \text{distance } m \ \text{mx} \ (x + 1) \wedge \\
& \quad \quad (m' = (\text{SOME } \text{mx}'. \exists a'. m = \text{sourcenode } a' \wedge \\
& \quad \quad \quad \text{distance } (\text{targetnode } a') \ \text{mx } x \wedge \\
& \quad \quad \quad \text{valid-edge } a' \wedge \text{intra-kind}(\text{kind } a') \wedge \\
& \quad \quad \quad \text{targetnode } a' = \text{mx}')) \\
& \quad \text{then } (\lambda cf. \text{True})_{\surd} \text{ else } (\lambda cf. \text{False})_{\surd}))) \\
& | \text{slice-kind-aux } m \ m' \ S \ (Q:r \leftrightarrow_p fs) = (\text{if } m \in [S]_{CFG} \text{ then } (Q:r \leftrightarrow_p (\text{cspp } m' \ S \\
& fs)) \\
& \quad \quad \quad \text{else } ((\lambda cf. \text{False}):r \leftrightarrow_p fs)) \\
& | \text{slice-kind-aux } m \ m' \ S \ (Q \leftrightarrow_p f) = (\text{if } m \in [S]_{CFG} \text{ then} \\
& \quad (\text{let } \text{outs} = \text{THE } \text{outs}. \exists \text{ins}. (p, \text{ins}, \text{outs}) \in \text{set procs in} \\
& \quad \quad (Q \leftrightarrow_p (\lambda cf \ cf'. \text{rspp } m' \ S \ \text{outs } cf' \ cf))) \\
& \quad \text{else } ((\lambda cf. \text{True}) \leftrightarrow_p (\lambda cf \ cf'. \ cf')))
\end{aligned}$$

definition *slice-kind* :: 'node SDG-node set \Rightarrow 'edge \Rightarrow
('var,'val,'ret,'pname) edge-kind
where *slice-kind* $S \ a \equiv$
slice-kind-aux (sourcenode a) (targetnode a) (HRB-slice S) (kind a)

definition *slice-kinds* :: 'node SDG-node set \Rightarrow 'edge list \Rightarrow
('var,'val,'ret,'pname) edge-kind list
where *slice-kinds* $S \ as \equiv \text{map } (\text{slice-kind } S) \ as$

lemma *slice-intra-kind-in-slice*:
 $[\text{sourcenode } a \in [HRB\text{-slice } S]_{CFG}; \text{intra-kind } (\text{kind } a)]$
 $\implies \text{slice-kind } S \ a = \text{kind } a$
by(fastforce simp:intra-kind-def slice-kind-def)

lemma *slice-kind-Upd*:
 $[\text{sourcenode } a \notin [HRB\text{-slice } S]_{CFG}; \text{kind } a = \uparrow f] \implies \text{slice-kind } S \ a = \uparrow id$
by(simp add:slice-kind-def)

lemma *slice-kind-Pred-empty-obs-nearer-SOME*:
assumes $\text{sourcenode } a \notin [HRB\text{-slice } S]_{CFG}$ **and** $\text{kind } a = (Q)_{\surd}$
and $\text{obs-intra } (\text{sourcenode } a) [HRB\text{-slice } S]_{CFG} = \{\}$
and $\text{method-exit } \text{mex}$ **and** $\text{get-proc } (\text{sourcenode } a) = \text{get-proc } \text{mex}$
and $\text{distance } (\text{targetnode } a) \ \text{mex } x$ **and** $\text{distance } (\text{sourcenode } a) \ \text{mex } (x + 1)$
and $\text{targetnode } a = (\text{SOME } n'. \exists a'. \text{sourcenode } a = \text{sourcenode } a' \wedge$
 $\quad \quad \quad \text{distance } (\text{targetnode } a') \ \text{mex } x \wedge$
 $\quad \quad \quad \text{valid-edge } a' \wedge \text{intra-kind}(\text{kind } a') \wedge$

$targetnode\ a' = n'$

shows $slice\text{-}kind\ S\ a = (\lambda s. True)_{\checkmark}$

proof –

from $\langle method\text{-}exit\ mex \rangle \langle get\text{-}proc\ (sourcnode\ a) = get\text{-}proc\ mex \rangle$

have $mex = (THE\ mex.\ method\text{-}exit\ mex \wedge get\text{-}proc\ (sourcnode\ a) = get\text{-}proc\ mex)$

by $(auto\ intro!:\textit{the-equality}[THEN\ sym]\ intro:\textit{method-exit-unique})$

with $\langle sourcnode\ a \notin [HRB\text{-}slice\ S]_{CFG} \rangle \langle kind\ a = (Q)_{\checkmark} \rangle$

$\langle obs\text{-}intra\ (sourcnode\ a)\ [HRB\text{-}slice\ S]_{CFG} = \{\} \rangle$

have $slice\text{-}kind\ S\ a =$

$(if\ (\exists x.\ distance\ (targetnode\ a)\ mex\ x \wedge distance\ (sourcnode\ a)\ mex\ (x + 1))$

\wedge

$(targetnode\ a = (SOME\ mx'. \exists a'. sourcnode\ a = sourcnode\ a' \wedge$

$distance\ (targetnode\ a')\ mex\ x \wedge valid\text{-}edge\ a' \wedge intra\text{-}kind(kind\ a') \wedge$

$targetnode\ a' = mx'))\ then\ (\lambda cf.\ True)_{\checkmark}\ else\ (\lambda cf.\ False)_{\checkmark})$

by $(simp\ add:\textit{slice-kind-def}\ Let\text{-}def)$

with $\langle distance\ (targetnode\ a)\ mex\ x \rangle \langle distance\ (sourcnode\ a)\ mex\ (x + 1) \rangle$

$\langle targetnode\ a = (SOME\ n'. \exists a'. sourcnode\ a = sourcnode\ a' \wedge$

$distance\ (targetnode\ a')\ mex\ x \wedge$

$valid\text{-}edge\ a' \wedge intra\text{-}kind(kind\ a') \wedge$

$targetnode\ a' = n') \rangle$

show $?thesis$ **by** $fastforce$

qed

lemma $slice\text{-}kind\text{-}Pred\text{-}empty\text{-}obs\text{-}nearer\text{-}not\text{-}SOME$:

assumes $sourcnode\ a \notin [HRB\text{-}slice\ S]_{CFG}$ **and** $kind\ a = (Q)_{\checkmark}$

and $obs\text{-}intra\ (sourcnode\ a)\ [HRB\text{-}slice\ S]_{CFG} = \{\}$

and $method\text{-}exit\ mex$ **and** $get\text{-}proc\ (sourcnode\ a) = get\text{-}proc\ mex$

and $distance\ (targetnode\ a)\ mex\ x$ **and** $distance\ (sourcnode\ a)\ mex\ (x + 1)$

and $targetnode\ a \neq (SOME\ n'. \exists a'. sourcnode\ a = sourcnode\ a' \wedge$

$distance\ (targetnode\ a')\ mex\ x \wedge$

$valid\text{-}edge\ a' \wedge intra\text{-}kind(kind\ a') \wedge$

$targetnode\ a' = n')$

shows $slice\text{-}kind\ S\ a = (\lambda s. False)_{\checkmark}$

proof –

from $\langle method\text{-}exit\ mex \rangle \langle get\text{-}proc\ (sourcnode\ a) = get\text{-}proc\ mex \rangle$

have $mex = (THE\ mex.\ method\text{-}exit\ mex \wedge get\text{-}proc\ (sourcnode\ a) = get\text{-}proc\ mex)$

by $(auto\ intro!:\textit{the-equality}[THEN\ sym]\ intro:\textit{method-exit-unique})$

with $\langle sourcnode\ a \notin [HRB\text{-}slice\ S]_{CFG} \rangle \langle kind\ a = (Q)_{\checkmark} \rangle$

$\langle obs\text{-}intra\ (sourcnode\ a)\ [HRB\text{-}slice\ S]_{CFG} = \{\} \rangle$

have $slice\text{-}kind\ S\ a =$

$(if\ (\exists x.\ distance\ (targetnode\ a)\ mex\ x \wedge distance\ (sourcnode\ a)\ mex\ (x + 1))$

\wedge

$(targetnode\ a = (SOME\ mx'. \exists a'. sourcnode\ a = sourcnode\ a' \wedge$

$distance\ (targetnode\ a')\ mex\ x \wedge valid\text{-}edge\ a' \wedge intra\text{-}kind(kind\ a') \wedge$

$targetnode\ a' = mx'))\ then\ (\lambda cf.\ True)_{\checkmark}\ else\ (\lambda cf.\ False)_{\checkmark})$

by $(simp\ add:\textit{slice-kind-def}\ Let\text{-}def)$

with $\langle \text{distance (targetnode } a) \text{ mex } x \rangle \langle \text{distance (sourcenode } a) \text{ mex } (x + 1) \rangle$
 $\langle \text{targetnode } a \neq (\text{SOME } n'. \exists a'. \text{sourcenode } a = \text{sourcenode } a' \wedge$
 $\text{distance (targetnode } a') \text{ mex } x \wedge$
 $\text{valid-edge } a' \wedge \text{intra-kind}(\text{kind } a') \wedge$
 $\text{targetnode } a' = n') \rangle$
show *?thesis* **by**(*auto dest:distance-det*)
qed

lemma *slice-kind-Pred-empty-obs-not-nearer:*

assumes $\text{sourcenode } a \notin [\text{HRB-slice } S]_{\text{CFG}}$ **and** $\text{kind } a = (Q)_{\checkmark}$
and $\text{obs-intra (sourcenode } a) [\text{HRB-slice } S]_{\text{CFG}} = \{\}$
and method-exit mex **and** $\text{get-proc (sourcenode } a) = \text{get-proc mex}$
and $\text{dist:distance (sourcenode } a) \text{ mex } (x + 1) \neg \text{distance (targetnode } a) \text{ mex } x$
shows $\text{slice-kind } S \ a = (\lambda s. \text{False})_{\checkmark}$

proof –

from $\langle \text{method-exit mex} \rangle \langle \text{get-proc (sourcenode } a) = \text{get-proc mex} \rangle$
have $\text{mex} = (\text{THE } \text{mex}. \text{method-exit } \text{mex} \wedge \text{get-proc (sourcenode } a) = \text{get-proc}$
 $\text{mex})$
by(*auto intro!:the-equality[THEN sym] intro:method-exit-unique*)
moreover
from **dist** **have** $\neg (\exists x. \text{distance (targetnode } a) \text{ mex } x \wedge$
 $\text{distance (sourcenode } a) \text{ mex } (x + 1))$
by(*fastforce dest:distance-det*)
ultimately show *?thesis* **using** *assms* **by**(*auto simp:slice-kind-def Let-def*)
qed

lemma *slice-kind-Pred-obs-nearer-SOME:*

assumes $\text{sourcenode } a \notin [\text{HRB-slice } S]_{\text{CFG}}$ **and** $\text{kind } a = (Q)_{\checkmark}$
and $m \in \text{obs-intra (sourcenode } a) [\text{HRB-slice } S]_{\text{CFG}}$
and $\text{distance (targetnode } a) \ m \ x \ \text{distance (sourcenode } a) \ m \ (x + 1)$
and $\text{targetnode } a = (\text{SOME } n'. \exists a'. \text{sourcenode } a = \text{sourcenode } a' \wedge$
 $\text{distance (targetnode } a') \ m \ x \wedge$
 $\text{valid-edge } a' \wedge \text{intra-kind}(\text{kind } a') \wedge$
 $\text{targetnode } a' = n')$
shows $\text{slice-kind } S \ a = (\lambda s. \text{True})_{\checkmark}$

proof –

from $\langle m \in \text{obs-intra (sourcenode } a) [\text{HRB-slice } S]_{\text{CFG}} \rangle$
have $m = (\text{THE } m. m \in \text{obs-intra (sourcenode } a) [\text{HRB-slice } S]_{\text{CFG}})$
by(*rule obs-intra-the-element[THEN sym]*)
with *assms* **show** *?thesis* **by**(*auto simp:slice-kind-def Let-def*)
qed

lemma *slice-kind-Pred-obs-nearer-not-SOME:*

assumes $\text{sourcenode } a \notin [\text{HRB-slice } S]_{\text{CFG}}$ **and** $\text{kind } a = (Q)_{\checkmark}$
and $m \in \text{obs-intra (sourcenode } a) [\text{HRB-slice } S]_{\text{CFG}}$
and $\text{distance (targetnode } a) \ m \ x \ \text{distance (sourcenode } a) \ m \ (x + 1)$

and *targetnode* $a \neq$ (*SOME* $nx'. \exists a'. \text{sourcenode } a = \text{sourcenode } a' \wedge$
 $\text{distance } (\text{targetnode } a') m x \wedge$
 $\text{valid-edge } a' \wedge \text{intra-kind}(\text{kind } a') \wedge$
 $\text{targetnode } a' = nx'$)
shows *slice-kind* $S a = (\lambda s. \text{False})_{\checkmark}$
proof –
from $\langle m \in \text{obs-intra } (\text{sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
have $m = (\text{THE } m. m \in \text{obs-intra } (\text{sourcenode } a) (\lfloor \text{HRB-slice } S \rfloor_{CFG}))$
by(*rule obs-intra-the-element*[*THEN sym*])
with *assms* **show** *?thesis* **by**(*auto dest:distance-det simp:slice-kind-def Let-def*)
qed

lemma *slice-kind-Pred-obs-not-nearer*:
assumes *sourcenode* $a \notin \lfloor \text{HRB-slice } S \rfloor_{CFG}$ **and** *kind* $a = (Q)_{\checkmark}$
and *in-obs*: $m \in \text{obs-intra } (\text{sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG}$
and *dist*: $\text{distance } (\text{sourcenode } a) m (x + 1)$
 $\neg \text{distance } (\text{targetnode } a) m x$
shows *slice-kind* $S a = (\lambda s. \text{False})_{\checkmark}$
proof –
from *in-obs* **have** *the*: $m = (\text{THE } m. m \in \text{obs-intra } (\text{sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG})$
by(*rule obs-intra-the-element*[*THEN sym*])
from *dist* **have** $\neg (\exists x. \text{distance } (\text{targetnode } a) m x \wedge$
 $\text{distance } (\text{sourcenode } a) m (x + 1))$
by(*fastforce dest:distance-det*)
with $\langle \text{sourcenode } a \notin \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle \langle \text{kind } a = (Q)_{\checkmark} \rangle$ *in-obs* **the** **show**
?thesis
by(*auto simp:slice-kind-def Let-def*)
qed

lemma *kind-Predicate-notin-slice-slice-kind-Predicate*:
assumes *sourcenode* $a \notin \lfloor \text{HRB-slice } S \rfloor_{CFG}$ **and** *valid-edge* a **and** *kind* $a =$
 $(Q)_{\checkmark}$
obtains Q' **where** *slice-kind* $S a = (Q')_{\checkmark}$ **and** $Q' = (\lambda s. \text{False}) \vee Q' = (\lambda s. \text{True})$
True)
proof(*atomize-elim*)
show $\exists Q'. \text{slice-kind } S a = (Q')_{\checkmark} \wedge (Q' = (\lambda s. \text{False}) \vee Q' = (\lambda s. \text{True}))$
proof(*cases obs-intra* (*sourcenode* a) $\lfloor \text{HRB-slice } S \rfloor_{CFG} = \{\}$)
case *True*
from $\langle \text{valid-edge } a \rangle$ **have** *valid-node* (*sourcenode* a) **by** *simp*
then obtain *as* **where** *sourcenode* $a -as \rightarrow_{\checkmark}^* (-\text{Exit-})$ **by**(*fastforce dest:Exit-path*)
then obtain $as' \text{ mex}$ **where** *sourcenode* $a -as' \rightarrow_l^* \text{mex}$ **and** *method-exit* mex

by $\neg(\text{erule } \text{valid-Exit-path-intra-path})$
from $\langle \text{sourcenode } a -as' \rightarrow_l^* \text{mex} \rangle$ **have** *get-proc* (*sourcenode* a) = *get-proc*
mex
by(*rule intra-path-get-procs*)

```

show ?thesis
proof(cases  $\exists x. \text{distance}(\text{targetnode } a) \text{ mex } x \wedge$ 
   $\text{distance}(\text{sourcenode } a) \text{ mex } (x + 1)$ )
  case True
  then obtain  $x$  where  $\text{distance}(\text{targetnode } a) \text{ mex } x$ 
  and  $\text{distance}(\text{sourcenode } a) \text{ mex } (x + 1)$  by blast
  show ?thesis
  proof(cases  $\text{targetnode } a = (\text{SOME } n'. \exists a'. \text{sourcenode } a = \text{sourcenode } a' \wedge$ 
     $\text{distance}(\text{targetnode } a') \text{ mex } x \wedge$ 
     $\text{valid-edge } a' \wedge \text{intra-kind}(\text{kind } a') \wedge$ 
     $\text{targetnode } a' = n')$ )

    case True
    with  $\langle \text{sourcenode } a \notin \llbracket \text{HRB-slice } S \rrbracket_{\text{CFG}} \rangle \langle \text{kind } a = (Q)_{\checkmark} \rangle$ 
     $\langle \text{obs-intra}(\text{sourcenode } a) \llbracket \text{HRB-slice } S \rrbracket_{\text{CFG}} = \{\} \rangle$ 
     $\langle \text{method-exit } \text{mex} \rangle \langle \text{get-proc}(\text{sourcenode } a) = \text{get-proc } \text{mex} \rangle$ 
     $\langle \text{distance}(\text{targetnode } a) \text{ mex } x \rangle \langle \text{distance}(\text{sourcenode } a) \text{ mex } (x + 1) \rangle$ 
    have  $\text{slice-kind } S \ a = (\lambda s. \text{True})_{\checkmark}$ 
    by(rule  $\text{slice-kind-Pred-empty-obs-nearer-SOME}$ )
    thus ?thesis by simp
  next
  case False
  with  $\langle \text{sourcenode } a \notin \llbracket \text{HRB-slice } S \rrbracket_{\text{CFG}} \rangle \langle \text{kind } a = (Q)_{\checkmark} \rangle$ 
   $\langle \text{obs-intra}(\text{sourcenode } a) \llbracket \text{HRB-slice } S \rrbracket_{\text{CFG}} = \{\} \rangle$ 
   $\langle \text{method-exit } \text{mex} \rangle \langle \text{get-proc}(\text{sourcenode } a) = \text{get-proc } \text{mex} \rangle$ 
   $\langle \text{distance}(\text{targetnode } a) \text{ mex } x \rangle \langle \text{distance}(\text{sourcenode } a) \text{ mex } (x + 1) \rangle$ 
  have  $\text{slice-kind } S \ a = (\lambda s. \text{False})_{\checkmark}$ 
  by(rule  $\text{slice-kind-Pred-empty-obs-nearer-not-SOME}$ )
  thus ?thesis by simp
  qed
next
  case False
  from  $\langle \text{method-exit } \text{mex} \rangle \langle \text{get-proc}(\text{sourcenode } a) = \text{get-proc } \text{mex} \rangle$ 
  have  $\text{mex} = (\text{THE } \text{mex}. \text{method-exit } \text{mex} \wedge \text{get-proc}(\text{sourcenode } a) = \text{get-proc } \text{mex})$ 
  by(auto  $\text{intro}!:\text{the-equality}[\text{THEN } \text{sym}] \text{intro}:\text{method-exit-unique}$ )
  with  $\langle \text{sourcenode } a \notin \llbracket \text{HRB-slice } S \rrbracket_{\text{CFG}} \rangle \langle \text{kind } a = (Q)_{\checkmark} \rangle$ 
   $\langle \text{obs-intra}(\text{sourcenode } a) \llbracket \text{HRB-slice } S \rrbracket_{\text{CFG}} = \{\} \rangle \text{False}$ 
  have  $\text{slice-kind } S \ a = (\lambda s. \text{False})_{\checkmark}$ 
  by(auto  $\text{simp}:\text{slice-kind-def } \text{Let-def}$ )
  thus ?thesis by simp
  qed
next
  case False
  then obtain  $m$  where  $m \in \text{obs-intra}(\text{sourcenode } a) \llbracket \text{HRB-slice } S \rrbracket_{\text{CFG}}$  by
  blast
  show ?thesis
  proof(cases  $\exists x. \text{distance}(\text{targetnode } a) \ m \ x \wedge$ 
     $\text{distance}(\text{sourcenode } a) \ m \ (x + 1)$ )
    case True

```

then obtain x **where** $\text{distance}(\text{targetnode } a) \ m \ x$
and $\text{distance}(\text{sourcenode } a) \ m \ (x + 1)$ **by** *blast*
show *?thesis*
proof($\text{cases } \text{targetnode } a = (\text{SOME } n'. \exists a'. \text{sourcenode } a = \text{sourcenode } a' \wedge$
 $\text{distance}(\text{targetnode } a') \ m \ x \wedge$
 $\text{valid-edge } a' \wedge \text{intra-kind}(\text{kind } a') \wedge$
 $\text{targetnode } a' = n')$)

case *True*
with $\langle \text{sourcenode } a \notin \lfloor \text{HRB-slice } S \rfloor_{CFG} \ \langle \text{kind } a = (Q)_{\checkmark} \rangle$
 $\langle m \in \text{obs-intra}(\text{sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
 $\langle \text{distance}(\text{targetnode } a) \ m \ x \rangle \ \langle \text{distance}(\text{sourcenode } a) \ m \ (x + 1) \rangle$
have $\text{slice-kind } S \ a = (\lambda s. \text{True})_{\checkmark}$
by(*rule slice-kind-Pred-obs-nearer-SOME*)
thus *?thesis* **by** *simp*

next
case *False*
with $\langle \text{sourcenode } a \notin \lfloor \text{HRB-slice } S \rfloor_{CFG} \ \langle \text{kind } a = (Q)_{\checkmark} \rangle$
 $\langle m \in \text{obs-intra}(\text{sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
 $\langle \text{distance}(\text{targetnode } a) \ m \ x \rangle \ \langle \text{distance}(\text{sourcenode } a) \ m \ (x + 1) \rangle$
have $\text{slice-kind } S \ a = (\lambda s. \text{False})_{\checkmark}$
by(*rule slice-kind-Pred-obs-nearer-not-SOME*)
thus *?thesis* **by** *simp*

qed

next
case *False*
from $\langle m \in \text{obs-intra}(\text{sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
have $m = (\text{THE } m. m \in \text{obs-intra}(\text{sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG})$
by(*rule obs-intra-the-element[THEN sym]*)
with $\langle \text{sourcenode } a \notin \lfloor \text{HRB-slice } S \rfloor_{CFG} \ \langle \text{kind } a = (Q)_{\checkmark} \rangle \ \text{False}$
 $\langle m \in \text{obs-intra}(\text{sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
have $\text{slice-kind } S \ a = (\lambda s. \text{False})_{\checkmark}$
by(*auto simp:slice-kind-def Let-def*)
thus *?thesis* **by** *simp*

qed

qed

qed

lemma *slice-kind-Call*:
 $\llbracket \text{sourcenode } a \notin \lfloor \text{HRB-slice } S \rfloor_{CFG}; \text{kind } a = Q:r \leftrightarrow_p fs \rrbracket$
 $\implies \text{slice-kind } S \ a = (\lambda cf. \text{False}):r \leftrightarrow_p fs$
by(*simp add:slice-kind-def*)

lemma *slice-kind-Call-in-slice*:
 $\llbracket \text{sourcenode } a \in \lfloor \text{HRB-slice } S \rfloor_{CFG}; \text{kind } a = Q:r \leftrightarrow_p fs \rrbracket$
 $\implies \text{slice-kind } S \ a = Q:r \leftrightarrow_p (\text{cspp}(\text{targetnode } a) (\text{HRB-slice } S) fs)$
by(*simp add:slice-kind-def*)

lemma *slice-kind-Call-in-slice-Formal-in-not:*

assumes *sourcenode* $a \in \lfloor \text{HRB-slice } S \rfloor_{CFG}$ **and** *kind* $a = Q:r \hookrightarrow_p fs$
and $\forall x < \text{length } fs. \text{Formal-in}(\text{targetnode } a, x) \notin \text{HRB-slice } S$
shows *slice-kind* $S \ a = Q:r \hookrightarrow_p \text{replicate } (\text{length } fs) \ \text{empty}$

proof –

from $\langle \text{sourcenode } a \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \ \langle \text{kind } a = Q:r \hookrightarrow_p fs \rangle$
have *slice-kind* $S \ a = Q:r \hookrightarrow_p (\text{cspp } (\text{targetnode } a) \ (\text{HRB-slice } S) \ fs)$
by (*simp add:slice-kind-def*)
from $\langle \forall x < \text{length } fs. \text{Formal-in}(\text{targetnode } a, x) \notin \text{HRB-slice } S \rangle$
have $\text{cspp } (\text{targetnode } a) \ (\text{HRB-slice } S) \ fs = \text{replicate } (\text{length } fs) \ \text{empty}$
by (*fastforce intro:nth-equalityI csppa-Formal-in-notin-slice simp:cspp-def*)
with $\langle \text{slice-kind } S \ a = Q:r \hookrightarrow_p (\text{cspp } (\text{targetnode } a) \ (\text{HRB-slice } S) \ fs) \rangle$
show *?thesis* **by** *simp*

qed

lemma *slice-kind-Call-in-slice-Formal-in-also:*

assumes *sourcenode* $a \in \lfloor \text{HRB-slice } S \rfloor_{CFG}$ **and** *kind* $a = Q:r \hookrightarrow_p fs$
and $\forall x < \text{length } fs. \text{Formal-in}(\text{targetnode } a, x) \in \text{HRB-slice } S$
shows *slice-kind* $S \ a = Q:r \hookrightarrow_p fs$

proof –

from $\langle \text{sourcenode } a \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \ \langle \text{kind } a = Q:r \hookrightarrow_p fs \rangle$
have *slice-kind* $S \ a = Q:r \hookrightarrow_p (\text{cspp } (\text{targetnode } a) \ (\text{HRB-slice } S) \ fs)$
by (*simp add:slice-kind-def*)
from $\langle \forall x < \text{length } fs. \text{Formal-in}(\text{targetnode } a, x) \in \text{HRB-slice } S \rangle$
have $\text{cspp } (\text{targetnode } a) \ (\text{HRB-slice } S) \ fs = fs$
by (*fastforce intro:nth-equalityI csppa-Formal-in-in-slice simp:cspp-def*)
with $\langle \text{slice-kind } S \ a = Q:r \hookrightarrow_p (\text{cspp } (\text{targetnode } a) \ (\text{HRB-slice } S) \ fs) \rangle$
show *?thesis* **by** *simp*

qed

lemma *slice-kind-Call-intra-notin-slice:*

assumes *sourcenode* $a \notin \lfloor \text{HRB-slice } S \rfloor_{CFG}$ **and** *valid-edge* a
and *intra-kind* (*kind* a) **and** *valid-edge* a' **and** *kind* $a' = Q:r \hookrightarrow_p fs$
and *sourcenode* $a' = \text{sourcenode } a$
shows *slice-kind* $S \ a = (\lambda s. \text{True}) \checkmark$

proof –

from $\langle \text{valid-edge } a' \ \langle \text{kind } a' = Q:r \hookrightarrow_p fs \rangle$ **obtain** a''
where $a'' \in \text{get-return-edges } a'$
by (*fastforce dest:get-return-edge-call*)
with $\langle \text{valid-edge } a' \rangle$ **obtain** ax **where** *valid-edge* ax
and *sourcenode* $ax = \text{sourcenode } a'$ **and** *targetnode* $ax = \text{targetnode } a''$
and *kind* $ax = (\lambda cf. \text{False}) \checkmark$
by (*fastforce dest:call-return-node-edge*)
from $\langle \text{valid-edge } a' \ \langle \text{kind } a' = Q:r \hookrightarrow_p fs \rangle$
have $\exists! a''. \text{valid-edge } a'' \wedge \text{sourcenode } a'' = \text{sourcenode } a' \wedge$
intra-kind (*kind* a'')

by(rule call-only-one-intra-edge)
with ⟨valid-edge a⟩ ⟨sourcenode a' = sourcenode a⟩ ⟨intra-kind (kind a)⟩
have all:∀ a''. valid-edge a'' ∧ sourcenode a'' = sourcenode a' ∧
intra-kind(kind a'') → a'' = a **by** fastforce
with ⟨valid-edge ax⟩ ⟨sourcenode ax = sourcenode a'⟩ ⟨kind ax = (λcf. False)√⟩
have [simp]:ax = a **by**(fastforce simp:intra-kind-def)
show ?thesis
proof(cases obs-intra (sourcenode a) [HRB-slice S] CFG = {})
case True
from ⟨valid-edge a⟩ **have** valid-node (sourcenode a) **by** simp
then obtain asx **where** sourcenode a - asx →√* (-Exit-) **by**(fastforce dest:Exit-path)
then obtain as pex **where** sourcenode a - as →i* pex **and** method-exit pex
by -(erule valid-Exit-path-intra-path)
from ⟨sourcenode a - as →i* pex⟩ **have** get-proc (sourcenode a) = get-proc pex
by(rule intra-path-get-procs)
from ⟨sourcenode a - as →i* pex⟩ **obtain** x **where** distance (sourcenode a) pex
x
and x ≤ length as **by**(erule every-path-distance)
from ⟨method-exit pex⟩ **have** sourcenode a ≠ pex
proof(rule method-exit-cases)
assume pex = (-Exit-)
show ?thesis
proof
assume sourcenode a = pex
with ⟨pex = (-Exit-)⟩ **have** sourcenode a = (-Exit-) **by** simp
with ⟨valid-edge a⟩ **show** False **by**(rule Exit-source)
qed
next
fix ax Qx px fx
assume pex = sourcenode ax **and** valid-edge ax **and** kind ax = Qx ↔_{px} fx
hence ∀ a'. valid-edge a' ∧ sourcenode a' = sourcenode ax →
(∃ Qx' fx'. kind a' = Qx' ↔_{px} fx') **by** -(rule return-edges-only)
with ⟨valid-edge a⟩ ⟨intra-kind (kind a)⟩ ⟨pex = sourcenode ax⟩
show ?thesis **by**(fastforce simp:intra-kind-def)
qed
have x ≠ 0
proof
assume x = 0
with ⟨distance (sourcenode a) pex x⟩ **have** sourcenode a = pex
by(fastforce elim:distance.cases simp:intra-path-def)
with ⟨sourcenode a ≠ pex⟩ **show** False **by** simp
qed
with ⟨distance (sourcenode a) pex x⟩ **obtain** ax' **where** valid-edge ax'
and sourcenode a = sourcenode ax' **and** intra-kind(kind ax')
and distance (targetnode ax') pex (x - 1)
and Some:targetnode ax' = (SOME nx. ∃ a'. sourcenode ax' = sourcenode a'
∧
distance (targetnode a') pex (x - 1) ∧
valid-edge a' ∧ intra-kind(kind a') ∧

$targetnode\ a' = nx$)

by(erule distance-successor-distance)

from $\langle valid-edge\ ax' \rangle \langle sourcenode\ a = sourcenode\ ax' \rangle \langle intra-kind(kind\ ax') \rangle$
 $\langle sourcenode\ a' = sourcenode\ a \rangle\ all$

have [simp]: $ax' = a$ **by** fastforce

from $\langle sourcenode\ a \notin [HRB-slice\ S]_{CFG} \rangle \langle kind\ ax = (\lambda cf. False)_{\checkmark} \rangle$
 $\langle True \rangle \langle method-exit\ pex \rangle \langle get-proc\ (sourcenode\ a) = get-proc\ pex \rangle \langle x \neq 0 \rangle$
 $\langle distance\ (targetnode\ ax')\ pex\ (x - 1) \rangle \langle distance\ (sourcenode\ a)\ pex\ x \rangle\ Some$

show ?thesis **by**(fastforce elim:slice-kind-Pred-empty-obs-nearer-SOME)

next

case False

then obtain m **where** $m \in obs-intra\ (sourcenode\ a)\ [HRB-slice\ S]_{CFG}$ **by**
fastforce

then obtain as **where** $sourcenode\ a - as \rightarrow_i^* m$ **and** $m \in [HRB-slice\ S]_{CFG}$
by $-(erule\ obs-intraE)$

from $\langle sourcenode\ a - as \rightarrow_i^* m \rangle$ **obtain** x **where** $distance\ (sourcenode\ a)\ m\ x$
and $x \leq length\ as$ **by**(erule every-path-distance)

from $\langle sourcenode\ a \notin [HRB-slice\ S]_{CFG} \rangle \langle m \in [HRB-slice\ S]_{CFG} \rangle$

have $sourcenode\ a \neq m$ **by** fastforce

have $x \neq 0$

proof

assume $x = 0$

with $\langle distance\ (sourcenode\ a)\ m\ x \rangle$ **have** $sourcenode\ a = m$
by(fastforce elim:distance.cases simp:intra-path-def)

with $\langle sourcenode\ a \neq m \rangle$ **show** False **by** simp

qed

with $\langle distance\ (sourcenode\ a)\ m\ x \rangle$ **obtain** ax' **where** $valid-edge\ ax'$
and $sourcenode\ a = sourcenode\ ax'$ **and** $intra-kind(kind\ ax')$
and $distance\ (targetnode\ ax')\ m\ (x - 1)$
and $Some:targetnode\ ax' = (SOME\ nx. \exists a'. sourcenode\ ax' = sourcenode\ a'$

\wedge

$distance\ (targetnode\ a')\ m\ (x - 1) \wedge$
 $valid-edge\ a' \wedge intra-kind(kind\ a') \wedge$
 $targetnode\ a' = nx$)

by(erule distance-successor-distance)

from $\langle valid-edge\ ax' \rangle \langle sourcenode\ a = sourcenode\ ax' \rangle \langle intra-kind(kind\ ax') \rangle$
 $\langle sourcenode\ a' = sourcenode\ a \rangle\ all$

have [simp]: $ax' = a$ **by** fastforce

from $\langle sourcenode\ a \notin [HRB-slice\ S]_{CFG} \rangle \langle kind\ ax = (\lambda cf. False)_{\checkmark} \rangle$
 $\langle m \in obs-intra\ (sourcenode\ a)\ [HRB-slice\ S]_{CFG} \rangle \langle x \neq 0 \rangle$
 $\langle distance\ (targetnode\ ax')\ m\ (x - 1) \rangle \langle distance\ (sourcenode\ a)\ m\ x \rangle\ Some$

show ?thesis **by**(fastforce elim:slice-kind-Pred-obs-nearer-SOME)

qed

qed

lemma slice-kind-Return:

$\llbracket sourcenode\ a \notin [HRB-slice\ S]_{CFG}; kind\ a = Q \leftrightarrow pf \rrbracket$
 $\implies slice-kind\ S\ a = (\lambda cf. True) \leftrightarrow_p (\lambda cf\ cf'. cf')$

by(*simp add:slice-kind-def*)

lemma *slice-kind-Return-in-slice*:

[[*sourcenode a* ∈ [*HRB-slice S*]*CFG*; *valid-edge a*; *kind a* = $Q \leftrightarrow pf$;

(*p,ins,outs*) ∈ *set procs*]]

⇒ *slice-kind S a* = $Q \leftrightarrow_p(\lambda cf\ cf'.\ rspp\ (targetnode\ a)\ (HRB-slice\ S)\ outs\ cf'\ cf)$

by(*simp add:slice-kind-def,unfold formal-out-THE,simp*)

lemma *length-transfer-kind-slice-kind*:

assumes *valid-edge a* and *length s₁ = length s₂*

and *transfer (kind a) s₁ = s₁'* and *transfer (slice-kind S a) s₂ = s₂'*

shows *length s₁' = length s₂'*

proof(*cases kind a rule:edge-kind-cases*)

case *Intra*

show *?thesis*

proof(*cases sourcenode a* ∈ [*HRB-slice S*]*CFG*)

case *True*

with *Intra assms* show *?thesis*

by(*cases s₁*)(*cases s₂,auto dest:slice-intra-kind-in-slice simp:intra-kind-def*)+

next

case *False*

with *Intra assms* show *?thesis*

by(*cases s₁*)(*cases s₂,auto dest:slice-kind-Upd*

elim:kind-Predicate-notin-slice-slice-kind-Predicate simp:intra-kind-def)+

qed

next

case (*Call Q r p fs*)

show *?thesis*

proof(*cases sourcenode a* ∈ [*HRB-slice S*]*CFG*)

case *True*

with *Call assms* show *?thesis*

by(*cases s₁*)(*cases s₂,auto dest:slice-kind-Call-in-slice*)+

next

case *False*

with *Call assms* show *?thesis*

by(*cases s₁*)(*cases s₂,auto dest:slice-kind-Call*)+

qed

next

case (*Return Q p f*)

show *?thesis*

proof(*cases sourcenode a* ∈ [*HRB-slice S*]*CFG*)

case *True*

from *Return (valid-edge a)* obtain *a' Q' r fs*

where *valid-edge a'* and *kind a' = Q':r↔pfs*

by $-(drule\ return-needs-call,auto)$

then obtain *ins outs* where (*p,ins,outs*) ∈ *set procs*

```

  by(fastforce dest!:callee-in-procs)
with True ⟨valid-edge a⟩ Return assms show ?thesis
  by(cases s1)(cases s2,auto dest:slice-kind-Return-in-slice split:list.split)+
next
  case False
with Return assms show ?thesis
  by(cases s1)(cases s2,auto dest:slice-kind-Return split:list.split)+
qed
qed

```

1.12.3 The sliced graph of a deterministic CFG is still deterministic

lemma *only-one-SOME-edge*:

```

assumes valid-edge a and intra-kind(kind a) and distance (targetnode a) mex x
shows ∃!a'. sourcenode a = sourcenode a' ∧ distance (targetnode a') mex x ∧
      valid-edge a' ∧ intra-kind(kind a') ∧
      targetnode a' = (SOME n'. ∃ a'. sourcenode a = sourcenode a' ∧
                             distance (targetnode a') mex x ∧
                             valid-edge a' ∧ intra-kind(kind a') ∧
                             targetnode a' = n')

```

proof(*rule ex-ex1I*)

```

show ∃ a'. sourcenode a = sourcenode a' ∧ distance (targetnode a') mex x ∧
      valid-edge a' ∧ intra-kind(kind a') ∧
      targetnode a' = (SOME n'. ∃ a'. sourcenode a = sourcenode a' ∧
                             distance (targetnode a') mex x ∧
                             valid-edge a' ∧ intra-kind(kind a') ∧
                             targetnode a' = n')

```

proof –

```

have (∃ a'. sourcenode a = sourcenode a' ∧ distance (targetnode a') mex x ∧
      valid-edge a' ∧ intra-kind(kind a') ∧
      targetnode a' = (SOME n'. ∃ a'. sourcenode a = sourcenode a' ∧
                             distance (targetnode a') mex x ∧
                             valid-edge a' ∧ intra-kind(kind a') ∧
                             targetnode a' = n')) =
(∃ n'. ∃ a'. sourcenode a = sourcenode a' ∧ distance (targetnode a') mex x ∧
      valid-edge a' ∧ intra-kind(kind a') ∧ targetnode a' = n')
apply(unfold some-eq-ex[of λn'. ∃ a'. sourcenode a = sourcenode a' ∧
                             distance (targetnode a') mex x ∧
                             valid-edge a' ∧ intra-kind(kind a') ∧
                             targetnode a' = n'])

```

by *simp*

also have ...

using ⟨valid-edge a⟩ ⟨intra-kind(kind a)⟩ ⟨distance (targetnode a) mex x⟩

by *blast*

finally show ?thesis .

qed

next

fix a' ax

assume $\text{sourcenode } a = \text{sourcenode } a' \wedge \text{distance } (\text{targetnode } a') \text{ mex } x \wedge$
 $\text{valid-edge } a' \wedge \text{intra-kind}(\text{kind } a') \wedge$
 $\text{targetnode } a' = (\text{SOME } n'. \exists a'. \text{sourcenode } a = \text{sourcenode } a' \wedge$
 $\text{distance } (\text{targetnode } a') \text{ mex } x \wedge$
 $\text{valid-edge } a' \wedge \text{intra-kind}(\text{kind } a') \wedge$
 $\text{targetnode } a' = n')$
and $\text{sourcenode } a = \text{sourcenode } ax \wedge \text{distance } (\text{targetnode } ax) \text{ mex } x \wedge$
 $\text{valid-edge } ax \wedge \text{intra-kind}(\text{kind } ax) \wedge$
 $\text{targetnode } ax = (\text{SOME } n'. \exists a'. \text{sourcenode } a = \text{sourcenode } a' \wedge$
 $\text{distance } (\text{targetnode } a') \text{ mex } x \wedge$
 $\text{valid-edge } a' \wedge \text{intra-kind}(\text{kind } a') \wedge$
 $\text{targetnode } a' = n')$
thus $a' = ax$ **by**(*fastforce intro!:edge-det*)
qed

lemma *slice-kind-only-one-True-edge*:

assumes $\text{sourcenode } a = \text{sourcenode } a'$ **and** $\text{targetnode } a \neq \text{targetnode } a'$
and $\text{valid-edge } a$ **and** $\text{valid-edge } a'$ **and** $\text{intra-kind } (\text{kind } a)$
and $\text{intra-kind } (\text{kind } a')$ **and** $\text{slice-kind } S a = (\lambda s. \text{True})_{\surd}$
shows $\text{slice-kind } S a' = (\lambda s. \text{False})_{\surd}$

proof –

from *assms* **obtain** $Q Q'$ **where** $\text{kind } a = (Q)_{\surd}$
and $\text{kind } a' = (Q')_{\surd}$ **and** $\text{det}:\forall s. (Q s \longrightarrow \neg Q' s) \wedge (Q' s \longrightarrow \neg Q s)$
by(*auto dest:deterministic*)

show *?thesis*

proof(*cases sourcenode a ∈ [HRB-slice S] CFG*)

case *True*

with $\langle \text{slice-kind } S a = (\lambda s. \text{True})_{\surd} \rangle \langle \text{kind } a = (Q)_{\surd} \rangle$ **have** $Q = (\lambda s. \text{True})_{\surd}$
by(*simp add:slice-kind-def Let-def*)

with *det* **have** $Q' = (\lambda s. \text{False})_{\surd}$ **by**(*simp add:fun-eq-iff*)

with *True* $\langle \text{kind } a' = (Q')_{\surd} \rangle \langle \text{sourcenode } a = \text{sourcenode } a' \rangle$ **show** *?thesis*
by(*simp add:slice-kind-def Let-def*)

next

case *False*

hence $\text{sourcenode } a \notin [\text{HRB-slice } S]_{CFG}$ **by** *simp*

thus *?thesis*

proof(*cases obs-intra (sourcenode a) [HRB-slice S] CFG = {}*)

case *True*

with $\langle \text{sourcenode } a \notin [\text{HRB-slice } S]_{CFG} \rangle \langle \text{slice-kind } S a = (\lambda s. \text{True})_{\surd} \rangle$
 $\langle \text{kind } a = (Q)_{\surd} \rangle$

obtain $\text{mex } x$ **where** $\text{mex}:\text{mex} = (\text{THE } \text{mex}. \text{method-exit } \text{mex} \wedge$
 $\text{get-proc } (\text{sourcenode } a) = \text{get-proc } \text{mex})$

and $\text{dist}:\text{distance } (\text{targetnode } a) \text{ mex } x \text{ distance } (\text{sourcenode } a) \text{ mex } (x +$

1)

and $\text{target}:\text{targetnode } a = (\text{SOME } n'. \exists a'. \text{sourcenode } a = \text{sourcenode } a' \wedge$
 $\text{distance } (\text{targetnode } a') \text{ mex } x \wedge$
 $\text{valid-edge } a' \wedge \text{intra-kind}(\text{kind } a') \wedge$
 $\text{targetnode } a' = n')$

by(*auto simp:slice-kind-def Let-def fun-eq-iff split:split-if-asm*)
from $\langle \text{valid-edge } a \rangle \langle \text{intra-kind } (\text{kind } a) \rangle \langle \text{distance } (\text{targetnode } a) \text{ mex } x \rangle$
have $\text{ex1}:\exists!a'. \text{sourcenode } a = \text{sourcenode } a' \wedge \text{distance } (\text{targetnode } a') \text{ mex } x \wedge$
 $\text{valid-edge } a' \wedge \text{intra-kind}(\text{kind } a') \wedge$
 $\text{targetnode } a' = (\text{SOME } n'. \exists a'. \text{sourcenode } a = \text{sourcenode } a' \wedge$
 $\text{distance } (\text{targetnode } a') \text{ mex } x \wedge$
 $\text{valid-edge } a' \wedge \text{intra-kind}(\text{kind } a') \wedge$
 $\text{targetnode } a' = n')$
by(*rule only-one-SOME-edge*)
have $\text{targetnode } a' \neq (\text{SOME } n'. \exists a'. \text{sourcenode } a = \text{sourcenode } a' \wedge$
 $\text{distance } (\text{targetnode } a') \text{ mex } x \wedge$
 $\text{valid-edge } a' \wedge \text{intra-kind}(\text{kind } a') \wedge$
 $\text{targetnode } a' = n')$
proof(*rule ccontr*)
assume $\neg \text{targetnode } a' \neq (\text{SOME } n'. \exists a'. \text{sourcenode } a = \text{sourcenode } a'$
 $\text{distance } (\text{targetnode } a') \text{ mex } x \wedge$
 $\text{valid-edge } a' \wedge \text{intra-kind}(\text{kind } a') \wedge$
 $\text{targetnode } a' = n')$
hence $\text{targetnode } a' = (\text{SOME } n'. \exists a'. \text{sourcenode } a = \text{sourcenode } a' \wedge$
 $\text{distance } (\text{targetnode } a') \text{ mex } x \wedge$
 $\text{valid-edge } a' \wedge \text{intra-kind}(\text{kind } a') \wedge$
 $\text{targetnode } a' = n')$
by *simp*
with $\text{ex1 target } \langle \text{sourcenode } a = \text{sourcenode } a' \rangle \langle \text{valid-edge } a \rangle \langle \text{valid-edge } a' \rangle$
 $\langle \text{intra-kind}(\text{kind } a) \rangle \langle \text{intra-kind}(\text{kind } a') \rangle \langle \text{distance } (\text{targetnode } a) \text{ mex } x \rangle$
have $a = a'$ **by** *fastforce*
with $\langle \text{targetnode } a \neq \text{targetnode } a' \rangle$ **show** *False* **by** *simp*
qed
with $\langle \text{sourcenode } a \notin [\text{HRB-slice } S]_{\text{CFG}} \rangle \text{True } \langle \text{kind } a' = (Q')_{\checkmark} \rangle$
 $\langle \text{sourcenode } a = \text{sourcenode } a' \rangle \text{mex } \text{dist}$
show *?thesis* **by**(*auto dest:distance-det*
simp:slice-kind-def Let-def fun-eq-iff split:split-if-asm)
next
case *False*
hence $\text{obs-intra } (\text{sourcenode } a) [\text{HRB-slice } S]_{\text{CFG}} \neq \{\}$.
then obtain m **where** $m \in \text{obs-intra } (\text{sourcenode } a) [\text{HRB-slice } S]_{\text{CFG}}$ **by**
auto
hence $m = (\text{THE } m. m \in \text{obs-intra } (\text{sourcenode } a) [\text{HRB-slice } S]_{\text{CFG}})$
by(*auto dest:obs-intra-the-element*)
with $\langle \text{sourcenode } a \notin [\text{HRB-slice } S]_{\text{CFG}} \rangle$
 $\langle \text{obs-intra } (\text{sourcenode } a) [\text{HRB-slice } S]_{\text{CFG}} \neq \{\} \rangle$
 $\langle \text{slice-kind } S a = (\lambda s. \text{True})_{\checkmark} \rangle \langle \text{kind } a = (Q)_{\checkmark} \rangle$
obtain $x x'$ **where** $\text{distance } (\text{targetnode } a) m x$
 $\text{distance } (\text{sourcenode } a) m (x + 1)$
and $\text{target:targetnode } a = (\text{SOME } n'. \exists a'. \text{sourcenode } a = \text{sourcenode } a' \wedge$
 $\text{distance } (\text{targetnode } a') m x \wedge$
 $\text{valid-edge } a' \wedge \text{intra-kind}(\text{kind } a') \wedge$

$targetnode\ a' = n'$

by (*auto simp:slice-kind-def Let-def fun-eq-iff split:split-if-asm*)

show *?thesis*

proof (*cases distance (targetnode a') m x*)

case *False*

with $\langle sourcenode\ a \notin [HRB\text{-}slice\ S]_{CFG} \rangle \langle kind\ a' = (Q')_{\surd} \rangle$
 $\langle m \in obs\text{-}intra\ (sourcenode\ a)\ [HRB\text{-}slice\ S]_{CFG} \rangle$
 $\langle distance\ (targetnode\ a)\ m\ x \rangle \langle distance\ (sourcenode\ a)\ m\ (x + 1) \rangle$
 $\langle sourcenode\ a = sourcenode\ a' \rangle$ **show** *?thesis*

by (*fastforce intro:slice-kind-Pred-obs-not-nearer*)

next

case *True*

from $\langle valid\text{-}edge\ a \rangle \langle intra\text{-}kind(kind\ a) \rangle \langle distance\ (targetnode\ a)\ m\ x \rangle$
 $\langle distance\ (sourcenode\ a)\ m\ (x + 1) \rangle$

have $ex1:\exists!a'.\ sourcenode\ a = sourcenode\ a' \wedge$
 $distance\ (targetnode\ a')\ m\ x \wedge valid\text{-}edge\ a' \wedge intra\text{-}kind(kind\ a') \wedge$
 $targetnode\ a' = (SOME\ nx.\ \exists a'.\ sourcenode\ a = sourcenode\ a' \wedge$
 $distance\ (targetnode\ a')\ m\ x \wedge$
 $valid\text{-}edge\ a' \wedge intra\text{-}kind(kind\ a') \wedge$
 $targetnode\ a' = nx)$

by $-(rule\ only\ one\ SOME\ dist\ edge)$

have $targetnode\ a' \neq (SOME\ n'.\ \exists a'.\ sourcenode\ a = sourcenode\ a' \wedge$
 $distance\ (targetnode\ a')\ m\ x \wedge$
 $valid\text{-}edge\ a' \wedge intra\text{-}kind(kind\ a') \wedge$
 $targetnode\ a' = n')$

proof (*rule ccontr*)

assume $\neg targetnode\ a' \neq (SOME\ n'.\ \exists a'.\ sourcenode\ a = sourcenode\ a'$

\wedge

$distance\ (targetnode\ a')\ m\ x \wedge$
 $valid\text{-}edge\ a' \wedge intra\text{-}kind(kind\ a') \wedge$
 $targetnode\ a' = n')$

hence $targetnode\ a' = (SOME\ n'.\ \exists a'.\ sourcenode\ a = sourcenode\ a' \wedge$
 $distance\ (targetnode\ a')\ m\ x \wedge$
 $valid\text{-}edge\ a' \wedge intra\text{-}kind(kind\ a') \wedge$
 $targetnode\ a' = n')$

by *simp*

with $ex1\ target\ \langle sourcenode\ a = sourcenode\ a' \rangle$
 $\langle valid\text{-}edge\ a \rangle \langle valid\text{-}edge\ a' \rangle \langle intra\text{-}kind(kind\ a) \rangle \langle intra\text{-}kind(kind\ a') \rangle$
 $\langle distance\ (targetnode\ a)\ m\ x \rangle \langle distance\ (sourcenode\ a)\ m\ (x + 1) \rangle$

have $a = a'$ **by** *auto*

with $\langle targetnode\ a \neq targetnode\ a' \rangle$ **show** *False* **by** *simp*

qed

with $\langle sourcenode\ a \notin [HRB\text{-}slice\ S]_{CFG} \rangle$
 $\langle kind\ a' = (Q')_{\surd} \rangle \langle m \in obs\text{-}intra\ (sourcenode\ a)\ [HRB\text{-}slice\ S]_{CFG} \rangle$
 $\langle distance\ (targetnode\ a)\ m\ x \rangle \langle distance\ (sourcenode\ a)\ m\ (x + 1) \rangle$
 $True\ \langle sourcenode\ a = sourcenode\ a' \rangle$ **show** *?thesis*

by (*fastforce intro:slice-kind-Pred-obs-nearer-not-SOME*)

qed

qed

qed
qed

lemma *slice-deterministic*:

assumes *valid-edge a* **and** *valid-edge a'*
and *intra-kind (kind a)* **and** *intra-kind (kind a')*
and *sourcenode a = sourcenode a'* **and** *targetnode a ≠ targetnode a'*
obtains $Q\ Q'$ **where** *slice-kind S a = (Q)_✓* **and** *slice-kind S a' = (Q')_✓*
and $\forall s. (Q\ s \longrightarrow \neg Q'\ s) \wedge (Q'\ s \longrightarrow \neg Q\ s)$

proof(*atomize-elim*)

from *assms* **obtain** $Q\ Q'$

where *kind a = (Q)_✓* **and** *kind a' = (Q')_✓*
and *det: $\forall s. (Q\ s \longrightarrow \neg Q'\ s) \wedge (Q'\ s \longrightarrow \neg Q\ s)$*
by(*auto dest:deterministic*)

show $\exists Q\ Q'. \text{slice-kind } S\ a = (Q)_✓ \wedge \text{slice-kind } S\ a' = (Q')_✓ \wedge$
 $(\forall s. (Q\ s \longrightarrow \neg Q'\ s) \wedge (Q'\ s \longrightarrow \neg Q\ s))$

proof(*cases sourcenode a ∈ [HRB-slice S] CFG*)

case *True*

with $\langle \text{kind } a = (Q)_✓ \rangle$ **have** *slice-kind S a = (Q)_✓*
by(*simp add:slice-kind-def Let-def*)

from *True* $\langle \text{kind } a' = (Q')_✓ \rangle$ $\langle \text{sourcenode } a = \text{sourcenode } a' \rangle$
have *slice-kind S a' = (Q')_✓*

by(*simp add:slice-kind-def Let-def*)

with $\langle \text{slice-kind } S\ a = (Q)_✓ \rangle$ *det* **show** *?thesis* **by** *blast*

next

case *False*

with $\langle \text{kind } a = (Q)_✓ \rangle$

have *slice-kind S a = (λs. True)_✓ ∨ slice-kind S a = (λs. False)_✓*
by(*simp add:slice-kind-def Let-def*)

thus *?thesis*

proof

assume *true:slice-kind S a = (λs. True)_✓*

with $\langle \text{sourcenode } a = \text{sourcenode } a' \rangle$ $\langle \text{targetnode } a \neq \text{targetnode } a' \rangle$

$\langle \text{valid-edge } a \rangle$ $\langle \text{valid-edge } a' \rangle$ $\langle \text{intra-kind } (kind\ a) \rangle$ $\langle \text{intra-kind } (kind\ a') \rangle$

have *slice-kind S a' = (λs. False)_✓*

by(*rule slice-kind-only-one-True-edge*)

with *true* **show** *?thesis* **by** *simp*

next

assume *false:slice-kind S a = (λs. False)_✓*

from *False* $\langle \text{kind } a' = (Q')_✓ \rangle$ $\langle \text{sourcenode } a = \text{sourcenode } a' \rangle$

have *slice-kind S a' = (λs. True)_✓ ∨ slice-kind S a' = (λs. False)_✓*

by(*simp add:slice-kind-def Let-def*)

with *false* **show** *?thesis* **by** *auto*

qed

qed

qed

end

end

1.13 The weak simulation

theory *WeakSimulation* imports *Slice* begin

context *SDG* begin

lemma *call-node-notin-slice-return-node-neither*:

assumes *call-of-return-node* $n\ n'$ and $n' \notin [HRB\text{-}slice\ S]_{CFG}$

shows $n \notin [HRB\text{-}slice\ S]_{CFG}$

proof –

from $\langle call\text{-}of\text{-}return\text{-}node\ n\ n' \rangle$ obtain $a\ a'$ where *return-node* n

and *valid-edge* a and $n' = sourcenode\ a$

and *valid-edge* a' and $a' \in get\text{-}return\text{-}edges\ a$

and $n = targetnode\ a'$ by(*fastforce simp:call-of-return-node-def*)

from $\langle valid\text{-}edge\ a \rangle\ \langle a' \in get\text{-}return\text{-}edges\ a \rangle$ obtain $Q\ p\ r\ fs$

where *kind* $a = Q:r \leftrightarrow pfs$ by(*fastforce dest!:only-call-get-return-edges*)

with $\langle valid\text{-}edge\ a \rangle\ \langle a' \in get\text{-}return\text{-}edges\ a \rangle$ obtain $Q'\ f'$ where *kind* $a' = Q' \leftrightarrow p f'$

by(*fastforce dest!:call-return-edges*)

from $\langle valid\text{-}edge\ a \rangle\ \langle kind\ a = Q:r \leftrightarrow pfs \rangle\ \langle a' \in get\text{-}return\text{-}edges\ a \rangle$

have *CFG-node* $(sourcenode\ a)\ s-p \rightarrow_{sum}\ \langle CFG\text{-}node\ (targetnode\ a') \rangle$

by(*fastforce intro:sum-SDG-call-summary-edge*)

show *?thesis*

proof

assume $n \in [HRB\text{-}slice\ S]_{CFG}$

with $\langle n = targetnode\ a' \rangle$ have *CFG-node* $(targetnode\ a') \in HRB\text{-}slice\ S$

by(*simp add:SDG-to-CFG-set-def*)

hence *CFG-node* $(sourcenode\ a) \in HRB\text{-}slice\ S$

proof(*induct CFG-node (targetnode a') rule:HRB-slice-cases*)

case (*phase1 nx*)

with $\langle CFG\text{-}node\ (sourcenode\ a)\ s-p \rightarrow_{sum}\ \langle CFG\text{-}node\ (targetnode\ a') \rangle$

show *?case* by(*fastforce intro:combine-SDG-slices.combSlice-refl sum-slice1 simp:HRB-slice-def*)

next

case (*phase2 nx n' n'' p'*)

from $\langle CFG\text{-}node\ (targetnode\ a') \in sum\text{-}SDG\text{-}slice2\ n' \rangle$

$\langle CFG\text{-}node\ (sourcenode\ a)\ s-p \rightarrow_{sum}\ \langle CFG\text{-}node\ (targetnode\ a') \rangle\ \langle valid\text{-}edge$

$a \rangle$

have *CFG-node* $(sourcenode\ a) \in sum\text{-}SDG\text{-}slice2\ n'$

by(*fastforce intro:sum-slice2*)

with $\langle n' \in sum\text{-}SDG\text{-}slice1\ nx \rangle\ \langle n''\ s-p' \rightarrow_{ret}\ \langle CFG\text{-}node\ (parent\text{-}node\ n') \rangle$

$\langle nx \in S \rangle$

show *?case* by(*fastforce intro:combine-SDG-slices.combSlice-Return-parent-node simp:HRB-slice-def*)

qed

```

with  $\langle n' \notin \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle \langle n' = \text{sourcenode } a \rangle$  show False
  by (simp add:SDG-to-CFG-set-def HRB-slice-def)
qed
qed

lemma edge-obs-intra-slice-eq:
assumes valid-edge a and intra-kind (kind a) and sourcenode a  $\notin \lfloor \text{HRB-slice } S \rfloor_{CFG}$ 
shows  $\text{obs-intra (targetnode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG} =$ 
   $\text{obs-intra (sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG}$ 
proof -
from assms have  $\text{obs-intra (targetnode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG} \subseteq$ 
   $\text{obs-intra (sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG}$ 
  by (rule edge-obs-intra-subset)
from  $\langle \text{valid-edge } a \rangle$  have valid-node (sourcenode a) by simp
{ fix x assume  $x \in \text{obs-intra (sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG}$ 
and  $\text{obs-intra (targetnode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG} = \{\}$ 
have  $\exists \text{ as. targetnode } a - \text{as} \rightarrow_i^* x$ 
proof (cases method-exit x)
  case True
from  $\langle \text{valid-edge } a \rangle$  have valid-node (targetnode a) by simp
then obtain asx where  $\text{targetnode } a - \text{asx} \rightarrow_{\sqrt{*}} (-\text{Exit-})$ 
  by (fastforce dest:Exit-path)
then obtain as pex where  $\text{targetnode } a - \text{as} \rightarrow_i^* \text{pex}$  and method-exit pex
  by  $-(\text{erule valid-Exit-path-intra-path})$ 
hence  $\text{get-proc } \text{pex} = \text{get-proc (targetnode } a)$ 
  by  $-(\text{rule intra-path-get-procs[THEN sym]})$ 
also from  $\langle \text{valid-edge } a \rangle \langle \text{intra-kind (kind } a) \rangle$ 
have  $\dots = \text{get-proc (sourcenode } a)$ 
  by  $-(\text{rule get-proc-intra[THEN sym]})$ 
also from  $\langle x \in \text{obs-intra (sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$  True
have  $\dots = \text{get-proc } x$ 
  by (fastforce elim:obs-intraE intro:intra-path-get-procs)
finally have  $\text{pex} = x$  using  $\langle \text{method-exit } \text{pex} \rangle$  True
  by  $-(\text{rule method-exit-unique})$ 
with  $\langle \text{targetnode } a - \text{as} \rightarrow_i^* \text{pex} \rangle$  show ?thesis by fastforce
next
case False
with  $\langle x \in \text{obs-intra (sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$ 
have x postdominates (sourcenode a) by (rule obs-intra-postdominate)
with  $\langle \text{valid-edge } a \rangle \langle \text{intra-kind (kind } a) \rangle \langle \text{sourcenode } a \notin \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$ 
   $\langle x \in \text{obs-intra (sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$ 
have x postdominates (targetnode a)
  by (fastforce elim:postdominate-inner-path-targetnode path-edge obs-intraE
  simp:intra-path-def sourcenodes-def)
thus ?thesis by (fastforce elim:postdominate-implies-inner-path)
qed
then obtain as where  $\text{targetnode } a - \text{as} \rightarrow_i^* x$  by blast

```

from $\langle x \in \text{obs-intra} (\text{sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
have $x \in \lfloor \text{HRB-slice } S \rfloor_{CFG}$ **by** $\text{-(erule obs-intraE)}$
have $\exists x' \in \lfloor \text{HRB-slice } S \rfloor_{CFG}. \exists as'. \text{targetnode } a \text{ --} as' \rightarrow_i^* x' \wedge$
 $(\forall a' \in \text{set} (\text{sourcenodes } as'). a' \notin \lfloor \text{HRB-slice } S \rfloor_{CFG})$
proof $(\text{cases } \exists a' \in \text{set} (\text{sourcenodes } as). a' \in \lfloor \text{HRB-slice } S \rfloor_{CFG})$
case *True*
then obtain $zs \ z \ zs'$ **where** $\text{sourcenodes } as = zs @ z \# zs'$
and $z \in \lfloor \text{HRB-slice } S \rfloor_{CFG}$ **and** $\forall z' \in \text{set } zs. z' \notin \lfloor \text{HRB-slice } S \rfloor_{CFG}$
by $(\text{erule split-list-first-propE})$
then obtain $ys \ y \ ys'$
where $\text{sourcenodes } ys = zs$ **and** $as = ys @ y \# ys'$
and $\text{sourcenode } y = z$
by $(\text{fastforce elim:map-append-append-maps simp:sourcenodes-def})$
from $\langle \text{targetnode } a \text{ --} as \rightarrow_i^* x \rangle \langle as = ys @ y \# ys' \rangle$
have $\text{targetnode } a \text{ --} ys @ y \# ys' \rightarrow^* x$ **and** $\forall y' \in \text{set } ys. \text{intra-kind} (\text{kind } y')$
by $(\text{simp-all add:intra-path-def})$
from $\langle \text{targetnode } a \text{ --} ys @ y \# ys' \rightarrow^* x \rangle$ **have** $\text{targetnode } a \text{ --} ys \rightarrow^* \text{sourcenode}$
 y
by (rule path-split)
with $\langle \forall y' \in \text{set } ys. \text{intra-kind} (\text{kind } y') \rangle \langle \text{sourcenode } y = z \rangle$
 $\langle \forall z' \in \text{set } zs. z' \notin \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle \langle z \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
 $\langle \text{sourcenodes } ys = zs \rangle$
show $?thesis$ **by** $(\text{fastforce simp:intra-path-def})$
next
case *False*
with $\langle \text{targetnode } a \text{ --} as \rightarrow_i^* x \rangle \langle x \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
show $?thesis$ **by** fastforce
qed
hence $\exists y. y \in \text{obs-intra} (\text{targetnode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG}$
by $(\text{fastforce intro:obs-intra-elem})$
with $\langle \text{obs-intra} (\text{targetnode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG} = \{\} \rangle$
have *False* **by** simp }
with $\langle \text{obs-intra} (\text{targetnode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG} \subseteq$
 $\text{obs-intra} (\text{sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle \langle \text{valid-node} (\text{sourcenode } a) \rangle$
show $?thesis$ **by** $(\text{cases obs-intra} (\text{targetnode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG} = \{\})$
 $(\text{auto dest!:obs-intra-singleton-disj})$
qed

lemma *intra-edge-obs-slice:*

assumes $ms \neq []$ **and** $ms'' \in \text{obs } ms' \lfloor \text{HRB-slice } S \rfloor_{CFG}$ **and** $\text{valid-edge } a$
and $\text{intra-kind} (\text{kind } a)$
and $\text{disj}:(\exists m \in \text{set} (\text{tl } ms). \exists m'. \text{call-of-return-node } m \ m' \wedge$
 $m' \notin \lfloor \text{HRB-slice } S \rfloor_{CFG}) \vee \text{hd } ms \notin \lfloor \text{HRB-slice } S \rfloor_{CFG}$
and $\text{hd } ms = \text{sourcenode } a$ **and** $ms' = \text{targetnode } a \# \text{tl } ms$
and $\forall n \in \text{set} (\text{tl } ms'). \text{return-node } n$
shows $ms'' \in \text{obs } ms \lfloor \text{HRB-slice } S \rfloor_{CFG}$
proof --
from $\langle ms'' \in \text{obs } ms' \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle \langle \forall n \in \text{set} (\text{tl } ms'). \text{return-node } n \rangle$

obtain $msx\ m\ msx'\ mx\ m'$ **where** $ms' = msx@m\#msx'$ **and** $ms'' = mx\#msx'$
and $mx \in \text{obs-intra } m \ [HRB\text{-slice } S]_{CFG}$
and $\forall nx \in \text{set } msx'. \exists nx'. \text{call-of-return-node } nx\ nx' \wedge nx' \in [HRB\text{-slice } S]_{CFG}$
and $\text{imp}:\forall xs\ x\ xs'. msx = xs@m\#xs' \wedge \text{obs-intra } x \ [HRB\text{-slice } S]_{CFG} \neq \{\}$
 $\longrightarrow (\exists x'' \in \text{set } (xs'@[m])). \exists mx. \text{call-of-return-node } x''\ mx \wedge$
 $mx \notin [HRB\text{-slice } S]_{CFG}$
by(*erule obsE*)
show *?thesis*
proof(*cases msx*)
case *Nil*
with $\langle \forall nx \in \text{set } msx'. \exists nx'. \text{call-of-return-node } nx\ nx' \wedge nx' \in [HRB\text{-slice } S]_{CFG} \rangle$
 $\langle \text{disj } (ms' = msx@m\#msx') \langle \text{hd } ms = \text{sourcenode } a \rangle (ms' = \text{targetnode } a\#\text{tl } ms) \rangle$
have $\text{sourcenode } a \notin [HRB\text{-slice } S]_{CFG}$ **by**(*cases ms*) *auto*
from $\langle ms' = msx@m\#msx' \rangle \langle ms' = \text{targetnode } a\#\text{tl } ms \rangle$ *Nil*
have $m = \text{targetnode } a$ **by** *simp*
with $\langle \text{valid-edge } a \rangle \langle \text{intra-kind } (kind\ a) \rangle \langle \text{sourcenode } a \notin [HRB\text{-slice } S]_{CFG} \rangle$
 $\langle mx \in \text{obs-intra } m \ [HRB\text{-slice } S]_{CFG} \rangle$
have $mx \in \text{obs-intra } (\text{sourcenode } a) \ [HRB\text{-slice } S]_{CFG}$
by(*fastforce dest:edge-obs-intra-subset*)
from $\langle ms' = msx@m\#msx' \rangle$ *Nil* $\langle ms' = \text{targetnode } a\#\text{tl } ms \rangle$
 $\langle \text{hd } ms = \text{sourcenode } a \rangle \langle ms \neq [] \rangle$
have $ms = []@sourcenode\ a\#\text{tl } ms$ **by**(*cases ms*) *auto*
with $\langle ms'' = mx\#msx' \rangle \langle mx \in \text{obs-intra } (\text{sourcenode } a) \ [HRB\text{-slice } S]_{CFG} \rangle$
 $\langle \forall nx \in \text{set } msx'. \exists nx'. \text{call-of-return-node } nx\ nx' \wedge nx' \in [HRB\text{-slice } S]_{CFG} \rangle$
Nil
show *?thesis* **by**(*fastforce intro!:obsI*)
next
case (*Cons x xs*)
with $\langle ms' = msx@m\#msx' \rangle \langle ms' = \text{targetnode } a\#\text{tl } ms \rangle$
have $msx = \text{targetnode } a\#xs$ **by** *simp*
from *Cons* $\langle ms' = msx@m\#msx' \rangle \langle ms' = \text{targetnode } a\#\text{tl } ms \rangle \langle \text{hd } ms = \text{sourcenode } a \rangle$
have $ms = (\text{sourcenode } a\#xs)@m\#msx'$ **by**(*cases ms*) *auto*
from $\text{disj } \langle ms = (\text{sourcenode } a\#xs)@m\#msx' \rangle$
 $\langle \forall nx \in \text{set } msx'. \exists nx'. \text{call-of-return-node } nx\ nx' \wedge nx' \in [HRB\text{-slice } S]_{CFG} \rangle$
have $\text{disj}2:(\exists m \in \text{set } (xs@[m])). \exists m'. \text{call-of-return-node } m\ m' \wedge$
 $m' \notin [HRB\text{-slice } S]_{CFG} \vee \text{hd } ms \notin [HRB\text{-slice } S]_{CFG}$
by *fastforce*
hence $\forall zs\ z\ zs'. \text{sourcenode } a\#xs = zs@z\#zs' \wedge \text{obs-intra } z \ [HRB\text{-slice } S]_{CFG} \neq \{\}$
 $\longrightarrow (\exists z'' \in \text{set } (zs'@[m])). \exists mx. \text{call-of-return-node } z''\ mx \wedge$
 $mx \notin [HRB\text{-slice } S]_{CFG}$
proof(*cases hd ms*) $\notin [HRB\text{-slice } S]_{CFG}$
case *True*
with $\langle \text{hd } ms = \text{sourcenode } a \rangle$ **have** $\text{sourcenode } a \notin [HRB\text{-slice } S]_{CFG}$ **by** *simp*

```

with ⟨valid-edge a⟩ ⟨intra-kind (kind a)⟩
have obs-intra (targetnode a) [HRB-slice S]CFG =
  obs-intra (sourcenode a) [HRB-slice S]CFG
  by(rule edge-obs-intra-slice-eq)
with imp ⟨msx = targetnode a#xs⟩ show ?thesis
  by auto(case-tac zs,fastforce,erule-tac x=targetnode a#list in alle,fastforce)
next
case False
with ⟨hd ms = sourcenode a⟩ ⟨valid-edge a⟩
have obs-intra (sourcenode a) [HRB-slice S]CFG = {sourcenode a}
  by(fastforce intro!:n-in-obs-intra)
from False disj2
have ∃ m ∈ set (xs@[m]). ∃ m'. call-of-return-node m m' ∧ m' ∉ [HRB-slice
S]CFG
  by simp
with imp ⟨obs-intra (sourcenode a) [HRB-slice S]CFG = {sourcenode a}⟩
  ⟨msx = targetnode a#xs⟩ show ?thesis
  by auto(case-tac zs,fastforce,erule-tac x=targetnode a#list in alle,fastforce)
qed
with ⟨ms' = msx@m#msx'⟩ ⟨ms' = targetnode a # tl ms⟩ ⟨hd ms = sourcenode
a⟩
  ⟨ms'' = mx#msx'⟩ ⟨mx ∈ obs-intra m [HRB-slice S]CFG⟩
  ⟨∀ nx ∈ set msx'. ∃ nx'. call-of-return-node nx nx' ∧ nx' ∈ [HRB-slice S]CFG⟩
  ⟨ms = (sourcenode a#xs)@m#msx'⟩
  show ?thesis by(simp del:obs.simps)(rule obsI,auto)
qed
qed

```

1.13.1 Silent moves

inductive silent-move ::

```

'node SDG-node set ⇒ ('edge ⇒ ('var,'val,'ret,'pname) edge-kind) ⇒ 'node list
⇒
((('var → 'val) × 'ret) list ⇒ 'edge ⇒ 'node list ⇒ (('var → 'val) × 'ret) list ⇒
bool
(-, - ⊢ '(-,-) ->>τ '(-,-) [51,50,0,0,50,0,0] 51)

```

where silent-move-intra:

```

[[pred (f a) s; transfer (f a) s = s'; valid-edge a; intra-kind(kind a);
(∃ m ∈ set (tl ms). ∃ m'. call-of-return-node m m' ∧ m' ∉ [HRB-slice S]CFG)]

```

∨

```

hd ms ∉ [HRB-slice S]CFG; ∀ m ∈ set (tl ms). return-node m;
length s' = length s; length ms = length s;
hd ms = sourcenode a; ms' = (targetnode a)#tl ms]]
⇒ S,f ⊢ (ms,s) -a->τ (ms',s')

```

| silent-move-call:

```

[[pred (f a) s; transfer (f a) s = s'; valid-edge a; kind a = Q:r↔pfs;
valid-edge a'; a' ∈ get-return-edges a;

```

$(\exists m \in \text{set } (tl \ ms). \exists m'. \text{call-of-return-node } m \ m' \wedge m' \notin [HRB\text{-slice } S]_{CFG})$
 \vee
 $hd \ ms \notin [HRB\text{-slice } S]_{CFG}; \forall m \in \text{set } (tl \ ms). \text{return-node } m;$
 $\text{length } ms = \text{length } s; \text{length } s' = \text{Suc}(\text{length } s);$
 $hd \ ms = \text{sourcenode } a; ms' = (\text{targetnode } a) \# (\text{targetnode } a') \# tl \ ms$
 $\implies S, f \vdash (ms, s) -a \rightarrow_{\tau} (ms', s')$

 $| \text{silent-move-return:}$
 $\llbracket \text{pred } (f \ a) \ s; \text{transfer } (f \ a) \ s = s'; \text{valid-edge } a; \text{kind } a = Q \leftrightarrow_p f';$
 $\exists m \in \text{set } (tl \ ms). \exists m'. \text{call-of-return-node } m \ m' \wedge m' \notin [HRB\text{-slice } S]_{CFG};$
 $\forall m \in \text{set } (tl \ ms). \text{return-node } m; \text{length } ms = \text{length } s; \text{length } s = \text{Suc}(\text{length } s');$
 $s' \neq []; hd \ ms = \text{sourcenode } a; hd(tl \ ms) = \text{targetnode } a; ms' = tl \ ms$
 $\implies S, f \vdash (ms, s) -a \rightarrow_{\tau} (ms', s')$

lemma *silent-move-valid-nodes*:
 $\llbracket S, f \vdash (ms, s) -a \rightarrow_{\tau} (ms', s'); \forall m \in \text{set } ms'. \text{valid-node } m \rrbracket$
 $\implies \forall m \in \text{set } ms. \text{valid-node } m$
by(*induct rule:silent-move.induct*)(*case-tac ms, auto*)+

lemma *silent-move-return-node*:
 $S, f \vdash (ms, s) -a \rightarrow_{\tau} (ms', s') \implies \forall m \in \text{set } (tl \ ms'). \text{return-node } m$
proof(*induct rule:silent-move.induct*)
case (*silent-move-intra f a s s' ms n_c ms'*)
thus ?*case by simp*
next
case (*silent-move-call f a s s' Q r p fs a' ms n_c ms'*)
from $\langle \text{valid-edge } a' \rangle \langle \text{valid-edge } a \rangle \langle a' \in \text{get-return-edges } a \rangle$
have *return-node (targetnode a')* **by**(*fastforce simp:return-node-def*)
with $\langle \forall m \in \text{set } (tl \ ms). \text{return-node } m \rangle \langle ms' = \text{targetnode } a \# \text{targetnode } a' \# tl \ ms \rangle$
show ?*case by simp*
next
case (*silent-move-return f a s s' Q p f' ms n_c ms'*)
thus ?*case by (cases tl ms) auto*
qed

lemma *silent-move-equal-length*:
assumes $S, f \vdash (ms, s) -a \rightarrow_{\tau} (ms', s')$
shows $\text{length } ms = \text{length } s$ **and** $\text{length } ms' = \text{length } s'$
proof –
from $\langle S, f \vdash (ms, s) -a \rightarrow_{\tau} (ms', s') \rangle$
have $\text{length } ms = \text{length } s \wedge \text{length } ms' = \text{length } s'$
proof(*induct rule:silent-move.induct*)
case (*silent-move-intra f a s s' ms n_c ms'*)
from $\langle \text{pred } (f \ a) \ s \rangle$ **obtain** *cf cfs* **where** [*simp*]: $s = cf \# cfs$ **by**(*cases s*) *auto*

```

from ⟨length ms = length s⟩ ⟨ms' = targetnode a # tl ms⟩
  ⟨length s' = length s⟩ show ?case by simp
next
  case (silent-move-call f a s s' Q r p fs a' ms nc ms')
  from ⟨pred (f a) s⟩ obtain cf cfs where [simp]:s = cf#cfs by(cases s) auto
  from ⟨length ms = length s⟩ ⟨length s' = Suc (length s)⟩
    ⟨ms' = targetnode a # targetnode a' # tl ms⟩ show ?case by simp
next
  case (silent-move-return f a s s' Q p f' ms nc ms')
  from ⟨length ms = length s⟩ ⟨length s = Suc (length s')⟩ ⟨ms' = tl ms⟩ ⟨s' ≠ []⟩
  show ?case by simp
qed
thus length ms = length s and length ms' = length s' by simp-all
qed

```

lemma *silent-move-obs-slice*:

```

[[S,kind ⊢ (ms,s) -a→τ (ms',s'); msx ∈ obs ms' [HRB-slice S]CFG;
  ∀ n ∈ set (tl ms'). return-node n]]
⇒ msx ∈ obs ms [HRB-slice S]CFG
proof(induct S f≡kind ms s a ms' s' rule:silent-move.induct)
  case (silent-move-intra a s s' ms nc ms')
  from ⟨pred (kind a) s⟩ ⟨length ms = length s⟩ have ms ≠ []
  by(cases s) auto
  with silent-move-intra show ?case by -(rule intra-edge-obs-slice)
next
  case (silent-move-call a s s' Q r p fs a' ms S ms')
  note disj = ⟨(∃ m∈set (tl ms). ∃ m'. call-of-return-node m m' ∧
    m' ∉ [HRB-slice S]CFG) ∨ hd ms ∉ [HRB-slice S]CFG⟩
  from ⟨valid-edge a'⟩ ⟨valid-edge a⟩ ⟨a' ∈ get-return-edges a⟩
  have return-node (targetnode a') by(fastforce simp:return-node-def)
  with ⟨valid-edge a⟩ ⟨a' ∈ get-return-edges a⟩ ⟨valid-edge a'⟩
  have call-of-return-node (targetnode a') (sourcnode a)
  by(simp add:call-of-return-node-def) blast
  from ⟨pred (kind a) s⟩ ⟨length ms = length s⟩
  have ms ≠ [] by(cases s) auto
  from disj
  show ?case
  proof
    assume hd ms ∉ [HRB-slice S]CFG
    with ⟨hd ms = sourcnode a⟩ have sourcnode a ∉ [HRB-slice S]CFG by simp
    with ⟨call-of-return-node (targetnode a') (sourcnode a)⟩
      ⟨ms' = targetnode a # targetnode a' # tl ms⟩
    have ∃ n' ∈ set (tl ms'). ∃ nx. call-of-return-node n' nx ∧ nx ∉ [HRB-slice
S]CFG
      by fastforce
    with ⟨msx ∈ obs ms' [HRB-slice S]CFG⟩ ⟨ms' = targetnode a # targetnode a'
# tl ms⟩
    have msx ∈ obs (targetnode a' # tl ms) [HRB-slice S]CFG by simp

```

```

from ⟨valid-edge a⟩ ⟨a' ∈ get-return-edges a⟩
obtain a'' where valid-edge a'' and [simp]:sourcenode a'' = sourcenode a
  and [simp]:targetnode a'' = targetnode a' and intra-kind(kind a'')
  by -(drule call-return-node-edge,auto simp:intra-kind-def)
from ⟨∀ m ∈ set (tl ms). return-node m⟩ ⟨ms' = targetnode a # targetnode a' #
tl ms⟩
have ∀ m ∈ set (tl ms). return-node m by simp
with ⟨ms ≠ []⟩ ⟨msx ∈ obs (targetnode a' # tl ms) [HRB-slice S] CFG⟩
  ⟨valid-edge a'⟩ ⟨intra-kind(kind a')⟩ disj
  ⟨hd ms = sourcenode a⟩
show ?case by -(rule intra-edge-obs-slice,fastforce+)
next
assume ∃ m ∈ set (tl ms).
  ∃ m'. call-of-return-node m m' ∧ m' ∉ [HRB-slice S] CFG
with ⟨ms ≠ []⟩ ⟨msx ∈ obs ms' [HRB-slice S] CFG⟩
  ⟨ms' = targetnode a # targetnode a' # tl ms⟩
show ?thesis by(cases ms) auto
qed
next
case (silent-move-return a s s' Q p f' ms S ms')
from ⟨length ms = length s⟩ ⟨length s = Suc (length s')⟩ ⟨s' ≠ []⟩
have ms ≠ [] and tl ms ≠ [] by(auto simp:length-Suc-conv)
from ⟨∃ m ∈ set (tl ms).
  ∃ m'. call-of-return-node m m' ∧ m' ∉ [HRB-slice S] CFG⟩
  ⟨tl ms ≠ []⟩ ⟨hd (tl ms) = targetnode a⟩
have (∃ m'. call-of-return-node (targetnode a) m' ∧ m' ∉ [HRB-slice S] CFG) ∨
  (∃ m ∈ set (tl (tl ms)). ∃ m'. call-of-return-node m m' ∧ m' ∉ [HRB-slice S] CFG)
  by(cases tl ms) auto
hence obs ms [HRB-slice S] CFG = obs (tl ms) [HRB-slice S] CFG
proof
assume ∃ m'. call-of-return-node (targetnode a) m' ∧ m' ∉ [HRB-slice S] CFG
from ⟨tl ms ≠ []⟩ have hd (tl ms) ∈ set (tl ms) by simp
with ⟨hd (tl ms) = targetnode a⟩ have targetnode a ∈ set (tl ms) by simp
with ⟨ms ≠ []⟩
  ⟨∃ m'. call-of-return-node (targetnode a) m' ∧ m' ∉ [HRB-slice S] CFG⟩
have ∃ m ∈ set (tl ms). ∃ m'. call-of-return-node m m' ∧
  m' ∉ [HRB-slice S] CFG by(cases ms) auto
with ⟨ms ≠ []⟩ show ?thesis by(cases ms) auto
next
assume ∃ m ∈ set (tl (tl ms)). ∃ m'. call-of-return-node m m' ∧
  m' ∉ [HRB-slice S] CFG
with ⟨ms ≠ []⟩ ⟨tl ms ≠ []⟩ show ?thesis
  by(cases ms,auto simp:Let-def)(case-tac list,auto)+
qed
with ⟨ms' = tl ms⟩ ⟨msx ∈ obs ms' [HRB-slice S] CFG⟩ show ?case by simp
qed

```


lemma *silent-move-empty-obs-slice*:
assumes $S, f \vdash (ms, s) -a \rightarrow_{\tau} (ms', s')$ **and** $obs\ ms' \ [HRB\text{-}slice\ S]_{CFG} = \{\}$
shows $obs\ ms \ [HRB\text{-}slice\ S]_{CFG} = \{\}$
proof(*rule ccontr*)
assume $obs\ ms \ [HRB\text{-}slice\ S]_{CFG} \neq \{\}$
then obtain xs **where** $xs \in obs\ ms \ [HRB\text{-}slice\ S]_{CFG}$ **by** *fastforce*
from $\langle S, f \vdash (ms, s) -a \rightarrow_{\tau} (ms', s') \rangle$
have $\forall m \in set\ (tl\ ms).$ *return-node* m
by(*fastforce elim!:silent-move.cases simp:call-of-return-node-def*)
with $\langle xs \in obs\ ms \ [HRB\text{-}slice\ S]_{CFG} \rangle$
obtain $msx\ m\ msx'\ m'$ **where** $assms:ms = msx @ m \# msx'\ xs = m' \# msx'$
 $m' \in obs\text{-intra}\ m \ [HRB\text{-}slice\ S]_{CFG}$
 $\forall mx \in set\ msx'. \exists mx'. call\text{-of}\text{-return}\text{-node}\ mx\ mx' \wedge mx' \in [HRB\text{-}slice\ S]_{CFG}$
 $\forall xs\ x\ xs'. msx = xs @ x \# xs' \wedge obs\text{-intra}\ x \ [HRB\text{-}slice\ S]_{CFG} \neq \{\}$
 $\longrightarrow (\exists x'' \in set\ (xs' @ [m]). \exists mx. call\text{-of}\text{-return}\text{-node}\ x''\ mx \wedge$
 $mx \notin [HRB\text{-}slice\ S]_{CFG})$
by(*erule obsE*)
from $\langle S, f \vdash (ms, s) -a \rightarrow_{\tau} (ms', s') \rangle \langle obs\ ms' \ [HRB\text{-}slice\ S]_{CFG} = \{\} \rangle$ *assms*
show *False*
proof(*induct rule:silent-move.induct*)
case (*silent-move-intra* $f\ a\ s\ s'\ ms\ S\ ms'$)
note $disj = \langle (\exists m \in set\ (tl\ ms). \exists m'. call\text{-of}\text{-return}\text{-node}\ m\ m' \wedge$
 $m' \notin [HRB\text{-}slice\ S]_{CFG}) \vee hd\ ms \notin [HRB\text{-}slice\ S]_{CFG} \rangle$
note $msx = \forall xs\ x\ xs'. msx = xs @ x \# xs' \wedge obs\text{-intra}\ x \ [HRB\text{-}slice\ S]_{CFG} \neq$
 $\{\} \longrightarrow$
 $(\exists x'' \in set\ (xs' @ [m]). \exists mx. call\text{-of}\text{-return}\text{-node}\ x''\ mx \wedge mx \notin [HRB\text{-}slice$
 $S]_{CFG})$
note $msx' = \forall mx \in set\ msx'. \exists mx'. call\text{-of}\text{-return}\text{-node}\ mx\ mx' \wedge$
 $mx' \in [HRB\text{-}slice\ S]_{CFG}$
show *False*
proof(*cases msx*)
case *Nil*
with $\langle ms = msx @ m \# msx' \rangle \langle hd\ ms = sourcenode\ a \rangle$ **have** $[simp]:m =$
sourcenode a
and $tl\ ms = msx'$ **by** *simp-all*
from *Nil* $\langle ms' = targetnode\ a \# tl\ ms \rangle \langle ms = msx @ m \# msx' \rangle$
have $ms' = msx @ targetnode\ a \# msx'$ **by** *simp*
from msx' *disj* $\langle tl\ ms = msx' \rangle \langle hd\ ms = sourcenode\ a \rangle$
have *sourcenode* $a \notin [HRB\text{-}slice\ S]_{CFG}$ **by** *fastforce*
with $\langle valid\text{-edge}\ a \rangle \langle intra\text{-kind}\ (kind\ a) \rangle$
have $obs\text{-intra}\ (targetnode\ a) \ [HRB\text{-}slice\ S]_{CFG} =$
 $obs\text{-intra}\ (sourcenode\ a) \ [HRB\text{-}slice\ S]_{CFG}$ **by**(*rule edge-obs-intra-slice-eq*)
with $\langle m' \in obs\text{-intra}\ m \ [HRB\text{-}slice\ S]_{CFG} \rangle$
have $m' \in obs\text{-intra}\ (targetnode\ a) \ [HRB\text{-}slice\ S]_{CFG}$ **by** *simp*
from $msx\ Nil$ **have** $\forall xs\ x\ xs'. msx = xs @ x \# xs' \wedge$
 $obs\text{-intra}\ x \ [HRB\text{-}slice\ S]_{CFG} \neq \{\} \longrightarrow$
 $(\exists x'' \in set\ (xs' @ [targetnode\ a]). \exists mx. call\text{-of}\text{-return}\text{-node}\ x''\ mx \wedge$
 $mx \notin [HRB\text{-}slice\ S]_{CFG})$ **by** *simp*
with $\langle m' \in obs\text{-intra}\ (targetnode\ a) \ [HRB\text{-}slice\ S]_{CFG} \rangle$ msx'

```

  ⟨ms' = msx @ targetnode a # msx'⟩
  have m'#msx' ∈ obs ms' [HRB-slice S]CFG by(rule obsI)
  with ⟨obs ms' [HRB-slice S]CFG = {}⟩ show False by simp
next
case (Cons y ys)
  with ⟨ms = msx @ m # msx'⟩ ⟨ms' = targetnode a # tl ms⟩ ⟨hd ms =
sourcename a⟩
  have ms' = targetnode a # ys @ m # msx' and y = sourcename a
  and tl ms = ys @ m # msx' by simp-all
  { fix x assume x ∈ obs-intra (targetnode a) [HRB-slice S]CFG
  have obs-intra (sourcename a) [HRB-slice S]CFG ≠ {}
  proof(cases sourcename a ∈ [HRB-slice S]CFG)
    case True
      from ⟨valid-edge a⟩ have valid-node (sourcename a) by simp
      from this True
      have obs-intra (sourcename a) [HRB-slice S]CFG = {sourcename a}
      by(rule n-in-obs-intra)
      thus ?thesis by simp
    next
      case False
      from ⟨valid-edge a⟩ ⟨intra-kind (kind a)⟩ False
      have obs-intra (targetnode a) [HRB-slice S]CFG =
        obs-intra (sourcename a) [HRB-slice S]CFG
      by(rule edge-obs-intra-slice-eq)
      with ⟨x ∈ obs-intra (targetnode a) [HRB-slice S]CFG⟩ show ?thesis
      by fastforce
    qed }
  with msx Cons ⟨y = sourcename a⟩
  have ∀ x x xs'. targetnode a # ys = xs @ x # xs' ∧
  obs-intra x [HRB-slice S]CFG ≠ {} → (∃ x'' ∈ set (xs' @ [m]).
  ∃ mx. call-of-return-node x'' mx ∧ mx ∉ [HRB-slice S]CFG)
  apply clarsimp apply(case-tac xs) apply auto
  apply(erule-tac x=[] in alle) apply clarsimp
  apply(erule-tac x=sourcename a # list in alle) apply auto
  done
  with ⟨m' ∈ obs-intra m [HRB-slice S]CFG⟩ msx'
  ⟨ms' = targetnode a # ys @ m # msx'⟩
  have m'#msx' ∈ obs ms' [HRB-slice S]CFG by -(rule obsI,auto)
  with ⟨obs ms' [HRB-slice S]CFG = {}⟩ show False by simp
qed
next
case (silent-move-call f a s s' Q r p fs a' ms S ms')
  note disj = ⟨(∃ m ∈ set (tl ms). ∃ m'. call-of-return-node m m' ∧
  m' ∉ [HRB-slice S]CFG) ∨ hd ms ∉ [HRB-slice S]CFG⟩
  note msx = ⟨∀ xs x xs'. msx = xs @ x # xs' ∧ obs-intra x [HRB-slice S]CFG ≠
{}⟩ →
  ⟨∃ x'' ∈ set (xs' @ [m]). ∃ mx. call-of-return-node x'' mx ∧ mx ∉ [HRB-slice
S]CFG⟩
  note msx' = ⟨∀ mx ∈ set msx'. ∃ mx'. call-of-return-node mx mx' ∧

```

```

  mx' ∈ [HRB-slice S]CFG
from ⟨valid-edge a⟩ ⟨a' ∈ get-return-edges a⟩ obtain a'' where valid-edge a''
  and sourcenode a'' = sourcenode a and targetnode a'' = targetnode a'
  and intra-kind (kind a'')
  by(fastforce dest:call-return-node-edge simp:intra-kind-def)
from ⟨valid-edge a'⟩ ⟨valid-edge a⟩ ⟨a' ∈ get-return-edges a⟩
have call-of-return-node (targetnode a') (sourcenode a)
  by(fastforce simp:call-of-return-node-def return-node-def)
show False
proof(cases msx)
  case Nil
    with ⟨ms = msx @ m # msx'⟩ ⟨hd ms = sourcenode a⟩ have [simp]:m =
sourcenode a
    and tl ms = msx' by simp-all
    from Nil ⟨ms' = targetnode a # targetnode a' # tl ms⟩ ⟨ms = msx @ m #
msx'⟩
    have ms' = targetnode a # targetnode a' # msx' by simp
    from msx' disj ⟨tl ms = msx'⟩ ⟨hd ms = sourcenode a⟩
    have sourcenode a ∉ [HRB-slice S]CFG by fastforce
from ⟨valid-edge a'⟩ ⟨intra-kind (kind a'')⟩ ⟨sourcenode a ∉ [HRB-slice S]CFG⟩
  ⟨sourcenode a'' = sourcenode a⟩ ⟨targetnode a'' = targetnode a'⟩
    have obs-intra (targetnode a') [HRB-slice S]CFG =
obs-intra (sourcenode a) [HRB-slice S]CFG
    by(fastforce dest:edge-obs-intra-slice-eq)
    with ⟨m' ∈ obs-intra m [HRB-slice S]CFG⟩
    have m' ∈ obs-intra (targetnode a') [HRB-slice S]CFG by simp
from this msx' have m' # msx' ∈ obs (targetnode a' # msx') [HRB-slice S]CFG
  by(fastforce intro:obsI)
    from ⟨call-of-return-node (targetnode a') (sourcenode a)⟩
  ⟨sourcenode a ∉ [HRB-slice S]CFG⟩
    have ∃ m' ∈ set (targetnode a' # msx').
  ∃ mx. call-of-return-node m' mx ∧ mx ∉ [HRB-slice S]CFG
    by fastforce
    with ⟨m' # msx' ∈ obs (targetnode a' # msx') [HRB-slice S]CFG⟩
    have m' # msx' ∈ obs (targetnode a # targetnode a' # msx') [HRB-slice S]CFG
  by simp
    with ⟨ms' = targetnode a # targetnode a' # msx'⟩ ⟨obs ms' [HRB-slice S]CFG
= {}⟩
    show False by simp
next
  case (Cons y ys)
    with ⟨ms = msx @ m # msx'⟩ ⟨ms' = targetnode a # targetnode a' # tl ms⟩
  ⟨hd ms = sourcenode a⟩
    have ms' = targetnode a # targetnode a' # ys @ m # msx'
    and y = sourcenode a and tl ms = ys @ m # msx' by simp-all
    show False
proof(cases obs-intra (targetnode a) [HRB-slice S]CFG ≠ {} →
  (∃ x'' ∈ set (targetnode a' # ys @ [m]).
  ∃ mx. call-of-return-node x'' mx ∧ mx ∉ [HRB-slice S]CFG))

```

case *True*
hence $\text{imp:obs-intra } (\text{targetnode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG} \neq \{\} \longrightarrow$
 $(\exists x'' \in \text{set } (\text{targetnode } a' \# \text{ys } @ [m])).$
 $\exists mx. \text{call-of-return-node } x'' mx \wedge mx \notin \lfloor \text{HRB-slice } S \rfloor_{CFG} .$
show *False*
proof($\text{cases } \text{obs-intra } (\text{targetnode } a') \lfloor \text{HRB-slice } S \rfloor_{CFG} \neq \{\} \longrightarrow$
 $(\exists x'' \in \text{set } (\text{ys } @ [m])). \exists mx. \text{call-of-return-node } x'' mx \wedge$
 $mx \notin \lfloor \text{HRB-slice } S \rfloor_{CFG}$)
case *True*
with $\text{imp } msx \text{ Cons } \langle y = \text{sourcenode } a \rangle$
have $\forall xs \ x \ xs'. \text{targetnode } a \# \text{targetnode } a' \# \text{ys} = xs @ x \# xs' \wedge$
 $\text{obs-intra } x \lfloor \text{HRB-slice } S \rfloor_{CFG} \neq \{\} \longrightarrow (\exists x'' \in \text{set } (xs' @ [m])).$
 $\exists mx. \text{call-of-return-node } x'' mx \wedge mx \notin \lfloor \text{HRB-slice } S \rfloor_{CFG}$
apply *clarsimp* **apply**(*case-tac* xs) **apply** *fastforce*
apply(*case-tac* $list$) **apply** *fastforce* **apply** *clarsimp*
apply(*erule-tac* $x = \text{sourcenode } a \# lista$ **in** *allE*) **apply** *auto*
done
with $\langle m' \in \text{obs-intra } m \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle msx'$
 $\langle ms' = \text{targetnode } a \# \text{targetnode } a' \# \text{ys } @ m \# msx' \rangle$
have $m' \# msx' \in \text{obs } ms' \lfloor \text{HRB-slice } S \rfloor_{CFG}$ **by** $\neg(\text{rule } \text{obsI, auto})$
with $\langle \text{obs } ms' \lfloor \text{HRB-slice } S \rfloor_{CFG} = \{\} \rangle$ **show** *False* **by** *simp*
next
case *False*
hence $\text{obs-intra } (\text{targetnode } a') \lfloor \text{HRB-slice } S \rfloor_{CFG} \neq \{\}$
and $\text{all:} \forall x'' \in \text{set } (\text{ys } @ [m]). \forall mx. \text{call-of-return-node } x'' mx \longrightarrow$
 $mx \in \lfloor \text{HRB-slice } S \rfloor_{CFG}$
by *fastforce+*
have $\text{obs-intra } (\text{sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG} \neq \{\}$
proof($\text{cases } \text{sourcenode } a \in \lfloor \text{HRB-slice } S \rfloor_{CFG}$)
case *True*
from $\langle \text{valid-edge } a \rangle$ **have** *valid-node* ($\text{sourcenode } a$) **by** *simp*
from *this* *True*
have $\text{obs-intra } (\text{sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG} = \{\text{sourcenode } a\}$
by(*rule* *n-in-obs-intra*)
thus *?thesis* **by** *simp*
next
case *False*
with $\langle \text{sourcenode } a'' = \text{sourcenode } a \rangle$
have $\text{sourcenode } a'' \notin \lfloor \text{HRB-slice } S \rfloor_{CFG}$ **by** *simp*
with $\langle \text{valid-edge } a'' \rangle \langle \text{intra-kind } (\text{kind } a'') \rangle$
have $\text{obs-intra } (\text{targetnode } a'') \lfloor \text{HRB-slice } S \rfloor_{CFG} =$
 $\text{obs-intra } (\text{sourcenode } a'') \lfloor \text{HRB-slice } S \rfloor_{CFG}$
by(*rule* *edge-obs-intra-slice-eq*)
with $\langle \text{obs-intra } (\text{targetnode } a') \lfloor \text{HRB-slice } S \rfloor_{CFG} \neq \{\} \rangle$
 $\langle \text{sourcenode } a'' = \text{sourcenode } a \rangle \langle \text{targetnode } a'' = \text{targetnode } a' \rangle$
show *?thesis* **by** *fastforce*
qed
with $msx \text{ Cons } \langle y = \text{sourcenode } a \rangle$ *all*
show *False* **by** *simp blast*

```

qed
next
case False
hence obs-intra (targetnode a)  $\lfloor \text{HRB-slice } S \rfloor_{CFG} \neq \{\}$ 
and all: $\forall x'' \in \text{set } (\text{targetnode } a' \# \text{ys } @ [m])$ .
 $\forall mx. \text{call-of-return-node } x'' \text{ } mx \longrightarrow mx \in \lfloor \text{HRB-slice } S \rfloor_{CFG}$ 
by fastforce+
with Cons  $\langle y = \text{sourcenode } a \rangle \text{ } msx$ 
have obs-intra (sourcenode a)  $\lfloor \text{HRB-slice } S \rfloor_{CFG} = \{\}$  by auto blast
from  $\langle \text{call-of-return-node } (\text{targetnode } a') (\text{sourcenode } a) \rangle \text{ } all$ 
have sourcenode a  $\in \lfloor \text{HRB-slice } S \rfloor_{CFG}$  by fastforce
from  $\langle \text{valid-edge } a \rangle \text{ } have \text{valid-node } (\text{sourcenode } a)$  by simp
from this  $\langle \text{sourcenode } a \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$ 
have obs-intra (sourcenode a)  $\lfloor \text{HRB-slice } S \rfloor_{CFG} = \{\text{sourcenode } a\}$ 
by (rule n-in-obs-intra)
with  $\langle \text{obs-intra } (\text{sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG} = \{\} \rangle$  show False by
simp
qed
qed
next
case (silent-move-return f a s s' Q p f' ms S ms')
note  $msx = \langle \forall xs \ x \ xs' . msx = xs @ x \# xs' \wedge \text{obs-intra } x \lfloor \text{HRB-slice } S \rfloor_{CFG} \neq \{\} \rangle \longrightarrow$ 
 $(\exists x'' \in \text{set } (xs' @ [m]). \exists mx. \text{call-of-return-node } x'' \text{ } mx \wedge mx \notin \lfloor \text{HRB-slice } S \rfloor_{CFG})$ 
note  $msx' = \langle \forall mx \in \text{set } msx' . \exists mx' . \text{call-of-return-node } mx \text{ } mx' \wedge mx' \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$ 
show False
proof(cases msx)
case Nil
with  $\langle ms = msx @ m \# msx' \rangle \langle \text{hd } ms = \text{sourcenode } a \rangle \text{ } have \text{tl } ms = msx'$ 
by simp
with  $\langle \exists m \in \text{set } (\text{tl } ms) . \exists m' . \text{call-of-return-node } m \text{ } m' \wedge m' \notin \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$ 
 $msx'$ 
show False by fastforce
next
case (Cons y ys)
with  $\langle ms = msx @ m \# msx' \rangle \langle \text{hd } ms = \text{sourcenode } a \rangle \langle ms' = \text{tl } ms \rangle$ 
have  $ms' = ys @ m \# msx'$  and  $y = \text{sourcenode } a$  by simp-all
from msx Cons have  $\forall xs \ x \ xs' . ys = xs @ x \# xs' \wedge$ 
 $\text{obs-intra } x \lfloor \text{HRB-slice } S \rfloor_{CFG} \neq \{\} \longrightarrow (\exists x'' \in \text{set } (xs' @ [m]).$ 
 $\exists mx. \text{call-of-return-node } x'' \text{ } mx \wedge mx \notin \lfloor \text{HRB-slice } S \rfloor_{CFG})$ 
by auto (erule-tac x=y # xs in allE, auto)
with  $\langle m' \in \text{obs-intra } m \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle \text{ } msx' \langle ms' = ys @ m \# msx' \rangle$ 
have  $m' \# msx' \in \text{obs } ms' \lfloor \text{HRB-slice } S \rfloor_{CFG}$  by (rule obsI)
with  $\langle \text{obs } ms' \lfloor \text{HRB-slice } S \rfloor_{CFG} = \{\} \rangle$  show False by simp
qed
qed

```

qed

inductive *silent-moves* ::

'node SDG-node set \Rightarrow ('edge \Rightarrow ('var,'val,'ret,'pname) edge-kind) \Rightarrow 'node list
 \Rightarrow
(('var \rightarrow 'val) \times 'ret) list \Rightarrow 'edge list \Rightarrow 'node list \Rightarrow (('var \rightarrow 'val) \times 'ret) list
 \Rightarrow bool
(-, - \vdash '(-,-) \Rightarrow_{τ} '(-,-) [51,50,0,0,50,0,0] 51)

where *silent-moves-Nil*: length ms = length s \Rightarrow S,f \vdash (ms,s) =[] \Rightarrow_{τ} (ms,s)

| *silent-moves-Cons*:

[S,f \vdash (ms,s) -a \rightarrow_{τ} (ms',s'); S,f \vdash (ms',s') =as \Rightarrow_{τ} (ms'',s'')]
 \Rightarrow S,f \vdash (ms,s) =a#as \Rightarrow_{τ} (ms'',s'')

lemma *silent-moves-equal-length*:

assumes S,f \vdash (ms,s) =as \Rightarrow_{τ} (ms',s')

shows length ms = length s **and** length ms' = length s'

proof -

from (S,f \vdash (ms,s) =as \Rightarrow_{τ} (ms',s'))

have length ms = length s \wedge length ms' = length s'

proof(*induct rule:silent-moves.induct*)

case (*silent-moves-Cons* S f ms s a ms' s' as ms'' s'')

from (S,f \vdash (ms,s) -a \rightarrow_{τ} (ms',s'))

have length ms = length s **and** length ms' = length s'

by(*rule silent-move-equal-length*)**+**

with (length ms' = length s' \wedge length ms'' = length s'')

show ?case **by** *simp*

qed *simp*

thus length ms = length s length ms' = length s' **by** *simp-all*

qed

lemma *silent-moves-Append*:

[S,f \vdash (ms,s) =as \Rightarrow_{τ} (ms'',s''); S,f \vdash (ms'',s'') =as' \Rightarrow_{τ} (ms',s')]

\Rightarrow S,f \vdash (ms,s) =as@as' \Rightarrow_{τ} (ms',s')

by(*induct rule:silent-moves.induct*)(*auto intro:silent-moves.intros*)

lemma *silent-moves-split*:

assumes S,f \vdash (ms,s) =as@as' \Rightarrow_{τ} (ms',s')

obtains ms'' s'' **where** S,f \vdash (ms,s) =as \Rightarrow_{τ} (ms'',s'')

and S,f \vdash (ms'',s'') =as' \Rightarrow_{τ} (ms',s')

proof(*atomize-elim*)

from (S,f \vdash (ms,s) =as@as' \Rightarrow_{τ} (ms',s'))

show \exists ms'' s''. S,f \vdash (ms,s) =as \Rightarrow_{τ} (ms'',s'') \wedge S,f \vdash (ms'',s'') =as' \Rightarrow_{τ}

(ms',s')
proof(*induct as arbitrary:ms s*)
 case *Nil*
 from $\langle S,f \vdash (ms,s) = [] @ as' \Rightarrow_{\tau} (ms',s') \rangle$ **have** $length\ ms = length\ s$
 by(*fastforce intro:silent-moves-equal-length*)
 hence $S,f \vdash (ms,s) = [] \Rightarrow_{\tau} (ms,s)$ **by**(*rule silent-moves-Nil*)
 with $\langle S,f \vdash (ms,s) = [] @ as' \Rightarrow_{\tau} (ms',s') \rangle$ **show** *?case* **by** *fastforce*
next
 case (*Cons ax asx*)
 note $IH = \langle \bigwedge ms\ s.\ S,f \vdash (ms,s) = asx @ as' \Rightarrow_{\tau} (ms',s') \implies$
 $\exists ms''\ s''.\ S,f \vdash (ms,s) = asx \Rightarrow_{\tau} (ms'',s'') \wedge S,f \vdash (ms'',s'') = as' \Rightarrow_{\tau} (ms',s') \rangle$
 from $\langle S,f \vdash (ms,s) = (ax \# asx) @ as' \Rightarrow_{\tau} (ms',s') \rangle$
 obtain $msx\ sx$ **where** $S,f \vdash (ms,s) - ax \rightarrow_{\tau} (msx,sx)$
 and $S,f \vdash (msx,sx) = asx @ as' \Rightarrow_{\tau} (ms',s')$
 by(*auto elim:silent-moves.cases*)
 from $IH[OF\ this(2)]$ **obtain** $ms''\ s''$ **where** $S,f \vdash (msx,sx) = asx \Rightarrow_{\tau} (ms'',s'')$
 and $S,f \vdash (ms'',s'') = as' \Rightarrow_{\tau} (ms',s')$ **by** *blast*
 from $\langle S,f \vdash (ms,s) - ax \rightarrow_{\tau} (msx,sx) \rangle \langle S,f \vdash (msx,sx) = asx \Rightarrow_{\tau} (ms'',s'') \rangle$
 have $S,f \vdash (ms,s) = ax \# asx \Rightarrow_{\tau} (ms'',s'')$ **by**(*rule silent-moves-Cons*)
 with $\langle S,f \vdash (ms'',s'') = as' \Rightarrow_{\tau} (ms',s') \rangle$ **show** *?case* **by** *blast*
qed
qed

lemma *valid-nodes-silent-moves*:
 $\llbracket S,f \vdash (ms,s) = as' \Rightarrow_{\tau} (ms',s'); \forall m \in set\ ms.\ valid-node\ m \rrbracket$
 $\implies \forall m \in set\ ms'.\ valid-node\ m$
proof(*induct rule:silent-moves.induct*)
 case (*silent-moves-Cons S f ms s a ms' s' as ms'' s''*)
 note $IH = \langle \forall m \in set\ ms'.\ valid-node\ m \implies \forall m \in set\ ms''.\ valid-node\ m \rangle$
 from $\langle S,f \vdash (ms,s) - a \rightarrow_{\tau} (ms',s') \rangle \langle \forall m \in set\ ms.\ valid-node\ m \rangle$
 have $\forall m \in set\ ms'.\ valid-node\ m$
 apply – **apply**(*erule silent-move.cases*) **apply** *auto*
 by(*cases ms,auto dest:get-return-edges-valid*)
 from $IH[OF\ this]$ **show** *?case* .
qed *simp*

lemma *return-nodes-silent-moves*:
 $\llbracket S,f \vdash (ms,s) = as' \Rightarrow_{\tau} (ms',s'); \forall m \in set\ (tl\ ms).\ return-node\ m \rrbracket$
 $\implies \forall m \in set\ (tl\ ms').\ return-node\ m$
by(*induct rule:silent-moves.induct,auto dest:silent-move-return-node*)

lemma *silent-moves-intra-path*:
 $\llbracket S,f \vdash (m \# ms,s) = as \Rightarrow_{\tau} (m' \# ms',s'); \forall a \in set\ as.\ intra-kind(kind\ a) \rrbracket$
 $\implies ms = ms' \wedge get-proc\ m = get-proc\ m'$
proof(*induct S f m # ms s as m' # ms' s' arbitrary:m*
rule:silent-moves.induct)

```

case (silent-moves-Cons  $S f sx a msx' sx' as s''$ )
thus ?case
proof(induct - -  $m \# ms$  - - - - rule:silent-move.induct)
  case (silent-move-intra  $f a s s' n_c msx'$ )
  note  $IH = \langle \bigwedge m. \llbracket msx' = m \# ms; \forall a \in set\ as. \text{intra-kind } (kind\ a) \rrbracket$ 
     $\implies ms = ms' \wedge \text{get-proc } m = \text{get-proc } m' \rangle$ 
  from  $\langle msx' = \text{targetnode } a \# tl\ (m \# ms) \rangle$ 
  have  $msx' = \text{targetnode } a \# ms$  by simp
  from  $\langle \forall a \in set\ (a \# as). \text{intra-kind } (kind\ a) \rangle$  have  $\forall a \in set\ as. \text{intra-kind } (kind\ a)$ 
  a)
    by simp
  from  $IH[OF\ \langle msx' = \text{targetnode } a \# ms \rangle\ \text{this}]$ 
  have  $ms = ms'$  and  $\text{get-proc } (\text{targetnode } a) = \text{get-proc } m'$  by simp-all
  moreover
  from  $\langle \text{valid-edge } a \rangle \langle \text{intra-kind } (kind\ a) \rangle$ 
  have  $\text{get-proc } (\text{sourcenode } a) = \text{get-proc } (\text{targetnode } a)$  by(rule get-proc-intra)
  moreover
  from  $\langle hd\ (m \# ms) = \text{sourcenode } a \rangle$  have  $m = \text{sourcenode } a$  by simp
  ultimately show ?case using  $\langle ms = ms' \rangle$  by simp
  qed (auto simp:intra-kind-def)
qed simp

```

lemma *silent-moves-nodestack-notempty*:

```

 $\llbracket S, f \vdash (ms, s) = as \Rightarrow_{\tau} (ms', s'); ms \neq [] \rrbracket \implies ms' \neq []$ 
apply(induct  $S f ms s as ms' s'$  rule:silent-moves.induct) apply auto
apply(erule silent-move.cases) apply auto
apply(case-tac tl msa) by auto

```

lemma *silent-moves-obs-slice*:

```

 $\llbracket S, kind \vdash (ms, s) = as \Rightarrow_{\tau} (ms', s'); mx \in obs\ ms' \llbracket HRB\text{-slice } S \rrbracket_{CFG};$ 
 $\forall n \in set\ (tl\ ms'). \text{return-node } n \rrbracket$ 
 $\implies mx \in obs\ ms \llbracket HRB\text{-slice } S \rrbracket_{CFG} \wedge (\forall n \in set\ (tl\ ms). \text{return-node } n)$ 
proof(induct  $S f \equiv kind\ ms\ s\ as\ ms'\ s'$  rule:silent-moves.induct)
  case silent-moves-Nil thus ?case by simp
next
  case (silent-moves-Cons  $S ms s a ms' s' as ms'' s''$ )
  note  $IH = \langle \llbracket mx \in obs\ ms'' \llbracket HRB\text{-slice } S \rrbracket_{CFG}; \forall m \in set\ (tl\ ms''). \text{return-node } m \rrbracket$ 
     $\implies mx \in obs\ ms' \llbracket HRB\text{-slice } S \rrbracket_{CFG} \wedge (\forall m \in set\ (tl\ ms'). \text{return-node } m) \rangle$ 
  from  $IH[OF\ \langle mx \in obs\ ms'' \llbracket HRB\text{-slice } S \rrbracket_{CFG} \rangle \langle \forall m \in set\ (tl\ ms''). \text{return-node } m \rangle]$ 
  have  $mx \in obs\ ms' \llbracket HRB\text{-slice } S \rrbracket_{CFG}$  and  $\forall m \in set\ (tl\ ms'). \text{return-node } m$ 
    by simp-all
  with  $\langle S, kind \vdash (ms, s) -a \rightarrow_{\tau} (ms', s') \rangle$ 
  have  $mx \in obs\ ms \llbracket HRB\text{-slice } S \rrbracket_{CFG}$  by(fastforce intro:silent-move-obs-slice)
  moreover
  from  $\langle S, kind \vdash (ms, s) -a \rightarrow_{\tau} (ms', s') \rangle$  have  $\forall m \in set\ (tl\ ms). \text{return-node } m$ 

```


by(*fastforce elim:silent-move.cases*)
ultimately show *?case* by *simp*
qed

lemma *silent-moves-empty-obs-slice*:

$\llbracket S, f \vdash (ms, s) = as \Rightarrow_{\tau} (ms', s') \rrbracket; \text{obs } ms' \llbracket HRB\text{-slice } S \rrbracket_{CFG} = \{\}$
 $\implies \text{obs } ms \llbracket HRB\text{-slice } S \rrbracket_{CFG} = \{\}$

proof(*induct rule:silent-moves.induct*)

case *silent-moves-Nil* **thus** *?case* by *simp*

next

case (*silent-moves-Cons* *S f ms s a ms' s' as ms'' s''*)

note $IH = \langle \text{obs } ms'' \llbracket HRB\text{-slice } S \rrbracket_{CFG} = \{\} \implies \text{obs } ms' \llbracket HRB\text{-slice } S \rrbracket_{CFG} = \{\} \rangle$

from $IH[OF \langle \text{obs } ms'' \llbracket HRB\text{-slice } S \rrbracket_{CFG} = \{\} \rangle]$

have $\text{obs } ms' \llbracket HRB\text{-slice } S \rrbracket_{CFG} = \{\}$ by *simp*

with $\langle S, f \vdash (ms, s) - a \rightarrow_{\tau} (ms', s') \rangle$

show *?case* by $-(\text{rule } \textit{silent-move-empty-obs-slice}, \textit{fastforce})$

qed

lemma *silent-moves-preds-transfers*:

assumes $S, f \vdash (ms, s) = as \Rightarrow_{\tau} (ms', s')$

shows *preds* (*map f as*) *s* **and** *transfers* (*map f as*) *s* = *s'*

proof –

from $\langle S, f \vdash (ms, s) = as \Rightarrow_{\tau} (ms', s') \rangle$

have *preds* (*map f as*) *s* \wedge *transfers* (*map f as*) *s* = *s'*

proof(*induct rule:silent-moves.induct*)

case *silent-moves-Nil* **thus** *?case* by *simp*

next

case (*silent-moves-Cons* *S f ms s a ms' s' as ms'' s''*)

from $\langle S, f \vdash (ms, s) - a \rightarrow_{\tau} (ms', s') \rangle$

have *pred* (*f a*) *s* **and** *transfer* (*f a*) *s* = *s'* by(*auto elim:silent-move.cases*)

with $\langle \text{preds } (\text{map } f \text{ as}) \text{ } s' \wedge \text{transfers } (\text{map } f \text{ as}) \text{ } s' = s'' \rangle$

show *?case* by *fastforce*

qed

thus *preds* (*map f as*) *s* **and** *transfers* (*map f as*) *s* = *s'* by *simp-all*

qed

lemma *silent-moves-intra-path-obs*:

assumes $m' \in \text{obs-intra } m \llbracket HRB\text{-slice } S \rrbracket_{CFG}$ **and** $\text{length } s = \text{length } (m \# msx')$

and $\forall m \in \text{set } msx'. \text{return-node } m$

obtains *as'* **where** $S, \text{slice-kind } S \vdash (m \# msx', s) = as' \Rightarrow_{\tau} (m' \# msx', s)$

proof(*atomize-elim*)

from $\langle m' \in \text{obs-intra } m \llbracket HRB\text{-slice } S \rrbracket_{CFG} \rangle$

obtain *as* **where** $m - as \rightarrow_{i^*} m'$ **and** $m' \in \llbracket HRB\text{-slice } S \rrbracket_{CFG}$

by $-(\text{erule } \textit{obs-intraE})$

from $\langle m -as \rightarrow_i^* m' \rangle$ **obtain** x **where** $\text{distance } m \ m' \ x$ **and** $x \leq \text{length } as$
by(*erule every-path-distance*)
from $\langle \text{distance } m \ m' \ x \rangle \langle m' \in \text{obs-intra } m \ [HRB\text{-slice } S]_{CFG} \rangle$
 $\langle \text{length } s = \text{length } (m \# msx') \rangle \langle \forall m \in \text{set } msx'. \text{return-node } m \rangle$
show $\exists as. S, \text{slice-kind } S \vdash (m \# msx', s) = as \Rightarrow_\tau (m' \# msx', s)$
proof(*induct x arbitrary:m s rule:nat.induct*)
fix m **fix** $s::('var \rightarrow 'val) \times 'ret$ *list*
assume $\text{distance } m \ m' \ 0$ **and** $\text{length } s = \text{length } (m \# msx')$
then obtain as' **where** $m -as' \rightarrow_i^* m'$ **and** $\text{length } as' = 0$
by(*auto elim:distance.cases*)
hence $m -[] \rightarrow_i^* m'$ **by**(*cases as*) *auto*
hence $[simp]: m = m'$ **by**(*fastforce elim:path.cases simp:intra-path-def*)
with $\langle \text{length } s = \text{length } (m \# msx') \rangle$ [*THEN sym*]
have $S, \text{slice-kind } S \vdash (m \# msx', s) = [] \Rightarrow_\tau (m \# msx', s)$
by $-(\text{rule silent-moves-Nil})$
thus $\exists as. S, \text{slice-kind } S \vdash (m \# msx', s) = as \Rightarrow_\tau (m' \# msx', s)$ **by** *simp blast*
next
fix $x \ m$ **fix** $s::('var \rightarrow 'val) \times 'ret$ *list*
assume $\text{distance } m \ m' (Suc \ x)$ **and** $m' \in \text{obs-intra } m \ [HRB\text{-slice } S]_{CFG}$
and $\text{length } s = \text{length } (m \# msx')$ **and** $\forall m \in \text{set } msx'. \text{return-node } m$
and $IH: \bigwedge m \ s. \llbracket \text{distance } m \ m' \ x; m' \in \text{obs-intra } m \ [HRB\text{-slice } S]_{CFG};$
 $\text{length } s = \text{length } (m \# msx'); \forall m \in \text{set } msx'. \text{return-node } m \rrbracket$
 $\implies \exists as. S, \text{slice-kind } S \vdash (m \# msx', s) = as \Rightarrow_\tau (m' \# msx', s)$
from $\langle m' \in \text{obs-intra } m \ [HRB\text{-slice } S]_{CFG} \rangle$ **have** *valid-node* m
by(*rule in-obs-intra-valid*)
with $\langle \text{distance } m \ m' (Suc \ x) \rangle$ **have** $m \neq m'$
by(*fastforce elim:distance.cases dest:empty-path simp:intra-path-def*)
have $m \notin [HRB\text{-slice } S]_{CFG}$
proof
assume $isin:m \in [HRB\text{-slice } S]_{CFG}$
with $\langle \text{valid-node } m \rangle$ **have** $\text{obs-intra } m \ [HRB\text{-slice } S]_{CFG} = \{m\}$
by(*fastforce intro!:n-in-obs-intra*)
with $\langle m' \in \text{obs-intra } m \ [HRB\text{-slice } S]_{CFG} \rangle \langle m \neq m' \rangle$ **show** *False* **by** *simp*
qed
from $\langle \text{distance } m \ m' (Suc \ x) \rangle$ **obtain** a **where** *valid-edge* a **and** $m = \text{sourcenode}$
 a
and *intra-kind*(*kind* a) **and** $\text{distance } (\text{targetnode } a) \ m' \ x$
and $\text{target:targetnode } a = (\text{SOME } mx. \exists a'. \text{sourcenode } a = \text{sourcenode } a' \wedge$
 $\text{distance } (\text{targetnode } a') \ m' \ x \wedge$
 $\text{valid-edge } a' \wedge \text{intra-kind } (\text{kind } a') \wedge$
 $\text{targetnode } a' = mx)$
by $-(\text{erule distance-successor-distance,simp+})$
from $\langle m' \in \text{obs-intra } m \ [HRB\text{-slice } S]_{CFG} \rangle$
have $\text{obs-intra } m \ [HRB\text{-slice } S]_{CFG} = \{m'\}$
by(*rule obs-intra-singleton-element*)
with $\langle \text{valid-edge } a \rangle \langle m \notin [HRB\text{-slice } S]_{CFG} \rangle \langle m = \text{sourcenode } a \rangle \langle \text{intra-kind } (\text{kind}$
 $a) \rangle$
have $\text{disj:obs-intra } (\text{targetnode } a) \ [HRB\text{-slice } S]_{CFG} = \{\} \vee$
 $\text{obs-intra } (\text{targetnode } a) \ [HRB\text{-slice } S]_{CFG} = \{m'\}$

by $-(\text{drule-tac } S = [\text{HRB-slice } S]_{CFG} \text{ in } \text{edge-obs-intra-subset, auto})$
from $\langle \text{intra-kind}(\text{kind } a) \rangle \langle \text{length } s = \text{length } (m \# \text{msx}') \rangle \langle m \notin [\text{HRB-slice } S]_{CFG} \rangle$
 $\langle m = \text{sourcenode } a \rangle$
have $\text{length} : \text{length } (\text{transfer } (\text{slice-kind } S \ a) \ s) = \text{length } (\text{targetnode } a \# \text{msx}')$
by $(\text{cases } s)$
 $(\text{auto split:split-if-asm simp add:Let-def slice-kind-def intra-kind-def})$
from $\langle \text{distance } (\text{targetnode } a) \ m' \ x \rangle$ **obtain** asx **where** $\text{targetnode } a - \text{asx} \rightarrow_i^* m'$
and $\text{length } \text{asx} = x$ **and** $\forall \text{as}' . \text{targetnode } a - \text{as}' \rightarrow_i^* m' \longrightarrow x \leq \text{length } \text{as}'$
by $(\text{auto elim:distance.cases})$
from $\langle \text{targetnode } a - \text{asx} \rightarrow_i^* m' \rangle \langle m' \in [\text{HRB-slice } S]_{CFG} \rangle$
obtain m_x **where** $m_x \in \text{obs-intra } (\text{targetnode } a) [\text{HRB-slice } S]_{CFG}$
by $(\text{erule path-ex-obs-intra})$
with disj **have** $m' \in \text{obs-intra } (\text{targetnode } a) [\text{HRB-slice } S]_{CFG}$ **by** fastforce
from $\text{IH}[\text{OF } \langle \text{distance } (\text{targetnode } a) \ m' \ x \rangle \text{ this length } \langle \forall m \in \text{set } \text{msx}' . \text{return-node } m \rangle]$
obtain asx' **where** $\text{moves} : S, \text{slice-kind } S \vdash$
 $(\text{targetnode } a \# \text{msx}' , \text{transfer } (\text{slice-kind } S \ a) \ s) = \text{asx}' \Rightarrow_\tau$
 $(m' \# \text{msx}' , \text{transfer } (\text{slice-kind } S \ a) \ s)$ **by** blast
have $\text{pred } (\text{slice-kind } S \ a) \ s \wedge \text{transfer } (\text{slice-kind } S \ a) \ s = s$
proof $(\text{cases } \text{kind } a)$
fix f **assume** $\text{kind } a = \uparrow f$
with $\langle m \notin [\text{HRB-slice } S]_{CFG} \rangle \langle m = \text{sourcenode } a \rangle$ **have** $\text{slice-kind } S \ a = \uparrow id$
by $(\text{fastforce intro:slice-kind-Upd})$
with $\langle \text{length } s = \text{length } (m \# \text{msx}') \rangle$ **show** $?thesis$ **by** $(\text{cases } s) \text{ auto}$
next
fix Q **assume** $\text{kind } a = (Q)_{\checkmark}$
with $\langle m \notin [\text{HRB-slice } S]_{CFG} \rangle \langle m = \text{sourcenode } a \rangle$
 $\langle m' \in \text{obs-intra } m [\text{HRB-slice } S]_{CFG} \rangle \langle \text{distance } (\text{targetnode } a) \ m' \ x \rangle$
 $\langle \text{distance } m \ m' \ (\text{Suc } x) \rangle \text{target}$
have $\text{slice-kind } S \ a = (\lambda s . \text{True})_{\checkmark}$
by $(\text{fastforce intro:slice-kind-Pred-obs-nearer-SOME})$
with $\langle \text{length } s = \text{length } (m \# \text{msx}') \rangle$ **show** $?thesis$ **by** $(\text{cases } s) \text{ auto}$
next
fix $Q \ r \ p \ fs$ **assume** $\text{kind } a = Q : r \hookrightarrow_p fs$
with $\langle \text{intra-kind}(\text{kind } a) \rangle$ **have** False **by** $(\text{simp add:intra-kind-def})$
thus $?thesis$ **by** simp
next
fix $Q \ p \ f$ **assume** $\text{kind } a = Q \leftrightarrow_p f$
with $\langle \text{intra-kind}(\text{kind } a) \rangle$ **have** False **by** $(\text{simp add:intra-kind-def})$
thus $?thesis$ **by** simp
qed
hence $\text{pred } (\text{slice-kind } S \ a) \ s$ **and** $\text{transfer } (\text{slice-kind } S \ a) \ s = s$
by simp-all
with $\langle m \notin [\text{HRB-slice } S]_{CFG} \rangle \langle m = \text{sourcenode } a \rangle \langle \text{valid-edge } a \rangle$
 $\langle \text{intra-kind}(\text{kind } a) \rangle \langle \text{length } s = \text{length } (m \# \text{msx}') \rangle \langle \forall m \in \text{set } \text{msx}' . \text{return-node } m \rangle$
have $S, \text{slice-kind } S \vdash (\text{sourcenode } a \# \text{msx}' , s) - a \rightarrow_\tau$

(targetnode a#msx',transfer (slice-kind S a) s)
 by(fastforce intro:silent-move-intra)
 with moves ⟨transfer (slice-kind S a) s = s⟩ ⟨m = sourcenode a⟩
 have S,slice-kind S ⊢ (m#msx',s) = a#asx'⇒_τ (m'#msx',s)
 by(fastforce intro:silent-moves-Cons)
 thus ∃ as. S,slice-kind S ⊢ (m#msx',s) = as⇒_τ (m'#msx',s) by blast
 qed
 qed

lemma silent-moves-intra-path-no-obs:

assumes obs-intra m [HRB-slice S]_{CFG} = {} and method-exit m'
 and get-proc m = get-proc m' and valid-node m and length s = length (m#msx')
 and ∀ m ∈ set msx'. return-node m
 obtains as where S,slice-kind S ⊢ (m#msx',s) = as⇒_τ (m'#msx',s)

proof(atomize-elim)

from ⟨method-exit m'⟩ ⟨get-proc m = get-proc m'⟩ ⟨valid-node m⟩
 obtain as where m -as→_ι* m' by(erule intra-path-to-matching-method-exit)
 then obtain x where distance m m' x and x ≤ length as
 by(erule every-path-distance)

from ⟨distance m m' x⟩ ⟨m -as→_ι* m'⟩ ⟨obs-intra m [HRB-slice S]_{CFG} = {}⟩
 ⟨length s = length (m#msx')⟩ ⟨∀ m ∈ set msx'. return-node m⟩

show ∃ as. S,slice-kind S ⊢ (m#msx',s) = as⇒_τ (m'#msx',s)

proof(induct x arbitrary:m as s rule:nat.induct)

fix m fix s::(('var → 'val) × 'ret) list

assume distance m m' 0 and length s = length (m#msx')

then obtain as' where m -as'→_ι* m' and length as' = 0

by(auto elim:distance.cases)

hence m -[]→_ι* m' by(cases as) auto

hence [simp]:m = m' by(fastforce elim:path.cases simp:intra-path-def)

with ⟨length s = length (m#msx')⟩[THEN sym]

have S,slice-kind S ⊢ (m#msx',s) = []⇒_τ (m#msx',s)

by(fastforce intro:silent-moves-Nil)

thus ∃ as. S,slice-kind S ⊢ (m#msx',s) = as⇒_τ (m'#msx',s) by simp blast

next

fix x m as fix s::(('var → 'val) × 'ret) list

assume distance m m' (Suc x) and m -as→_ι* m'

and obs-intra m [HRB-slice S]_{CFG} = {}

and length s = length (m#msx') and ∀ m ∈ set msx'. return-node m

and IH:∧ m as s. [distance m m' x; m -as→_ι* m';

obs-intra m [HRB-slice S]_{CFG} = {}]; length s = length (m#msx');

∀ m ∈ set msx'. return-node m]

⇒ ∃ as. S,slice-kind S ⊢ (m#msx',s) = as⇒_τ (m'#msx',s)

from ⟨m -as→_ι* m'⟩ have valid-node m

by(fastforce intro:path-valid-node simp:intra-path-def)

from ⟨m -as→_ι* m'⟩ have get-proc m = get-proc m' by(rule intra-path-get-procs)

have m ∉ [HRB-slice S]_{CFG}

proof

assume m ∈ [HRB-slice S]_{CFG}

with $\langle \text{valid-node } m \rangle$ **have** $\text{obs-intra } m \llbracket \text{HRB-slice } S \rrbracket_{CFG} = \{m\}$
by $(\text{fastforce intro!:n-in-obs-intra})$
with $\langle \text{obs-intra } m \llbracket \text{HRB-slice } S \rrbracket_{CFG} = \{\} \rangle$ **show** *False* **by** *simp*
qed
from $\langle \text{distance } m \ m' \ (Suc \ x) \rangle$ **obtain** *a* **where** *valid-edge a* **and** $m = \text{sourcenode}$
a
and $\text{intra-kind}(\text{kind } a)$ **and** $\text{distance}(\text{targetnode } a) \ m' \ x$
and $\text{target:targetnode } a = (\text{SOME } mx. \exists a'. \text{sourcenode } a = \text{sourcenode } a' \wedge$
 $\text{distance}(\text{targetnode } a') \ m' \ x \wedge$
 $\text{valid-edge } a' \wedge \text{intra-kind}(\text{kind } a') \wedge$
 $\text{targetnode } a' = mx)$
by $-(\text{erule distance-successor-distance, simp+})$
from $\langle \text{intra-kind}(\text{kind } a) \rangle \langle \text{length } s = \text{length}(m \# msx') \rangle \langle m \notin \llbracket \text{HRB-slice}$
 $S \rrbracket_{CFG} \rangle$
 $\langle m = \text{sourcenode } a \rangle$
have $\text{length:length}(\text{transfer}(\text{slice-kind } S \ a) \ s) = \text{length}(\text{targetnode } a \# msx')$
by $(\text{cases } s)$
 $(\text{auto split:split-if-asm simp add:Let-def slice-kind-def intra-kind-def})$
from $\langle \text{distance}(\text{targetnode } a) \ m' \ x \rangle$ **obtain** *asx* **where** $\text{targetnode } a - asx \rightarrow_i^* m'$
m'
and $\text{length } asx = x$ **and** $\forall as'. \text{targetnode } a - as' \rightarrow_i^* m' \longrightarrow x \leq \text{length } as'$
by $(\text{auto elim:distance.cases})$
from $\langle \text{valid-edge } a \rangle \langle \text{intra-kind}(\text{kind } a) \rangle \langle m \notin \llbracket \text{HRB-slice } S \rrbracket_{CFG} \rangle$
 $\langle m = \text{sourcenode } a \rangle \langle \text{obs-intra } m \llbracket \text{HRB-slice } S \rrbracket_{CFG} = \{\} \rangle$
have $\text{obs-intra}(\text{targetnode } a) \llbracket \text{HRB-slice } S \rrbracket_{CFG} = \{\}$
by $(\text{fastforce dest:edge-obs-intra-subset})$
from $\text{IH}[\text{OF } \langle \text{distance}(\text{targetnode } a) \ m' \ x \rangle \langle \text{targetnode } a - asx \rightarrow_i^* m' \rangle \text{this}$
 $\text{length } \langle \forall m \in \text{set } msx'. \text{return-node } m \rangle]$ **obtain** *as'*
where $\text{moves:S, slice-kind } S \vdash$
 $(\text{targetnode } a \# msx', \text{transfer}(\text{slice-kind } S \ a) \ s) = as' \Rightarrow_\tau$
 $(m' \# msx', \text{transfer}(\text{slice-kind } S \ a) \ s)$ **by** *blast*
have $\text{pred}(\text{slice-kind } S \ a) \ s \wedge \text{transfer}(\text{slice-kind } S \ a) \ s = s$
proof $(\text{cases kind } a)$
fix *f* **assume** $\text{kind } a = \uparrow f$
with $\langle m \notin \llbracket \text{HRB-slice } S \rrbracket_{CFG} \rangle \langle m = \text{sourcenode } a \rangle$ **have** $\text{slice-kind } S \ a = \uparrow id$
by $(\text{fastforce intro:slice-kind-Upd})$
with $\langle \text{length } s = \text{length}(m \# msx') \rangle$ **show** *?thesis* **by** $(\text{cases } s)$ *auto*
next
fix *Q* **assume** $\text{kind } a = (Q) \surd$
with $\langle m \notin \llbracket \text{HRB-slice } S \rrbracket_{CFG} \rangle \langle m = \text{sourcenode } a \rangle$
 $\langle \text{obs-intra } m \llbracket \text{HRB-slice } S \rrbracket_{CFG} = \{\} \rangle \langle \text{distance}(\text{targetnode } a) \ m' \ x \rangle$
 $\langle \text{distance } m \ m' \ (Suc \ x) \rangle \langle \text{method-exit } m' \rangle \langle \text{get-proc } m = \text{get-proc } m' \rangle \text{target}$
have $\text{slice-kind } S \ a = (\lambda s. \text{True}) \surd$
by $(\text{fastforce intro:slice-kind-Pred-empty-obs-nearer-SOME})$
with $\langle \text{length } s = \text{length}(m \# msx') \rangle$ **show** *?thesis* **by** $(\text{cases } s)$ *auto*
next
fix *Q r p fs* **assume** $\text{kind } a = Q:r \hookrightarrow pfs$
with $\langle \text{intra-kind}(\text{kind } a) \rangle$ **have** *False* **by** $(\text{simp add:intra-kind-def})$
thus *?thesis* **by** *simp*

```

next
  fix Q p f assume kind a = Q ↔ pf
  with ⟨intra-kind(kind a)⟩ have False by (simp add:intra-kind-def)
  thus ?thesis by simp
qed
hence pred (slice-kind S a) s and transfer (slice-kind S a) s = s
  by simp-all
with ⟨m ∉ [HRB-slice S]_{CFG}⟩ ⟨m = sourcenode a⟩ ⟨valid-edge a⟩
  ⟨intra-kind(kind a)⟩ ⟨length s = length (m#msx')⟩ ⟨∀ m ∈ set msx'. return-node
m)⟩
  have S, slice-kind S ⊢ (sourcenode a#msx',s) -a→τ
    (targetnode a#msx',transfer (slice-kind S a) s)
    by (fastforce intro:silent-move-intra)
  with moves ⟨transfer (slice-kind S a) s = s⟩ ⟨m = sourcenode a⟩
  have S, slice-kind S ⊢ (m#msx',s) = a#as' ⇒τ (m'#msx',s)
    by (fastforce intro:silent-moves-Cons)
  thus ∃ as. S, slice-kind S ⊢ (m#msx',s) = as ⇒τ (m'#msx',s) by blast
qed
qed

```

lemma *silent-moves-vpa-path*:

```

assumes S, f ⊢ (m#ms,s) = as ⇒τ (m'#ms',s') and valid-node m
and ∀ i < length rs. rs!i ∈ get-return-edges (cs!i)
and ms = targetnodes rs and valid-return-list rs m
and length rs = length cs
shows m -as→* m' and valid-path-aux cs as
proof -
  from assms have m -as→* m' ∧ valid-path-aux cs as
  proof (induct S f m#ms s as m'#ms' s' arbitrary:m cs ms rs
    rule:silent-moves.induct)
    case (silent-moves-Nil msx sx nc f)
    from ⟨valid-node m'⟩ have m' -[]→* m'
    by (rule empty-path)
    thus ?case by fastforce
  next
    case (silent-moves-Cons S f sx a msx' sx' as s'')
    thus ?case
  proof (induct - - m # ms - - - rule:silent-move.induct)
    case (silent-move-intra f a sx sx' nc msx')
    note IH = ⟨∧ m cs ms rs. [msx' = m # ms; valid-node m;
      ∀ i < length rs. rs ! i ∈ get-return-edges (cs ! i);
      ms = targetnodes rs; valid-return-list rs m;
      length rs = length cs]
      ⇒ m -as→* m' ∧ valid-path-aux cs as⟩
    from ⟨msx' = targetnode a # tl (m # ms)⟩
    have msx' = targetnode a # ms by simp
    from ⟨valid-edge a⟩ ⟨intra-kind (kind a)⟩
    have get-proc (sourcenode a) = get-proc (targetnode a)

```

```

    by(rule get-proc-intra)
  with ⟨valid-return-list rs m⟩ ⟨hd (m # ms) = sourcenode a⟩
  have valid-return-list rs (targetnode a)
    apply(clarsimp simp:valid-return-list-def)
    apply(erule-tac x=cs' in allE) apply clarsimp
    by(case-tac cs') auto
  from ⟨valid-edge a⟩ have valid-node (targetnode a) by simp
  from IH[OF ⟨msx' = targetnode a # ms⟩ this
    ⟨∀ i < length rs. rs ! i ∈ get-return-edges (cs ! i)⟩
    ⟨ms = targetnodes rs⟩ ⟨valid-return-list rs (targetnode a)⟩
    ⟨length rs = length cs⟩]
  have targetnode a -as→* m' and valid-path-aux cs as by simp-all
  from ⟨valid-edge a⟩ ⟨targetnode a -as→* m'⟩
    ⟨hd (m # ms) = sourcenode a⟩
  have m -a#as→* m' by(fastforce intro:Cons-path)
  moreover
  from ⟨intra-kind (kind a)⟩ ⟨valid-path-aux cs as⟩
  have valid-path-aux cs (a # as) by(fastforce simp:intra-kind-def)
  ultimately show ?case by simp
next
  case (silent-move-call f a sx sx' Q r p fs a' n_c msx')
  note IH = ⟨∧ m cs ms rs. ⟦msx' = m # ms; valid-node m;
    ∀ i < length rs. rs ! i ∈ get-return-edges (cs ! i);
    ms = targetnodes rs; valid-return-list rs m;
    length rs = length cs⟧
    ⇒ m -as→* m' ∧ valid-path-aux cs as⟩
  from ⟨valid-edge a⟩ have valid-node (targetnode a) by simp
  from ⟨length rs = length cs⟩
  have length (a'#rs) = length (a#cs) by simp
  from ⟨msx' = targetnode a # targetnode a' # tl (m # ms)⟩
  have msx' = targetnode a # targetnode a' # ms by simp
  from ⟨ms = targetnodes rs⟩ have targetnode a' # ms = targetnodes (a' #
rs)
    by(simp add:targetnodes-def)
  from ⟨valid-edge a⟩ ⟨kind a = Q:r↔pfs⟩ have get-proc (targetnode a) = p
    by(rule get-proc-call)
  from ⟨valid-edge a⟩ ⟨a' ∈ get-return-edges a⟩ have valid-edge a'
    by(rule get-return-edges-valid)
  from ⟨valid-edge a⟩ ⟨kind a = Q:r↔pfs⟩ ⟨a' ∈ get-return-edges a⟩
  obtain Q' f' where kind a' = Q'↔pf' by(fastforce dest!:call-return-edges)
  from ⟨valid-edge a⟩ ⟨a' ∈ get-return-edges a⟩
  have get-proc (sourcenode a) = get-proc (targetnode a')
    by(rule get-proc-get-return-edge)
  with ⟨valid-return-list rs m⟩ ⟨hd (m # ms) = sourcenode a⟩
    ⟨get-proc (targetnode a) = p⟩ ⟨valid-edge a'⟩ ⟨kind a' = Q'↔pf'⟩
  have valid-return-list (a' # rs) (targetnode a)
    apply(clarsimp simp:valid-return-list-def)
    apply(case-tac cs') apply auto
    apply(erule-tac x=list in allE) apply clarsimp

```

```

    by(case-tac list)(auto simp:targetnodes-def)
  from (∀i<length rs. rs ! i ∈ get-return-edges (cs ! i))
    ⟨a' ∈ get-return-edges a⟩
  have ∀i<length (a'#rs). (a'#rs) ! i ∈ get-return-edges ((a#cs) ! i)
    by auto(case-tac i,auto)
    from IH[OF ⟨msx' = targetnode a # targetnode a' # ms⟩ ⟨valid-node
(targetnode a)⟩ this
    ⟨targetnode a' # ms = targetnodes (a' # rs)⟩
    ⟨valid-return-list (a' # rs) (targetnode a)⟩ ⟨length (a'#rs) = length (a#cs)⟩]
  have targetnode a -as→* m' and valid-path-aux (a # cs) as by simp-all
  from (valid-edge a) ⟨targetnode a -as→* m'⟩
    ⟨hd (m # ms) = sourcenode a⟩
  have m -a#as→* m' by(fastforce intro:Cons-path)
  moreover
  from (valid-path-aux (a # cs) as) ⟨kind a = Q:r↪pfs⟩
  have valid-path-aux cs (a # as) by simp
  ultimately show ?case by simp
next
case (silent-move-return f a sx sx' Q p f' n_c msx')
note IH = ⟨∧m cs ms rs. [msx' = m # ms; valid-node m;
  ∀i<length rs. rs ! i ∈ get-return-edges (cs ! i);
  ms = targetnodes rs; valid-return-list rs m;
  length rs = length cs]
  ⇒ m -as→* m' ∧ valid-path-aux cs as)
from (valid-edge a) have valid-node (targetnode a) by simp
from ⟨length (m # ms) = length sx⟩ ⟨length sx = Suc (length sx')⟩
  ⟨sx' ≠ []⟩
obtain x xs where ms = x#xs by(cases ms) auto
with ⟨ms = targetnodes rs⟩ obtain r' rs' where rs = r'#rs'
  and x = targetnode r' and xs = targetnodes rs'
  by(auto simp:targetnodes-def)
with ⟨length rs = length cs⟩ obtain c' cs' where cs = c'#cs'
  and length rs' = length cs'
  by(cases cs) auto
from ⟨ms = x#xs⟩ ⟨length (m # ms) = length sx⟩
  ⟨length sx = Suc (length sx')⟩
have length sx' = Suc (length xs) by simp
from ⟨ms = x#xs⟩ ⟨msx' = tl (m # ms)⟩ ⟨hd (tl (m # ms)) = targetnode a⟩
  ⟨length (m # ms) = length sx⟩ ⟨length sx = Suc (length sx')⟩ ⟨sx' ≠ []⟩
have msx' = targetnode a#xs by simp
from (∀i<length rs. rs ! i ∈ get-return-edges (cs ! i))
  ⟨rs = r'#rs'⟩ ⟨cs = c'#cs'⟩
have r' ∈ get-return-edges c' by fastforce
from ⟨ms = x#xs⟩ ⟨hd (tl (m # ms)) = targetnode a⟩
have x = targetnode a by simp
with ⟨valid-return-list rs m⟩ ⟨rs = r'#rs'⟩ ⟨x = targetnode r'⟩
have valid-return-list rs' (targetnode a)
  apply(clarsimp simp:valid-return-list-def)
  apply(erule-tac x=r'#cs' in allE) apply clarsimp

```



```

    by(case-tac cs')(auto simp:targetnodes-def)
  from (∀i<length rs. rs ! i ∈ get-return-edges (cs ! i))
    ⟨rs = r'#rs'⟩ ⟨cs = c'#cs'⟩
  have ∀i<length rs'. rs' ! i ∈ get-return-edges (cs' ! i)
    and r' ∈ get-return-edges c' by auto
  from IH[OF ⟨msx' = targetnode a#xs⟩ ⟨valid-node (targetnode a)⟩
    ⟨∀i<length rs'. rs' ! i ∈ get-return-edges (cs' ! i)⟩ ⟨xs = targetnodes rs'⟩
    ⟨valid-return-list rs' (targetnode a)⟩ ⟨length rs' = length cs'⟩]
  have targetnode a -as→* m' and valid-path-aux cs' as by simp-all
  from ⟨valid-edge a⟩ ⟨targetnode a -as→* m'⟩
    ⟨hd (m # ms) = sourcenode a⟩
  have m -a#as→* m' by(fastforce intro:Cons-path)
  moreover
  from ⟨ms = x#xs⟩ ⟨hd (tl (m # ms)) = targetnode a⟩
  have x = targetnode a by simp
  from ⟨valid-edge a⟩ ⟨kind a = Q↔pf'⟩
  have method-exit (sourcenode a) by(fastforce simp:method-exit-def)
  from ⟨valid-return-list rs m⟩ ⟨hd (m # ms) = sourcenode a⟩
    ⟨rs = r'#rs'⟩
  have get-proc (sourcenode a) = get-proc (sourcenode r') ∧
    method-exit (sourcenode r') ∧ valid-edge r'
  apply(clarsimp simp:valid-return-list-def method-exit-def)
  apply(erule-tac x=[] in allE)
  by(auto dest:get-proc-return)
  hence get-proc (sourcenode a) = get-proc (sourcenode r')
    and method-exit (sourcenode r') and valid-edge r' by simp-all
  with ⟨method-exit (sourcenode a)⟩ have sourcenode r' = sourcenode a
    by(fastforce intro:method-exit-unique)
  with ⟨valid-edge a⟩ ⟨valid-edge r'⟩ ⟨x = targetnode r'⟩ ⟨x = targetnode a⟩
  have r' = a by(fastforce intro:edge-det)
  with ⟨r' ∈ get-return-edges c'⟩ ⟨valid-path-aux cs' as⟩ ⟨cs = c'#cs'⟩
    ⟨kind a = Q↔pf'⟩
  have valid-path-aux cs (a # as) by simp
  ultimately show ?case by simp
qed
qed
thus m -as→* m' and valid-path-aux cs as by simp-all
qed

```

1.13.2 Observable moves

inductive *observable-move* ::

```

  'node SDG-node set ⇒ ('edge ⇒ ('var,'val,'ret,'pname) edge-kind) ⇒ 'node list
⇒
  (('var → 'val) × 'ret) list ⇒ 'edge ⇒ 'node list ⇒ (('var → 'val) × 'ret) list
⇒ bool
(-,- ⊢ '(-,-)) ->> '(-,-) [51,50,0,0,50,0,0] 51)

```

where *observable-move-intra*:

$\llbracket \text{pred } (f \ a) \ s; \text{transfer } (f \ a) \ s = s'; \text{valid-edge } a; \text{intra-kind}(kind \ a);$
 $\forall m \in \text{set } (tl \ ms). \exists m'. \text{call-of-return-node } m \ m' \wedge m' \in \llbracket \text{HRB-slice } S \rrbracket_{CFG};$
 $\text{hd } ms \in \llbracket \text{HRB-slice } S \rrbracket_{CFG}; \text{length } s' = \text{length } s; \text{length } ms = \text{length } s;$
 $\text{hd } ms = \text{sourcenode } a; ms' = (\text{targetnode } a) \# tl \ ms \rrbracket$
 $\implies S, f \vdash (ms, s) -a \rightarrow (ms', s')$

| *observable-move-call*:
 $\llbracket \text{pred } (f \ a) \ s; \text{transfer } (f \ a) \ s = s'; \text{valid-edge } a; \text{kind } a = Q:r \hookrightarrow pfs;$
 $\text{valid-edge } a'; a' \in \text{get-return-edges } a;$
 $\forall m \in \text{set } (tl \ ms). \exists m'. \text{call-of-return-node } m \ m' \wedge m' \in \llbracket \text{HRB-slice } S \rrbracket_{CFG};$
 $\text{hd } ms \in \llbracket \text{HRB-slice } S \rrbracket_{CFG}; \text{length } ms = \text{length } s; \text{length } s' = \text{Suc}(\text{length } s);$
 $\text{hd } ms = \text{sourcenode } a; ms' = (\text{targetnode } a) \# (\text{targetnode } a') \# tl \ ms \rrbracket$
 $\implies S, f \vdash (ms, s) -a \rightarrow (ms', s')$

| *observable-move-return*:
 $\llbracket \text{pred } (f \ a) \ s; \text{transfer } (f \ a) \ s = s'; \text{valid-edge } a; \text{kind } a = Q \leftarrow p f';$
 $\forall m \in \text{set } (tl \ ms). \exists m'. \text{call-of-return-node } m \ m' \wedge m' \in \llbracket \text{HRB-slice } S \rrbracket_{CFG};$
 $\text{length } ms = \text{length } s; \text{length } s = \text{Suc}(\text{length } s'); s' \neq [];$
 $\text{hd } ms = \text{sourcenode } a; \text{hd}(tl \ ms) = \text{targetnode } a; ms' = tl \ ms \rrbracket$
 $\implies S, f \vdash (ms, s) -a \rightarrow (ms', s')$

inductive *observable-moves* ::

$'node \ SDG\text{-node} \ \text{set} \Rightarrow ('edge \Rightarrow ('var, 'val, 'ret, 'pname) \ \text{edge-kind}) \Rightarrow 'node \ \text{list}$
 \Rightarrow
 $(('var \rightarrow 'val) \times 'ret) \ \text{list} \Rightarrow 'edge \ \text{list} \Rightarrow 'node \ \text{list} \Rightarrow (('var \rightarrow 'val) \times 'ret)$
 $\text{list} \Rightarrow \text{bool}$
 $(-, \vdash) \ '(-, -) \ \Rightarrow \ '(-, -) \ [51, 50, 0, 0, 50, 0, 0] \ 51)$

where *observable-moves-snoc*:

$\llbracket S, f \vdash (ms, s) = as \Rightarrow_{\tau} (ms', s'); S, f \vdash (ms', s') -a \rightarrow (ms'', s'') \rrbracket$
 $\implies S, f \vdash (ms, s) = as @ [a] \Rightarrow (ms'', s'')$

lemma *observable-move-equal-length*:

assumes $S, f \vdash (ms, s) -a \rightarrow (ms', s')$

shows $\text{length } ms = \text{length } s$ **and** $\text{length } ms' = \text{length } s'$

proof –

from $\langle S, f \vdash (ms, s) -a \rightarrow (ms', s') \rangle$

have $\text{length } ms = \text{length } s \wedge \text{length } ms' = \text{length } s'$

proof (*induct rule:observable-move.induct*)

case (*observable-move-intra* $f \ a \ s \ s' \ ms \ S \ ms'$)

from $\langle \text{pred } (f \ a) \ s \rangle$ **obtain** $cf \ cfs$ **where** $[simp]: s = cf \# cfs$ **by** (*cases* s) *auto*

from $\langle \text{length } ms = \text{length } s \rangle$ $\langle ms' = \text{targetnode } a \ \# \ tl \ ms \rangle$

$\langle \text{length } s' = \text{length } s \rangle$ **show** ?*case* **by** *simp*

next

case (*observable-move-call* $f \ a \ s \ s' \ Q \ r \ p \ fs \ a' \ ms \ S \ ms'$)

from $\langle \text{pred } (f \ a) \ s \rangle$ **obtain** $cf \ cfs$ **where** $[simp]: s = cf \# cfs$ **by** (*cases* s) *auto*

from $\langle \text{length } ms = \text{length } s \rangle \langle \text{length } s' = \text{Suc } (\text{length } s) \rangle$
 $\langle ms' = \text{targetnode } a \# \text{targetnode } a' \# \text{tl } ms \rangle$ **show** *?case by simp*
next
case $(\text{observable-move-return } f \ a \ s \ s' \ Q \ p \ f' \ ms \ S \ ms')$
from $\langle \text{length } ms = \text{length } s \rangle \langle \text{length } s = \text{Suc } (\text{length } s') \rangle \langle ms' = \text{tl } ms \rangle \langle s' \neq [] \rangle$
show *?case by simp*
qed
thus $\text{length } ms = \text{length } s$ **and** $\text{length } ms' = \text{length } s'$ **by** *simp-all*
qed

lemma *observable-moves-equal-length*:
assumes $S, f \vdash (ms, s) = as \Rightarrow (ms', s')$
shows $\text{length } ms = \text{length } s$ **and** $\text{length } ms' = \text{length } s'$
using $\langle S, f \vdash (ms, s) = as \Rightarrow (ms', s') \rangle$
proof (*induct rule: observable-moves.induct*)
case $(\text{observable-moves-snoc } S \ f \ ms \ s \ as \ ms' \ s' \ a \ ms'' \ s'')$
from $\langle S, f \vdash (ms', s') - a \rightarrow (ms'', s'') \rangle$
have $\text{length } ms' = \text{length } s'$ $\text{length } ms'' = \text{length } s''$
by (*rule observable-move-equal-length*) +
moreover
from $\langle S, f \vdash (ms, s) = as \Rightarrow_{\tau} (ms', s') \rangle$
have $\text{length } ms = \text{length } s$ **and** $\text{length } ms' = \text{length } s'$
by (*rule silent-moves-equal-length*) +
ultimately show $\text{length } ms = \text{length } s$ $\text{length } ms'' = \text{length } s''$ **by** *simp-all*
qed

lemma *observable-move-notempty*:
 $\llbracket S, f \vdash (ms, s) = as \Rightarrow (ms', s'); as = [] \rrbracket \Longrightarrow \text{False}$
by (*induct rule: observable-moves.induct, simp*)

lemma *silent-move-observable-moves*:
 $\llbracket S, f \vdash (ms'', s'') = as \Rightarrow (ms', s'); S, f \vdash (ms, s) - a \rightarrow_{\tau} (ms'', s'') \rrbracket$
 $\Longrightarrow S, f \vdash (ms, s) = a \# as \Rightarrow (ms', s')$
proof (*induct rule: observable-moves.induct*)
case $(\text{observable-moves-snoc } S \ f \ msx \ sx \ as \ ms' \ s' \ a' \ ms'' \ s'')$
from $\langle S, f \vdash (ms, s) - a \rightarrow_{\tau} (msx, sx) \rangle \langle S, f \vdash (msx, sx) = as \Rightarrow_{\tau} (ms', s') \rangle$
have $S, f \vdash (ms, s) = a \# as \Rightarrow_{\tau} (ms', s')$ **by** (*fastforce intro: silent-moves-Cons*)
with $\langle S, f \vdash (ms', s') - a' \rightarrow (ms'', s'') \rangle$
have $S, f \vdash (ms, s) = (a \# as) @ [a'] \Rightarrow (ms'', s'')$
by (*fastforce intro: observable-moves.observable-moves-snoc*)
thus *?case by simp*
qed

lemma *silent-append-observable-moves*:
 $\llbracket S, f \vdash (ms, s) = as \Rightarrow_{\tau} (ms'', s''); S, f \vdash (ms'', s'') = as' \Rightarrow (ms', s') \rrbracket$

$\Rightarrow S, f \vdash (ms, s) = as @ as' \Rightarrow (ms', s')$
by(*induct rule:silent-moves.induct*)(*auto elim:silent-move-observable-moves*)

lemma *observable-moves-preds-transfers*:

assumes $S, f \vdash (ms, s) = as \Rightarrow (ms', s')$
shows *preds* (map f as) s **and** *transfers* (map f as) s = s'
proof –
from $\langle S, f \vdash (ms, s) = as \Rightarrow (ms', s') \rangle$
have *preds* (map f as) s \wedge *transfers* (map f as) s = s'
proof(*induct rule:observable-moves.induct*)
case (*observable-moves-snoc* S f ms s as ms' s' a ms'' s'')
from $\langle S, f \vdash (ms, s) = as \Rightarrow_{\tau} (ms', s') \rangle$
have *preds* (map f as) s **and** *transfers* (map f as) s = s'
by(*rule silent-moves-preds-transfers*)+
from $\langle S, f \vdash (ms', s') - a \rightarrow (ms'', s'') \rangle$
have *pred* (f a) s' **and** *transfer* (f a) s' = s''
by(*auto elim:observable-move.cases*)
with $\langle \text{preds} (map f as) s \rangle \langle \text{transfers} (map f as) s = s' \rangle$
show ?case **by**(*simp add:preds-split transfers-split*)
qed
thus *preds* (map f as) s **and** *transfers* (map f as) s = s' **by** *simp-all*
qed

lemma *observable-move-vpa-path*:

$\llbracket S, f \vdash (m \# ms, s) - a \rightarrow (m' \# ms', s'); \text{valid-node } m; \forall i < \text{length } rs. rs!i \in \text{get-return-edges } (cs!i); ms = \text{targetnodes } rs; \text{valid-return-list } rs \ m; \text{length } rs = \text{length } cs \rrbracket \Rightarrow \text{valid-path-aux } cs \ [a]$
proof(*induct* S f m # ms s a m' # ms' s' *rule:observable-move.induct*)
case (*observable-move-return* f a sx sx' Q p f' n_c)
from $\langle \text{length } (m \# ms) = \text{length } sx \rangle \langle \text{length } sx = \text{Suc } (\text{length } sx') \rangle$
 $\langle sx' \neq [] \rangle$
obtain x xs **where** ms = x # xs **by**(*cases* ms) *auto*
with $\langle ms = \text{targetnodes } rs \rangle$ **obtain** r' rs' **where** rs = r' # rs'
and x = *targetnode* r' **and** xs = *targetnodes* rs'
by(*auto simp:targetnodes-def*)
with $\langle \text{length } rs = \text{length } cs \rangle$ **obtain** c' cs' **where** cs = c' # cs'
and $\text{length } rs' = \text{length } cs'$
by(*cases* cs) *auto*
from $\langle \forall i < \text{length } rs. rs ! i \in \text{get-return-edges } (cs ! i) \rangle$
 $\langle rs = r' \# rs' \rangle \langle cs = c' \# cs' \rangle$
have $\forall i < \text{length } rs'. rs' ! i \in \text{get-return-edges } (cs' ! i)$
and r' $\in \text{get-return-edges } c'$ **by** *auto*
from $\langle ms = x \# xs \rangle \langle \text{hd } (tl (m \# ms)) = \text{targetnode } a \rangle$
have x = *targetnode* a **by** *simp*
from $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q \leftrightarrow pf \rangle$
have *method-exit* (*sourcenode* a) **by**(*fastforce simp:method-exit-def*)
from $\langle \text{valid-return-list } rs \ m \rangle \langle \text{hd } (m \# ms) = \text{sourcenode } a \rangle$

```

  ⟨rs = r'#rs'⟩
have get-proc (sourcenode a) = get-proc (sourcenode r') ∧
  method-exit (sourcenode r') ∧ valid-edge r'
apply(clarsimp simp:valid-return-list-def method-exit-def)
apply(erule-tac x=[] in allE)
by(auto dest:get-proc-return)
hence get-proc (sourcenode a) = get-proc (sourcenode r')
  and method-exit (sourcenode r') and valid-edge r' by simp-all
with ⟨method-exit (sourcenode a)⟩ have sourcenode r' = sourcenode a
  by(fastforce intro:method-exit-unique)
with ⟨valid-edge a⟩ ⟨valid-edge r'⟩ ⟨x = targetnode r'⟩ ⟨x = targetnode a⟩
have r' = a by(fastforce intro:edge-det)
with ⟨r' ∈ get-return-edges c'⟩ ⟨cs = c'#cs'⟩ ⟨kind a = Q↔pf'⟩
show ?case by simp
qed(auto simp:intra-kind-def)

```

1.13.3 Relevant variables

inductive-set *relevant-vars* ::

```

  'node SDG-node set ⇒ 'node SDG-node ⇒ 'var set (rv -)
for S :: 'node SDG-node set and n :: 'node SDG-node

```

where *rvI*:

```

  ⟦parent-node n -as→i* parent-node n'; n' ∈ HRB-slice S; V ∈ UseSDG n';
  ∀ n''. valid-SDG-node n'' ∧ parent-node n'' ∈ set (sourcenodes as)
  → V ∉ DefSDG n'⟧
  ⇒ V ∈ rv S n

```

lemma *rvE*:

```

assumes rv: V ∈ rv S n
obtains as n' where parent-node n -as→i* parent-node n'
and n' ∈ HRB-slice S and V ∈ UseSDG n'
and ∀ n''. valid-SDG-node n'' ∧ parent-node n'' ∈ set (sourcenodes as)
  → V ∉ DefSDG n''
using rv
by(atomize-elim,auto elim!:relevant-vars.cases)

```

lemma *rv-parent-node*:

```

  parent-node n = parent-node n' ⇒ rv (S::'node SDG-node set) n = rv S n'
by(fastforce elim:rvE intro:rvI)

```

lemma *obs-intra-empty-rv-empty*:

```

assumes obs-intra m [HRB-slice S] CFG = {} shows rv S (CFG-node m) = {}
proof(rule ccontr)
assume rv S (CFG-node m) ≠ {}
then obtain x where x ∈ rv S (CFG-node m) by fastforce

```

then obtain n' **as where** $m -as \rightarrow_{\iota}^* \text{parent-node } n'$ **and** $n' \in \text{HRB-slice } S$
by(*fastforce elim:rvE*)
hence $\text{parent-node } n' \in \lfloor \text{HRB-slice } S \rfloor_{CFG}$
by(*fastforce intro:valid-SDG-node-in-slice-parent-node-in-slice simp:SDG-to-CFG-set-def*)
with $\langle m -as \rightarrow_{\iota}^* \text{parent-node } n' \rangle$ **obtain** mx **where** $mx \in \text{obs-intra } m \lfloor \text{HRB-slice } S \rfloor_{CFG}$
by(*erule path-ex-obs-intra*)
with $\langle \text{obs-intra } m \lfloor \text{HRB-slice } S \rfloor_{CFG} = \{\} \rangle$ **show** *False* **by** *simp*
qed

lemma *eq-obs-intra-in-rv*:

assumes $\text{obs-eq:obs-intra } (\text{parent-node } n) \lfloor \text{HRB-slice } S \rfloor_{CFG} =$
 $\text{obs-intra } (\text{parent-node } n') \lfloor \text{HRB-slice } S \rfloor_{CFG}$

and $x \in \text{rv } S \ n$ **shows** $x \in \text{rv } S \ n'$

proof –

from $\langle x \in \text{rv } S \ n \rangle$ **obtain** $as \ n''$

where $\text{parent-node } n -as \rightarrow_{\iota}^* \text{parent-node } n''$ **and** $n'' \in \text{HRB-slice } S$

and $x \in \text{Use}_{SDG} \ n''$

and $\forall n''. \text{valid-SDG-node } n'' \wedge \text{parent-node } n'' \in \text{set } (\text{sourcenodes } as)$

$\longrightarrow x \notin \text{Def}_{SDG} \ n''$

by(*erule rvE*)

from $\langle \text{parent-node } n -as \rightarrow_{\iota}^* \text{parent-node } n'' \rangle$ **have** $\text{valid-node } (\text{parent-node } n'')$

by(*fastforce dest:path-valid-node simp:intra-path-def*)

from $\langle \text{parent-node } n -as \rightarrow_{\iota}^* \text{parent-node } n'' \rangle \langle n'' \in \text{HRB-slice } S \rangle$

have $\exists nx \ as' \ as''. \text{parent-node } nx \in \text{obs-intra } (\text{parent-node } n) \lfloor \text{HRB-slice } S \rfloor_{CFG}$

\wedge

$\text{parent-node } n -as' \rightarrow_{\iota}^* \text{parent-node } nx \wedge$

$\text{parent-node } nx -as'' \rightarrow_{\iota}^* \text{parent-node } n'' \wedge as = as' @ as''$

proof(*cases* $\forall nx. \text{parent-node } nx \in \text{set } (\text{sourcenodes } as) \longrightarrow nx \notin \text{HRB-slice } S$)

case *True*

with $\langle \text{parent-node } n -as \rightarrow_{\iota}^* \text{parent-node } n'' \rangle \langle n'' \in \text{HRB-slice } S \rangle$

have $\text{parent-node } n'' \in \text{obs-intra } (\text{parent-node } n) \lfloor \text{HRB-slice } S \rfloor_{CFG}$

by(*fastforce intro:obs-intra-elem valid-SDG-node-in-slice-parent-node-in-slice simp:SDG-to-CFG-set-def*)

with $\langle \text{parent-node } n -as \rightarrow_{\iota}^* \text{parent-node } n'' \rangle \langle \text{valid-node } (\text{parent-node } n'') \rangle$

show *?thesis* **by**(*fastforce intro:empty-path simp:intra-path-def*)

next

case *False*

hence $\exists nx. \text{parent-node } nx \in \text{set } (\text{sourcenodes } as) \wedge nx \in \text{HRB-slice } S$ **by**

simp

hence $\exists mx \in \text{set } (\text{sourcenodes } as). \exists nx. mx = \text{parent-node } nx \wedge nx \in \text{HRB-slice } S$

by *fastforce*

then obtain $mx \ ms \ ms'$ **where** $\text{sourcenodes } as = ms @ mx \# ms'$

and $\exists nx. mx = \text{parent-node } nx \wedge nx \in \text{HRB-slice } S$

and $\text{all:} \forall x \in \text{set } ms. \neg (\exists nx. x = \text{parent-node } nx \wedge nx \in \text{HRB-slice } S)$

by(*fastforce elim!:split-list-first-propE*)

then obtain nx' **where** $mx = \text{parent-node } nx'$ **and** $nx' \in \text{HRB-slice } S$ **by**
blast
from $\langle \text{sourcenodes } as = ms @ mx \# ms' \rangle$
obtain $as' a' as''$ **where** $ms = \text{sourcenodes } as'$
and $[simp]: as = as' @ a' \# as''$ **and** $\text{sourcenode } a' = mx$
by $(\text{fastforce elim:map-append-append-maps simp:sourcenodes-def})$
from all $\langle ms = \text{sourcenodes } as' \rangle$
have $\forall nx \in \text{set } (\text{sourcenodes } as'). nx \notin \lfloor \text{HRB-slice } S \rfloor_{CFG}$
by $(\text{fastforce simp:SDG-to-CFG-set-def})$
from $\langle \text{parent-node } n - as \rightarrow_{\iota} * \text{parent-node } n'' \rangle \langle \text{sourcenode } a' = mx \rangle$
have $\text{parent-node } n - as' \rightarrow_{\iota} * mx$ **and** $\text{valid-edge } a'$ **and** $\text{intra-kind } (\text{kind } a')$
and $\text{targetnode } a' - as'' \rightarrow_{\iota} * \text{parent-node } n''$
by $(\text{fastforce dest:path-split simp:intra-path-def}) +$
with $\langle \text{sourcenode } a' = mx \rangle$ **have** $mx - a' \# as'' \rightarrow_{\iota} * \text{parent-node } n''$
by $(\text{fastforce intro:Cons-path simp:intra-path-def})$
from $\langle \text{parent-node } n - as' \rightarrow_{\iota} * mx \rangle \langle mx = \text{parent-node } nx' \rangle \langle nx' \in \text{HRB-slice } S \rangle$
 $\langle \forall nx \in \text{set } (\text{sourcenodes } as'). nx \notin \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle \langle ms = \text{sourcenodes } as' \rangle$
have $mx \in \text{obs-intra } (\text{parent-node } n) \lfloor \text{HRB-slice } S \rfloor_{CFG}$
by $(\text{fastforce intro:obs-intra-elem valid-SDG-node-in-slice-parent-node-in-slice simp:SDG-to-CFG-set-def})$
with $\langle \text{parent-node } n - as' \rightarrow_{\iota} * mx \rangle \langle mx - a' \# as'' \rightarrow_{\iota} * \text{parent-node } n'' \rangle$
 $\langle mx = \text{parent-node } nx' \rangle$
show *?thesis* **by** *simp blast*
qed
then obtain $nx as' as''$
where $\text{parent-node } nx \in \text{obs-intra } (\text{parent-node } n) \lfloor \text{HRB-slice } S \rfloor_{CFG}$
and $\text{parent-node } n - as' \rightarrow_{\iota} * \text{parent-node } nx$
and $\text{parent-node } nx - as'' \rightarrow_{\iota} * \text{parent-node } n''$ **and** $[simp]: as = as' @ as''$
by *blast*
from $\langle \text{parent-node } nx \in \text{obs-intra } (\text{parent-node } n) \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle \text{obs-eq}$
have $\text{parent-node } nx \in \text{obs-intra } (\text{parent-node } n') \lfloor \text{HRB-slice } S \rfloor_{CFG}$ **by** *auto*
then obtain asx **where** $\text{parent-node } n' - asx \rightarrow_{\iota} * \text{parent-node } nx$
and $\forall ni \in \text{set } (\text{sourcenodes } asx). ni \notin \lfloor \text{HRB-slice } S \rfloor_{CFG}$
and $\text{parent-node } nx \in \lfloor \text{HRB-slice } S \rfloor_{CFG}$
by $(\text{erule obs-intraE})$
from $\langle \forall n''. \text{valid-SDG-node } n'' \wedge \text{parent-node } n'' \in \text{set } (\text{sourcenodes } as) \rightarrow x \notin \text{Def}_{SDG} n'' \rangle$
have $\forall ni. \text{valid-SDG-node } ni \wedge \text{parent-node } ni \in \text{set } (\text{sourcenodes } as'') \rightarrow x \notin \text{Def}_{SDG} ni$
by $(\text{auto simp:sourcenodes-def})$
from $\langle \forall ni \in \text{set } (\text{sourcenodes } asx). ni \notin \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
 $\langle \text{parent-node } n' - asx \rightarrow_{\iota} * \text{parent-node } nx \rangle$
have $\forall ni. \text{valid-SDG-node } ni \wedge \text{parent-node } ni \in \text{set } (\text{sourcenodes } asx) \rightarrow x \notin \text{Def}_{SDG} ni$
proof $(\text{induct } asx \text{ arbitrary: } n')$
case Nil thus *?case* **by** $(\text{simp add:sourcenodes-def})$
next
case $(\text{Cons } ax' asx')$

note $IH = \langle \bigwedge n'. [\forall ni \in \text{set}(\text{sourcenodes } ax'). ni \notin \text{[HRB-slice } S]_{CFG};$
 $\text{parent-node } n' - ax' \rightarrow_i^* \text{parent-node } nx]$
 $\implies \forall ni. \text{valid-SDG-node } ni \wedge \text{parent-node } ni \in \text{set}(\text{sourcenodes } ax')$
 $\longrightarrow x \notin \text{Def}_{SDG} ni \rangle$
from $\langle \text{parent-node } n' - ax' \# ax' \rightarrow_i^* \text{parent-node } nx \rangle$
have $\text{parent-node } n' - [] @ ax' \# ax' \rightarrow_i^* \text{parent-node } nx$
and $\forall a \in \text{set}(ax' \# ax'). \text{intra-kind}(\text{kind } a) \text{ by } (\text{simp-all add:intra-path-def})$
hence $\text{targetnode } ax' - ax' \rightarrow_i^* \text{parent-node } nx$ **and** $\text{valid-edge } ax'$
and $\text{parent-node } n' = \text{sourcenode } ax' \text{ by } (\text{fastforce dest:path-split}) +$
with $\langle \forall a \in \text{set}(ax' \# ax'). \text{intra-kind}(\text{kind } a) \rangle$
have $\text{path:parent-node } (CFG\text{-node } (\text{targetnode } ax')) - ax' \rightarrow_i^* \text{parent-node } nx$
by $(\text{simp add:intra-path-def})$
from $\langle \forall ni \in \text{set}(\text{sourcenodes } (ax' \# ax')). ni \notin \text{[HRB-slice } S]_{CFG} \rangle$
have $\text{all:} \forall ni \in \text{set}(\text{sourcenodes } ax'). ni \notin \text{[HRB-slice } S]_{CFG}$
and $\text{sourcenode } ax' \notin \text{[HRB-slice } S]_{CFG}$
by $(\text{auto simp:sourcenodes-def})$
from $IH[OF \text{ all path}]$
have $\forall ni. \text{valid-SDG-node } ni \wedge \text{parent-node } ni \in \text{set}(\text{sourcenodes } ax')$
 $\longrightarrow x \notin \text{Def}_{SDG} ni .$
with $\langle \forall ni. \text{valid-SDG-node } ni \wedge \text{parent-node } ni \in \text{set}(\text{sourcenodes } as'') \rangle$
 $\longrightarrow x \notin \text{Def}_{SDG} ni \rangle$
have $\text{all:} \forall ni. \text{valid-SDG-node } ni \wedge \text{parent-node } ni \in \text{set}(\text{sourcenodes } (ax' @ as''))$
 $\longrightarrow x \notin \text{Def}_{SDG} ni$
by $(\text{auto simp:sourcenodes-def})$
from $\langle \text{parent-node } n' - ax' \# ax' \rightarrow_i^* \text{parent-node } nx \rangle$
 $\langle \text{parent-node } nx - as'' \rightarrow_i^* \text{parent-node } n'' \rangle$
have $\text{path:parent-node } n' - ax' \# ax' @ as'' \rightarrow_i^* \text{parent-node } n''$
by $(\text{fastforce intro:path-Append[of - ax' \# ax',simplified] simp:intra-path-def})$
have $\forall nx'. \text{parent-node } nx' = \text{sourcenode } ax' \longrightarrow x \notin \text{Def}_{SDG} nx'$
proof
fix nx'
show $\text{parent-node } nx' = \text{sourcenode } ax' \longrightarrow x \notin \text{Def}_{SDG} nx'$
proof
assume $\text{parent-node } nx' = \text{sourcenode } ax'$
show $x \notin \text{Def}_{SDG} nx'$
proof
assume $x \in \text{Def}_{SDG} nx'$
from $\langle \text{parent-node } n' = \text{sourcenode } ax' \rangle \langle \text{parent-node } nx' = \text{sourcenode } ax' \rangle$
have $\text{parent-node } nx' = \text{parent-node } n' \text{ by } \text{simp}$
with $\langle x \in \text{Def}_{SDG} nx' \rangle \langle x \in \text{Use}_{SDG} n'' \rangle \text{ all path}$
have $nx' \text{ influences } x \text{ in } n'' \text{ by } (\text{fastforce simp:data-dependence-def})$
hence $nx' s-x \rightarrow_{dd} n'' \text{ by } (\text{rule sum-SDG-ddep-edge})$
with $\langle n'' \in \text{HRB-slice } S \rangle \text{ have } nx' \in \text{HRB-slice } S$
by $(\text{fastforce elim:combine-SDG-slices.cases}$
 $\text{intro:combine-SDG-slices.intros ddep-slice1 ddep-slice2}$
 $\text{simp:HRB-slice-def})$
hence $CFG\text{-node } (\text{parent-node } nx') \in \text{HRB-slice } S$
by $(\text{rule valid-SDG-node-in-slice-parent-node-in-slice})$


```

with ⟨sourcenode ax' ∉ [HRB-slice S]CFG⟩ ⟨parent-node n' = sourcenode
ax'⟩
  ⟨parent-node nx' = sourcenode ax'⟩ show False
by(simp add:SDG-to-CFG-set-def)
qed
qed
qed
with all show ?case by(auto simp add:sourcenodes-def)
qed
with ⟨∀ ni. valid-SDG-node ni ∧ parent-node ni ∈ set (sourcenodes as'')
  → x ∉ DefSDG ni⟩
have all:∀ ni. valid-SDG-node ni ∧ parent-node ni ∈ set (sourcenodes (asx@as''))
  → x ∉ DefSDG ni
by(auto simp:sourcenodes-def)
with ⟨parent-node n' -asx→i* parent-node nx⟩
  ⟨parent-node nx -as''→i* parent-node n''⟩
have parent-node n' -asx@as''→i* parent-node n''
by(fastforce intro:path-Append simp:intra-path-def)
from this ⟨n'' ∈ HRB-slice S⟩ ⟨x ∈ UseSDG n''⟩ all
show x ∈ rv S n' by(rule rvI)
qed

```

```

lemma closed-eq-obs-eq-rvs:
  fixes S :: 'node SDG-node set
  assumes obs-eq:obs-intra (parent-node n) [HRB-slice S]CFG =
    obs-intra (parent-node n') [HRB-slice S]CFG
  shows rv S n = rv S n'
proof
  show rv S n ⊆ rv S n'
  proof
    fix x assume x ∈ rv S n
    with obs-eq show x ∈ rv S n' by(rule eq-obs-intra-in-rv)
  qed
next
  show rv S n' ⊆ rv S n
  proof
    fix x assume x ∈ rv S n'
    with obs-eq[THEN sym] show x ∈ rv S n by(rule eq-obs-intra-in-rv)
  qed
qed

```

```

lemma closed-eq-obs-eq-rvs':
  fixes S :: 'node SDG-node set
  assumes obs-eq:obs-intra m [HRB-slice S]CFG = obs-intra m' [HRB-slice S]CFG
  shows rv S (CFG-node m) = rv S (CFG-node m')

```

```

proof
  show  $rv\ S\ (CFG\text{-}node\ m) \subseteq rv\ S\ (CFG\text{-}node\ m')$ 
  proof
    fix  $x$  assume  $x \in rv\ S\ (CFG\text{-}node\ m)$ 
    with obs-eq show  $x \in rv\ S\ (CFG\text{-}node\ m')$ 
    by  $-(rule\ eq\ obs\ intra\ in\ rv, auto)$ 
  qed
next
  show  $rv\ S\ (CFG\text{-}node\ m') \subseteq rv\ S\ (CFG\text{-}node\ m)$ 
  proof
    fix  $x$  assume  $x \in rv\ S\ (CFG\text{-}node\ m')$ 
    with obs-eq[THEN sym] show  $x \in rv\ S\ (CFG\text{-}node\ m)$ 
    by  $-(rule\ eq\ obs\ intra\ in\ rv, auto)$ 
  qed
qed

```

lemma *rv-branching-edges-slice-kinds-False*:

```

assumes valid-edge a and valid-edge ax
and sourcenode a = sourcenode ax and targetnode a  $\neq$  targetnode ax
and intra-kind (kind a) and intra-kind (kind ax)
and preds (slice-kinds S (a#as)) s
and preds (slice-kinds S (ax#asx)) s'
and length s = length s' and snd (hd s) = snd (hd s')
and  $\forall V \in rv\ S\ (CFG\text{-}node\ (sourcenode\ a)).\ state\text{-}val\ s\ V = state\text{-}val\ s'\ V$ 
shows False
proof  $-$ 
  from  $\langle ivalid\text{-}edge\ a \rangle \langle ivalid\text{-}edge\ ax \rangle \langle isourcenode\ a = sourcenode\ ax \rangle$ 
   $\langle itargetnode\ a \neq targetnode\ ax \rangle \langle iintra\text{-}kind\ (kind\ a) \rangle \langle iintra\text{-}kind\ (kind\ ax) \rangle$ 
  obtain  $Q\ Q'$  where  $kind\ a = (Q)_{\surd}$  and  $kind\ ax = (Q')_{\surd}$ 
  and  $\forall s. (Q\ s \longrightarrow \neg Q'\ s) \wedge (Q'\ s \longrightarrow \neg Q\ s)$ 
  by (auto dest:deterministic)
  from  $\langle ivalid\text{-}edge\ a \rangle \langle ivalid\text{-}edge\ ax \rangle \langle isourcenode\ a = sourcenode\ ax \rangle$ 
   $\langle itargetnode\ a \neq targetnode\ ax \rangle \langle iintra\text{-}kind\ (kind\ a) \rangle \langle iintra\text{-}kind\ (kind\ ax) \rangle$ 
  obtain  $P\ P'$  where  $slice\text{-}kind\ S\ a = (P)_{\surd}$ 
  and  $slice\text{-}kind\ S\ ax = (P')_{\surd}$ 
  and  $\forall s. (P\ s \longrightarrow \neg P'\ s) \wedge (P'\ s \longrightarrow \neg P\ s)$ 
  by  $-(erule\ slice\text{-}deterministic, auto)$ 
  show ?thesis
proof (cases sourcenode a  $\in$  [HRB-slice S] CFG)
  case True
  with  $\langle iintra\text{-}kind\ (kind\ a) \rangle$ 
  have  $slice\text{-}kind\ S\ a = kind\ a$  by  $-(rule\ slice\text{-}intra\text{-}kind\ in\ slice)$ 
  with  $\langle i preds\ (slice\text{-}kinds\ S\ (a\#as))\ s \rangle \langle i kind\ a = (Q)_{\surd} \rangle$ 
   $\langle i slice\text{-}kind\ S\ a = (P)_{\surd} \rangle$  have  $pred\ (kind\ a)\ s$ 
  by (simp add:slice-kinds-def)
  from True  $\langle i sourcenode\ a = sourcenode\ ax \rangle \langle i intra\text{-}kind\ (kind\ ax) \rangle$ 
  have  $slice\text{-}kind\ S\ ax = kind\ ax$ 
  by (fastforce intro:slice-intra-kind-in-slice)

```

```

with ⟨preds (slice-kinds S (ax#asx)) s'⟩ ⟨kind ax = (Q')✓⟩
  ⟨slice-kind S ax = (P')✓⟩ have pred (kind ax) s'
  by(simp add:slice-kinds-def)
with ⟨kind ax = (Q')✓⟩ have Q' (fst (hd s')) by(cases s') auto
from ⟨valid-edge a⟩ have sourcenode a -[]→i* sourcenode a
  by(fastforce intro:empty-path simp:intra-path-def)
with True ⟨valid-edge a⟩
have ∀ V ∈ Use (sourcenode a). V ∈ rv S (CFG-node (sourcenode a))
by(auto intro!:rvI CFG-Use-SDG-Use simp:sourcenodes-def SDG-to-CFG-set-def)
with ⟨∀ V ∈ rv S (CFG-node (sourcenode a)). state-val s V = state-val s' V⟩
have ∀ V ∈ Use (sourcenode a). state-val s V = state-val s' V by blast
with ⟨valid-edge a⟩ ⟨pred (kind a) s⟩ ⟨pred (kind ax) s'⟩ ⟨length s = length s'⟩
  ⟨snd (hd s) = snd (hd s')⟩
have pred (kind a) s' by(auto intro:CFG-edge-Uses-pred-equal)
with ⟨kind a = (Q)✓⟩ have Q (fst (hd s')) by(cases s') auto
with ⟨Q' (fst (hd s'))⟩ ⟨∀ s. (Q s → ¬ Q' s) ∧ (Q' s → ¬ Q s)⟩
have False by simp
thus ?thesis by simp
next
case False
with ⟨kind a = (Q)✓⟩ ⟨slice-kind S a = (P)✓⟩ ⟨valid-edge a⟩
have P = (λs. False) ∨ P = (λs. True)
  by(fastforce elim:kind-Predicate-notin-slice-slice-kind-Predicate)
with ⟨slice-kind S a = (P)✓⟩
  ⟨preds (slice-kinds S (a#as)) s⟩
have P = (λs. True) by(cases s)(auto simp:slice-kinds-def)
from ⟨sourcenode a = sourcenode ax⟩ False
have sourcenode ax ∉ [HRB-slice S]CFG by simp
with ⟨kind ax = (Q')✓⟩ ⟨slice-kind S ax = (P')✓⟩ ⟨valid-edge ax⟩
have P' = (λs. False) ∨ P' = (λs. True)
  by(fastforce elim:kind-Predicate-notin-slice-slice-kind-Predicate)
with ⟨slice-kind S ax = (P')✓⟩
  ⟨preds (slice-kinds S (ax#asx)) s'⟩
have P' = (λs. True) by(cases s')(auto simp:slice-kinds-def)
with ⟨P = (λs. True)⟩ ⟨∀ s. (P s → ¬ P' s) ∧ (P' s → ¬ P s)⟩
have False by blast
thus ?thesis by simp
qed
qed

```

lemma *rv-edge-slice-kinds*:

```

assumes valid-edge a and intra-kind (kind a)
and ∀ V ∈ rv S (CFG-node (sourcenode a)). state-val s V = state-val s' V
and preds (slice-kinds S (a#as)) s and preds (slice-kinds S (a#asx)) s'
shows ∀ V ∈ rv S (CFG-node (targetnode a)).
  state-val (transfer (slice-kind S a) s) V =
  state-val (transfer (slice-kind S a) s') V

```

proof

```

fix  $V$  assume  $V \in rv\ S\ (CFG\text{-node}\ (targetnode\ a))$ 
from  $\langle preds\ (slice\text{-kinds}\ S\ (a\#\ as))\ s \rangle$ 
have  $s \neq []$  by  $(cases\ s, auto\ simp: slice\text{-kinds}\text{-def})$ 
from  $\langle preds\ (slice\text{-kinds}\ S\ (a\#\ asx))\ s' \rangle$ 
have  $s' \neq []$  by  $(cases\ s', auto\ simp: slice\text{-kinds}\text{-def})$ 
show  $state\text{-val}\ (transfer\ (slice\text{-kind}\ S\ a)\ s)\ V =$ 
 $state\text{-val}\ (transfer\ (slice\text{-kind}\ S\ a)\ s')\ V$ 
proof  $(cases\ V \in Def\ (sourcenode\ a))$ 
  case  $True$ 
  show  $?thesis$ 
  proof  $(cases\ sourcenode\ a \in [HRB\text{-slice}\ S]_{CFG})$ 
    case  $True$ 
    with  $\langle intra\text{-kind}\ (kind\ a) \rangle$  have  $slice\text{-kind}\ S\ a = kind\ a$ 
      by  $-(rule\ slice\text{-intra}\text{-kind}\text{-in}\text{-slice})$ 
    with  $\langle preds\ (slice\text{-kinds}\ S\ (a\#\ as))\ s \rangle$  have  $pred\ (kind\ a)\ s$ 
      by  $(simp\ add: slice\text{-kinds}\text{-def})$ 
    from  $\langle slice\text{-kind}\ S\ a = kind\ a \rangle$ 
       $\langle preds\ (slice\text{-kinds}\ S\ (a\#\ asx))\ s' \rangle$ 
    have  $pred\ (kind\ a)\ s'$  by  $(simp\ add: slice\text{-kinds}\text{-def})$ 
    from  $\langle valid\text{-edge}\ a \rangle$  have  $sourcenode\ a - [] \rightarrow_i^* sourcenode\ a$ 
      by  $(fastforce\ intro: empty\text{-path}\ simp: intra\text{-path}\text{-def})$ 
    with  $True\ \langle valid\text{-edge}\ a \rangle$ 
    have  $\forall V \in Use\ (sourcenode\ a). V \in rv\ S\ (CFG\text{-node}\ (sourcenode\ a))$ 
by  $(auto\ intro!: rvI\ CFG\text{-Use}\text{-SDG}\text{-Use}\ simp: sourcenodes\text{-def}\ SDG\text{-to}\text{-CFG}\text{-set}\text{-def})$ 
    with  $\langle \forall V \in rv\ S\ (CFG\text{-node}\ (sourcenode\ a)). state\text{-val}\ s\ V = state\text{-val}\ s'\ V \rangle$ 
    have  $\forall V \in Use\ (sourcenode\ a). state\text{-val}\ s\ V = state\text{-val}\ s'\ V$  by  $blast$ 
    from  $\langle valid\text{-edge}\ a \rangle$  this  $\langle pred\ (kind\ a)\ s \rangle\ \langle pred\ (kind\ a)\ s' \rangle$ 
       $\langle intra\text{-kind}\ (kind\ a) \rangle$ 
    have  $\forall V \in Def\ (sourcenode\ a).$ 
 $state\text{-val}\ (transfer\ (kind\ a)\ s)\ V = state\text{-val}\ (transfer\ (kind\ a)\ s')\ V$ 
      by  $-(rule\ CFG\text{-intra}\text{-edge}\text{-transfer}\text{-uses}\text{-only}\text{-Use}, auto)$ 
    with  $\langle V \in Def\ (sourcenode\ a) \rangle\ \langle slice\text{-kind}\ S\ a = kind\ a \rangle$ 
    show  $?thesis$  by  $simp$ 
  next
  case  $False$ 
  from  $\langle V \in rv\ S\ (CFG\text{-node}\ (targetnode\ a)) \rangle$ 
  obtain  $xs\ nx$  where  $targetnode\ a - xs \rightarrow_i^* parent\text{-node}\ nx$ 
    and  $nx \in HRB\text{-slice}\ S$  and  $V \in Use_{SDG}\ nx$ 
    and  $\forall n''. valid\text{-SDG}\text{-node}\ n'' \wedge parent\text{-node}\ n'' \in set\ (sourcenodes\ xs)$ 
       $\longrightarrow V \notin Def_{SDG}\ n''$  by  $(fastforce\ elim: rvE)$ 
  from  $\langle valid\text{-edge}\ a \rangle$  have  $valid\text{-node}\ (sourcenode\ a)$  by  $simp$ 
  from  $\langle valid\text{-edge}\ a \rangle\ \langle targetnode\ a - xs \rightarrow_i^* parent\text{-node}\ nx \rangle\ \langle intra\text{-kind}\ (kind$ 
 $a) \rangle$ 
  have  $sourcenode\ a - a\#\ xs \rightarrow_i^* parent\text{-node}\ nx$ 
    by  $(fastforce\ intro: path.\ Cons\text{-path}\ simp: intra\text{-path}\text{-def})$ 
  with  $\langle V \in Def\ (sourcenode\ a) \rangle\ \langle V \in Use_{SDG}\ nx \rangle\ \langle valid\text{-node}\ (sourcenode$ 
 $a) \rangle$ 
   $\langle \forall n''. valid\text{-SDG}\text{-node}\ n'' \wedge parent\text{-node}\ n'' \in set\ (sourcenodes\ xs)$ 
 $\longrightarrow V \notin Def_{SDG}\ n'' \rangle$ 

```

```

have (CFG-node (sourcenode a)) influences V in nx
  by(fastforce intro:CFG-Def-SDG-Def simp:data-dependence-def)
hence (CFG-node (sourcenode a)) s-V→dd nx by(rule sum-SDG-ddep-edge)
from ⟨nx ∈ HRB-slice S⟩ ⟨CFG-node (sourcenode a)) s-V→dd nx⟩
have CFG-node (sourcenode a) ∈ HRB-slice S
proof(induct rule:HRB-slice-cases)
  case (phase1 n nx^)
  with ⟨CFG-node (sourcenode a)) s-V→dd nx⟩ show ?case
    by(fastforce intro:intro:ddep-slice1 combine-SDG-slices.combSlice-refl
      simp:HRB-slice-def)
next
  case (phase2 nx' n' n'' p n)
  from ⟨CFG-node (sourcenode a)) s-V→dd n⟩ ⟨n ∈ sum-SDG-slice2 n'⟩
  have CFG-node (sourcenode a) ∈ sum-SDG-slice2 n' by(rule ddep-slice2)
  with phase2 show ?thesis
    by(fastforce intro:combine-SDG-slices.combSlice-Return-parent-node
      simp:HRB-slice-def)
qed
with False have False by(simp add:SDG-to-CFG-set-def)
thus ?thesis by simp
qed
next
case False
from ⟨V ∈ rv S (CFG-node (targetnode a))⟩
obtain xs nx where targetnode a -xs→i* parent-node nx
  and nx ∈ HRB-slice S and V ∈ UseSDG nx
  and all:∀ n''. valid-SDG-node n'' ∧ parent-node n'' ∈ set (sourcenodes xs)
    → V ∉ DefSDG n'' by(fastforce elim:rvE)
from ⟨valid-edge a⟩ have valid-node (sourcenode a) by simp
from ⟨valid-edge a⟩ ⟨targetnode a -xs→i* parent-node nx⟩ ⟨intra-kind (kind a)⟩
have sourcenode a -a#xs →i* parent-node nx
  by(fastforce intro:path.Cons-path simp:intra-path-def)
from False all
have ∀ n''. valid-SDG-node n'' ∧ parent-node n'' ∈ set (sourcenodes (a#xs))
  → V ∉ DefSDG n''
  by(fastforce dest:SDG-Def-parent-Def simp:sourcenodes-def)
with ⟨sourcenode a -a#xs →i* parent-node nx⟩ ⟨nx ∈ HRB-slice S⟩
  ⟨V ∈ UseSDG nx⟩
have V ∈ rv S (CFG-node (sourcenode a)) by(fastforce intro:rvI)
from ⟨intra-kind (kind a)⟩ show ?thesis
proof(cases kind a)
  case (UpdateEdge f)
  show ?thesis
  proof(cases sourcenode a ∈ [HRB-slice S]CFG)
    case True
    with ⟨intra-kind (kind a)⟩ have slice-kind S a = kind a
      by(fastforce intro:slice-intra-kind-in-slice)
    from UpdateEdge ⟨s ≠ []⟩ have pred (kind a) s by(cases s) auto
    with ⟨valid-edge a⟩ ⟨V ∉ Def (sourcenode a)⟩ ⟨intra-kind (kind a)⟩

```

```

have state-val (transfer (kind a) s) V = state-val s V
  by(fastforce intro:CFG-intra-edge-no-Def-equal)
from UpdateEdge ⟨s' ≠ []⟩ have pred (kind a) s' by(cases s') auto
with ⟨valid-edge a⟩ ⟨V ∉ Def (sourcenode a)⟩ ⟨intra-kind (kind a)⟩
have state-val (transfer (kind a) s') V = state-val s' V
  by(fastforce intro:CFG-intra-edge-no-Def-equal)
with ⟨∀ V ∈ rv S (CFG-node (sourcenode a)). state-val s V = state-val s' V⟩
  ⟨state-val (transfer (kind a) s) V = state-val s V⟩
  ⟨V ∈ rv S (CFG-node (sourcenode a))⟩ ⟨slice-kind S a = kind a⟩
show ?thesis by fastforce
next
case False
with UpdateEdge have slice-kind S a = ↑id
  by -(rule slice-kind-Upd)
with ⟨∀ V ∈ rv S (CFG-node (sourcenode a)). state-val s V = state-val s' V⟩
  ⟨V ∈ rv S (CFG-node (sourcenode a))⟩ ⟨s ≠ []⟩ ⟨s' ≠ []⟩
show ?thesis by(cases s,auto,cases s',auto)
qed
next
case (PredicateEdge Q)
show ?thesis
proof(cases sourcenode a ∈ [HRB-slice S]CFG)
  case True
  with PredicateEdge ⟨intra-kind (kind a)⟩
  have slice-kind S a = (Q)√
    by(simp add:slice-intra-kind-in-slice)
  with ⟨∀ V ∈ rv S (CFG-node (sourcenode a)). state-val s V = state-val s' V⟩
  ⟨V ∈ rv S (CFG-node (sourcenode a))⟩ ⟨s ≠ []⟩ ⟨s' ≠ []⟩
  show ?thesis by(cases s,auto,cases s',auto)
  next
  case False
  with PredicateEdge ⟨valid-edge a⟩
  obtain Q' where slice-kind S a = (Q')√
    by -(erule kind-Predicate-notin-slice-slice-kind-Predicate)
  with ⟨∀ V ∈ rv S (CFG-node (sourcenode a)). state-val s V = state-val s' V⟩
  ⟨V ∈ rv S (CFG-node (sourcenode a))⟩ ⟨s ≠ []⟩ ⟨s' ≠ []⟩
  show ?thesis by(cases s,auto,cases s',auto)
  qed
qed (auto simp:intra-kind-def)
qed
qed

```

1.13.4 The weak simulation relational set WS

inductive-set $WS :: 'node\ SDG\text{-}node\ set \Rightarrow (('node\ list \times (('var \rightarrow 'val) \times 'ret)\ list) \times ('node\ list \times (('var \rightarrow 'val) \times 'ret)\ list))\ set$

for $S :: 'node\ SDG\text{-}node\ set$

where $WSI: [\forall m \in set\ ms.\ valid\text{-}node\ m; \forall m' \in set\ ms'. valid\text{-}node\ m'];$

$length\ ms = length\ s; length\ ms' = length\ s'; s \neq []; s' \neq []; ms = msx@mx\#tl\ ms'$;
 $get\text{-}proc\ mx = get\text{-}proc\ (hd\ ms')$;
 $\forall m \in set\ (tl\ ms'). \exists m'. call\text{-}of\text{-}return\text{-}node\ m\ m' \wedge m' \in [HRB\text{-}slice\ S]_{CFG}$;
 $msx \neq [] \longrightarrow (\exists mx'. call\text{-}of\text{-}return\text{-}node\ mx\ mx' \wedge mx' \notin [HRB\text{-}slice\ S]_{CFG})$;
 $\forall i < length\ ms'. snd\ (s!(length\ msx + i)) = snd\ (s'!i)$;
 $\forall m \in set\ (tl\ ms). return\text{-}node\ m$;
 $\forall i < length\ ms'. \forall V \in rv\ S\ (CFG\text{-}node\ ((mx\#tl\ ms')!i)).$
 $(fst\ (s!(length\ msx + i)))\ V = (fst\ (s'!i))\ V$;
 $obs\ ms\ [HRB\text{-}slice\ S]_{CFG} = obs\ ms'\ [HRB\text{-}slice\ S]_{CFG}$
 $\implies ((ms,s),(ms',s')) \in WS\ S$

lemma *WS-silent-move*:

assumes $S, kind \vdash (ms_1, s_1) -a \rightarrow_\tau (ms_1', s_1')$ **and** $((ms_1, s_1), (ms_2, s_2)) \in WS\ S$
shows $((ms_1', s_1'), (ms_2, s_2)) \in WS\ S$

proof –

from $\langle (ms_1, s_1), (ms_2, s_2) \rangle \in WS\ S$ **obtain** $msx\ mx$
where $WSE: \forall m \in set\ ms_1. valid\text{-}node\ m \ \forall m \in set\ ms_2. valid\text{-}node\ m$
 $length\ ms_1 = length\ s_1\ length\ ms_2 = length\ s_2\ s_1 \neq []\ s_2 \neq []$
 $ms_1 = msx@mx\#tl\ ms_2\ get\text{-}proc\ mx = get\text{-}proc\ (hd\ ms_2)$
 $\forall m \in set\ (tl\ ms_2). \exists m'. call\text{-}of\text{-}return\text{-}node\ m\ m' \wedge m' \in [HRB\text{-}slice\ S]_{CFG}$
 $msx \neq [] \longrightarrow (\exists mx'. call\text{-}of\text{-}return\text{-}node\ mx\ mx' \wedge mx' \notin [HRB\text{-}slice\ S]_{CFG})$
 $\forall m \in set\ (tl\ ms_1). return\text{-}node\ m$
 $\forall i < length\ ms_2. snd\ (s_1!(length\ msx + i)) = snd\ (s_2!i)$
 $\forall i < length\ ms_2. \forall V \in rv\ S\ (CFG\text{-}node\ ((mx\#tl\ ms_2)!i)).$
 $(fst\ (s_1!(length\ msx + i)))\ V = (fst\ (s_2!i))\ V$
 $obs\ ms_1\ [HRB\text{-}slice\ S]_{CFG} = obs\ ms_2\ [HRB\text{-}slice\ S]_{CFG}$
by(*fastforce elim: WS.cases*)
{ assume $\forall m \in set\ (tl\ ms_1'). return\text{-}node\ m$
have $obs\ ms_1'\ [HRB\text{-}slice\ S]_{CFG} = obs\ ms_2\ [HRB\text{-}slice\ S]_{CFG}$
proof(*cases obs ms_1' [HRB-slice S]_{CFG} = {}*)
case *True*
with $\langle S, kind \vdash (ms_1, s_1) -a \rightarrow_\tau (ms_1', s_1') \rangle$ **have** $obs\ ms_1\ [HRB\text{-}slice\ S]_{CFG}$
 $= \{\}$
by(*rule silent-move-empty-obs-slice*)
with $\langle obs\ ms_1\ [HRB\text{-}slice\ S]_{CFG} = obs\ ms_2\ [HRB\text{-}slice\ S]_{CFG} \rangle$
 $\langle obs\ ms_1'\ [HRB\text{-}slice\ S]_{CFG} = \{\} \rangle$
show *?thesis by simp*
next
case *False*
from *this* $\langle \forall m \in set\ (tl\ ms_1'). return\text{-}node\ m \rangle$
obtain ms' **where** $obs\ ms_1'\ [HRB\text{-}slice\ S]_{CFG} = \{ms'\}$
by(*fastforce dest: obs-singleton-element*)
hence $ms' \in obs\ ms_1'\ [HRB\text{-}slice\ S]_{CFG}$ **by** *fastforce*
from $\langle S, kind \vdash (ms_1, s_1) -a \rightarrow_\tau (ms_1', s_1') \rangle$ $\langle ms' \in obs\ ms_1'\ [HRB\text{-}slice\ S]_{CFG} \rangle$
 $\langle \forall m \in set\ (tl\ ms_1'). return\text{-}node\ m \rangle$
have $ms' \in obs\ ms_1\ [HRB\text{-}slice\ S]_{CFG}$ **by**(*fastforce intro: silent-move-obs-slice*)

```

from this  $\langle \forall m \in \text{set } (tl \ ms_1). \text{return-node } m \rangle$ 
have  $\text{obs } ms_1 \ [HRB\text{-slice } S]_{CFG} = \{ms'\}$  by (rule obs-singleton-element)
with  $\langle \text{obs } ms_1' \ [HRB\text{-slice } S]_{CFG} = \{ms'\} \rangle$ 
 $\langle \text{obs } ms_1 \ [HRB\text{-slice } S]_{CFG} = \text{obs } ms_2 \ [HRB\text{-slice } S]_{CFG} \rangle$ 
show ?thesis by simp
qed }
with  $\langle S, \text{kind} \vdash (ms_1, s_1) -a \rightarrow_{\tau} (ms_1', s_1') \rangle$  WSE
show ?thesis
proof (induct S f  $\equiv$  kind ms1 s1 a ms1' s1' rule:silent-move.induct)
  case (silent-move-intra a s1 s1' ms1 S ms1')
  note obs-eq =  $\langle \forall a \in \text{set } (tl \ ms_1'). \text{return-node } a \implies$ 
 $\text{obs } ms_1' \ [HRB\text{-slice } S]_{CFG} = \text{obs } ms_2 \ [HRB\text{-slice } S]_{CFG} \rangle$ 
  from  $\langle s_1 \neq [] \rangle \langle s_2 \neq [] \rangle$  obtain  $cf_1 \ cfs_1 \ cf_2 \ cfs_2$  where  $[simp]: s_1 = cf_1 \# cfs_1$ 
and  $[simp]: s_2 = cf_2 \# cfs_2$  by (cases s1, auto, cases s2, fastforce+)
  from  $\langle \text{transfer } (kind \ a) \ s_1 = s_1' \rangle \langle \text{intra-kind } (kind \ a) \rangle$ 
obtain  $cf_1'$  where  $[simp]: s_1' = cf_1' \# cfs_1$ 
  by (cases cf1, cases kind a, auto simp: intra-kind-def)
  from  $\langle \forall m \in \text{set } ms_1. \text{valid-node } m \rangle \langle ms_1' = \text{targetnode } a \ \# \ \text{tl } ms_1 \rangle \langle \text{valid-edge}$ 
 $a \rangle$ 
  have  $\forall m \in \text{set } ms_1'. \text{valid-node } m$  by (cases ms1) auto
  from  $\langle \text{length } ms_1 = \text{length } s_1 \rangle \langle \text{length } s_1' = \text{length } s_1 \rangle$ 
 $\langle ms_1' = \text{targetnode } a \ \# \ \text{tl } ms_1 \rangle$ 
  have  $\text{length } ms_1' = \text{length } s_1'$  by (cases ms1) auto
  from  $\langle \forall m \in \text{set } (tl \ ms_1). \text{return-node } m \rangle \langle ms_1' = \text{targetnode } a \ \# \ \text{tl } ms_1 \rangle$ 
  have  $\forall m \in \text{set } (tl \ ms_1'). \text{return-node } m$  by simp
  from obs-eq[OF this] have  $\text{obs } ms_1' \ [HRB\text{-slice } S]_{CFG} = \text{obs } ms_2 \ [HRB\text{-slice}$ 
 $S]_{CFG} \cdot$ 
  from  $\langle \forall i < \text{length } ms_2. \forall V \in rv \ S \ (CFG\text{-node } ((mx \# \text{tl } ms_2)!i)).$ 
 $(fst \ (s_1!(\text{length } ms_2 + i))) \ V = (fst \ (s_2!i)) \ V \rangle \langle \text{length } ms_2 = \text{length } s_2 \rangle$ 
  have  $\forall V \in rv \ S \ (CFG\text{-node } mx). (fst \ (s_1 \ ! \ \text{length } ms_2)) \ V = \text{state-val } s_2 \ V$ 
by (cases ms2) auto
  show ?case
  proof (cases msx)
    case Nil
    with  $\langle ms_1 = ms_x @ mx \# \text{tl } ms_2 \rangle \langle \text{hd } ms_1 = \text{sourcenode } a \rangle$ 
    have  $[simp]: mx = \text{sourcenode } a$  and  $[simp]: \text{tl } ms_1 = \text{tl } ms_2$  by simp-all
    from  $\langle \forall m \in \text{set } (tl \ ms_2). \exists m'. \text{call-of-return-node } m \ m' \wedge m' \in [HRB\text{-slice}$ 
 $S]_{CFG} \rangle$ 
 $\langle (\exists m \in \text{set } (tl \ ms_1). \exists m'. \text{call-of-return-node } m \ m' \wedge m' \notin [HRB\text{-slice } S]_{CFG}) \rangle$ 
     $\vee$ 
 $\text{hd } ms_1 \notin [HRB\text{-slice } S]_{CFG} \rangle$ 
    have  $\text{hd } ms_1 \notin [HRB\text{-slice } S]_{CFG}$  by fastforce
    with  $\langle \text{hd } ms_1 = \text{sourcenode } a \rangle$  have  $\text{sourcenode } a \notin [HRB\text{-slice } S]_{CFG}$  by
simp
    from  $\langle ms_1' = \text{targetnode } a \ \# \ \text{tl } ms_1 \rangle$  have  $ms_1' = [] \ @ \ \text{targetnode } a \ \# \ \text{tl } ms_2$ 
by simp
    from  $\langle \text{valid-edge } a \rangle \langle \text{intra-kind } (kind \ a) \rangle$ 
    have  $\text{get-proc } (\text{sourcenode } a) = \text{get-proc } (\text{targetnode } a)$  by (rule get-proc-intra)
    with  $\langle \text{get-proc } mx = \text{get-proc } (\text{hd } ms_2) \rangle$ 

```


have $\text{get-proc}(\text{targetnode } a) = \text{get-proc}(\text{hd } ms_2)$ **by** *simp*
from $\langle \text{transfer } (\text{kind } a) \ s_1 = s_1' \rangle \langle \text{intra-kind } (\text{kind } a) \rangle$
have $\text{snd } cf_1' = \text{snd } cf_1$ **by** *(auto simp:intra-kind-def)*
with $\langle \forall i < \text{length } ms_2. \text{snd } (s_1 ! (\text{length } msx + i)) = \text{snd } (s_2 ! i) \rangle \text{Nil}$
have $\forall i < \text{length } ms_2. \text{snd } (s_1' ! i) = \text{snd } (s_2 ! i)$
by *auto(case-tac i,auto)*
have $\forall V \in \text{rv } S \ (\text{CFG-node } (\text{targetnode } a)). \text{fst } cf_1' V = \text{fst } cf_2 V$
proof
fix V **assume** $V \in \text{rv } S \ (\text{CFG-node } (\text{targetnode } a))$
from $\langle \text{valid-edge } a \rangle \langle \text{intra-kind } (\text{kind } a) \rangle \langle \text{sourcenode } a \notin \lfloor \text{HRB-slice } S \rfloor_{\text{CFG}} \rangle$
have $\text{obs-intra } (\text{targetnode } a) \lfloor \text{HRB-slice } S \rfloor_{\text{CFG}} =$
 $\text{obs-intra } (\text{sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{\text{CFG}}$
by *(rule edge-obs-intra-slice-eq)*
hence $\text{rv } S \ (\text{CFG-node } (\text{targetnode } a)) = \text{rv } S \ (\text{CFG-node } (\text{sourcenode } a))$
by *(rule closed-eq-obs-eq-rvs')*
with $\langle V \in \text{rv } S \ (\text{CFG-node } (\text{targetnode } a)) \rangle$
have $V \in \text{rv } S \ (\text{CFG-node } (\text{sourcenode } a))$ **by** *simp*
then obtain $as \ n'$ **where** $\text{sourcenode } a \text{ --as--}_{i^*} \text{parent-node } n'$
and $n' \in \text{HRB-slice } S$ **and** $V \in \text{Use}_{\text{SDG}} \ n'$
and $\forall n''. \text{valid-SDG-node } n'' \wedge \text{parent-node } n'' \in \text{set } (\text{sourcenodes } as)$
 $\longrightarrow V \notin \text{Def}_{\text{SDG}} \ n''$
by *(fastforce elim:rvE)*
with $\langle \text{sourcenode } a \notin \lfloor \text{HRB-slice } S \rfloor_{\text{CFG}} \rangle \langle \text{valid-edge } a \rangle$
have $V \notin \text{Def}_{\text{SDG}} \ (\text{CFG-node } (\text{sourcenode } a))$
apply *(clarsimp simp:intra-path-def)*
apply *(erule path.cases)*
by *(auto dest:valid-SDG-node-in-slice-parent-node-in-slice*
 $\text{simp:sourcenodes-def SDG-to-CFG-set-def})$
from $\langle \text{valid-edge } a \rangle$ **have** $\text{valid-node } (\text{sourcenode } a)$ **by** *simp*
with $\langle V \notin \text{Def}_{\text{SDG}} \ (\text{CFG-node } (\text{sourcenode } a)) \rangle$ **have** $V \notin \text{Def} \ (\text{sourcenode } a)$
by *(fastforce intro:CFG-Def-SDG-Def valid-SDG-CFG-node)*
with $\langle \text{valid-edge } a \rangle \langle \text{intra-kind } (\text{kind } a) \rangle \langle \text{pred } (\text{kind } a) \ s_1 \rangle$
have $\text{state-val } (\text{transfer } (\text{kind } a) \ s_1) \ V = \text{state-val } s_1 \ V$
by *(fastforce intro:CFG-intra-edge-no-Def-equal)*
with $\langle \text{transfer } (\text{kind } a) \ s_1 = s_1' \rangle$ **have** $\text{fst } cf_1' V = \text{fst } cf_1 V$ **by** *simp*
from $\langle V \in \text{rv } S \ (\text{CFG-node } (\text{sourcenode } a)) \rangle \langle \text{msx} = [] \rangle$
 $\langle \forall V \in \text{rv } S \ (\text{CFG-node } mx). (\text{fst } (s_1 ! \text{length } msx)) V = \text{state-val } s_2 \ V \rangle$
have $\text{fst } cf_1 V = \text{fst } cf_2 V$ **by** *simp*
with $\langle \text{fst } cf_1' V = \text{fst } cf_1 V \rangle$ **show** $\text{fst } cf_1' V = \text{fst } cf_2 V$ **by** *simp*
qed
with $\langle \forall i < \text{length } ms_2. \forall V \in \text{rv } S \ (\text{CFG-node } ((mx \# \text{tl } ms_2) ! i)).$
 $(\text{fst } (s_1 ! (\text{length } msx + i))) V = (\text{fst } (s_2 ! i)) V \rangle \text{Nil}$
have $\forall i < \text{length } ms_2. \forall V \in \text{rv } S \ (\text{CFG-node } ((\text{targetnode } a \# \text{tl } ms_2) ! i)).$
 $(\text{fst } (s_1' ! (\text{length } [] + i))) V = (\text{fst } (s_2 ! i)) V$
by *auto(case-tac i,auto)*
with $\langle \forall m \in \text{set } ms_1'. \text{valid-node } m \rangle \langle \forall m \in \text{set } ms_2. \text{valid-node } m \rangle$
 $\langle \text{length } ms_1' = \text{length } s_1' \rangle \langle \text{length } ms_2 = \text{length } s_2 \rangle$
 $\langle ms_1' = [] \ @ \ \text{targetnode } a \ \# \ \text{tl } ms_2 \rangle$

$\langle \text{get-proc } (\text{targetnode } a) = \text{get-proc } (\text{hd } ms_2) \rangle$
 $\langle \forall m \in \text{set } (\text{tl } ms_2). \exists m'. \text{call-of-return-node } m \ m' \wedge m' \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$

$S \rfloor_{CFG}$
 $\langle \forall m \in \text{set } (\text{tl } ms_1). \text{return-node } m \rangle$
 $\langle \text{obs } ms_1' \lfloor \text{HRB-slice } S \rfloor_{CFG} = \text{obs } ms_2 \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
 $\langle \forall i < \text{length } ms_2. \text{snd } (s_1' ! i) = \text{snd } (s_2 ! i) \rangle$
show *?thesis* **by** (*auto intro!; WSI*)

next
case (*Cons mx' msx'*)
with $\langle ms_1 = msx @ mx \# \text{tl } ms_2 \rangle \langle \text{hd } ms_1 = \text{sourcenode } a \rangle$
have [*simp*]: $mx' = \text{sourcenode } a$ **and** [*simp*]: $\text{tl } ms_1 = msx' @ mx \# \text{tl } ms_2$
by *simp-all*
from $\langle ms_1' = \text{targetnode } a \# \text{tl } ms_1 \rangle$ **have** $ms_1' = ((\text{targetnode } a) \# msx') @ mx \# \text{tl } ms_2$
 ms_2
by *simp*
from $\langle \forall V \in rv \ S \ (CFG\text{-node } mx). (\text{fst } (s_1 ! \text{length } msx)) \ V = \text{state-val } s_2 \ V \rangle$

$Cons$
have $rv: \forall V \in rv \ S \ (CFG\text{-node } mx).$
 $\langle \text{fst } (s_1' ! \text{length } (\text{targetnode } a \# msx')) \ V = \text{state-val } s_2 \ V \rangle$ **by** *fastforce*
from $\langle ms_1 = msx @ mx \# \text{tl } ms_2 \rangle$ $Cons \ \langle ms_1' = \text{targetnode } a \# \text{tl } ms_1 \rangle$
have $ms_1' = ((\text{targetnode } a) \# msx') @ mx \# \text{tl } ms_2$ **by** *simp*
from $\langle \forall i < \text{length } ms_2. \text{snd } (s_1 ! (\text{length } msx + i)) = \text{snd } (s_2 ! i) \rangle$ $Cons$
have $\forall i < \text{length } ms_2. \text{snd } (s_1' ! (\text{length } msx + i)) = \text{snd } (s_2 ! i)$ **by** *fastforce*
from $\langle \forall V \in rv \ S \ (CFG\text{-node } mx). (\text{fst } (s_1 ! \text{length } msx)) \ V = \text{state-val } s_2 \ V \rangle$

$Cons$
have $\forall V \in rv \ S \ (CFG\text{-node } mx). (\text{fst } (s_1' ! \text{length } msx)) \ V = \text{state-val } s_2 \ V$
by *simp*
with $\langle \forall i < \text{length } ms_2. \forall V \in rv \ S \ (CFG\text{-node } ((mx \# \text{tl } ms_2) ! i)).$
 $\langle \text{fst } (s_1 ! (\text{length } msx + i)) \ V = (\text{fst } (s_2 ! i)) \ V \rangle$ $Cons$
have $\forall i < \text{length } ms_2. \forall V \in rv \ S \ (CFG\text{-node } ((mx \# \text{tl } ms_2) ! i)).$
 $\langle \text{fst } (s_1' ! (\text{length } (\text{targetnode } a \# msx') + i)) \ V = (\text{fst } (s_2 ! i)) \ V \rangle$
by *clarsimp*
with $\langle \forall m \in \text{set } ms_1'. \text{valid-node } m \rangle \langle \forall m \in \text{set } ms_2. \text{valid-node } m \rangle$
 $\langle \text{length } ms_1' = \text{length } s_1' \rangle \langle \text{length } ms_2 = \text{length } s_2 \rangle$
 $\langle ms_1' = ((\text{targetnode } a) \# msx') @ mx \# \text{tl } ms_2 \rangle$
 $\langle \forall m \in \text{set } (\text{tl } ms_2). \exists m'. \text{call-of-return-node } m \ m' \wedge m' \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
 $\langle \forall m \in \text{set } (\text{tl } ms_1'). \text{return-node } m \rangle \langle \text{get-proc } mx = \text{get-proc } (\text{hd } ms_2) \rangle$
 $\langle msx \neq [] \longrightarrow (\exists mx'. \text{call-of-return-node } mx \ mx' \wedge mx' \notin \lfloor \text{HRB-slice } S \rfloor_{CFG}) \rangle$

$S \rfloor_{CFG}$
 $\langle \text{obs } ms_1' \lfloor \text{HRB-slice } S \rfloor_{CFG} = \text{obs } ms_2 \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$ $Cons$
 $\langle \forall i < \text{length } ms_2. \text{snd } (s_1' ! (\text{length } msx + i)) = \text{snd } (s_2 ! i) \rangle$
show *?thesis* **by** $-(\text{rule } WSI, \text{clarsimp+}, \text{fastforce}, \text{clarsimp+})$

qed
next
case (*silent-move-call a s1 s1' Q r p fs a' ms1 S ms1'*)
note $\text{obs-eq} = \langle \forall a \in \text{set } (\text{tl } ms_1'). \text{return-node } a \implies$
 $\text{obs } ms_1' \lfloor \text{HRB-slice } S \rfloor_{CFG} = \text{obs } ms_2 \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
from $\langle s_1 \neq [] \rangle \langle s_2 \neq [] \rangle$ **obtain** $cf_1 \ cfs_1 \ cf_2 \ cfs_2$ **where** [*simp*]: $s_1 = cf_1 \# cfs_1$
and [*simp*]: $s_2 = cf_2 \# cfs_2$ **by** (*cases s1, auto, cases s2, fastforce+*)

```

from ⟨valid-edge a⟩ ⟨kind a = Q:r↔pfs⟩
obtain ins outs where (p,ins,outs) ∈ set procs
  by(fastforce dest!:callee-in-procs)
with ⟨transfer (kind a) s1 = s1'⟩ ⟨valid-edge a⟩ ⟨kind a = Q:r↔pfs⟩
have [simp]:s1' = (Map.empty(ins [:=] params fs (fst cf1)), r) # cf1 # cfs1
  by simp(unfold formal-in-THE,simp)
from ⟨length ms1 = length s1'⟩ ⟨ms1' = targetnode a # targetnode a' # tl ms1'⟩
have length ms1' = length s1' by simp
from ⟨valid-edge a⟩ ⟨a' ∈ get-return-edges a⟩ have valid-edge a'
  by(rule get-return-edges-valid)
with ⟨∀ m ∈ set ms1. valid-node m⟩ ⟨valid-edge a⟩
  ⟨ms1' = targetnode a # targetnode a' # tl ms1'⟩
have ∀ m ∈ set ms1'. valid-node m by(cases ms1') auto
from ⟨valid-edge a'⟩ ⟨valid-edge a⟩ ⟨a' ∈ get-return-edges a⟩
have return-node (targetnode a') by(fastforce simp:return-node-def)
with ⟨valid-edge a⟩ ⟨a' ∈ get-return-edges a⟩ ⟨valid-edge a'⟩
have call-of-return-node (targetnode a') (sourcenode a)
  by(simp add:call-of-return-node-def) blast
from ⟨∀ m ∈ set (tl ms1'). return-node m⟩ ⟨return-node (targetnode a')⟩
  ⟨ms1' = targetnode a # targetnode a' # tl ms1'⟩
have ∀ m ∈ set (tl ms1'). return-node m by simp
from obs-eq[OF this] have obs ms1' [HRB-slice S] CFG = obs ms2 [HRB-slice
S] CFG .
from ⟨∀ i < length ms2. ∀ V ∈ rv S (CFG-node ((mx#tl ms2)!i)).
  (fst (s1!(length ms2 + i))) V = (fst (s2!i)) V⟩ ⟨length ms2 = length s2'⟩
have ∀ V ∈ rv S (CFG-node mx). (fst (s1! length ms2)) V = state-val s2 V
  by(erule-tac x=0 in allE) auto
show ?case
proof(cases ms2)
  case Nil
    with ⟨ms1 = msx@mx#tl ms2'⟩ ⟨hd ms1 = sourcenode a⟩
    have [simp]:mx = sourcenode a and [simp]:tl ms1 = tl ms2' by simp-all
    from ⟨∀ m ∈ set (tl ms2'). ∃ m'. call-of-return-node m m' ∧ m' ∈ [HRB-slice
S] CFG⟩
    ⟨(∃ m ∈ set (tl ms1'). ∃ m'. call-of-return-node m m' ∧ m' ∉ [HRB-slice S] CFG)
  ∨
    hd ms1 ∉ [HRB-slice S] CFG⟩
    have hd ms1 ∉ [HRB-slice S] CFG by fastforce
    with ⟨hd ms1 = sourcenode a⟩ have sourcenode a ∉ [HRB-slice S] CFG by
simp
from ⟨valid-edge a⟩ ⟨a' ∈ get-return-edges a⟩
obtain a'' where valid-edge a'' and sourcenode a'' = sourcenode a
  and targetnode a'' = targetnode a' and intra-kind(kind a'')
  by -(drule call-return-node-edge,auto simp:intra-kind-def)
from ⟨valid-edge a''⟩ ⟨intra-kind(kind a'')⟩
have get-proc (sourcenode a'') = get-proc (targetnode a'')
  by(rule get-proc-intra)
with ⟨sourcenode a'' = sourcenode a⟩ ⟨targetnode a'' = targetnode a'⟩
  ⟨get-proc mx = get-proc (hd ms2')⟩

```

```

have get-proc (targetnode a') = get-proc (hd ms2) by simp
from ⟨valid-edge a⟩ ⟨kind a = Q:r↔pfs⟩ ⟨a' ∈ get-return-edges a⟩
have CFG-node (sourcnode a) s-p→sum CFG-node (targetnode a')
  by (fastforce intro:sum-SDG-call-summary-edge)
have targetnode a' ∉ [HRB-slice S]CFG
proof
  assume targetnode a' ∈ [HRB-slice S]CFG
hence CFG-node (targetnode a') ∈ HRB-slice S by (simp add:SDG-to-CFG-set-def)
hence CFG-node (sourcnode a) ∈ HRB-slice S
proof (induct CFG-node (targetnode a') rule:HRB-slice-cases)
  case (phase1 nx)
    with ⟨CFG-node (sourcnode a) s-p→sum CFG-node (targetnode a')⟩
    show ?case by (fastforce intro:combine-SDG-slices.combSlice-refl sum-slice1
      simp:HRB-slice-def)
  next
    case (phase2 nx n' n'' p')
    from ⟨CFG-node (targetnode a') ∈ sum-SDG-slice2 n'⟩
    ⟨CFG-node (sourcnode a) s-p→sum CFG-node (targetnode a')⟩ ⟨valid-edge
a)
    have CFG-node (sourcnode a) ∈ sum-SDG-slice2 n'
      by (fastforce intro:sum-slice2)
    with ⟨n' ∈ sum-SDG-slice1 nx⟩ ⟨n'' s-p'→ret CFG-node (parent-node n')⟩

      ⟨nx ∈ S⟩
    show ?case
      by (fastforce intro:combine-SDG-slices.combSlice-Return-parent-node
        simp:HRB-slice-def)
    qed
  with ⟨sourcnode a ∉ [HRB-slice S]CFG⟩ show False
    by (simp add:SDG-to-CFG-set-def HRB-slice-def)
  qed
from ⟨ms1' = targetnode a # targetnode a' # tl ms1⟩
have ms1' = [targetnode a] @ targetnode a' # tl ms2 by simp
from ⟨∀ i < length ms2. snd (s1 ! (length ms1 + i)) = snd (s2 ! i)⟩ Nil
have ∀ i < length ms2. snd (s1' ! (length [targetnode a] + i)) = snd (s2 ! i)
  by fastforce
have ∀ V ∈ rv S (CFG-node (targetnode a')). (fst (s1' ! 1)) V = state-val s2
V
proof
  fix V assume V ∈ rv S (CFG-node (targetnode a'))
  from ⟨valid-edge a⟩ ⟨a' ∈ get-return-edges a⟩
  obtain a'' where edge:valid-edge a'' sourcnode a'' = sourcnode a
    targetnode a'' = targetnode a' intra-kind(kind a')
  by -(drule call-return-node-edge, auto simp:intra-kind-def)
  from ⟨V ∈ rv S (CFG-node (targetnode a'))⟩
  obtain as n' where targetnode a' -as→l* parent-node n'
    and n' ∈ HRB-slice S and V ∈ UseSDG n'
    and ∀ n''. valid-SDG-node n'' ∧ parent-node n'' ∈ set (sourcnodes as)
      → V ∉ DefSDG n''

```

by(*fastforce elim:rvE*)
from $\langle \text{targetnode } a' - \text{as} \rightarrow_i * \text{parent-node } n' \rangle \text{ edge}$
have $\text{sourcenode } a - a'' \# \text{as} \rightarrow_i * \text{parent-node } n'$
by(*fastforce intro:Cons-path simp:intra-path-def*)
from $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q:r \hookrightarrow \text{pfs} \rangle$
have $V \notin \text{Def} (\text{sourcenode } a)$
by(*fastforce dest:call-source-Def-empty*)
with $\langle \forall n''. \text{valid-SDG-node } n'' \wedge \text{parent-node } n'' \in \text{set} (\text{sourcenodes } \text{as})$
 $\rightarrow V \notin \text{Def}_{SDG} n'' \rangle \langle \text{sourcenode } a'' = \text{sourcenode } a \rangle$
have $\forall n''. \text{valid-SDG-node } n'' \wedge \text{parent-node } n'' \in \text{set} (\text{sourcenodes } (a'' \# \text{as}))$
 $\rightarrow V \notin \text{Def}_{SDG} n''$
by(*fastforce dest:SDG-Def-parent-Def simp:sourcenodes-def*)
with $\langle \text{sourcenode } a - a'' \# \text{as} \rightarrow_i * \text{parent-node } n' \rangle \langle n' \in \text{HRB-slice } S \rangle$
 $\langle V \in \text{Use}_{SDG} n' \rangle$
have $V \in \text{rv } S (\text{CFG-node } (\text{sourcenode } a))$ **by**(*fastforce intro:rvI*)
from $\langle \forall V \in \text{rv } S (\text{CFG-node } mx). (\text{fst } (s_1 ! \text{length } msx)) V = \text{state-val } s_2$
 $V \rangle \text{Nil}$
have $\forall V \in \text{rv } S (\text{CFG-node } (\text{sourcenode } a)). \text{fst } cf_1 V = \text{fst } cf_2 V$ **by** *simp*
with $\langle V \in \text{rv } S (\text{CFG-node } (\text{sourcenode } a)) \rangle$ **have** $\text{fst } cf_1 V = \text{fst } cf_2 V$
by *simp*
thus $(\text{fst } (s_1' ! 1)) V = \text{state-val } s_2 V$ **by** *simp*
qed
with $\langle \forall i < \text{length } ms_2. \forall V \in \text{rv } S (\text{CFG-node } ((mx \# \text{tl } ms_2) ! i)).$
 $(\text{fst } (s_1 ! (\text{length } msx + i))) V = (\text{fst } (s_2 ! i)) V \rangle \text{Nil}$
have $\forall i < \text{length } ms_2. \forall V \in \text{rv } S (\text{CFG-node } ((\text{targetnode } a' \# \text{tl } ms_2) ! i)).$
 $(\text{fst } (s_1' ! (\text{length } [\text{targetnode } a] + i))) V = (\text{fst } (s_2 ! i)) V$
by *clarsimp(case-tac i,auto)*
with $\langle \forall m \in \text{set } ms_1'. \text{valid-node } m \rangle \langle \forall m \in \text{set } ms_2. \text{valid-node } m \rangle$
 $\langle \text{length } ms_1' = \text{length } s_1' \rangle \langle \text{length } ms_2 = \text{length } s_2 \rangle$
 $\langle \forall m \in \text{set } (\text{tl } ms_2). \exists m'. \text{call-of-return-node } m m' \wedge m' \in [\text{HRB-slice } S]_{CFG} \rangle$
 $\langle ms_1' = [\text{targetnode } a] @ \text{targetnode } a' \# \text{tl } ms_2 \rangle$
 $\langle \text{targetnode } a' \notin [\text{HRB-slice } S]_{CFG} \rangle \langle \text{return-node } (\text{targetnode } a') \rangle$
 $\langle \text{obs } ms_1' [\text{HRB-slice } S]_{CFG} = \text{obs } ms_2 [\text{HRB-slice } S]_{CFG} \rangle$
 $\langle \text{get-proc } (\text{targetnode } a') = \text{get-proc } (\text{hd } ms_2) \rangle$
 $\langle \forall m \in \text{set } (\text{tl } ms_1'). \text{return-node } m \rangle \langle \text{sourcenode } a \notin [\text{HRB-slice } S]_{CFG} \rangle$
 $\langle \text{call-of-return-node } (\text{targetnode } a') (\text{sourcenode } a) \rangle$
 $\langle \forall i < \text{length } ms_2. \text{snd } (s_1' ! (\text{length } [\text{targetnode } a] + i)) = \text{snd } (s_2 ! i) \rangle$
show *?thesis* **by**(*auto intro!:WSI*)
next
case $(\text{Cons } mx' msx')$
with $\langle ms_1 = msx @ mx \# \text{tl } ms_2 \rangle \langle \text{hd } ms_1 = \text{sourcenode } a \rangle$
have $[\text{simp}]: mx' = \text{sourcenode } a$ **and** $[\text{simp}]: \text{tl } ms_1 = msx' @ mx \# \text{tl } ms_2$
by *simp-all*
from $\langle ms_1' = \text{targetnode } a \# \text{targetnode } a' \# \text{tl } ms_1 \rangle$
have $ms_1' = (\text{targetnode } a \# \text{targetnode } a' \# msx') @ mx \# \text{tl } ms_2$
by *simp*
from $\langle \forall i < \text{length } ms_2. \text{snd } (s_1 ! (\text{length } msx + i)) = \text{snd } (s_2 ! i) \rangle \text{Cons}$
have $\forall i < \text{length } ms_2.$

$snd (s_1' ! (length (targetnode a \# targetnode a' \# msx') + i)) = snd (s_2 !$
i)
by *fastforce*
from $\langle \forall V \in rv S (CFG\text{-node } mx). (fst (s_1 ! length msx)) V = state\text{-val } s_2 V \rangle$
Cons
have $\forall V \in rv S (CFG\text{-node } mx).$
 $(fst (s_1' ! length(targetnode a \# targetnode a' \# msx')) V = state\text{-val } s_2$
V
by *simp*
with $\langle \forall i < length ms_2. \forall V \in rv S (CFG\text{-node } ((mx \# tl ms_2)!i)).$
 $(fst (s_1!(length msx + i))) V = (fst (s_2!i)) V \rangle$ *Cons*
have $\forall i < length ms_2. \forall V \in rv S (CFG\text{-node } ((mx \# tl ms_2)!i)).$
 $(fst (s_1'!(length (targetnode a \# targetnode a' \# msx') + i))) V =$
 $(fst (s_2!i)) V$
by *clarsimp*
with $\langle \forall m \in set ms_1'. valid\text{-node } m \rangle \langle \forall m \in set ms_2. valid\text{-node } m \rangle$
 $\langle length ms_1' = length s_1' \rangle \langle length ms_2 = length s_2 \rangle$
 $\langle ms_1' = (targetnode a \# targetnode a' \# msx') @ mx \# tl ms_2 \rangle$
 $\langle return\text{-node } (targetnode a') \rangle$
 $\langle \forall m \in set (tl ms_2). \exists m'. call\text{-of}\text{-return}\text{-node } m m' \wedge m' \in [HRB\text{-slice } S]_{CFG} \rangle$
 $\langle msx \neq [] \longrightarrow (\exists mx'. call\text{-of}\text{-return}\text{-node } mx mx' \wedge mx' \notin [HRB\text{-slice}$
 $S]_{CFG}) \rangle$
 $\langle obs ms_1' [HRB\text{-slice } S]_{CFG} = obs ms_2 [HRB\text{-slice } S]_{CFG} \rangle$ *Cons*
 $\langle get\text{-proc } mx = get\text{-proc } (hd ms_2) \rangle \langle \forall m \in set (tl ms_1'). return\text{-node } m \rangle$
 $\langle \forall i < length ms_2.$
 $snd (s_1' ! (length (targetnode a \# targetnode a' \# msx') + i)) = snd (s_2 !$
i)
show *?thesis by* $-(rule WSI,clarsimp+,fastforce,clarsimp+)$
qed
next
case $(silent\text{-move}\text{-return } a s_1 s_1' Q p f' ms_1 S ms_1')$
note $obs\text{-eq} = \langle \forall a \in set (tl ms_1'). return\text{-node } a \implies$
 $obs ms_1' [HRB\text{-slice } S]_{CFG} = obs ms_2 [HRB\text{-slice } S]_{CFG} \rangle$
from $\langle transfer (kind a) s_1 = s_1' \rangle \langle kind a = Q \leftrightarrow pf' \rangle \langle s_1 \neq [] \rangle \langle s_1' \neq [] \rangle$
obtain $cf_1 cfx_1 cfs_1 cf_1'$ **where** $[simp]: s_1 = cf_1 \# cfx_1 \# cfs_1$
and $s_1' = (f' (fst cf_1) (fst cfx_1), snd cfx_1) \# cfs_1$
by $(cases s_1, auto, case\text{-tac } list, fastforce+)$
from $\langle s_2 \neq [] \rangle$ **obtain** $cf_2 cfs_2$ **where** $[simp]: s_2 = cf_2 \# cfs_2$ **by** $(cases s_2) auto$
from $\langle length ms_1 = length s_1 \rangle$ **have** $ms_1 \neq []$ **and** $tl ms_1 \neq []$ **by** $(cases$
 $ms_1, auto) +$
from $\langle valid\text{-edge } a \rangle \langle kind a = Q \leftrightarrow pf' \rangle$
obtain $a' Q' r' fs'$ **where** $valid\text{-edge } a'$ **and** $kind a' = Q':r' \leftrightarrow pfs'$
and $a \in get\text{-return}\text{-edges } a'$
by $-(drule return\text{-needs}\text{-call}, auto)$
then obtain $ins outs$ **where** $(p, ins, outs) \in set\ proc s$
by $(fastforce dest!: callee\text{-in}\text{-procs})$
with $\langle valid\text{-edge } a \rangle \langle kind a = Q \leftrightarrow pf' \rangle$
have $f' (fst cf_1) (fst cfx_1) =$
 $(fst cfx_1)(ParamDefs (targetnode a) [:=] map (fst cf_1) outs)$

```

    by(rule CFG-return-edge-fun)
  with ⟨s1' = (f' (fst cf1) (fst cfx1),snd cfx1)#cfs1⟩
  have [simp]:s1' = ((fst cfx1)
    (ParamDefs (targetnode a) [:=] map (fst cf1) outs),snd cfx1)#cfs1 by simp
  from ⟨∀ m∈set ms1. valid-node m⟩ ⟨ms1' = tl ms1⟩ have ∀ m∈set ms1'. valid-node
m
    by(cases ms1) auto
  from ⟨length ms1 = length s1⟩ ⟨ms1' = tl ms1⟩
  have length ms1' = length s1' by simp
  from ⟨∀ m∈set (tl ms1). return-node m⟩ ⟨ms1' = tl ms1⟩ ⟨ms1 ≠ []⟩ ⟨tl ms1 ≠
[]⟩
  have ∀ m∈set (tl ms1'). return-node m by(cases ms1)(auto,cases ms1',auto)
  from obs-eq[OF this] have obs ms1' [HRB-slice S]CFG = obs ms2 [HRB-slice
S]CFG .
  show ?case
  proof(cases msx)
    case Nil
    with ⟨ms1 = msx@mx#tl ms2⟩ ⟨hd ms1 = sourcenode a⟩
    have mx = sourcenode a and tl ms1 = tl ms2 by simp-all
    with ⟨∃ m∈set (tl ms1). ∃ m'. call-of-return-node m m' ∧ m' ∉ [HRB-slice
S]CFG⟩
    ⟨∀ m∈set (tl ms2). ∃ m'. call-of-return-node m m' ∧ m' ∈ [HRB-slice S]CFG⟩
    have False by fastforce
    thus ?thesis by simp
  next
  case (Cons mx' msx')
  with ⟨ms1 = msx@mx#tl ms2⟩ ⟨hd ms1 = sourcenode a⟩
  have [simp]:mx' = sourcenode a and [simp]:tl ms1 = msx'@mx#tl ms2
    by simp-all
  from ⟨ms1' = tl ms1⟩ have ms1' = msx'@mx#tl ms2 by simp
  with ⟨ms1 = msx@mx#tl ms2⟩ ⟨∀ m∈set (tl ms1). return-node m⟩ Cons
  have ∀ m∈set (tl ms1'). return-node m
    by(cases msx') auto
  from ⟨∀ i<length ms2. snd (s1 ! (length msx + i)) = snd (s2 ! i)⟩ Cons
  have ∀ i<length ms2. snd (s1' ! (length msx' + i)) = snd (s2 ! i)
    by auto(case-tac i,auto,cases msx',auto)
  from ⟨∀ i<length ms2. ∀ V∈rv S (CFG-node ((mx # tl ms2) ! i)).
    (fst (s1 ! (length msx + i))) V = (fst (s2 ! i)) V⟩
    ⟨length ms2 = length s2⟩ ⟨s2 ≠ []⟩
  have ∀ V∈rv S (CFG-node mx). (fst (s1 ! length msx)) V = state-val s2 V
    by fastforce
  have ∀ V∈rv S (CFG-node mx). (fst (s1' ! length msx')) V = state-val s2 V
  proof(cases msx')
    case Nil
    with ⟨∀ V∈rv S (CFG-node mx). (fst (s1 ! length msx)) V = state-val s2
V⟩
    ⟨msx = mx'#msx'⟩
    have rv:∀ V∈rv S (CFG-node mx). fst cfx1 V = fst cf2 V by fastforce
    from Nil ⟨tl ms1 = msx'@mx#tl ms2⟩ ⟨hd (tl ms1) = targetnode a⟩

```

```

have [simp]:mx = targetnode a by simp
from Cons
  ⟨msx ≠ [] ⟶ (∃ mx'. call-of-return-node mx mx' ∧ mx' ∉ [HRB-slice
S]CFG)⟩
obtain mx'' where call-of-return-node mx mx'' and mx'' ∉ [HRB-slice
S]CFG
by blast
hence mx ∉ [HRB-slice S]CFG
by(rule call-node-notin-slice-return-node-neither)
have ∀ V∈rv S (CFG-node mx).
  (fst cfx1)(ParamDefs (targetnode a) [:=] map (fst cf1) outs) V = fst cf2
V
proof
fix V assume V∈rv S (CFG-node mx)
show (fst cfx1)(ParamDefs (targetnode a) [:=] map (fst cf1) outs) V =
  fst cf2 V
proof(cases V ∈ set (ParamDefs (targetnode a)))
  case True
    with ⟨valid-edge a⟩ have V ∈ Def (targetnode a)
      by(fastforce intro:ParamDefs-in-Def)
    with ⟨valid-edge a⟩ have V ∈ DefSDG (CFG-node (targetnode a))
      by(auto intro!:CFG-Def-SDG-Def)
    from ⟨V∈rv S (CFG-node mx)⟩ obtain as n'
      where targetnode a -as→l* parent-node n'
      and n' ∈ HRB-slice S V ∈ UseSDG n'
      and ∀ n''. valid-SDG-node n'' ∧ parent-node n'' ∈ set (sourcenodes as)
        ⟶ V ∉ DefSDG n'' by(fastforce elim:rvE)
    from ⟨targetnode a -as→l* parent-node n'⟩ ⟨n' ∈ HRB-slice S⟩
      ⟨mx ∉ [HRB-slice S]CFG⟩
    obtain ax asx where as = ax#asx
      by(auto simp:intra-path-def)(erule path.cases,
        auto dest:valid-SDG-node-in-slice-parent-node-in-slice
        simp:SDG-to-CFG-set-def)
    with ⟨targetnode a -as→l* parent-node n'⟩
    have targetnode a = sourcnode ax and valid-edge ax
      by(auto elim:path.cases simp:intra-path-def)
    with ⟨∀ n''. valid-SDG-node n'' ∧ parent-node n'' ∈ set (sourcenodes as)
      ⟶ V ∉ DefSDG n''⟩ ⟨as = ax#asx⟩ ⟨V ∈ DefSDG (CFG-node
(targetnode a))⟩
      have False by(fastforce simp:sourcenodes-def)
      thus ?thesis by simp
    next
      case False
        with ⟨V∈rv S (CFG-node mx)⟩ rv show ?thesis
          by(fastforce dest:fun-upds-notin[of - - fst cfx1])
    qed
  qed
with Nil ⟨msx = mx'#msx'⟩ show ?thesis by fastforce
next

```


case *Cons*
with $\langle \forall V \in rv\ S\ (CFG\text{-node}\ mx). (fst\ (s_1\ !\ length\ msx))\ V = state\text{-val}\ s_2\ V \rangle$
 $\langle msx = mx\ \#\ msx' \rangle$
show *?thesis* **by** *fastforce*
qed
with $\langle \forall V \in rv\ S\ (CFG\text{-node}\ mx). (fst\ (s_1\ !\ length\ msx))\ V = state\text{-val}\ s_2\ V \rangle$
Cons
have $\forall V \in rv\ S\ (CFG\text{-node}\ mx). (fst\ (s_1'\ !\ length\ msx'))\ V = state\text{-val}\ s_2\ V$
by *(cases\ msx')* *auto*
with $\langle \forall i < length\ ms_2. \forall V \in rv\ S\ (CFG\text{-node}\ ((mx\ \#\ tl\ ms_2)\ !\ i)).$
 $(fst\ (s_1'\ !\ (length\ msx + i)))\ V = (fst\ (s_2\ !\ i))\ V \rangle$ *Cons*
have $\forall i < length\ ms_2. \forall V \in rv\ S\ (CFG\text{-node}\ ((mx\ \#\ tl\ ms_2)\ !\ i)).$
 $(fst\ (s_1'\ !\ (length\ msx' + i)))\ V = (fst\ (s_2\ !\ i))\ V$
by *clarsimp(case-tac\ i,auto)*
with $\langle \forall m \in set\ ms_1'.\ valid\text{-node}\ m \rangle \langle \forall m \in set\ ms_2.\ valid\text{-node}\ m \rangle$
 $\langle length\ ms_1' = length\ s_1' \rangle \langle length\ ms_2 = length\ s_2 \rangle$
 $\langle ms_1' = msx'\ @\ mx\ \#\ tl\ ms_2 \rangle \langle get\text{-proc}\ mx = get\text{-proc}\ (hd\ ms_2) \rangle$
 $\langle \forall m \in set\ (tl\ ms_2). \exists m'. call\text{-of}\text{-return}\text{-node}\ m\ m' \wedge m' \in [HRB\text{-slice}\ S]_{CFG} \rangle$
 $\langle msx \neq [] \longrightarrow (\exists mx'. call\text{-of}\text{-return}\text{-node}\ mx\ mx' \wedge mx' \notin [HRB\text{-slice}\ S]_{CFG}) \rangle$
 $\langle \forall m \in set\ (tl\ ms_1').\ return\text{-node}\ m \rangle$ *Cons* $\langle get\text{-proc}\ mx = get\text{-proc}\ (hd\ ms_2) \rangle$
 $\langle \forall m \in set\ (tl\ ms_2). \exists m'. call\text{-of}\text{-return}\text{-node}\ m\ m' \wedge m' \in [HRB\text{-slice}\ S]_{CFG} \rangle$
 $\langle obs\ ms_1'\ [HRB\text{-slice}\ S]_{CFG} = obs\ ms_2\ [HRB\text{-slice}\ S]_{CFG} \rangle$
 $\langle \forall i < length\ ms_2. snd\ (s_1'\ !\ (length\ msx' + i)) = snd\ (s_2\ !\ i) \rangle$
show *?thesis* **by** *(auto\ intro!\ WSI)*
qed
qed
qed

lemma *WS-silent-moves*:

$\llbracket S, kind \vdash (ms_1, s_1) = as \Rightarrow_{\tau} (ms_1', s_1'); ((ms_1, s_1), (ms_2, s_2)) \in WS\ S \rrbracket$
 $\implies ((ms_1', s_1'), (ms_2, s_2)) \in WS\ S$

by *(induct\ S\ f \equiv kind\ ms_1\ s_1\ as\ ms_1'\ s_1'\ rule:\ silent\text{-moves}.\ induct,*
auto\ dest:\ WS\text{-silent}\text{-move})

lemma *WS-observable-move*:

assumes $((ms_1, s_1), (ms_2, s_2)) \in WS\ S$
and $S, kind \vdash (ms_1, s_1) \text{-} a \rightarrow (ms_1', s_1')$ **and** $s_1' \neq []$
obtains *as* **where** $((ms_1', s_1'), (ms_1', transfer\ (slice\text{-kind}\ S\ a)\ s_2)) \in WS\ S$
and $S, slice\text{-kind}\ S \vdash (ms_2, s_2) = as @ [a] \Rightarrow (ms_1', transfer\ (slice\text{-kind}\ S\ a)\ s_2)$

proof *(atomize-elim)*

from $\langle ((ms_1, s_1), (ms_2, s_2)) \in WS\ S \rangle$ **obtain** $msx\ mx$
where *assms*: $\forall m \in set\ ms_1.\ valid\text{-node}\ m\ \forall m \in set\ ms_2.\ valid\text{-node}\ m$
 $length\ ms_1 = length\ s_1\ length\ ms_2 = length\ s_2\ s_1 \neq []\ s_2 \neq []$
 $ms_1 = msx @ mx\ \#\ tl\ ms_2\ get\text{-proc}\ mx = get\text{-proc}\ (hd\ ms_2)$
 $\forall m \in set\ (tl\ ms_2). \exists m'. call\text{-of}\text{-return}\text{-node}\ m\ m' \wedge m' \in [HRB\text{-slice}\ S]_{CFG}$

$msx \neq [] \longrightarrow (\exists mx'. \text{call-of-return-node } mx \ mx' \wedge mx' \notin \lfloor \text{HRB-slice } S \rfloor_{CFG})$
 $\forall m \in \text{set } (tl \ ms_1). \text{return-node } m$
 $\forall i < \text{length } ms_2. \text{snd } (s_1!(\text{length } msx + i)) = \text{snd } (s_2!i)$
 $\forall i < \text{length } ms_2. \forall V \in \text{rv } S \text{ (CFG-node } ((mx\#tl \ ms_2)!i)).$
 $(fst \ (s_1!(\text{length } msx + i))) \ V = (fst \ (s_2!i)) \ V$
 $\text{obs } ms_1 \lfloor \text{HRB-slice } S \rfloor_{CFG} = \text{obs } ms_2 \lfloor \text{HRB-slice } S \rfloor_{CFG}$
by(*fastforce elim:WS.cases*)
from $\langle S, \text{kind } \vdash (ms_1, s_1) - a \rightarrow (ms_1', s_1') \rangle \text{ assms}$
show $\exists as. ((ms_1', s_1'), (ms_1', \text{transfer } (\text{slice-kind } S \ a) \ s_2)) \in \text{WS } S \wedge$
 $S, \text{slice-kind } S \vdash (ms_2, s_2) = as \ @ \ [a] \Rightarrow (ms_1', \text{transfer } (\text{slice-kind } S \ a) \ s_2)$
proof(*induct S f≡kind ms₁ s₁ a ms₁' s₁' rule:observable-move.induct*)
case (*observable-move-intra a s₁ s₁' ms₁ S ms₁'*)
from $\langle s_1 \neq [] \rangle \langle s_2 \neq [] \rangle$ **obtain** $cf_1 \ cfs_1 \ cf_2 \ cfs_2$ **where** $[simp]: s_1 = cf_1 \# \ cfs_1$
and $[simp]: s_2 = cf_2 \# \ cfs_2$ **by**(*cases s₁, auto, cases s₂, fastforce+*)
from $\langle \text{length } ms_1 = \text{length } s_1 \rangle \langle s_1 \neq [] \rangle$ **have** $[simp]: ms_1 \neq []$ **by**(*cases ms₁*)
auto
from $\langle \text{length } ms_2 = \text{length } s_2 \rangle \langle s_2 \neq [] \rangle$ **have** $[simp]: ms_2 \neq []$ **by**(*cases ms₂*)
auto
from $\langle \forall m \in \text{set } (tl \ ms_1). \exists m'. \text{call-of-return-node } m \ m' \wedge m' \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
 $\langle \text{hd } ms_1 = \text{sourcenode } a \rangle \langle ms_1 = msx @ mx \# tl \ ms_2 \rangle$
 $\langle msx \neq [] \longrightarrow (\exists mx'. \text{call-of-return-node } mx \ mx' \wedge mx' \notin \lfloor \text{HRB-slice } S \rfloor_{CFG}) \rangle$
have $[simp]: mx = \text{sourcenode } a \ msx = []$ **and** $[simp]: tl \ ms_2 = tl \ ms_1$
by(*cases msx, auto*)
hence $\text{length } ms_1 = \text{length } ms_2$ **by**(*cases ms₂*) *auto*
with $\langle \text{length } ms_1 = \text{length } s_1 \rangle \langle \text{length } ms_2 = \text{length } s_2 \rangle$
have $\text{length } s_1 = \text{length } s_2$ **by** *simp*
from $\langle \text{hd } ms_1 \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle \langle \text{hd } ms_1 = \text{sourcenode } a \rangle$
have $\text{sourcenode } a \in \lfloor \text{HRB-slice } S \rfloor_{CFG}$ **by** *simp*
with $\langle \text{valid-edge } a \rangle$
have $\text{obs-intra } (\text{sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG} = \{ \text{sourcenode } a \}$
by(*fastforce intro!: n-in-obs-intra*)
from $\langle \forall m \in \text{set } (tl \ ms_2). \exists m'. \text{call-of-return-node } m \ m' \wedge m' \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
 $\langle \text{obs-intra } (\text{sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG} = \{ \text{sourcenode } a \} \rangle$
 $\langle \text{hd } ms_1 = \text{sourcenode } a \rangle$
have $(\text{hd } ms_1 \# tl \ ms_1) \in \text{obs } ([] @ \text{hd } ms_1 \# tl \ ms_1) \lfloor \text{HRB-slice } S \rfloor_{CFG}$
by(*cases ms₁*)(*auto intro!: obsI*)
hence $ms_1 \in \text{obs } ms_1 \lfloor \text{HRB-slice } S \rfloor_{CFG}$ **by** *simp*
with $\langle \text{obs } ms_1 \lfloor \text{HRB-slice } S \rfloor_{CFG} = \text{obs } ms_2 \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
have $ms_1 \in \text{obs } ms_2 \lfloor \text{HRB-slice } S \rfloor_{CFG}$ **by** *simp*
from $\langle ms_2 \neq [] \rangle \langle \text{length } ms_2 = \text{length } s_2 \rangle$ **have** $\text{length } s_2 = \text{length } (\text{hd } ms_2 \# tl \ ms_2)$
by(*fastforce dest!: hd-Cons-tl*)
from $\langle \forall m \in \text{set } (tl \ ms_1). \text{return-node } m \rangle$ **have** $\forall m \in \text{set } (tl \ ms_2). \text{return-node } m$
by *simp*
with $\langle ms_1 \in \text{obs } ms_2 \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
have $\text{hd } ms_1 \in \text{obs-intra } (\text{hd } ms_2) \lfloor \text{HRB-slice } S \rfloor_{CFG}$

```

proof(rule obsE)
  fix nsx n nsx' n'
  assume ms2 = nsx @ n # nsx' and ms1 = n' # nsx'
  and n' ∈ obs-intra n [HRB-slice S]CFG
  from ⟨ms2 = nsx @ n # nsx'⟩ ⟨ms1 = n' # nsx'⟩ ⟨tl ms2 = tl ms1⟩
  have [simp]:nsx = [] by(cases nsx) auto
  with ⟨ms2 = nsx @ n # nsx'⟩ have [simp]:n = hd ms2 by simp
  from ⟨ms1 = n' # nsx'⟩ have [simp]:n' = hd ms1 by simp
  with ⟨n' ∈ obs-intra n [HRB-slice S]CFG⟩ show ?thesis by simp
qed
with ⟨length s2 = length (hd ms2#tl ms2)⟩ ⟨∀ m ∈ set (tl ms2). return-node m⟩
  obtain as where S,slice-kind S ⊢ (hd ms2#tl ms2,s2) = as⇒τ (hd ms1#tl
ms1,s2)
  by(fastforce elim:silent-moves-intra-path-obs[of - - s2 tl ms2])
  with ⟨ms2 ≠ []⟩ have S,slice-kind S ⊢ (ms2,s2) = as⇒τ (ms1,s2)
  by(fastforce dest!:hd-Cons-tl)
  from ⟨valid-edge a⟩ have valid-node (sourcenode a) by simp
  hence sourcenode a -[]→i* sourcenode a
  by(fastforce intro:empty-path simp:intra-path-def)
  with ⟨sourcenode a ∈ [HRB-slice S]CFG⟩
  have ∀ V. V ∈ UseSDG (CFG-node (sourcenode a))
  → V ∈ rv S (CFG-node (sourcenode a))
  by auto(rule rvI,auto simp:SDG-to-CFG-set-def sourcenodes-def)
  with ⟨valid-node (sourcenode a)⟩
  have ∀ V ∈ Use (sourcenode a). V ∈ rv S (CFG-node (sourcenode a))
  by(fastforce intro:CFG-Use-SDG-Use)
  from ⟨∀ i < length ms2. ∀ V ∈ rv S (CFG-node ((mx#tl ms2)!i)).
(fst (s1!(length msx + i))) V = (fst (s2!i)) V⟩ ⟨length ms2 = length s2⟩
  have ∀ V ∈ rv S (CFG-node mx). (fst (s1 ! length msx)) V = state-val s2 V
  by(cases ms2) auto
  with ⟨∀ V ∈ Use (sourcenode a). V ∈ rv S (CFG-node (sourcenode a))⟩
  have ∀ V ∈ Use (sourcenode a). fst cf1 V = fst cf2 V by fastforce
moreover
  from ⟨∀ i < length ms2. snd (s1 ! (length msx + i)) = snd (s2 ! i)⟩
  have snd (hd s1) = snd (hd s2) by(erule-tac x=0 in allE) auto
  ultimately have pred (kind a) s2
  using ⟨valid-edge a⟩ ⟨pred (kind a) s1⟩ ⟨length s1 = length s2⟩
  by(fastforce intro:CFG-edge-Uses-pred-equal)
  from ⟨ms1' = targetnode a # tl ms1⟩ ⟨length s1' = length s1⟩
  ⟨length ms1 = length s1⟩ have length ms1' = length s1' by simp
  from ⟨transfer (kind a) s1 = s1'⟩ ⟨intra-kind (kind a)⟩
  obtain cf1' where [simp]:s1' = cf1'#cfs1
  by(cases cf1,cases kind a,auto simp:intra-kind-def)
  from ⟨intra-kind (kind a)⟩ ⟨sourcenode a ∈ [HRB-slice S]CFG⟩ ⟨pred (kind a)
s2⟩
  have pred (slice-kind S a) s2 by(simp add:slice-intra-kind-in-slice)
  from ⟨valid-edge a⟩ ⟨length s1 = length s2⟩ ⟨transfer (kind a) s1 = s1'⟩
  have length s1' = length (transfer (slice-kind S a) s2)
  by(fastforce intro:length-transfer-kind-slice-kind)

```

with $\langle \text{length } s_1 = \text{length } s_2 \rangle$
have $\text{length } s_2 = \text{length } (\text{transfer } (\text{slice-kind } S \ a) \ s_2)$ **by** *simp*
with $\langle \text{pred } (\text{slice-kind } S \ a) \ s_2 \rangle \langle \text{valid-edge } a \rangle \langle \text{intra-kind } (\text{kind } a) \rangle$
 $\langle \forall m \in \text{set } (\text{tl } ms_1). \exists m'. \text{call-of-return-node } m \ m' \wedge m' \in \llbracket \text{HRB-slice } S \rrbracket_{CFG} \rangle$
 $\langle \text{hd } ms_1 \in \llbracket \text{HRB-slice } S \rrbracket_{CFG} \rangle \langle \text{hd } ms_1 = \text{sourcenode } a \rangle$
 $\langle \text{length } ms_1 = \text{length } s_1 \rangle \langle \text{length } s_1 = \text{length } s_2 \rangle$
 $\langle ms_1' = \text{targetnode } a \ \# \ \text{tl } ms_1 \rangle \langle \forall m \in \text{set } (\text{tl } ms_2). \text{return-node } m \rangle$
have $S, \text{slice-kind } S \vdash (ms_1, s_2) - a \rightarrow (ms_1', \text{transfer } (\text{slice-kind } S \ a) \ s_2)$
by (*auto intro: observable-move.observable-move-intra*)
with $\langle S, \text{slice-kind } S \vdash (ms_2, s_2) = as \Rightarrow_{\tau} (ms_1, s_2) \rangle$
have $S, \text{slice-kind } S \vdash (ms_2, s_2) = as @ [a] \Rightarrow (ms_1', \text{transfer } (\text{slice-kind } S \ a) \ s_2)$
by (*rule observable-moves-snoc*)
from $\langle \forall m \in \text{set } ms_1. \text{valid-node } m \rangle \langle ms_1' = \text{targetnode } a \ \# \ \text{tl } ms_1 \rangle \langle \text{valid-edge}$
a)
have $\forall m \in \text{set } ms_1'. \text{valid-node } m$ **by** (*cases ms₁' auto*)
from $\langle \forall m \in \text{set } (\text{tl } ms_2). \text{return-node } m \rangle \langle ms_1' = \text{targetnode } a \ \# \ \text{tl } ms_1 \rangle$
 $\langle ms_1' = \text{targetnode } a \ \# \ \text{tl } ms_1 \rangle$
have $\forall m \in \text{set } (\text{tl } ms_1'). \text{return-node } m$ **by** *fastforce*
from $\langle ms_1' = \text{targetnode } a \ \# \ \text{tl } ms_1 \rangle \langle \text{tl } ms_2 = \text{tl } ms_1 \rangle$
have $ms_1' = [] @ \text{targetnode } a \ \# \ \text{tl } ms_2$ **by** *simp*
from $\langle \text{intra-kind } (\text{kind } a) \rangle \langle \text{sourcenode } a \in \llbracket \text{HRB-slice } S \rrbracket_{CFG} \rangle$
have $cf_2' : \exists cf_2'. \text{transfer } (\text{slice-kind } S \ a) \ s_2 = cf_2' \# cfs_2 \wedge \text{snd } cf_2' = \text{snd } cf_2$
by (*cases cf₂'*) (*auto dest:slice-intra-kind-in-slice simp:intra-kind-def*)
from $\langle \text{transfer } (\text{kind } a) \ s_1 = s_1' \rangle \langle \text{intra-kind } (\text{kind } a) \rangle$
have $\text{snd } cf_1' = \text{snd } cf_1$ **by** (*auto simp:intra-kind-def*)
with $\langle \forall i < \text{length } ms_2. \text{snd } (s_1 ! (\text{length } ms_2 + i)) = \text{snd } (s_2 ! i) \rangle$
 $\langle \text{snd } (\text{hd } s_1) = \text{snd } (\text{hd } s_2) \rangle \langle ms_1' = [] @ \text{targetnode } a \ \# \ \text{tl } ms_2 \rangle$
 $cf_2' \langle \text{length } ms_1 = \text{length } ms_2 \rangle$
have $\forall i < \text{length } ms_1'. \text{snd } (s_1' ! i) = \text{snd } (\text{transfer } (\text{slice-kind } S \ a) \ s_2 ! i)$
by *auto(case-tac i, auto)*
have $\forall V \in \text{rv } S \ (\text{CFG-node } (\text{targetnode } a)).$
 $\text{fst } cf_1' \ V = \text{state-val } (\text{transfer } (\text{slice-kind } S \ a) \ s_2) \ V$
proof
fix V **assume** $V \in \text{rv } S \ (\text{CFG-node } (\text{targetnode } a))$
show $\text{fst } cf_1' \ V = \text{state-val } (\text{transfer } (\text{slice-kind } S \ a) \ s_2) \ V$
proof (*cases V ∈ Def (sourcenode a)*)
case *True*
from $\langle \text{intra-kind } (\text{kind } a) \rangle$ **have** $(\exists f. \text{kind } a = \uparrow f) \vee (\exists Q. \text{kind } a = (Q)_{\checkmark})$

by (*simp add:intra-kind-def*)
thus *?thesis*
proof
assume $\exists f. \text{kind } a = \uparrow f$
then obtain f' **where** $\text{kind } a = \uparrow f'$ **by** *blast*
with $\langle \text{transfer } (\text{kind } a) \ s_1 = s_1' \rangle$
have $s_1' = (f' (\text{fst } cf_1), \text{snd } cf_1) \ \# \ cfs_1$ **by** *simp*
from $\langle \text{sourcenode } a \in \llbracket \text{HRB-slice } S \rrbracket_{CFG} \rangle \langle \text{kind } a = \uparrow f' \rangle$
have $\text{slice-kind } S \ a = \uparrow f'$
by (*fastforce dest:slice-intra-kind-in-slice simp:intra-kind-def*)

hence $\text{transfer } (\text{slice-kind } S \ a) \ s_2 = (f' \ (\text{fst } cf_2), \text{snd } cf_2) \ \# \ cfs_2$ **by** *simp*
from $\langle \text{valid-edge } a \rangle \langle \forall V \in \text{Use } (\text{sourcenode } a). \text{fst } cf_1 \ V = \text{fst } cf_2 \ V \rangle$
 $\langle \text{intra-kind } (\text{kind } a) \rangle \langle \text{pred } (\text{kind } a) \ s_1 \rangle \langle \text{pred } (\text{kind } a) \ s_2 \rangle$
have $\forall V \in \text{Def } (\text{sourcenode } a). \text{state-val } (\text{transfer } (\text{kind } a) \ s_1) \ V =$
 $\text{state-val } (\text{transfer } (\text{kind } a) \ s_2) \ V$
by $-(\text{erule } \text{CFG-intra-edge-transfer-uses-only-Use, auto})$
with $\langle \text{kind } a = \uparrow f' \rangle \langle s_1' = (f' \ (\text{fst } cf_1), \text{snd } cf_1) \ \# \ cfs_1 \rangle \text{True}$
 $\langle \text{transfer } (\text{slice-kind } S \ a) \ s_2 = (f' \ (\text{fst } cf_2), \text{snd } cf_2) \ \# \ cfs_2 \rangle$
show *?thesis by simp*
next
assume $\exists Q. \text{kind } a = (Q)_{\surd}$
then obtain Q **where** $\text{kind } a = (Q)_{\surd}$ **by** *blast*
with $\langle \text{transfer } (\text{kind } a) \ s_1 = s_1' \rangle$ **have** $s_1' = cf_1 \ \# \ cfs_1$ **by** *simp*
from $\langle \text{sourcenode } a \in \lfloor \text{HRB-slice } S \rfloor_{\text{CFG}} \rangle \langle \text{kind } a = (Q)_{\surd} \rangle$
have $\text{slice-kind } S \ a = (Q)_{\surd}$
by $(\text{fastforce } \text{dest: slice-intra-kind-in-slice simp: intra-kind-def})$
hence $\text{transfer } (\text{slice-kind } S \ a) \ s_2 = s_2$ **by** *simp*
from $\langle \text{valid-edge } a \rangle \langle \forall V \in \text{Use } (\text{sourcenode } a). \text{fst } cf_1 \ V = \text{fst } cf_2 \ V \rangle$
 $\langle \text{intra-kind } (\text{kind } a) \rangle \langle \text{pred } (\text{kind } a) \ s_1 \rangle \langle \text{pred } (\text{kind } a) \ s_2 \rangle$
have $\forall V \in \text{Def } (\text{sourcenode } a). \text{state-val } (\text{transfer } (\text{kind } a) \ s_1) \ V =$
 $\text{state-val } (\text{transfer } (\text{kind } a) \ s_2) \ V$
by $-(\text{erule } \text{CFG-intra-edge-transfer-uses-only-Use, auto simp: intra-kind-def})$
with $\text{True } \langle \text{kind } a = (Q)_{\surd} \rangle \langle s_1' = cf_1 \ \# \ cfs_1 \rangle$
 $\langle \text{transfer } (\text{slice-kind } S \ a) \ s_2 = s_2 \rangle$
show *?thesis by simp*
qed
next
case *False*
with $\langle \text{valid-edge } a \rangle \langle \text{intra-kind } (\text{kind } a) \rangle \langle \text{pred } (\text{kind } a) \ s_1 \rangle$
have $\text{state-val } (\text{transfer } (\text{kind } a) \ s_1) \ V = \text{state-val } s_1 \ V$
by $(\text{fastforce } \text{intro: CFG-intra-edge-no-Def-equal})$
with $\langle \text{transfer } (\text{kind } a) \ s_1 = s_1' \rangle$ **have** $\text{fst } cf_1' \ V = \text{fst } cf_1 \ V$ **by** *simp*
from $\langle \text{sourcenode } a \in \lfloor \text{HRB-slice } S \rfloor_{\text{CFG}} \rangle \langle \text{intra-kind } (\text{kind } a) \rangle$
have $\text{slice-kind } S \ a = \text{kind } a$ **by** $(\text{fastforce } \text{intro: slice-intra-kind-in-slice})$
from *False* $\langle \text{valid-edge } a \rangle \langle \text{pred } (\text{kind } a) \ s_2 \rangle \langle \text{intra-kind } (\text{kind } a) \rangle$
have $\text{state-val } (\text{transfer } (\text{kind } a) \ s_2) \ V = \text{state-val } s_2 \ V$
by $(\text{fastforce } \text{intro: CFG-intra-edge-no-Def-equal})$
with $\langle \text{slice-kind } S \ a = \text{kind } a \rangle$
have $\text{state-val } (\text{transfer } (\text{slice-kind } S \ a) \ s_2) \ V = \text{fst } cf_2 \ V$ **by** *simp*
from $\langle V \in \text{rv } S \ (\text{CFG-node } (\text{targetnode } a)) \rangle$ **obtain** $as' \ nx$
where $\text{targetnode } a -as' \rightarrow_l^* \text{parent-node } nx$
and $nx \in \text{HRB-slice } S$ **and** $V \in \text{Use}_{\text{SDG}} \ nx$
and $\forall n''. \text{valid-SDG-node } n'' \wedge \text{parent-node } n'' \in \text{set } (\text{sourcenodes } as')$
 $\longrightarrow V \notin \text{Def}_{\text{SDG}} \ n''$
by $(\text{fastforce } \text{elim: rvE})$
with $\langle \forall n''. \text{valid-SDG-node } n'' \wedge \text{parent-node } n'' \in \text{set } (\text{sourcenodes } as')$
 $\longrightarrow V \notin \text{Def}_{\text{SDG}} \ n'' \rangle$ *False*
have $\text{all: } \forall n''. \text{valid-SDG-node } n'' \wedge$
 $\text{parent-node } n'' \in \text{set } (\text{sourcenodes } (a \# as')) \longrightarrow V \notin \text{Def}_{\text{SDG}} \ n''$

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    by(fastforce dest:SDG-Def-parent-Def simp:sourcenodes-def)
  from ⟨valid-edge a⟩ ⟨targetnode a -as'→i* parent-node nx⟩
    ⟨intra-kind (kind a)⟩
  have sourcenode a -a#as'→i* parent-node nx
    by(fastforce intro:Cons-path simp:intra-path-def)
  with ⟨nx ∈ HRB-slice S⟩ ⟨V ∈ UseSDG nx⟩ all
  have V ∈ rv S (CFG-node (sourcenode a)) by(fastforce intro:rvI)
  with ⟨∀ V ∈ rv S (CFG-node mx). (fst (s1!(length msx))) V = state-val s2
V⟩
    ⟨state-val (transfer (slice-kind S a) s2) V = fst cf2 V⟩
    ⟨fst cf1' V = fst cf1 V⟩
  show ?thesis by fastforce
qed
qed
with ⟨∀ i < length ms2. ∀ V ∈ rv S (CFG-node ((mx#tl ms2)!i)).
  (fst (s1!(length msx + i))) V = (fst (s2!i)) V⟩ cf2'
  ⟨ms1' = [] @ targetnode a # tl ms2⟩
  ⟨length ms1 = length s1⟩ ⟨length ms2 = length s2⟩ ⟨length s1 = length s2⟩
  have ∀ i < length ms1'. ∀ V ∈ rv S (CFG-node ((targetnode a # tl ms1')!i)).
    (fst (s1'!(length [] + i))) V = (fst (transfer (slice-kind S a) s2 ! i)) V
  by clarsimp(case-tac i,auto)
  with ⟨∀ m ∈ set ms2. valid-node m⟩ ⟨∀ m ∈ set ms1'. valid-node m⟩
    ⟨length ms2 = length s2⟩ ⟨length s1' = length (transfer (slice-kind S a) s2)⟩
    ⟨length ms1' = length s1'⟩ ⟨∀ m ∈ set (tl ms1'). return-node m⟩
    ⟨ms1' = [] @ targetnode a # tl ms2⟩ ⟨get-proc mx = get-proc (hd ms2)⟩
    ⟨∀ m ∈ set (tl ms1'). ∃ m'. call-of-return-node m m' ∧ m' ∈ [HRB-slice S]CFG⟩
    ⟨∀ i < length ms1'. snd (s1' ! i) = snd (transfer (slice-kind S a) s2 ! i)⟩
  have ((ms1',s1'),(ms1',transfer (slice-kind S a) s2)) ∈ WS S
    by(fastforce intro!:WSI)
  with ⟨S,slice-kind S ⊢ (ms2,s2) = as@[a]⇒ (ms1',transfer (slice-kind S a) s2)⟩
  show ?case by blast
next
  case (observable-move-call a s1 s1' Q r p fs a' ms1 S ms1')
  from ⟨s1 ≠ []⟩ ⟨s2 ≠ []⟩ obtain cf1 cfs1 cf2 cfs2 where [simp]:s1 = cf1#cfs1
    and [simp]:s2 = cf2#cfs2 by(cases s1,auto,cases s2,fastforce+)
  from ⟨length ms1 = length s1⟩ ⟨s1 ≠ []⟩ have [simp]:ms1 ≠ [] by(cases ms1)
auto
  from ⟨length ms2 = length s2⟩ ⟨s2 ≠ []⟩ have [simp]:ms2 ≠ [] by(cases ms2)
auto
  from ⟨∀ m ∈ set (tl ms1'). ∃ m'. call-of-return-node m m' ∧ m' ∈ [HRB-slice
S]CFG⟩
    ⟨hd ms1 = sourcenode a⟩ ⟨ms1 = msx@mx#tl ms2⟩
    ⟨msx ≠ [] ⟹ (∃ mx'. call-of-return-node mx mx' ∧ mx' ∉ [HRB-slice S]CFG)⟩
  have [simp]:mx = sourcenode a msx = [] and [simp]:tl ms2 = tl ms1
    by(cases msx,auto)+
  hence length ms1 = length ms2 by(cases ms2) auto
  with ⟨length ms1 = length s1⟩ ⟨length ms2 = length s2⟩
  have length s1 = length s2 by simp
  from ⟨hd ms1 ∈ [HRB-slice S]CFG⟩ ⟨hd ms1 = sourcenode a⟩

```

have *sourcenode* $a \in \lfloor \text{HRB-slice } S \rfloor_{CFG}$ **by** *simp*
with $\langle \text{valid-edge } a \rangle$
have $\text{obs-intra } (\text{sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG} = \{\text{sourcenode } a\}$
by $(\text{fastforce intro!}:n\text{-in-obs-intra})$
from $\langle \forall m \in \text{set } (tl \ ms_2). \exists m'. \text{call-of-return-node } m \ m' \wedge m' \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
 $\langle \text{obs-intra } (\text{sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG} = \{\text{sourcenode } a\} \rangle$
 $\langle \text{hd } ms_1 = \text{sourcenode } a \rangle$
have $(\text{hd } ms_1 \# tl \ ms_1) \in \text{obs } (\llbracket @ \text{hd } ms_1 \# tl \ ms_1 \rrbracket \lfloor \text{HRB-slice } S \rfloor_{CFG})$
by $(\text{cases } ms_1)(\text{auto intro!}:\text{obsI})$
hence $ms_1 \in \text{obs } ms_1 \lfloor \text{HRB-slice } S \rfloor_{CFG}$ **by** *simp*
with $\langle \text{obs } ms_1 \lfloor \text{HRB-slice } S \rfloor_{CFG} = \text{obs } ms_2 \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
have $ms_1 \in \text{obs } ms_2 \lfloor \text{HRB-slice } S \rfloor_{CFG}$ **by** *simp*
from $\langle ms_2 \neq [] \rangle \langle \text{length } ms_2 = \text{length } s_2 \rangle$ **have** $\text{length } s_2 = \text{length } (\text{hd } ms_2 \# tl \ ms_2)$
by $(\text{fastforce dest!}:\text{hd-Cons-tl})$
from $\langle \forall m \in \text{set } (tl \ ms_1). \text{return-node } m \rangle$ **have** $\forall m \in \text{set } (tl \ ms_2). \text{return-node } m$
by *simp*
with $\langle ms_1 \in \text{obs } ms_2 \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
have $\text{hd } ms_1 \in \text{obs-intra } (\text{hd } ms_2) \lfloor \text{HRB-slice } S \rfloor_{CFG}$
proof $(\text{rule } \text{obsE})$
fix $nsx \ n \ nsx' \ n'$
assume $ms_2 = nsx @ n \# nsx'$ **and** $ms_1 = n' \# nsx'$
and $n' \in \text{obs-intra } n \lfloor \text{HRB-slice } S \rfloor_{CFG}$
from $\langle ms_2 = nsx @ n \# nsx' \rangle \langle ms_1 = n' \# nsx' \rangle \langle tl \ ms_2 = tl \ ms_1 \rangle$
have $[\text{simp}]: nsx = []$ **by** $(\text{cases } nsx) \text{ auto}$
with $\langle ms_2 = nsx @ n \# nsx' \rangle$ **have** $[\text{simp}]: n = \text{hd } ms_2$ **by** *simp*
from $\langle ms_1 = n' \# nsx' \rangle$ **have** $[\text{simp}]: n' = \text{hd } ms_1$ **by** *simp*
with $\langle n' \in \text{obs-intra } n \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$ **show** *?thesis* **by** *simp*
qed
with $\langle \text{length } s_2 = \text{length } (\text{hd } ms_2 \# tl \ ms_2) \rangle \langle \forall m \in \text{set } (tl \ ms_2). \text{return-node } m \rangle$
obtain *as* **where** $S, \text{slice-kind } S \vdash (\text{hd } ms_2 \# tl \ ms_2, s_2) = \text{as} \Rightarrow_{\tau} (\text{hd } ms_1 \# tl \ ms_1, s_2)$
by $(\text{fastforce elim}:\text{silent-moves-intra-path-obs}[of \ - \ - \ s_2 \ tl \ ms_2])$
with $\langle ms_2 \neq [] \rangle$ **have** $S, \text{slice-kind } S \vdash (ms_2, s_2) = \text{as} \Rightarrow_{\tau} (ms_1, s_2)$
by $(\text{fastforce dest!}:\text{hd-Cons-tl})$
from $\langle \text{valid-edge } a \rangle$ **have** *valid-node* $(\text{sourcenode } a)$ **by** *simp*
hence $\text{sourcenode } a - [] \rightarrow_i^* \text{sourcenode } a$
by $(\text{fastforce intro}:\text{empty-path } \text{simp}:\text{intra-path-def})$
with $\langle \text{sourcenode } a \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
have $\forall V. V \in \text{Use}_{SDG} (\text{CFG-node } (\text{sourcenode } a))$
 $\rightarrow V \in \text{rv } S (\text{CFG-node } (\text{sourcenode } a))$
by $\text{auto}(\text{rule } \text{rvI}, \text{auto } \text{simp}:\text{SDG-to-CFG-set-def } \text{sourcenodes-def})$
with $\langle \text{valid-node } (\text{sourcenode } a) \rangle$
have $\forall V \in \text{Use } (\text{sourcenode } a). V \in \text{rv } S (\text{CFG-node } (\text{sourcenode } a))$
by $(\text{fastforce intro}:\text{CFG-Use-SDG-Use})$
from $\langle \forall i < \text{length } ms_2. \forall V \in \text{rv } S (\text{CFG-node } ((mx \# tl \ ms_2)!i)) \rangle$
 $(fst (s_1!(\text{length } msx + i))) \ V = (fst (s_2!i)) \ V \rangle \langle \text{length } ms_2 = \text{length } s_2 \rangle$

have $\forall V \in rv\ S\ (CFG\text{-node}\ msx). (fst\ (s_1\ !\ length\ msx))\ V = state\text{-val}\ s_2\ V$
by $(cases\ ms_2)\ auto$
with $\langle \forall V \in Use\ (sourcnode\ a). V \in rv\ S\ (CFG\text{-node}\ (sourcnode\ a)) \rangle$
have $\forall V \in Use\ (sourcnode\ a). fst\ cf_1\ V = fst\ cf_2\ V$ **by** $fastforce$
moreover
from $\langle \forall i < length\ ms_2. snd\ (s_1\ !\ (length\ msx + i)) = snd\ (s_2\ !\ i) \rangle$
have $snd\ (hd\ s_1) = snd\ (hd\ s_2)$ **by** $(erule\text{-tac}\ x=0\ in\ allE)\ auto$
ultimately have $pred\ (kind\ a)\ s_2$
using $\langle valid\text{-edge}\ a \rangle \langle pred\ (kind\ a)\ s_1 \rangle \langle length\ s_1 = length\ s_2 \rangle$
by $(fastforce\ intro: CFG\text{-edge}\text{-Uses}\text{-pred}\text{-equal})$
from $\langle ms_1' = (targetnode\ a)\ \#\ (targetnode\ a')\ \#\ tl\ ms_1 \rangle \langle length\ s_1' = Suc\ (length\ s_1) \rangle$
 $\langle length\ ms_1 = length\ s_1 \rangle$ **have** $length\ ms_1' = length\ s_1'$ **by** $simp$
from $\langle valid\text{-edge}\ a \rangle \langle kind\ a = Q:r \hookrightarrow pfs \rangle$ **obtain** $ins\ outs$
where $(p, ins, outs) \in set\ procs$ **by** $(fastforce\ dest!: callee\text{-in}\text{-procs})$
with $\langle valid\text{-edge}\ a \rangle \langle kind\ a = Q:r \hookrightarrow pfs \rangle$
have $(THE\ ins.\ \exists\ outs.\ (p, ins, outs) \in set\ procs) = ins$
by $(rule\ formal\text{-in}\text{-THE})$
with $\langle transfer\ (kind\ a)\ s_1 = s_1' \rangle \langle kind\ a = Q:r \hookrightarrow pfs \rangle$
have $[simp]: s_1' = (empty\ (ins\ [:=]\ params\ fs\ (fst\ cf_1)), r)\ \#\ cf_1\ \#\ cfs_1$ **by** $simp$
from $\langle valid\text{-edge}\ a' \rangle \langle a' \in get\text{-return}\text{-edges}\ a \rangle \langle valid\text{-edge}\ a \rangle$
have $return\text{-node}\ (targetnode\ a')$ **by** $(fastforce\ simp: return\text{-node}\text{-def})$
with $\langle valid\text{-edge}\ a \rangle \langle valid\text{-edge}\ a' \rangle \langle a' \in get\text{-return}\text{-edges}\ a \rangle$
have $call\text{-of}\text{-return}\text{-node}\ (targetnode\ a')\ (sourcnode\ a)$
by $(simp\ add: call\text{-of}\text{-return}\text{-node}\text{-def})\ blast$
from $\langle sourcnode\ a \in [HRB\text{-slice}\ S]_{CFG} \rangle \langle pred\ (kind\ a)\ s_2 \rangle \langle kind\ a = Q:r \hookrightarrow pfs \rangle$
have $pred\ (slice\text{-kind}\ S\ a)\ s_2$ **by** $(fastforce\ dest: slice\text{-kind}\text{-Call}\text{-in}\text{-slice})$
from $\langle valid\text{-edge}\ a \rangle \langle length\ s_1 = length\ s_2 \rangle \langle transfer\ (kind\ a)\ s_1 = s_1' \rangle$
have $length\ s_1' = length\ (transfer\ (slice\text{-kind}\ S\ a)\ s_2)$
by $(fastforce\ intro: length\text{-transfer}\text{-kind}\text{-slice}\text{-kind})$
with $\langle pred\ (slice\text{-kind}\ S\ a)\ s_2 \rangle \langle valid\text{-edge}\ a \rangle \langle kind\ a = Q:r \hookrightarrow pfs \rangle$
 $\langle \forall m \in set\ (tl\ ms_1). \exists m'. call\text{-of}\text{-return}\text{-node}\ m\ m' \wedge m' \in [HRB\text{-slice}\ S]_{CFG} \rangle$
 $\langle hd\ ms_1 \in [HRB\text{-slice}\ S]_{CFG} \rangle \langle hd\ ms_1 = sourcnode\ a \rangle$
 $\langle length\ ms_1 = length\ s_1 \rangle \langle length\ s_1 = length\ s_2 \rangle \langle valid\text{-edge}\ a' \rangle$
 $\langle ms_1' = (targetnode\ a)\ \#\ (targetnode\ a')\ \#\ tl\ ms_1 \rangle \langle a' \in get\text{-return}\text{-edges}\ a \rangle$
 $\langle \forall m \in set\ (tl\ ms_2). return\text{-node}\ m \rangle$
have $S, slice\text{-kind}\ S \vdash (ms_1, s_2) \text{-} a \rightarrow (ms_1', transfer\ (slice\text{-kind}\ S\ a)\ s_2)$
by $(auto\ intro: observable\text{-move}. observable\text{-move}\text{-call})$
with $\langle S, slice\text{-kind}\ S \vdash (ms_2, s_2) = as \Rightarrow_{\tau} (ms_1, s_2) \rangle$
have $S, slice\text{-kind}\ S \vdash (ms_2, s_2) = as @ [a] \Rightarrow (ms_1', transfer\ (slice\text{-kind}\ S\ a)\ s_2)$
by $(rule\ observable\text{-moves}\text{-snoc})$
from $\langle \forall m \in set\ ms_1. valid\text{-node}\ m \rangle \langle ms_1' = (targetnode\ a)\ \#\ (targetnode\ a')\ \#\ tl\ ms_1 \rangle$
 $\langle valid\text{-edge}\ a \rangle \langle valid\text{-edge}\ a' \rangle$
have $\forall m \in set\ ms_1'. valid\text{-node}\ m$ **by** $(cases\ ms_1)\ auto$
from $\langle kind\ a = Q:r \hookrightarrow pfs \rangle \langle sourcnode\ a \in [HRB\text{-slice}\ S]_{CFG} \rangle$
have $cf_2' : \exists cf_2'. transfer\ (slice\text{-kind}\ S\ a)\ s_2 = cf_2' \#\ s_2 \wedge snd\ cf_2' = r$
by $(auto\ dest: slice\text{-kind}\text{-Call}\text{-in}\text{-slice})$
with $\langle \forall i < length\ ms_2. snd\ (s_1\ !\ (length\ msx + i)) = snd\ (s_2\ !\ i) \rangle$


```

  ⟨length ms1' = length s1'⟩ ⟨msx = []⟩ ⟨length ms1 = length ms2⟩
  ⟨length ms1 = length s1⟩
have ∀ i < length ms1'. snd (s1' ! i) = snd (transfer (slice-kind S a) s2 ! i)
  by auto(case-tac i, auto)
have ∀ V ∈ rv S (CFG-node (targetnode a')).
  V ∈ rv S (CFG-node (sourcenode a))
proof
fix V assume V ∈ rv S (CFG-node (targetnode a'))
then obtain as n' where targetnode a' -as→i* parent-node n'
  and n' ∈ HRB-slice S and V ∈ UseSDG n'
  and ∀ n''. valid-SDG-node n'' ∧ parent-node n'' ∈ set (sourcenodes as)
  → V ∉ DefSDG n'' by (fastforce elim:rvE)
from (valid-edge a) ⟨a' ∈ get-return-edges a⟩
obtain a'' where valid-edge a'' and sourcenode a'' = sourcenode a
  and targetnode a'' = targetnode a' and intra-kind(kind a'')
  by -(drule call-return-node-edge, auto simp:intra-kind-def)
with ⟨targetnode a' -as→i* parent-node n'⟩
have sourcenode a -a''#as→i* parent-node n'
  by (fastforce intro:Cons-path simp:intra-path-def)
from ⟨sourcenode a'' = sourcenode a⟩ ⟨valid-edge a⟩ ⟨kind a = Q:r↔pfs⟩
have ∀ n''. valid-SDG-node n'' ∧ parent-node n'' = sourcenode a''
  → V ∉ DefSDG n''
  by (fastforce dest:SDG-Def-parent-Def call-source-Def-empty)
with ⟨∀ n''. valid-SDG-node n'' ∧ parent-node n'' ∈ set (sourcenodes as)⟩
  → V ∉ DefSDG n''
have ∀ n''. valid-SDG-node n'' ∧ parent-node n'' ∈ set (sourcenodes (a''#as))
  → V ∉ DefSDG n'' by (fastforce simp:sourcenodes-def)
with ⟨sourcenode a -a''#as→i* parent-node n'⟩ ⟨n' ∈ HRB-slice S⟩
  ⟨V ∈ UseSDG n'⟩
show V ∈ rv S (CFG-node (sourcenode a)) by (fastforce intro:rvI)
qed
have ∀ V ∈ rv S (CFG-node (targetnode a)).
  (empty(ins [:=] params fs (fst cf1))) V =
  state-val (transfer (slice-kind S a) s2) V
proof
fix V assume V ∈ rv S (CFG-node (targetnode a))
from ⟨sourcenode a ∈ [HRB-slice S]CFG⟩ ⟨kind a = Q:r↔pfs⟩
  ⟨(THE ins. ∃ outs. (p, ins, outs) ∈ set procs) = ins⟩
have eq:fst (hd (transfer (slice-kind S a) s2)) =
  empty(ins [:=] params (cspp (targetnode a) (HRB-slice S) fs) (fst cf2))
  by (auto dest:slice-kind-Call-in-slice)
show (empty(ins [:=] params fs (fst cf1))) V =
  state-val (transfer (slice-kind S a) s2) V
proof (cases V ∈ set ins)
case True
then obtain i where V = ins!i and i < length ins
  by (auto simp:in-set-conv-nth)
from ⟨valid-edge a⟩ ⟨kind a = Q:r↔pfs⟩ ⟨(p, ins, outs) ∈ set procs⟩

```

$\langle i < \text{length } \text{ins} \rangle$
have *valid-SDG-node* (*Formal-in* (*targetnode a, i*)) **by** *fastforce*
from $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q:r \mapsto pfs \rangle$ **have** *get-proc*(*targetnode a*) = *p*
by(*rule get-proc-call*)
with $\langle \text{valid-SDG-node} (\text{Formal-in} (\text{targetnode } a, i)) \rangle$
 $\langle (p, \text{ins}, \text{outs}) \in \text{set } \text{procs} \rangle \langle V = \text{ins}!i \rangle$
have $V \in \text{Def}_{SDG} (\text{Formal-in} (\text{targetnode } a, i))$
by(*fastforce intro:Formal-in-SDG-Def*)
from $\langle V \in \text{rv } S (\text{CFG-node} (\text{targetnode } a)) \rangle$ **obtain** *as' nx*
where *targetnode a* $\dashv\text{as}' \rightarrow_i^*$ *parent-node nx*
and $nx \in \text{HRB-slice } S$ **and** $V \in \text{Use}_{SDG} \text{ } nx$
and $\forall n''. \text{valid-SDG-node } n'' \wedge$
 $\text{parent-node } n'' \in \text{set} (\text{sourcenodes } \text{as}') \longrightarrow V \notin \text{Def}_{SDG} \text{ } n''$
by(*fastforce elim:rvE*)
with $\langle \text{valid-SDG-node} (\text{Formal-in} (\text{targetnode } a, i)) \rangle$
 $\langle V \in \text{Def}_{SDG} (\text{Formal-in} (\text{targetnode } a, i)) \rangle$
have *targetnode a* = *parent-node nx*
apply(*auto simp:intra-path-def sourcenodes-def*)
apply(*erule path.cases*) **apply** *fastforce*
apply(*erule tac x=Formal-in (targetnode a, i) in allE*) **by** *fastforce*
with $\langle V \in \text{Use}_{SDG} \text{ } nx \rangle$ **have** $V \in \text{Use} (\text{targetnode } a)$
by(*fastforce intro:SDG-Use-parent-Use*)
with $\langle \text{valid-edge } a \rangle$ **have** $V \in \text{Use}_{SDG} (\text{CFG-node} (\text{targetnode } a))$
by(*auto intro!:CFG-Use-SDG-Use*)
from $\langle \text{targetnode } a = \text{parent-node } nx \rangle$ [THEN *sym*] $\langle \text{valid-edge } a \rangle$
have *parent-node* (*Formal-in* (*targetnode a, i*)) $\dashv\rightarrow_i^*$ *parent-node nx*
by(*fastforce intro:empty-path simp:intra-path-def*)
with $\langle V \in \text{Def}_{SDG} (\text{Formal-in} (\text{targetnode } a, i)) \rangle$
 $\langle V \in \text{Use}_{SDG} (\text{CFG-node} (\text{targetnode } a)) \rangle \langle \text{targetnode } a = \text{parent-node } nx \rangle$
have *Formal-in* (*targetnode a, i*) *influences V in* (*CFG-node* (*targetnode a*))
by(*fastforce simp:data-dependence-def sourcenodes-def*)
hence *ddep:Formal-in* (*targetnode a, i*) *s* $\dashv V \rightarrow_{dd}$ (*CFG-node* (*targetnode*
a))
by(*rule sum-SDG-ddep-edge*)
from $\langle \text{targetnode } a = \text{parent-node } nx \rangle \langle nx \in \text{HRB-slice } S \rangle$
have *CFG-node* (*targetnode a*) $\in \text{HRB-slice } S$
by(*fastforce dest:valid-SDG-node-in-slice-parent-node-in-slice*)
hence *Formal-in* (*targetnode a, i*) $\in \text{HRB-slice } S$
proof(*induct CFG-node (targetnode a) rule:HRB-slice-cases*)
case (*phase1 nx*)
with *ddep show* ?*case*
by(*fastforce intro:ddep-slice1 combine-SDG-slices.combSlice-refl*
simp:HRB-slice-def)
next
case (*phase2 nx n' n'' p*)
from $\langle \text{CFG-node} (\text{targetnode } a) \in \text{sum-SDG-slice2 } n' \rangle$ *ddep*
have *Formal-in* (*targetnode a, i*) $\in \text{sum-SDG-slice2 } n'$
by(*fastforce intro:ddep-slice2*)
with $\langle n'' \text{ s-p} \rightarrow_{\text{ret}} \text{CFG-node} (\text{parent-node } n') \rangle \langle n' \in \text{sum-SDG-slice1 } nx \rangle$

```

      ⟨nx ∈ S⟩
  show ?case by (fastforce intro: combine-SDG-slices.combSlice-Return-parent-node
simp:HRB-slice-def)
  qed
  from ⟨sourcnode a ∈ [HRB-slice S]CFG⟩ ⟨kind a = Q:r↦pfs⟩
  have slice-kind:slice-kind S a =
    Q:r↦p(cspp (targetnode a) (HRB-slice S) fs)
    by (rule slice-kind-Call-in-slice)
  from ⟨valid-edge a⟩ ⟨kind a = Q:r↦pfs⟩ ⟨(p,ins,outs) ∈ set procs⟩
  have length fs = length ins by (rule CFG-call-edge-length)
  from ⟨Formal-in (targetnode a,i) ∈ HRB-slice S⟩
    ⟨length fs = length ins⟩ ⟨i < length ins⟩
  have cspp:(cspp (targetnode a) (HRB-slice S) fs)!i = fs!i
    by (fastforce intro: csppa-Formal-in-in-slice simp:cspp-def)
  from ⟨i < length ins⟩ ⟨length fs = length ins⟩
  have (params (cspp (targetnode a) (HRB-slice S) fs) (fst cf2))!i =
    ((cspp (targetnode a) (HRB-slice S) fs)!i) (fst cf2)
    by (fastforce intro:params-nth)
  with cspp
  have eq:(params (cspp (targetnode a) (HRB-slice S) fs) (fst cf2))!i =
    (fs!i) (fst cf2) by simp
  from ⟨valid-edge a⟩ ⟨kind a = Q:r↦pfs⟩ ⟨(p,ins,outs) ∈ set procs⟩
  have (THE ins. ∃ outs. (p,ins,outs) ∈ set procs) = ins
    by (rule formal-in-THE)
  with slice-kind
  have fst (hd (transfer (slice-kind S a) s2)) =
    empty(ins :=) params (cspp (targetnode a) (HRB-slice S) fs) (fst cf2)
    by simp
  moreover
  from ⟨(p,ins,outs) ∈ set procs⟩ have distinct ins
    by (rule distinct-formal-ins)
  ultimately have state-val (transfer (slice-kind S a) s2) V =
    (params (cspp (targetnode a) (HRB-slice S) fs) (fst cf2))!i
    using ⟨V = ins!i⟩ ⟨i < length ins⟩ ⟨length fs = length ins⟩
    by (fastforce intro:fun-upds-nth)
  with eq
  have 2:state-val (transfer (slice-kind S a) s2) V = (fs!i) (fst cf2)
    by simp
  from ⟨V = ins!i⟩ ⟨i < length ins⟩ ⟨length fs = length ins⟩
    ⟨distinct ins⟩
  have empty(ins :=) params fs (fst cf1) V = (params fs (fst cf1))!i
    by (fastforce intro:fun-upds-nth)
  with ⟨i < length ins⟩ ⟨length fs = length ins⟩
  have 1:empty(ins :=) params fs (fst cf1) V = (fs!i) (fst cf1)
    by (fastforce intro:params-nth)
  from ⟨∀ i < length ms2. ∀ V ∈ rv S (CFG-node ((mx#tl ms2)!i)).
    (fst (s1!(length msx + i))) V = (fst (s2!i)) V⟩
  have rv:∀ V ∈ rv S (CFG-node (sourcnode a)). fst cf1 V = fst cf2 V
    by (erule-tac x=0 in alle) auto

```

from $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q:r \rightarrow pfs \rangle \langle (p, ins, outs) \in \text{set procs} \rangle$
 $\langle i < \text{length } ins \rangle$ **have** $\forall V \in (\text{ParamUses } (\text{sourcenode } a)!i).$
 $V \in \text{Use}_{SDG} (\text{Actual-in } (\text{sourcenode } a, i))$
by(*fastforce intro:Actual-in-SDG-Use*)
with $\langle \text{valid-edge } a \rangle$ **have** $\forall V \in (\text{ParamUses } (\text{sourcenode } a)!i).$
 $V \in \text{Use}_{SDG} (\text{CFG-node } (\text{sourcenode } a))$
by(*auto intro!:CFG-Use-SDG-Use dest:SDG-Use-parent-Use*)
moreover
from $\langle \text{valid-edge } a \rangle$ **have** $\text{parent-node } (\text{CFG-node } (\text{sourcenode } a)) - \square \rightarrow_i *$
 $\text{parent-node } (\text{CFG-node } (\text{sourcenode } a))$
by(*fastforce intro:empty-path simp:intra-path-def*)
ultimately
have $\forall V \in (\text{ParamUses } (\text{sourcenode } a)!i). V \in \text{rv } S (\text{CFG-node } (\text{sourcenode } a))$
using $\langle \text{sourcenode } a \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle \langle \text{valid-edge } a \rangle$
by(*fastforce intro:rvI simp:SDG-to-CFG-set-def sourcenodes-def*)
with *rv* **have** $\forall V \in (\text{ParamUses } (\text{sourcenode } a)!i). \text{fst } cf_1 \ V = \text{fst } cf_2 \ V$
by *fastforce*
with $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q:r \rightarrow pfs \rangle \langle i < \text{length } ins \rangle$
 $\langle (p, ins, outs) \in \text{set procs} \rangle \langle \text{pred } (\text{kind } a) \ s_1 \rangle \langle \text{pred } (\text{kind } a) \ s_2 \rangle$
have $(\text{params } fs \ (\text{fst } cf_1))!i = (\text{params } fs \ (\text{fst } cf_2))!i$
by(*fastforce dest!:CFG-call-edge-params*)
moreover
from $\langle i < \text{length } ins \rangle \langle \text{length } fs = \text{length } ins \rangle$
have $(\text{params } fs \ (\text{fst } cf_1))!i = (fs!i) \ (\text{fst } cf_1)$
and $(\text{params } fs \ (\text{fst } cf_2))!i = (fs!i) \ (\text{fst } cf_2)$
by(*auto intro:params-nth*)
ultimately show *?thesis using 1 2 by simp*
next
case *False*
with *eq* **show** *?thesis by(fastforce simp:fun-upds-notin)*
qed
qed
with $\langle \forall i < \text{length } ms_2. \forall V \in \text{rv } S (\text{CFG-node } ((mx \# tl \ ms_2)!i)).$
 $(\text{fst } (s_1!(\text{length } ms_2 + i))) \ V = (\text{fst } (s_2!i)) \ V \rangle \langle \text{cf2}' \ \langle \text{tl } ms_2 = \text{tl } ms_1 \rangle$
 $\langle \text{length } ms_2 = \text{length } s_2 \rangle \langle \text{length } ms_1 = \text{length } s_1 \rangle \langle \text{length } s_1 = \text{length } s_2 \rangle$
 $\langle ms_1' = (\text{targetnode } a) \# (\text{targetnode } a') \# \text{tl } ms_1 \rangle$
 $\langle \forall V \in \text{rv } S (\text{CFG-node } (\text{targetnode } a')). V \in \text{rv } S (\text{CFG-node } (\text{sourcenode } a)) \rangle$
have $\forall i < \text{length } ms_1'. \forall V \in \text{rv } S (\text{CFG-node } ((\text{targetnode } a \ \# \ \text{tl } ms_1')!i)).$
 $(\text{fst } (s_1!(\text{length } [] + i))) \ V = (\text{fst } (\text{transfer } (\text{slice-kind } S \ a) \ s_2!i)) \ V$
apply *clarsimp* **apply**(*case-tac i*) **apply** *auto*
apply(*erule-tac x=nat in allE*)
apply(*case-tac nat*) **apply** *auto done*
with $\langle \forall m \in \text{set } ms_2. \text{valid-node } m \rangle \langle \forall m \in \text{set } ms_1'. \text{valid-node } m \rangle$
 $\langle \text{length } ms_2 = \text{length } s_2 \rangle \langle \text{length } s_1' = \text{length } (\text{transfer } (\text{slice-kind } S \ a) \ s_2) \rangle$
 $\langle \text{length } ms_1' = \text{length } s_1' \rangle \langle ms_1' = (\text{targetnode } a) \# (\text{targetnode } a') \# \text{tl } ms_1 \rangle$
 $\langle \text{get-proc } mx = \text{get-proc } (\text{hd } ms_2) \rangle \langle \text{sourcenode } a \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
 $\langle \forall m \in \text{set } (\text{tl } ms_1). \exists m'. \text{call-of-return-node } m \ m' \wedge m' \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$

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  ⟨return-node (targetnode a')⟩ ⟨∀ m ∈ set (tl ms1). return-node m⟩
  ⟨call-of-return-node (targetnode a') (sourcenode a)⟩
  ⟨∀ i < length ms1'. snd (s1' ! i) = snd (transfer (slice-kind S a) s2 ! i)⟩
  have ((ms1',s1'),(ms1',transfer (slice-kind S a) s2)) ∈ WS S
  by(fastforce intro!:WSI)
  with ⟨S,slice-kind S ⊢ (ms2,s2) = as@[a]⇒ (ms1',transfer (slice-kind S a) s2)⟩
  show ?case by blast
next
  case (observable-move-return a s1 s1' Q p f' ms1 S ms1')
  from ⟨s1 ≠ []⟩ ⟨s2 ≠ []⟩ obtain cf1 cfs1 cf2 cfs2 where [simp]:s1 = cf1#cfs1
  and [simp]:s2 = cf2#cfs2 by(cases s1,auto,cases s2,fastforce+)
  from ⟨length ms1 = length s1⟩ ⟨s1 ≠ []⟩ have [simp]:ms1 ≠ [] by(cases ms1)
  auto
  from ⟨length ms2 = length s2⟩ ⟨s2 ≠ []⟩ have [simp]:ms2 ≠ [] by(cases ms2)
  auto
  from ⟨∀ m ∈ set (tl ms1). ∃ m'. call-of-return-node m m' ∧ m' ∈ [HRB-slice S]CFG⟩
  ⟨hd ms1 = sourcenode a⟩ ⟨ms1 = msx@mx#tl ms2⟩
  ⟨msx ≠ [] ⟶ (∃ mx'. call-of-return-node mx mx' ∧ mx' ∉ [HRB-slice S]CFG)⟩
  have [simp]:mx = sourcenode a msx = [] and [simp]:tl ms2 = tl ms1
  by(cases msx,auto)+
  hence length ms1 = length ms2 by(cases ms2) auto
  with ⟨length ms1 = length s1⟩ ⟨length ms2 = length s2⟩
  have length s1 = length s2 by simp
  have ∃ as. S,slice-kind S ⊢ (ms2,s2) = as⇒τ (ms1,s2)
  proof(cases obs-intra (hd ms2) [HRB-slice S]CFG = {})
  case True
  from ⟨valid-edge a⟩ ⟨hd ms1 = sourcenode a⟩ ⟨kind a = Q↔pf'⟩
  have method-exit (hd ms1) by(fastforce simp:method-exit-def)
  from ⟨∀ m ∈ set ms2. valid-node m⟩ have valid-node (hd ms2) by(cases ms2)
  auto
  then obtain asx where hd ms2 - asx →√* (-Exit-) by(fastforce dest!:Exit-path)
  then obtain as pex where hd ms2 - as →l* pex and method-exit pex
  by(fastforce elim:valid-Exit-path-intra-path)
  from ⟨hd ms2 - as →l* pex⟩ have get-proc (hd ms2) = get-proc pex
  by(rule intra-path-get-procs)
  with ⟨get-proc mx = get-proc (hd ms2)⟩
  have get-proc mx = get-proc pex by simp
  with ⟨method-exit (hd ms1)⟩ ⟨hd ms1 = sourcenode a⟩ ⟨method-exit pex⟩
  have [simp]:pex = hd ms1 by(fastforce intro:method-exit-unique)
  from ⟨obs-intra (hd ms2) [HRB-slice S]CFG = {}⟩ ⟨method-exit pex⟩
  ⟨get-proc (hd ms2) = get-proc pex⟩ ⟨valid-node (hd ms2)⟩
  ⟨length ms2 = length s2⟩ ⟨∀ m ∈ set (tl ms1). return-node m⟩ ⟨ms2 ≠ []⟩
  obtain as'
  where S,slice-kind S ⊢ (hd ms2#tl ms2,s2) = as'⇒τ (hd ms1#tl ms1,s2)
  by(fastforce elim!:silent-moves-intra-path-no-obs[of - - s2 tl ms2]
  dest:hd-Cons-tl)
  with ⟨ms2 ≠ []⟩ have S,slice-kind S ⊢ (ms2,s2) = as'⇒τ (ms1,s2)
  by(fastforce dest!:hd-Cons-tl)

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thus ?thesis by blast
next
  case False
then obtain  $x$  where  $x \in \text{obs-intra} (\text{hd } ms_2) \llbracket \text{HRB-slice } S \rrbracket_{CFG}$  by fastforce
hence  $\text{obs-intra} (\text{hd } ms_2) \llbracket \text{HRB-slice } S \rrbracket_{CFG} = \{x\}$ 
  by(rule obs-intra-singleton-element)
with  $\langle \forall m \in \text{set} (\text{tl } ms_2). \exists m'. \text{call-of-return-node } m \ m' \wedge m' \in \llbracket \text{HRB-slice } S \rrbracket_{CFG} \rangle$ 
have  $x \# \text{tl } ms_1 \in \text{obs} (\llbracket @ \text{hd } ms_2 \# \text{tl } ms_2 \rrbracket \llbracket \text{HRB-slice } S \rrbracket_{CFG})$ 
  by(fastforce intro:obsI)
with  $\langle ms_2 \neq [] \rangle$  have  $x \# \text{tl } ms_1 \in \text{obs } ms_2 \llbracket \text{HRB-slice } S \rrbracket_{CFG}$ 
  by(fastforce dest:hd-Cons-tl simp del:obs.simps)
with  $\langle \text{obs } ms_1 \llbracket \text{HRB-slice } S \rrbracket_{CFG} = \text{obs } ms_2 \llbracket \text{HRB-slice } S \rrbracket_{CFG} \rangle$ 
have  $x \# \text{tl } ms_1 \in \text{obs } ms_1 \llbracket \text{HRB-slice } S \rrbracket_{CFG}$  by simp
from this  $\langle \forall m \in \text{set} (\text{tl } ms_1). \text{return-node } m \rangle$ 
have  $x \in \text{obs-intra} (\text{hd } ms_1) \llbracket \text{HRB-slice } S \rrbracket_{CFG}$ 
proof(rule obsE)
  fix  $nsx \ n \ nsx' \ n'$ 
  assume  $ms_1 = nsx @ n \# nsx'$  and  $x \# \text{tl } ms_1 = n' \# nsx'$ 
  and  $n' \in \text{obs-intra } n \llbracket \text{HRB-slice } S \rrbracket_{CFG}$ 
from  $\langle ms_1 = nsx @ n \# nsx' \rangle \langle x \# \text{tl } ms_1 = n' \# nsx' \rangle \langle \text{tl } ms_2 = \text{tl } ms_1 \rangle$ 
have  $[simp]: nsx = []$  by(cases nsx) auto
with  $\langle ms_1 = nsx @ n \# nsx' \rangle$  have  $[simp]: n = \text{hd } ms_1$  by simp
from  $\langle x \# \text{tl } ms_1 = n' \# nsx' \rangle$  have  $[simp]: n' = x$  by simp
with  $\langle n' \in \text{obs-intra } n \llbracket \text{HRB-slice } S \rrbracket_{CFG} \rangle$  show ?thesis by simp
qed
{ fix  $m$  as assume  $\text{hd } ms_1 - \text{as} \rightarrow_i^* m$ 
  hence  $\text{hd } ms_1 - \text{as} \rightarrow^* m$  and  $\forall a \in \text{set } \text{as}. \text{intra-kind} (\text{kind } a)$ 
  by(simp-all add:intra-path-def)
  hence  $m = \text{hd } ms_1$ 
  proof(induct  $\text{hd } ms_1$  as  $m$  rule:path.induct)
    case (Cons-path  $m'' \ \text{as}' \ m' \ a'$ )
    from  $\langle \forall a \in \text{set} (a' \# \text{as}'). \text{intra-kind} (\text{kind } a) \rangle$ 
    have  $\text{intra-kind} (\text{kind } a')$  by simp
    with  $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q \leftrightarrow_p f' \rangle \langle \text{valid-edge } a' \rangle$ 
       $\langle \text{sourcenode } a' = \text{hd } ms_1 \rangle \langle \text{hd } ms_1 = \text{sourcenode } a \rangle$ 
    have False by(fastforce dest:return-edges-only simp:intra-kind-def)
    thus ?case by simp
  qed simp }
with  $\langle x \in \text{obs-intra} (\text{hd } ms_1) \llbracket \text{HRB-slice } S \rrbracket_{CFG} \rangle$ 
have  $x = \text{hd } ms_1$  by(fastforce elim:obs-intraE)
with  $\langle x \in \text{obs-intra} (\text{hd } ms_2) \llbracket \text{HRB-slice } S \rrbracket_{CFG} \rangle \langle \text{length } ms_2 = \text{length } s_2 \rangle$ 
   $\langle \forall m \in \text{set} (\text{tl } ms_1). \text{return-node } m \rangle \langle ms_2 \neq [] \rangle$ 
obtain  $as$  where  $S, \text{slice-kind } S \vdash (\text{hd } ms_2 \# \text{tl } ms_2, s_2) = \text{as} \Rightarrow_\tau (\text{hd } ms_1 \# \text{tl } ms_1, s_2)$ 
  by(fastforce elim!:silent-moves-intra-path-obs[of - - -  $s_2$   $\text{tl } ms_2$ ]
    dest:hd-Cons-tl)
with  $\langle ms_2 \neq [] \rangle$  have  $S, \text{slice-kind } S \vdash (ms_2, s_2) = \text{as} \Rightarrow_\tau (ms_1, s_2)$ 
  by(fastforce dest!:hd-Cons-tl)

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thus ?thesis by blast
 qed
 then obtain as where $S, \text{slice-kind } S \vdash (ms_2, s_2) = as \Rightarrow_{\tau} (ms_1, s_2)$ by blast
 from $\langle ms_1' = tl \ ms_1 \rangle \langle \text{length } s_1 = \text{Suc}(\text{length } s_1') \rangle$
 $\langle \text{length } ms_1 = \text{length } s_1 \rangle$ have $\text{length } ms_1' = \text{length } s_1'$ by simp
 from $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q \leftrightarrow_{pf} \rangle$ obtain $a'' \ Q' \ r' \ fs'$ where *valid-edge*
 a''
 and $\text{kind } a'' = Q' : r' \hookrightarrow_{pf} fs'$ and $a \in \text{get-return-edges } a''$
 by $-(\text{drule return-needs-call}, \text{auto})$
 then obtain ins outs where $(p, ins, outs) \in \text{set procs}$
 by $(\text{fastforce dest!: callee-in-procs})$
 from $\langle \text{length } s_1 = \text{Suc}(\text{length } s_1') \rangle \langle s_1' \neq [] \rangle$
 obtain $cfx \ cfsx$ where $[simp]: cfs_1 = cfx \# cfsx$ by $(\text{cases } cfs_1) \text{ auto}$
 with $\langle \text{length } s_1 = \text{length } s_2 \rangle$ obtain $cfx' \ cfsx'$ where $[simp]: cfs_2 = cfx' \# cfsx'$
 by $(\text{cases } cfs_2) \text{ auto}$
 from $\langle \text{length } ms_1 = \text{length } s_1 \rangle$ have $tl \ ms_1 = [] @ hd(tl \ ms_1) \# tl(tl \ ms_1)$
 by $(\text{auto simp: length-Suc-conv})$
 from $\langle \text{kind } a = Q \leftrightarrow_{pf} \rangle \langle \text{transfer } (\text{kind } a) \ s_1 = s_1' \rangle$
 have $s_1' = (f' (fst \ cf_1) (fst \ cfx), snd \ cfx) \# cfsx$ by simp
 from $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q \leftrightarrow_{pf} \rangle \langle (p, ins, outs) \in \text{set procs} \rangle$
 have $f' (fst \ cf_1) (fst \ cfx) =$
 $(fst \ cfx)(\text{ParamDefs } (\text{targetnode } a) [:=] \text{map } (fst \ cf_1) \ \text{outs})$
 by $(\text{rule CFG-return-edge-fun})$
 with $\langle s_1' = (f' (fst \ cf_1) (fst \ cfx), snd \ cfx) \# cfsx \rangle$
 have $[simp]: s_1' =$
 $((fst \ cfx)(\text{ParamDefs } (\text{targetnode } a) [:=] \text{map } (fst \ cf_1) \ \text{outs}), snd \ cfx) \# cfsx$
 by simp
 have $\text{pred } (\text{slice-kind } S \ a) \ s_2$
 proof $(\text{cases } \text{sourcenode } a \in [\text{HRB-slice } S] \ \text{CFG})$
 case True
 from $\langle \text{valid-edge } a \rangle$ have *valid-node* $(\text{sourcenode } a)$ by simp
 hence $\text{sourcenode } a - [] \rightarrow_{\tau} \text{sourcenode } a$
 by $(\text{fastforce intro: empty-path simp: intra-path-def})$
 with $\langle \text{sourcenode } a \in [\text{HRB-slice } S] \ \text{CFG} \rangle$
 have $\forall V. V \in \text{Use}_{SDG} (\text{CFG-node } (\text{sourcenode } a))$
 $\rightarrow V \in \text{rv } S (\text{CFG-node } (\text{sourcenode } a))$
 by $(\text{auto rule rvI, auto simp: SDG-to-CFG-set-def sourcenodes-def})$
 with $\langle \text{valid-node } (\text{sourcenode } a) \rangle$
 have $\forall V \in \text{Use } (\text{sourcenode } a). V \in \text{rv } S (\text{CFG-node } (\text{sourcenode } a))$
 by $(\text{fastforce intro: CFG-Use-SDG-Use})$
 from $\langle \forall i < \text{length } ms_2. \forall V \in \text{rv } S (\text{CFG-node } ((mx \# tl \ ms_2) ! i)).$
 $(fst \ (s_1 ! (\text{length } msx + i))) \ V = (fst \ (s_2 ! i)) \ V \rangle \langle \text{length } ms_2 = \text{length } s_2 \rangle$
 have $\forall V \in \text{rv } S (\text{CFG-node } mx). (fst \ (s_1 ! \text{length } msx)) \ V = \text{state-val } s_2 \ V$
 by $(\text{cases } ms_2) \text{ auto}$
 with $\langle \forall V \in \text{Use } (\text{sourcenode } a). V \in \text{rv } S (\text{CFG-node } (\text{sourcenode } a)) \rangle$
 have $\forall V \in \text{Use } (\text{sourcenode } a). fst \ cf_1 \ V = fst \ cf_2 \ V$ by *fastforce*
 moreover
 from $\langle \forall i < \text{length } ms_2. snd \ (s_1 ! (\text{length } msx + i)) = snd \ (s_2 ! i) \rangle$
 have $snd \ (hd \ s_1) = snd \ (hd \ s_2)$ by $(\text{erule tac } x=0 \ \text{in } \text{allE}) \ \text{auto}$

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ultimately have pred (kind a) s2
  using ⟨valid-edge a⟩ ⟨pred (kind a) s1⟩ ⟨length s1 = length s2⟩
  by(fastforce intro:CFG-edge-Uses-pred-equal)
with ⟨valid-edge a⟩ ⟨kind a = Q↔pf'⟩ ⟨(p,ins,outs) ∈ set procs⟩
  ⟨sourcenode a ∈ [HRB-slice S]CFG⟩
show ?thesis by(fastforce dest:slice-kind-Return-in-slice)
next
case False
with ⟨kind a = Q↔pf'⟩ have slice-kind S a = (λcf. True)↔p(λcf cf'. cf')
  by -(rule slice-kind-Return)
thus ?thesis by simp
qed
from ⟨valid-edge a⟩ ⟨length s1 = length s2⟩ ⟨transfer (kind a) s1 = s1'⟩
have length s1' = length (transfer (slice-kind S a) s2)
  by(fastforce intro:length-transfer-kind-slice-kind)
with ⟨pred (slice-kind S a) s2⟩ ⟨valid-edge a⟩ ⟨kind a = Q↔pf'⟩
  ⟨∀ m ∈ set (tl ms1). ∃ m'. call-of-return-node m m' ∧ m' ∈ [HRB-slice S]CFG⟩
  ⟨hd ms1 = sourcenode a⟩
  ⟨length ms1 = length s1⟩ ⟨length s1 = length s2⟩
  ⟨ms1' = tl ms1⟩ ⟨hd(tl ms1) = targetnode a⟩ ⟨∀ m ∈ set (tl ms1). return-node
m)
have S,slice-kind S ⊢ (ms1,s2) -a→ (ms1',transfer (slice-kind S a) s2)
  by(fastforce intro!:observable-move.observable-move-return)
with ⟨S,slice-kind S ⊢ (ms2,s2) =as⇒τ (ms1,s2)⟩
have S,slice-kind S ⊢ (ms2,s2) =as@[a]⇒ (ms1',transfer (slice-kind S a) s2)
  by(rule observable-moves-snoc)
from ⟨∀ m ∈ set ms1. valid-node m⟩ ⟨ms1' = tl ms1⟩
have ∀ m ∈ set ms1'. valid-node m by(cases ms1) auto
from ⟨length ms1' = length s1'⟩ have ms1' = []@hd ms1'#tl ms1'
  by(cases ms1') auto
from ⟨∀ i<length ms2. snd (s1 ! (length msx + i)) = snd (s2 ! i)⟩
  ⟨length ms1 = length ms2⟩ ⟨length ms1 = length s1⟩
have snd cfx = snd cfx' by(erule-tac x=1 in alle) auto
from ⟨valid-edge a⟩ ⟨kind a = Q↔pf'⟩ ⟨(p,ins,outs) ∈ set procs⟩
have cf2':∃ cf2'. transfer (slice-kind S a) s2 = cf2'#cfsx' ∧ snd cf2' = snd
cfx'
  by(cases cfx',cases sourcenode a ∈ [HRB-slice S]CFG,
    auto dest:slice-kind-Return slice-kind-Return-in-slice)
with ⟨∀ i<length ms2. snd (s1 ! (length msx + i)) = snd (s2 ! i)⟩
  ⟨length ms1' = length s1'⟩ ⟨msx = []⟩ ⟨length ms1 = length ms2⟩
  ⟨length ms1 = length s1⟩ ⟨snd cfx = snd cfx'⟩
have ∀ i<length ms1'. snd (s1' ! i) = snd (transfer (slice-kind S a) s2 ! i)
  apply auto apply(case-tac i) apply auto
by(erule-tac x=Suc(Suc nat) in alle) auto
from ⟨∀ m ∈ set (tl ms1). ∃ m'. call-of-return-node m m' ∧ m' ∈ [HRB-slice
S]CFG⟩
have ∀ m ∈ set (tl (tl ms1)).
  ∃ m'. call-of-return-node m m' ∧ m' ∈ [HRB-slice S]CFG
  by(cases tl ms1) auto

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from  $\langle \forall m \in \text{set } (tl \ ms_1). \text{ return-node } m \rangle$ 
have  $\langle \forall m \in \text{set } (tl \ (tl \ ms_1)). \text{ return-node } m \text{ by } (cases \ tl \ ms_1) \text{ auto} \rangle$ 
have  $\langle \forall V \in rv \ S \ (CFG\text{-node } (hd \ (tl \ ms_1))).$ 
   $(fst \ cf_x)(ParamDefs \ (targetnode \ a) \ [:=] \ map \ (fst \ cf_1) \ outs) \ V =$ 
   $state\text{-val } (transfer \ (slice\text{-kind } S \ a) \ s_2) \ V \rangle$ 
proof
fix  $V$  assume  $V \in rv \ S \ (CFG\text{-node } (hd \ (tl \ ms_1)))$ 
with  $\langle hd(tl \ ms_1) = targetnode \ a \rangle$  have  $V \in rv \ S \ (CFG\text{-node } (targetnode \ a))$ 
  by simp
show  $(fst \ cf_x)(ParamDefs \ (targetnode \ a) \ [:=] \ map \ (fst \ cf_1) \ outs) \ V =$ 
   $state\text{-val } (transfer \ (slice\text{-kind } S \ a) \ s_2) \ V$ 
proof(cases  $V \in \text{set } (ParamDefs \ (targetnode \ a))$ )
  case True
  then obtain  $i$  where  $V = (ParamDefs \ (targetnode \ a))!i$ 
    and  $i < length(ParamDefs \ (targetnode \ a))$ 
    by(auto simp:in-set-conv-nth)
  moreover
  from  $\langle valid\text{-edge } a \ \langle kind \ a = Q \leftarrow_{pf} \ \langle (p,ins,outs) \in \text{set } procs \rangle \rangle \rangle$ 
  have  $length:length(ParamDefs \ (targetnode \ a)) = length \ outs$ 
    by(fastforce intro:ParamDefs-return-target-length)
  from  $\langle valid\text{-edge } a \ \langle kind \ a = Q \leftarrow_{pf} \ \langle (p,ins,outs) \in \text{set } procs \rangle \rangle \rangle$ 
     $\langle i < length(ParamDefs \ (targetnode \ a)) \rangle$ 
     $\langle length(ParamDefs \ (targetnode \ a)) = length \ outs \rangle$ 
  have  $valid\text{-SDG-node } (Actual\text{-out}(targetnode \ a, i))$  by fastforce
  with  $\langle V = (ParamDefs \ (targetnode \ a))!i \rangle$ 
  have  $V \in Def_{SDG} \ (Actual\text{-out}(targetnode \ a, i))$ 
    by(fastforce intro:Actual-out-SDG-Def)
  from  $\langle V \in rv \ S \ (CFG\text{-node } (targetnode \ a)) \rangle$  obtain  $as' \ nx$ 
    where  $targetnode \ a \ -as' \rightarrow_i^* \ parent\text{-node } nx$ 
    and  $nx \in HRB\text{-slice } S$  and  $V \in Use_{SDG} \ nx$ 
    and  $\forall n''. \ valid\text{-SDG-node } n'' \wedge$ 
       $parent\text{-node } n'' \in \text{set } (sourcenodes \ as') \longrightarrow V \notin Def_{SDG} \ n''$ 
    by(fastforce elim:rvE)
  with  $\langle valid\text{-SDG-node } (Actual\text{-out}(targetnode \ a, i)) \rangle$ 
     $\langle V \in Def_{SDG} \ (Actual\text{-out}(targetnode \ a, i)) \rangle$ 
  have  $targetnode \ a = parent\text{-node } nx$ 
    apply(auto simp:intra-path-def sourcenodes-def)
    apply(erule path.cases) apply fastforce
    apply(erule-tac x=(Actual-out(targetnode a,i)) in alle) by fastforce
  with  $\langle V \in Use_{SDG} \ nx \rangle$  have  $V \in Use \ (targetnode \ a)$ 
    by(fastforce intro:SDG-Use-parent-Use)
  with  $\langle valid\text{-edge } a \rangle$  have  $V \in Use_{SDG} \ (CFG\text{-node } (targetnode \ a))$ 
    by(auto intro!:CFG-Use-SDG-Use)
  from  $\langle targetnode \ a = parent\text{-node } nx \rangle [THEN \ sym] \ \langle valid\text{-edge } a \rangle$ 
  have  $parent\text{-node } (Actual\text{-out}(targetnode \ a, i)) \ -[] \rightarrow_i^* \ parent\text{-node } nx$ 
    by(fastforce intro:empty-path simp:intra-path-def)
  with  $\langle V \in Def_{SDG} \ (Actual\text{-out}(targetnode \ a, i)) \rangle$ 
     $\langle V \in Use_{SDG} \ (CFG\text{-node } (targetnode \ a)) \rangle$ 
  have  $Actual\text{-out}(targetnode \ a, i) \ influences \ V \ in \ (CFG\text{-node } (targetnode \ a))$ 

```

```

  by(fastforce simp:data-dependence-def sourcenodes-def)
hence ddep:Actual-out(targetnode a,i) s - V →dd (CFG-node (targetnode a))
  by(rule sum-SDG-ddep-edge)
from (targetnode a = parent-node nx) (nx ∈ HRB-slice S)
have CFG-node (targetnode a) ∈ HRB-slice S
  by(fastforce dest:valid-SDG-node-in-slice-parent-node-in-slice)
hence Actual-out(targetnode a,i) ∈ HRB-slice S
proof(induct CFG-node (targetnode a) rule:HRB-slice-cases)
  case (phase1 nx')
  with ddep show ?case
    by(fastforce intro: ddep-slice1 combine-SDG-slices.combSlice-refl
      simp:HRB-slice-def)
next
  case (phase2 nx' n' n'' p)
  from (CFG-node (targetnode a) ∈ sum-SDG-slice2 n') ddep
  have Actual-out(targetnode a,i) ∈ sum-SDG-slice2 n'
    by(fastforce intro:ddep-slice2)
  with (n'' s - p →ret CFG-node (parent-node n')) (n' ∈ sum-SDG-slice1 nx')
    (nx' ∈ S)
show ?case by(fastforce intro:combine-SDG-slices.combSlice-Return-parent-node
  simp:HRB-slice-def)
qed
from (valid-edge a) (kind a = Q ↔p pf') (valid-edge a'')
  (kind a'' = Q' : r' ↔p pfs') (a ∈ get-return-edges a'')
  (CFG-node (targetnode a) ∈ HRB-slice S)
have CFG-node (sourcnode a) ∈ HRB-slice S
  by(rule call-return-nodes-in-slice)
hence sourcnode a ∈ [HRB-slice S]CFG by(simp add:SDG-to-CFG-set-def)
from (sourcnode a ∈ [HRB-slice S]CFG) (valid-edge a) (kind a = Q ↔p pf')
  (p,ins,outs) ∈ set procs)
have slice-kind:slice-kind S a =
  Q ↔p (λcf cf'. rspp (targetnode a) (HRB-slice S) outs cf' cf)
  by(rule slice-kind-Return-in-slice)
from (Actual-out(targetnode a,i) ∈ HRB-slice S)
  (i < length(ParamDefs (targetnode a))) (valid-edge a)
  (V = (ParamDefs (targetnode a))!i) length
have 2:rspp (targetnode a) (HRB-slice S) outs (fst cfx') (fst cf2) V =
  (fst cf2)(outs!i)
  by(fastforce intro:rspp-Actual-out-in-slice)
from (i < length(ParamDefs (targetnode a))) length (valid-edge a)
have (fst cfx)(ParamDefs (targetnode a) [:=] map (fst cf1) outs)
  ((ParamDefs (targetnode a))!i) = (map (fst cf1) outs)!i
  by(fastforce intro:fun-upds-nth distinct-ParamDefs)
with (V = (ParamDefs (targetnode a))!i)
  (i < length(ParamDefs (targetnode a))) length
have 1:(fst cfx)(ParamDefs (targetnode a) [:=] map (fst cf1) outs) V =
  (fst cf1)(outs!i)
  by simp
from (valid-edge a) (kind a = Q ↔p pf') (p,ins,outs) ∈ set procs)

```

```

    ⟨i < length(ParamDefs (targetnode a))⟩ length
  have po:Formal-out(sourcenode a,i) s-p:outs!i→out Actual-out(targetnode
a,i)
    by(fastforce intro:sum-SDG-param-out-edge)
  from ⟨valid-edge a⟩ ⟨kind a = Q↔pf'⟩
  have CFG-node (sourcenode a) s-p→ret CFG-node (targetnode a)
    by(fastforce intro:sum-SDG-return-edge)
  from ⟨Actual-out(targetnode a,i) ∈ HRB-slice S⟩
  have Formal-out(sourcenode a,i) ∈ HRB-slice S
  proof(induct Actual-out(targetnode a,i) rule:HRB-slice-cases)
    case (phase1 nx')
    let ?AO = Actual-out(targetnode a,i)
    from ⟨valid-SDG-node ?AO⟩ have ?AO ∈ sum-SDG-slice2 ?AO
      by(rule refl-slice2)
    with po have Formal-out(sourcenode a,i) ∈ sum-SDG-slice2 ?AO
      by(rule param-out-slice2)
    with ⟨CFG-node (sourcenode a) s-p→ret CFG-node (targetnode a)⟩
      ⟨Actual-out (targetnode a, i) ∈ sum-SDG-slice1 nx'⟩ ⟨nx' ∈ S⟩
    show ?case
      by(fastforce intro:combSlice-Return-parent-node simp:HRB-slice-def)
  next
    case (phase2 nx' n' n'' p)
    from ⟨Actual-out (targetnode a, i) ∈ sum-SDG-slice2 n'⟩ po
    have Formal-out(sourcenode a,i) ∈ sum-SDG-slice2 n'
      by(fastforce intro:param-out-slice2)
    with ⟨n' ∈ sum-SDG-slice1 nx'⟩ ⟨n'' s-p→ret CFG-node (parent-node n')⟩
      ⟨nx' ∈ S⟩
    show ?case by(fastforce intro:combine-SDG-slices.combSlice-Return-parent-node
      simp:HRB-slice-def)
  qed
  with ⟨valid-edge a⟩ ⟨kind a = Q↔pf'⟩ ⟨(p,ins,outs) ∈ set procs⟩
    ⟨i < length(ParamDefs (targetnode a))⟩ length
  have outs!i ∈ UseSDG Formal-out(sourcenode a,i)
    by(fastforce intro!:Formal-out-SDG-Use get-proc-return)
  with ⟨valid-edge a⟩ have outs!i ∈ UseSDG (CFG-node (sourcenode a))
    by(auto intro!:CFG-Use-SDG-Use dest:SDG-Use-parent-Use)
  moreover
  from ⟨valid-edge a⟩ have parent-node (CFG-node (sourcenode a)) -[]→ι*
    parent-node (CFG-node (sourcenode a))
    by(fastforce intro:empty-path simp:intra-path-def)
  ultimately have outs!i ∈ rv S (CFG-node (sourcenode a))
    using ⟨sourcenode a ∈ [HRB-slice S]CFG⟩ ⟨valid-edge a⟩
    by(fastforce intro:rvI simp:SDG-to-CFG-set-def sourcenodes-def)
  with ⟨∀ i < length ms2. ∀ V ∈ rv S (CFG-node ((mx#tl ms2)!i)).
    (fst (s1!(length msx + i))) V = (fst (s2!i)) V⟩
  have (fst cf1)(outs!i) = (fst cf2)(outs!i)
    by auto(erule-tac x=0 in allE,auto)
  with 1 2 slice-kind show ?thesis by simp

```

```

next
case False
with ⟨transfer (kind a) s1 = s1'⟩
have (fst cfx)(ParamDefs (targetnode a) [:=] map (fst cf1) outs) =
  (fst (hd cfs1))(ParamDefs (targetnode a) [:=] map (fst cf1) outs)
  by(cases cfs1, auto intro: CFG-return-edge-fun)
show ?thesis
proof(cases sourcenode a ∈ [HRB-slice S]CFG)
  case True
  from ⟨sourcenode a ∈ [HRB-slice S]CFG ⟨valid-edge a⟩ ⟨kind a = Q↔pf'⟩
    ⟨(p, ins, outs) ∈ set procs⟩
  have slice-kind S a =
    Q↔p(λcf cf'. rspp (targetnode a) (HRB-slice S) outs cf' cf)
    by(rule slice-kind-Return-in-slice)
  with ⟨length s1' = length (transfer (slice-kind S a) s2)⟩
    ⟨length s1 = length s2⟩
  have state-val (transfer (slice-kind S a) s2) V =
    rspp (targetnode a) (HRB-slice S) outs (fst cfx') (fst cf2) V
    by simp
  with ⟨V ∉ set (ParamDefs (targetnode a))⟩
  have state-val (transfer (slice-kind S a) s2) V = state-val cfs2 V
    by(fastforce simp:rspp-def map-merge-def)
  with ⟨∀ i < length ms2. ∀ V ∈ rv S (CFG-node ((mx#tl ms2)!i)).
    (fst (s1!(length msx + i))) V = (fst (s2!i)) V⟩
    ⟨hd(tl ms1) = targetnode a⟩
    ⟨length ms1 = length s1⟩ ⟨length s1 = length s2⟩[THEN sym] False
    ⟨tl ms2 = tl ms1⟩ ⟨length ms2 = length s2⟩
    ⟨V ∈ rv S (CFG-node (targetnode a))⟩
  show ?thesis by(fastforce simp:length-Suc-conv fun-upds-notin)
next
case False
from ⟨sourcenode a ∉ [HRB-slice S]CFG⟩ ⟨kind a = Q↔pf'⟩
have slice-kind S a = (λcf. True)↔p(λcf cf'. cf')
  by(rule slice-kind-Return)
from ⟨length ms2 = length s2⟩ have 1 < length ms2 by simp
with ⟨∀ i < length ms2. ∀ V ∈ rv S (CFG-node ((mx#tl ms2)!i)).
  (fst (s1!(length msx + i))) V = (fst (s2!i)) V⟩
  ⟨V ∈ rv S (CFG-node (hd (tl ms1)))⟩
  ⟨ms1' = tl ms1⟩ ⟨ms1' = []@hd ms1'#tl ms1'⟩
have fst cfx V = fst cfx' V apply auto
  apply(erule-tac x=1 in allE)
  by(cases tl ms1) auto
with ⟨∀ i < length ms2. ∀ V ∈ rv S (CFG-node ((mx#tl ms2)!i)).
  (fst (s1!(length msx + i))) V = (fst (s2!i)) V⟩
  ⟨hd(tl ms1) = targetnode a⟩
  ⟨length ms1 = length s1⟩ ⟨length s1 = length s2⟩[THEN sym] False
  ⟨tl ms2 = tl ms1⟩ ⟨length ms2 = length s2⟩
  ⟨V ∈ rv S (CFG-node (targetnode a))⟩
  ⟨V ∉ set (ParamDefs (targetnode a))⟩

```

$\langle \text{slice-kind } S \ a = (\lambda cf. \text{ True}) \leftrightarrow_p (\lambda cf \ cf'. \ cf') \rangle$
show $?thesis$ **by** $(\text{auto simp: fun-upds-notin})$
qed
qed
qed
with $\langle \text{hd}(tl \ ms_1) = \text{targetnode } a \rangle \langle \text{tl } ms_2 = \text{tl } ms_1 \rangle \langle ms_1' = \text{tl } ms_1 \rangle$
 $\langle \forall i < \text{length } ms_2. \forall V \in \text{rv } S \ (\text{CFG-node } ((\text{mx} \# \text{tl } ms_2)!i)) \rangle$
 $\langle \text{fst } (s_1!(\text{length } ms_2 + i)) \rangle V = \langle \text{fst } (s_2!i) \rangle V \rangle \langle \text{length } ms_1' = \text{length } s_1' \rangle$
 $\langle \text{length } ms_1 = \text{length } s_1 \rangle \langle \text{length } ms_2 = \text{length } s_2 \rangle \langle \text{length } s_1 = \text{length } s_2 \rangle \text{cf2}'$
have $\forall i < \text{length } ms_1'. \forall V \in \text{rv } S \ (\text{CFG-node } ((\text{hd } (tl \ ms_1) \# \text{tl } ms_1')!i)) \rangle$
 $\langle \text{fst } (s_1!(\text{length } [] + i)) \rangle V = \langle \text{fst } (\text{transfer } (\text{slice-kind } S \ a) \ s_2!i) \rangle V$
apply $(\text{case-tac } tl \ ms_1)$ **apply** auto
apply $(\text{cases } ms_2)$ **apply** auto
apply $(\text{case-tac } i)$ **apply** auto
by $(\text{erule-tac } x = \text{Suc}(\text{Suc } \text{nat}) \ \text{in } \text{allE}, \text{auto})$
with $\langle \forall m \in \text{set } ms_2. \text{valid-node } m \rangle \langle \forall m \in \text{set } ms_1'. \text{valid-node } m \rangle$
 $\langle \text{length } ms_2 = \text{length } s_2 \rangle \langle \text{length } s_1' = \text{length } (\text{transfer } (\text{slice-kind } S \ a) \ s_2) \rangle$
 $\langle \text{length } ms_1' = \text{length } s_1' \rangle \langle ms_1' = \text{tl } ms_1 \rangle \langle ms_1' = [] @ \text{hd } ms_1' \# \text{tl } ms_1' \rangle$
 $\langle \text{tl } ms_1 = [] @ \text{hd}(tl \ ms_1) \# \text{tl}(tl \ ms_1) \rangle$
 $\langle \text{get-proc } mx = \text{get-proc } (\text{hd } ms_2) \rangle$
 $\langle \forall m \in \text{set } (tl \ (tl \ ms_1)). \exists m'. \text{call-of-return-node } m \ m' \wedge m' \in \lfloor \text{HRB-slice } S \rfloor \text{CFG} \rangle$
 $\langle \forall m \in \text{set } (tl \ (tl \ ms_1)). \text{return-node } m \rangle$
 $\langle \forall i < \text{length } ms_1'. \text{snd } (s_1'!i) = \text{snd } (\text{transfer } (\text{slice-kind } S \ a) \ s_2!i) \rangle$
have $((ms_1', s_1'), (ms_1', \text{transfer } (\text{slice-kind } S \ a) \ s_2)) \in \text{WS } S$
by $(\text{auto intro!: WSI})$
with $\langle S, \text{slice-kind } S \vdash (ms_2, s_2) = \text{as}@[a] \Rightarrow (ms_1', \text{transfer } (\text{slice-kind } S \ a) \ s_2) \rangle$
show $?case$ **by** blast
qed
qed

1.13.5 The weak simulation

definition $is\text{-weak-sim} ::$

$((\text{'node list} \times ((\text{'var} \rightarrow \text{'val}) \times \text{'ret}) \text{list}) \times$
 $(\text{'node list} \times ((\text{'var} \rightarrow \text{'val}) \times \text{'ret}) \text{list})) \text{set} \Rightarrow \text{'node SDG-node set} \Rightarrow \text{bool}$

where $is\text{-weak-sim } R \ S \equiv$

$\forall ms_1 \ s_1 \ ms_2 \ s_2 \ ms_1' \ s_1' \ as.$

$((ms_1, s_1), (ms_2, s_2)) \in R \wedge S, \text{kind} \vdash (ms_1, s_1) = \text{as} \Rightarrow (ms_1', s_1') \wedge s_1' \neq []$
 $\rightarrow (\exists ms_2' \ s_2' \ as'. ((ms_1', s_1'), (ms_2', s_2')) \in R \wedge$
 $S, \text{slice-kind } S \vdash (ms_2, s_2) = \text{as}' \Rightarrow (ms_2', s_2'))$

lemma $WS\text{-weak-sim}:$

assumes $((ms_1, s_1), (ms_2, s_2)) \in \text{WS } S$

and $S, \text{kind} \vdash (ms_1, s_1) = \text{as} \Rightarrow (ms_1', s_1')$ **and** $s_1' \neq []$

obtains as' **where** $((ms_1', s_1'), (ms_1', \text{transfer } (\text{slice-kind } S \ (\text{last } as)) \ s_2)) \in \text{WS } S$

and $S, \text{slice-kind } S \vdash (ms_2, s_2) = \text{as}' @ [\text{last } as] \Rightarrow$

$(ms_1', \text{transfer } (\text{slice-kind } S \text{ (last as)}) s_2)$

proof(*atomize-elim*)
from $\langle S, \text{kind} \vdash (ms_1, s_1) = as \Rightarrow (ms_1', s_1') \rangle$ **obtain** $ms' s' as' a'$
where $S, \text{kind} \vdash (ms_1, s_1) = as' \Rightarrow_{\tau} (ms_1', s_1')$
and $S, \text{kind} \vdash (ms_1', s_1') - a' \rightarrow (ms_1', s_1')$ **and** $as = as'@[a']$
by(*fastforce elim:observable-moves.cases*)
from $\langle S, \text{kind} \vdash (ms_1', s_1') - a' \rightarrow (ms_1', s_1') \rangle$
have $\forall m \in \text{set } (tl \ ms_1'). \exists m'. \text{call-of-return-node } m \ m' \wedge m' \in \lfloor \text{HRB-slice } S \rfloor_{CFG}$
and $\forall n \in \text{set } (tl \ ms_1'). \text{return-node } n$ **and** $ms' \neq []$
by(*auto elim:observable-move.cases simp:call-of-return-node-def*)
from $\langle S, \text{kind} \vdash (ms_1, s_1) = as' \Rightarrow_{\tau} (ms_1', s_1') \rangle \langle ((ms_1, s_1), (ms_2, s_2)) \in WS \ S \rangle$
have $((ms_1', s_1'), (ms_2, s_2)) \in WS \ S$ **by**(*rule WS-silent-moves*)
with $\langle S, \text{kind} \vdash (ms_1', s_1') - a' \rightarrow (ms_1', s_1') \rangle \langle s_1' \neq [] \rangle$
obtain as'' **where** $((ms_1', s_1'), (ms_1', \text{transfer } (\text{slice-kind } S \ a') \ s_2)) \in WS \ S$
and $S, \text{slice-kind } S \vdash (ms_2, s_2) = as''@[a'] \Rightarrow$
 $(ms_1', \text{transfer } (\text{slice-kind } S \ a') \ s_2)$
by(*fastforce elim:WS-observable-move*)
with $\langle ((ms_1', s_1'), (ms_1', \text{transfer } (\text{slice-kind } S \ a') \ s_2)) \in WS \ S \rangle \langle as = as'@[a'] \rangle$
show $\exists as'. ((ms_1', s_1'), (ms_1', \text{transfer } (\text{slice-kind } S \ \text{last as}) \ s_2)) \in WS \ S \wedge$
 $S, \text{slice-kind } S \vdash (ms_2, s_2) = as'@[last \ as] \Rightarrow$
 $(ms_1', \text{transfer } (\text{slice-kind } S \ \text{last as}) \ s_2)$
by *fastforce*
qed

The following lemma states the correctness of static intraprocedural slicing:
the simulation $WS \ S$ is a desired weak simulation

theorem *WS-is-weak-sim:is-weak-sim* ($WS \ S$) S
by(*fastforce elim:WS-weak-sim simp:is-weak-sim-def*)

end

end

1.14 The fundamental property of slicing

theory *FundamentalProperty* **imports** *WeakSimulation SemanticsCFG* **begin**

context *SDG* **begin**

1.14.1 Auxiliary lemmas for moves in the graph

lemma *observable-set-stack-in-slice*:

$S, f \vdash (ms, s) - a \rightarrow (ms', s')$

$\Rightarrow \forall mx \in \text{set } (tl \ ms'). \exists mx'. \text{call-of-return-node } mx \ mx' \wedge mx' \in \lfloor \text{HRB-slice } S \rfloor_{CFG}$

$S \rfloor_{CFG}$

proof(*induct rule:observable-move.induct*)

```

case (observable-move-intra f a s s' ms S ms') thus ?case by simp
next
case (observable-move-call f a s s' Q r p fs a' ms S ms')
from ⟨valid-edge a⟩ ⟨valid-edge a'⟩ ⟨a' ∈ get-return-edges a⟩
have call-of-return-node (targetnode a') (sourcenode a)
  by(fastforce simp:return-node-def call-of-return-node-def)
with ⟨hd ms = sourcenode a⟩ ⟨hd ms ∈ [HRB-slice S]CFG⟩
  ⟨ms' = targetnode a # targetnode a' # tl ms⟩
  ⟨∀ mx ∈ set (tl ms). ∃ mx'. call-of-return-node mx mx' ∧ mx' ∈ [HRB-slice
S]CFG⟩
show ?case by fastforce
next
case (observable-move-return f a s s' Q p f' ms S ms')
thus ?case by(cases tl ms) auto
qed

```

lemma *silent-move-preserves-stacks*:

```

assumes S, f ⊢ (m # ms, s) -a →τ (m' # ms', s') and valid-call-list cs m
and ∀ i < length rs. rs!i ∈ get-return-edges (cs!i) and valid-return-list rs m
and length rs = length cs and ms = targetnodes rs
obtains cs' rs' where valid-node m' and valid-call-list cs' m'
and ∀ i < length rs'. rs'!i ∈ get-return-edges (cs'!i)
and valid-return-list rs' m' and length rs' = length cs'
and ms' = targetnodes rs' and upd-cs cs [a] = cs'
proof(atomize-elim)
from assms show ∃ cs' rs'. valid-node m' ∧ valid-call-list cs' m' ∧
  (∀ i < length rs'. rs'!i ∈ get-return-edges (cs'!i)) ∧
  valid-return-list rs' m' ∧ length rs' = length cs' ∧ ms' = targetnodes rs' ∧
  upd-cs cs [a] = cs'
proof(induct S f m # ms s a m' # ms' s' rule:silent-move.induct)
case (silent-move-intra f a s s' nc)
from ⟨hd (m # ms) = sourcenode a⟩ have m = sourcenode a by simp
from ⟨m' # ms' = targetnode a # tl (m # ms)⟩
have [simp]: m' = targetnode a ms' = ms by simp-all
from ⟨valid-edge a⟩ have valid-node m' by simp
moreover
from ⟨valid-edge a⟩ ⟨intra-kind (kind a)⟩
have get-proc (sourcenode a) = get-proc (targetnode a) by(rule get-proc-intra)
from ⟨valid-call-list cs m⟩ ⟨m = sourcenode a⟩
  ⟨get-proc (sourcenode a) = get-proc (targetnode a)⟩
have valid-call-list cs m'
  apply(clarsimp simp:valid-call-list-def)
  apply(erule-tac x=cs' in allE)
  apply(erule-tac x=c in allE)
  by(auto split:list.split)
moreover
from ⟨valid-return-list rs m⟩ ⟨m = sourcenode a⟩
  ⟨get-proc (sourcenode a) = get-proc (targetnode a)⟩

```

```

have valid-return-list rs m'
  apply(clarsimp simp:valid-return-list-def)
  apply(erule-tac x=cs' in allE) apply clarsimp
  by(case-tac cs') auto
moreover
from ⟨intra-kind (kind a)⟩ have upd-cs cs [a] = cs
  by(fastforce simp:intra-kind-def)
ultimately show ?case using ⟨∀ i < length rs. rs ! i ∈ get-return-edges (cs ! i)⟩
  ⟨length rs = length cs⟩ ⟨ms = targetnodes rs⟩
  apply(rule-tac x=cs in exI)
  apply(rule-tac x=rs in exI)
  by clarsimp
next
case (silent-move-call f a s s' Q r p fs a' S)
from ⟨hd (m # ms) = sourcenode a⟩
  ⟨m' # ms' = targetnode a # targetnode a' # tl (m # ms)⟩
have [simp]:m = sourcenode a m' = targetnode a
  ms' = targetnode a' # tl (m # ms)
  by simp-all
from ⟨valid-edge a⟩ have valid-node m' by simp
moreover
from ⟨valid-edge a⟩ ⟨kind a = Q:r↔pfs⟩ have get-proc (targetnode a) = p
  by(rule get-proc-call)
with ⟨valid-call-list cs m⟩ ⟨valid-edge a⟩ ⟨kind a = Q:r↔pfs⟩ ⟨m = sourcenode
a)
have valid-call-list (a # cs) (targetnode a)
  apply(clarsimp simp:valid-call-list-def)
  apply(case-tac cs') apply auto
  apply(erule-tac x=list in allE)
  by(case-tac list)(auto simp:sourcenodes-def)
moreover
from ⟨∀ i < length rs. rs ! i ∈ get-return-edges (cs ! i)⟩ ⟨a' ∈ get-return-edges a⟩
have ∀ i < length (a'#rs). (a'#rs) ! i ∈ get-return-edges ((a#cs) ! i)
  by auto(case-tac i,auto)
moreover
from ⟨valid-edge a⟩ ⟨a' ∈ get-return-edges a⟩ have valid-edge a'
  by(rule get-return-edges-valid)
from ⟨valid-edge a⟩ ⟨kind a = Q:r↔pfs⟩ ⟨a' ∈ get-return-edges a⟩
obtain Q' f' where kind a' = Q'↔pf' by(fastforce dest!:call-return-edges)
from ⟨valid-edge a'⟩ ⟨kind a' = Q'↔pf'⟩ have get-proc (sourcenode a') = p
  by(rule get-proc-return)
from ⟨valid-edge a⟩ ⟨a' ∈ get-return-edges a⟩
have get-proc (sourcenode a) = get-proc (targetnode a')
  by(rule get-proc-get-return-edge)
with ⟨valid-return-list rs m⟩ ⟨valid-edge a'⟩ ⟨kind a' = Q'↔pf'⟩
  ⟨get-proc (sourcenode a') = p⟩ ⟨get-proc (targetnode a) = p⟩ ⟨m = sourcenode
a)
have valid-return-list (a'#rs) (targetnode a)
  apply(clarsimp simp:valid-return-list-def)

```



```

    apply(case-tac cs') apply auto
    apply(erule-tac x=list in allE)
    by(case-tac list)(auto simp:targetnodes-def)
  moreover
  from ⟨length rs = length cs⟩ have length (a'#rs) = length (a#cs) by simp
  moreover
  from ⟨ms = targetnodes rs⟩ have targetnode a' # ms = targetnodes (a' # rs)
    by(simp add:targetnodes-def)
  moreover
  from ⟨kind a = Q:r↔pfs⟩ have upd-cs cs [a] = a#cs by simp
  ultimately show ?case
    apply(rule-tac x=a#cs in exI)
    apply(rule-tac x=a'#rs in exI)
    by clarsimp
next
case (silent-move-return f a s s' Q p f' S)
from ⟨hd (m # ms) = sourcenode a⟩
  ⟨hd (tl (m # ms)) = targetnode a⟩ ⟨m' # ms' = tl (m # ms)⟩ [symmetric]
have [simp]:m = sourcenode a m' = targetnode a by simp-all
from ⟨length (m # ms) = length s⟩ ⟨length s = Suc (length s')⟩ ⟨s' ≠ []⟩
  ⟨hd (tl (m # ms)) = targetnode a⟩ ⟨m' # ms' = tl (m # ms)⟩
have ms = targetnode a # ms'
  by(cases ms) auto
with ⟨ms = targetnodes rs⟩
obtain r' rs' where rs = r' # rs'
  and targetnode a = targetnode r' and ms' = targetnodes rs'
  by(cases rs)(auto simp:targetnodes-def)
moreover
from ⟨rs = r' # rs'⟩ ⟨length rs = length cs⟩ obtain c' cs' where cs = c' #
cs'
  and length rs' = length cs' by(cases cs) auto
moreover
from ⟨∀ i<length rs. rs ! i ∈ get-return-edges (cs ! i)⟩
  ⟨rs = r' # rs'⟩ ⟨cs = c' # cs'⟩
have ∀ i<length rs'. rs' ! i ∈ get-return-edges (cs' ! i)
  and r' ∈ get-return-edges c' by auto
moreover
from ⟨valid-edge a⟩ have valid-node (targetnode a) by simp
moreover
from ⟨valid-call-list cs m⟩ ⟨cs = c' # cs'⟩
obtain p' Q' r fs' where valid-edge c' and kind c' = Q':r↔p'fs'
  and p' = get-proc m
  apply(auto simp:valid-call-list-def)
  by(erule-tac x=[] in allE) auto
from ⟨valid-edge a⟩ ⟨kind a = Q↔pf'⟩
have get-proc (sourcenode a) = p by(rule get-proc-return)
with ⟨p' = get-proc m⟩ have [simp]:p' = p by simp
from ⟨valid-edge c'⟩ ⟨kind c' = Q':r↔p'fs'⟩
have get-proc (targetnode c') = p by(fastforce intro:get-proc-call)

```

from $\langle \text{valid-edge } c' \rangle \langle r' \in \text{get-return-edges } c' \rangle$ **have** $\text{valid-edge } r'$
by(rule get-return-edges-valid)
from $\langle \text{valid-edge } c' \rangle \langle \text{kind } c' = Q':r \leftrightarrow_p fs' \rangle \langle r' \in \text{get-return-edges } c' \rangle$
obtain $Q'' f''$ **where** $\text{kind } r' = Q'' \leftrightarrow_p f''$ **by**(fastforce dest!:call-return-edges)
with $\langle \text{valid-edge } r' \rangle$ **have** $\text{get-proc } (\text{sourcenode } r') = p$ **by**(rule get-proc-return)
from $\langle \text{valid-edge } r' \rangle \langle \text{kind } r' = Q'' \leftrightarrow_p f'' \rangle$ **have** $\text{method-exit } (\text{sourcenode } r')$
by(fastforce simp:method-exit-def)
from $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q \leftrightarrow_p f' \rangle$ **have** $\text{method-exit } (\text{sourcenode } a)$
by(fastforce simp:method-exit-def)
with $\langle \text{method-exit } (\text{sourcenode } r') \rangle \langle \text{get-proc } (\text{sourcenode } r') = p \rangle$
 $\langle \text{get-proc } (\text{sourcenode } a) = p \rangle$
have $\text{sourcenode } a = \text{sourcenode } r'$ **by**(fastforce intro:method-exit-unique)
with $\langle \text{valid-edge } a \rangle \langle \text{valid-edge } r' \rangle \langle \text{targetnode } a = \text{targetnode } r' \rangle$
have $a = r'$ **by**(fastforce intro:edge-det)
from $\langle \text{valid-edge } c' \rangle \langle r' \in \text{get-return-edges } c' \rangle \langle \text{targetnode } a = \text{targetnode } r' \rangle$
have $\text{get-proc } (\text{sourcenode } c') = \text{get-proc } (\text{targetnode } a)$
by(fastforce intro:get-proc-get-return-edge)
from $\langle \text{valid-call-list } cs \ m \rangle \langle cs = c' \ \# \ cs' \rangle$
 $\langle \text{get-proc } (\text{sourcenode } c') = \text{get-proc } (\text{targetnode } a) \rangle$
have $\text{valid-call-list } cs' (\text{targetnode } a)$
apply(clarsimp simp:valid-call-list-def)
apply(erule-tac $x=c' \ \# \ cs'$ **in** allE)
by(case-tac cs')(auto simp:sourcenodes-def)
moreover
from $\langle \text{valid-return-list } rs \ m \rangle \langle rs = r' \ \# \ rs' \rangle \langle \text{targetnode } a = \text{targetnode } r' \rangle$
have $\text{valid-return-list } rs' (\text{targetnode } a)$
apply(clarsimp simp:valid-return-list-def)
apply(erule-tac $x=r' \ \# \ rs'$ **in** allE)
by(case-tac rs')(auto simp:targetnodes-def)
moreover
from $\langle \text{kind } a = Q \leftrightarrow_p f' \rangle \langle cs = c' \ \# \ cs' \rangle$ **have** $\text{upd-cs } cs \ [a] = cs'$ **by** simp
ultimately show ?case
apply(rule-tac $x=cs'$ **in** exI)
apply(rule-tac $x=rs'$ **in** exI)
by clarsimp
qed
qed

lemma *silent-moves-preserves-stacks*:
assumes $S, f \vdash (m \ \# \ ms, s) = as \Rightarrow_{\tau} (m' \ \# \ ms', s')$
and $\text{valid-node } m$ **and** $\text{valid-call-list } cs \ m$
and $\forall i < \text{length } rs. rs!i \in \text{get-return-edges } (cs!i)$ **and** $\text{valid-return-list } rs \ m$
and $\text{length } rs = \text{length } cs$ **and** $ms = \text{targetnodes } rs$
obtains $cs' \ rs'$ **where** $\text{valid-node } m'$ **and** $\text{valid-call-list } cs' \ m'$
and $\forall i < \text{length } rs'. rs'!i \in \text{get-return-edges } (cs'!i)$
and $\text{valid-return-list } rs' \ m'$ **and** $\text{length } rs' = \text{length } cs'$
and $ms' = \text{targetnodes } rs'$ **and** $\text{upd-cs } cs \ as = cs'$
proof(atomize-elim)

from *assms* **show** $\exists cs' rs'. \text{valid-node } m' \wedge \text{valid-call-list } cs' m' \wedge$
 $(\forall i < \text{length } rs'. rs' ! i \in \text{get-return-edges } (cs' ! i)) \wedge$
 $\text{valid-return-list } rs' m' \wedge \text{length } rs' = \text{length } cs' \wedge ms' = \text{targetnodes } rs' \wedge$
 $\text{upd-cs } cs \text{ as} = cs'$
proof (*induct* $S f m \# ms s \text{ as } m' \# ms' s'$
arbitrary: $m \ ms \ cs \ rs$ *rule*: *silent-moves.induct*)
case (*silent-moves-Nil* $s \ n_c \ f$)
thus *?case*
apply (*rule-tac* $x = cs$ **in** *exI*)
apply (*rule-tac* $x = rs$ **in** *exI*)
by *clarsimp*
next
case (*silent-moves-Cons* $S f s a \ msx'' s'' \text{ as } sx'$)
note $IH = \langle \wedge m \ ms \ cs \ rs. \llbracket msx'' = m \ \# \ ms; \text{valid-node } m; \text{valid-call-list } cs \ m; \$
 $\forall i < \text{length } rs. rs' ! i \in \text{get-return-edges } (cs' ! i);$
 $\text{valid-return-list } rs \ m; \text{length } rs = \text{length } cs; ms = \text{targetnodes } rs \rrbracket$
 $\implies \exists cs' rs'. \text{valid-node } m' \wedge \text{valid-call-list } cs' m' \wedge$
 $(\forall i < \text{length } rs'. rs' ! i \in \text{get-return-edges } (cs' ! i)) \wedge$
 $\text{valid-return-list } rs' m' \wedge \text{length } rs' = \text{length } cs' \wedge ms' = \text{targetnodes } rs' \wedge$
 $\text{upd-cs } cs \text{ as} = cs' \rangle$
from $\langle S, f \vdash (m \ \# \ ms, s) -a \rightarrow_{\tau} (msx'', s'') \rangle$
obtain $m'' \ ms''$ **where** $msx'' = m'' \ \# \ ms''$
by (*cases* msx'') (*auto elim*: *silent-move.cases*)
with $\langle S, f \vdash (m \ \# \ ms, s) -a \rightarrow_{\tau} (msx'', s'') \rangle$ $\langle \text{valid-call-list } cs \ m \rangle$
 $\langle \forall i < \text{length } rs. rs' ! i \in \text{get-return-edges } (cs' ! i) \rangle$ $\langle \text{valid-return-list } rs \ m \rangle$
 $\langle \text{length } rs = \text{length } cs \rangle$ $\langle ms = \text{targetnodes } rs \rangle$
obtain $cs'' \ rs''$ **where** *hyps*: $\text{valid-node } m'' \ \text{valid-call-list } cs'' \ m''$
 $\forall i < \text{length } rs''. rs'' ! i \in \text{get-return-edges } (cs'' ! i)$
 $\text{valid-return-list } rs'' \ m'' \ \text{length } rs'' = \text{length } cs''$
 $ms'' = \text{targetnodes } rs''$ **and** $\text{upd-cs } cs \ [a] = cs''$
by (*auto elim!*: *silent-move-preserves-stacks*)
from $IH[OF - \text{hyps}] \langle msx'' = m'' \ \# \ ms'' \rangle$
obtain $cs' \ rs'$ **where** *results*: $\text{valid-node } m' \ \text{valid-call-list } cs' \ m'$
 $\forall i < \text{length } rs'. rs' ! i \in \text{get-return-edges } (cs' ! i)$
 $\text{valid-return-list } rs' \ m' \ \text{length } rs' = \text{length } cs' \ ms' = \text{targetnodes } rs'$
and $\text{upd-cs } cs'' \text{ as} = cs'$ **by** *blast*
from $\langle \text{upd-cs } cs \ [a] = cs'' \rangle$ $\langle \text{upd-cs } cs'' \text{ as} = cs' \rangle$
have $\text{upd-cs } cs \ ([a] \ @ \text{ as}) = cs'$ **by** (*rule upd-cs-Append*)
with *results* **show** *?case*
apply (*rule-tac* $x = cs'$ **in** *exI*)
apply (*rule-tac* $x = rs'$ **in** *exI*)
by *clarsimp*
qed
qed

lemma *observable-move-preserves-stacks*:

assumes $S, f \vdash (m \ \# \ ms, s) -a \rightarrow (m' \ \# \ ms', s')$ **and** *valid-call-list* $cs \ m$
and $\forall i < \text{length } rs. rs' ! i \in \text{get-return-edges } (cs' ! i)$ **and** *valid-return-list* $rs \ m$

and $\text{length } rs = \text{length } cs$ **and** $ms = \text{targetnodes } rs$
obtains $cs' rs'$ **where** $\text{valid-node } m'$ **and** $\text{valid-call-list } cs' m'$
and $\forall i < \text{length } rs'. rs' ! i \in \text{get-return-edges } (cs' ! i)$
and $\text{valid-return-list } rs' m'$ **and** $\text{length } rs' = \text{length } cs'$
and $ms' = \text{targetnodes } rs'$ **and** $\text{upd-cs } cs [a] = cs'$

proof(*atomize-elim*)

from *assms* **show** $\exists cs' rs'. \text{valid-node } m' \wedge \text{valid-call-list } cs' m' \wedge$
 $(\forall i < \text{length } rs'. rs' ! i \in \text{get-return-edges } (cs' ! i)) \wedge$
 $\text{valid-return-list } rs' m' \wedge \text{length } rs' = \text{length } cs' \wedge ms' = \text{targetnodes } rs' \wedge$
 $\text{upd-cs } cs [a] = cs'$

proof(*induct S f m # ms s a m' # ms' s' rule:observable-move.induct*)

case (*observable-move-intra f a s s' n_c*)
from $\langle \text{hd } (m \# ms) = \text{sourcenode } a \rangle$ **have** $m = \text{sourcenode } a$ **by** *simp*
from $\langle m' \# ms' = \text{targetnode } a \# \text{tl } (m \# ms) \rangle$
have $[simp]: m' = \text{targetnode } a$ $ms' = ms$ **by** *simp-all*
from $\langle \text{valid-edge } a \rangle$ **have** $\text{valid-node } m'$ **by** *simp*
moreover
from $\langle \text{valid-edge } a \rangle$ $\langle \text{intra-kind } (kind \ a) \rangle$
have $\text{get-proc } (\text{sourcenode } a) = \text{get-proc } (\text{targetnode } a)$ **by**(*rule get-proc-intra*)
from $\langle \text{valid-call-list } cs \ m \rangle$ $\langle m = \text{sourcenode } a \rangle$
 $\langle \text{get-proc } (\text{sourcenode } a) = \text{get-proc } (\text{targetnode } a) \rangle$
have $\text{valid-call-list } cs \ m'$
apply(*clarsimp simp:valid-call-list-def*)
apply(*erule-tac x=cs' in allE*)
apply(*erule-tac x=c in allE*)
by(*auto split:list.split*)

moreover

from $\langle \text{valid-return-list } rs \ m \rangle$ $\langle m = \text{sourcenode } a \rangle$
 $\langle \text{get-proc } (\text{sourcenode } a) = \text{get-proc } (\text{targetnode } a) \rangle$
have $\text{valid-return-list } rs \ m'$
apply(*clarsimp simp:valid-return-list-def*)
apply(*erule-tac x=cs' in allE*) **apply** *clarsimp*
by(*case-tac cs' auto*)

moreover

from $\langle \text{intra-kind } (kind \ a) \rangle$ **have** $\text{upd-cs } cs [a] = cs$
by(*fastforce simp:intra-kind-def*)

ultimately show *?case* **using** $\langle \forall i < \text{length } rs. rs' ! i \in \text{get-return-edges } (cs' ! i) \rangle$
 $\langle \text{length } rs = \text{length } cs \rangle$ $\langle ms = \text{targetnodes } rs \rangle$
apply(*rule-tac x=cs in exI*)
apply(*rule-tac x=rs in exI*)
by *clarsimp*

next

case (*observable-move-call f a s s' Q r p fs a' S*)
from $\langle \text{hd } (m \# ms) = \text{sourcenode } a \rangle$
 $\langle m' \# ms' = \text{targetnode } a \# \text{targetnode } a' \# \text{tl } (m \# ms) \rangle$
have $[simp]: m = \text{sourcenode } a$ $m' = \text{targetnode } a$
 $ms' = \text{targetnode } a' \# \text{tl } (m \# ms)$
by *simp-all*
from $\langle \text{valid-edge } a \rangle$ **have** $\text{valid-node } m'$ **by** *simp*

```

moreover
from  $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q:r \hookrightarrow pfs \rangle$  have  $\text{get-proc } (\text{targetnode } a) = p$ 
  by  $(\text{rule } \text{get-proc-call})$ 
with  $\langle \text{valid-call-list } cs \ m \rangle \langle \text{valid-edge } a \rangle \langle \text{kind } a = Q:r \hookrightarrow pfs \rangle \langle m = \text{sourcenode}$ 
 $a \rangle$ 
have  $\text{valid-call-list } (a \ \# \ cs) \ (\text{targetnode } a)$ 
  apply  $(\text{clarsimp } \text{simp}:\text{valid-call-list-def})$ 
  apply  $(\text{case-tac } cs')$  apply  $\text{auto}$ 
  apply  $(\text{erule-tac } x=\text{list} \ \text{in} \ \text{all}E)$ 
  by  $(\text{case-tac } list)(\text{auto } \text{simp}:\text{sourcenodes-def})$ 
moreover
from  $\langle \forall i < \text{length } rs. rs \ ! \ i \in \text{get-return-edges } (cs \ ! \ i) \rangle \langle a' \in \text{get-return-edges } a \rangle$ 
have  $\forall i < \text{length } (a' \ \# \ rs). (a' \ \# \ rs) \ ! \ i \in \text{get-return-edges } ((a \ \# \ cs) \ ! \ i)$ 
  by  $\text{auto}(\text{case-tac } i, \text{auto})$ 
moreover
from  $\langle \text{valid-edge } a \rangle \langle a' \in \text{get-return-edges } a \rangle$  have  $\text{valid-edge } a'$ 
  by  $(\text{rule } \text{get-return-edges-valid})$ 
from  $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q:r \hookrightarrow pfs \rangle \langle a' \in \text{get-return-edges } a \rangle$ 
obtain  $Q' \ f'$  where  $\text{kind } a' = Q' \ \leftarrow \ p f'$  by  $(\text{fastforce } \text{dest}!\text{:call-return-edges})$ 
from  $\langle \text{valid-edge } a' \rangle \langle \text{kind } a' = Q' \ \leftarrow \ p f' \rangle$  have  $\text{get-proc } (\text{sourcenode } a') = p$ 
  by  $(\text{rule } \text{get-proc-return})$ 
from  $\langle \text{valid-edge } a \rangle \langle a' \in \text{get-return-edges } a \rangle$ 
have  $\text{get-proc } (\text{sourcenode } a) = \text{get-proc } (\text{targetnode } a')$ 
  by  $(\text{rule } \text{get-proc-get-return-edge})$ 
with  $\langle \text{valid-return-list } rs \ m \rangle \langle \text{valid-edge } a' \rangle \langle \text{kind } a' = Q' \ \leftarrow \ p f' \rangle$ 
 $\langle \text{get-proc } (\text{sourcenode } a') = p \rangle \langle \text{get-proc } (\text{targetnode } a) = p \rangle \langle m = \text{sourcenode}$ 
 $a \rangle$ 
have  $\text{valid-return-list } (a' \ \# \ rs) \ (\text{targetnode } a)$ 
  apply  $(\text{clarsimp } \text{simp}:\text{valid-return-list-def})$ 
  apply  $(\text{case-tac } cs')$  apply  $\text{auto}$ 
  apply  $(\text{erule-tac } x=\text{list} \ \text{in} \ \text{all}E)$ 
  by  $(\text{case-tac } list)(\text{auto } \text{simp}:\text{targetnodes-def})$ 
moreover
from  $\langle \text{length } rs = \text{length } cs \rangle$  have  $\text{length } (a' \ \# \ rs) = \text{length } (a \ \# \ cs)$  by  $\text{simp}$ 
moreover
from  $\langle ms = \text{targetnodes } rs \rangle$  have  $\text{targetnode } a' \ \# \ ms = \text{targetnodes } (a' \ \# \ rs)$ 
  by  $(\text{simp } \text{add}:\text{targetnodes-def})$ 
moreover
from  $\langle \text{kind } a = Q:r \hookrightarrow pfs \rangle$  have  $\text{upd-cs } cs \ [a] = a \ \# \ cs$  by  $\text{simp}$ 
ultimately show  $?case$ 
  apply  $(\text{rule-tac } x=a \ \# \ cs \ \text{in} \ \text{ex}I)$ 
  apply  $(\text{rule-tac } x=a' \ \# \ rs \ \text{in} \ \text{ex}I)$ 
  by  $\text{clarsimp}$ 
next
case  $(\text{observable-move-return } f \ a \ s \ s' \ Q \ p \ f' \ S)$ 
from  $\langle \text{hd } (m \ \# \ ms) = \text{sourcenode } a \rangle$ 
 $\langle \text{hd } (\text{tl } (m \ \# \ ms)) = \text{targetnode } a \rangle \langle m' \ \# \ ms' = \text{tl } (m \ \# \ ms) \rangle$   $[symmetric]$ 
have  $[simp]: m = \text{sourcenode } a \ m' = \text{targetnode } a$  by  $\text{simp-all}$ 
from  $\langle \text{length } (m \ \# \ ms) = \text{length } s \rangle \langle \text{length } s = \text{Suc } (\text{length } s') \rangle \langle s' \neq [] \rangle$ 

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```

  ⟨hd (tl (m # ms)) = targetnode a⟩ ⟨m' # ms' = tl (m # ms)⟩
have ms = targetnode a # ms'
  by(cases ms) auto
with ⟨ms = targetnodes rs⟩
obtain r' rs' where rs = r' # rs'
  and targetnode a = targetnode r' and ms' = targetnodes rs'
  by(cases rs)(auto simp:targetnodes-def)
moreover
from ⟨rs = r' # rs'⟩ ⟨length rs = length cs⟩ obtain c' cs' where cs = c' #
cs'
  and length rs' = length cs' by(cases cs) auto
moreover
from ⟨∀ i < length rs. rs ! i ∈ get-return-edges (cs ! i)⟩
  ⟨rs = r' # rs'⟩ ⟨cs = c' # cs'⟩
have ∀ i < length rs'. rs' ! i ∈ get-return-edges (cs' ! i)
  and r' ∈ get-return-edges c' by auto
moreover
from ⟨valid-edge a⟩ have valid-node (targetnode a) by simp
moreover
from ⟨valid-call-list cs m⟩ ⟨cs = c' # cs'⟩
obtain p' Q' r fs' where valid-edge c' and kind c' = Q':r↔p'fs'
  and p' = get-proc m
  apply(auto simp:valid-call-list-def)
  by(erule-tac x=[] in allE) auto
from ⟨valid-edge a⟩ ⟨kind a = Q↔p'f'⟩
have get-proc (sourcenode a) = p by(rule get-proc-return)
with ⟨p' = get-proc m⟩ have [simp]:p' = p by simp
from ⟨valid-edge c'⟩ ⟨kind c' = Q':r↔p'fs'⟩
have get-proc (targetnode c') = p by(fastforce intro:get-proc-call)
from ⟨valid-edge c'⟩ ⟨r' ∈ get-return-edges c'⟩ have valid-edge r'
  by(rule get-return-edges-valid)
from ⟨valid-edge c'⟩ ⟨kind c' = Q':r↔p'fs'⟩ ⟨r' ∈ get-return-edges c'⟩
obtain Q'' f'' where kind r' = Q''↔p'f'' by(fastforce dest!:call-return-edges)
with ⟨valid-edge r'⟩ have get-proc (sourcenode r') = p by(rule get-proc-return)
from ⟨valid-edge r'⟩ ⟨kind r' = Q''↔p'f''⟩ have method-exit (sourcenode r')
  by(fastforce simp:method-exit-def)
from ⟨valid-edge a⟩ ⟨kind a = Q↔p'f'⟩ have method-exit (sourcenode a)
  by(fastforce simp:method-exit-def)
with ⟨method-exit (sourcenode r')⟩ ⟨get-proc (sourcenode r') = p⟩
  ⟨get-proc (sourcenode a) = p⟩
have sourcenode a = sourcenode r' by(fastforce intro:method-exit-unique)
with ⟨valid-edge a⟩ ⟨valid-edge r'⟩ ⟨targetnode a = targetnode r'⟩
have a = r' by(fastforce intro:edge-det)
from ⟨valid-edge c'⟩ ⟨r' ∈ get-return-edges c'⟩ ⟨targetnode a = targetnode r'⟩
have get-proc (sourcenode c') = get-proc (targetnode a)
  by(fastforce intro:get-proc-get-return-edge)
from ⟨valid-call-list cs m⟩ ⟨cs = c' # cs'⟩
  ⟨get-proc (sourcenode c') = get-proc (targetnode a)⟩
have valid-call-list cs' (targetnode a)

```

```

  apply(clarsimp simp:valid-call-list-def)
  apply(erule-tac x=c' # cs' in allE)
  by(case-tac cs')(auto simp:sourcenodes-def)
moreover
from ⟨valid-return-list rs m⟩ ⟨rs = r' # rs'⟩ ⟨targetnode a = targetnode r'⟩
have valid-return-list rs' (targetnode a)
  apply(clarsimp simp:valid-return-list-def)
  apply(erule-tac x=r' # cs' in allE)
  by(case-tac cs')(auto simp:targetnodes-def)
moreover
from ⟨kind a = Q↔pf⟩ ⟨cs = c' # cs'⟩ have upd-cs cs [a] = cs' by simp
ultimately show ?case
  apply(rule-tac x=cs' in exI)
  apply(rule-tac x=rs' in exI)
  by clarsimp
qed
qed

```

lemma *observable-moves-preserves-stack*:

```

assumes S,f ⊢ (m#ms,s) = as ⇒ (m'#ms',s')
and valid-node m and valid-call-list cs m
and ∀ i < length rs. rs!i ∈ get-return-edges (cs!i) and valid-return-list rs m
and length rs = length cs and ms = targetnodes rs
obtains cs' rs' where valid-node m' and valid-call-list cs' m'
and ∀ i < length rs'. rs'!i ∈ get-return-edges (cs'!i)
and valid-return-list rs' m' and length rs' = length cs'
and ms' = targetnodes rs' and upd-cs cs as = cs'
proof(atomize-elim)
from ⟨S,f ⊢ (m#ms,s) = as ⇒ (m'#ms',s')⟩ obtain msx s'' as' a'
  where as = as'@[a'] and S,f ⊢ (m#ms,s) = as' ⇒τ (msx,s'')
  and S,f ⊢ (msx,s'') -a'→ (m'#ms',s')
  by(fastforce elim:observable-moves.cases)
from ⟨S,f ⊢ (msx,s'') -a'→ (m'#ms',s')⟩ obtain m'' ms''
  where [simp]:msx = m''#ms'' by(cases msx)(auto elim:observable-move.cases)
from ⟨S,f ⊢ (m#ms,s) = as' ⇒τ (msx,s'')⟩ ⟨valid-node m⟩ ⟨valid-call-list cs m⟩
  ⟨∀ i < length rs. rs!i ∈ get-return-edges (cs!i)⟩ ⟨valid-return-list rs m⟩
  ⟨length rs = length cs⟩ ⟨ms = targetnodes rs⟩
obtain cs'' rs'' where valid-node m'' and valid-call-list cs'' m''
  and ∀ i < length rs''. rs''!i ∈ get-return-edges (cs''!i)
  and valid-return-list rs'' m'' and length rs'' = length cs''
  and ms'' = targetnodes rs'' and upd-cs cs as' = cs''
  by(auto elim!:silent-moves-preserves-stacks)
with ⟨S,f ⊢ (msx,s'') -a'→ (m'#ms',s')⟩
obtain cs' rs' where results:valid-node m' valid-call-list cs' m'
  ∀ i < length rs'. rs'!i ∈ get-return-edges (cs'!i)
  valid-return-list rs' m' length rs' = length cs' ms' = targetnodes rs'
  and upd-cs cs'' [a'] = cs'
  by(auto elim!:observable-move-preserves-stacks)

```

from $\langle \text{upd-cs } cs \text{ as}' = cs'' \rangle \langle \text{upd-cs } cs'' [a'] = cs' \rangle$
have $\text{upd-cs } cs \text{ (as}'@[a'] = cs' \text{ by (rule upd-cs-Append))}$
with $\langle as = as''@[a'] \rangle \text{ results}$
show $\exists cs' rs'. \text{valid-node } m' \wedge \text{valid-call-list } cs' m' \wedge$
 $(\forall i < \text{length } rs'. rs' ! i \in \text{get-return-edges } (cs' ! i)) \wedge$
 $\text{valid-return-list } rs' m' \wedge \text{length } rs' = \text{length } cs' \wedge ms' = \text{targetnodes } rs' \wedge$
 $\text{upd-cs } cs \text{ as} = cs'$
apply(rule-tac $x=cs'$ in exI)
apply(rule-tac $x=rs'$ in exI)
by *clarsimp*
qed

lemma *silent-moves-slpa-path*:

$\llbracket S, f \vdash (m \# ms'' @ ms, s) = as \Rightarrow_{\tau} (m' \# ms', s') ; \text{valid-node } m ; \text{valid-call-list } cs \ m ;$
 $\forall i < \text{length } rs. rs' ! i \in \text{get-return-edges } (cs' ! i) ; \text{valid-return-list } rs \ m ;$
 $\text{length } rs = \text{length } cs ; ms'' = \text{targetnodes } rs ;$
 $\forall mx \in \text{set } ms. \exists mx'. \text{call-of-return-node } mx \ mx' \wedge mx' \in \llbracket \text{HRB-slice } S \rrbracket_{CFG} ;$
 $ms'' \neq [] \longrightarrow (\exists mx'. \text{call-of-return-node } (\text{last } ms'') \ mx' \wedge mx' \notin \llbracket \text{HRB-slice } S \rrbracket_{CFG}) ;$

$\forall mx \in \text{set } ms'. \exists mx'. \text{call-of-return-node } mx \ mx' \wedge mx' \in \llbracket \text{HRB-slice } S \rrbracket_{CFG}$
 $\implies \text{same-level-path-aux } cs \ as \wedge \text{upd-cs } cs \ as = [] \wedge m -as \rightarrow^* m' \wedge ms = ms'$

proof(induct $S \ f \ m \# ms'' @ ms \ s \ as \ m' \# ms' \ s'$ arbitrary: $m \ ms'' \ ms \ cs \ rs$
rule:silent-moves.induct)

case (*silent-moves-Nil* $sx \ S \ f$) **thus** ?*case*

apply(cases ms'' *rule:rev-cases*) **apply**(auto *intro:empty-path simp:targetnodes-def*)
by(cases rs *rule:rev-cases, auto*)+

next

case (*silent-moves-Cons* $S \ f \ sx \ a \ msx' \ sx' \ as \ sx''$)

thus ?*case*

proof(induct - - $m \# ms'' @ ms$ - - - *rule:silent-move.induct*)

case (*silent-move-intra* $f \ a \ s \ s' \ S \ msx'$)

note $IH = \langle \bigwedge m \ ms'' \ ms \ cs \ rs. \llbracket msx' = m \# ms'' @ ms ; \text{valid-node } m ;$

$\text{valid-call-list } cs \ m ; \forall i < \text{length } rs. rs' ! i \in \text{get-return-edges } (cs' ! i) ;$

$\text{valid-return-list } rs \ m ; \text{length } rs = \text{length } cs ; ms'' = \text{targetnodes } rs ;$

$\forall mx \in \text{set } ms. \exists mx'. \text{call-of-return-node } mx \ mx' \wedge mx' \in \llbracket \text{HRB-slice } S \rrbracket_{CFG} ;$

$ms'' \neq [] \longrightarrow$

$(\exists mx'. \text{call-of-return-node } (\text{last } ms'') \ mx' \wedge mx' \notin \llbracket \text{HRB-slice } S \rrbracket_{CFG}) ;$

$\forall mx \in \text{set } ms'. \exists mx'. \text{call-of-return-node } mx \ mx' \wedge mx' \in \llbracket \text{HRB-slice } S \rrbracket_{CFG}$

$\implies \text{same-level-path-aux } cs \ as \wedge \text{upd-cs } cs \ as = [] \wedge m -as \rightarrow^* m' \wedge ms =$

ms'

note $\text{callstack} = \langle \forall mx \in \text{set } ms. \exists mx'. \text{call-of-return-node } mx \ mx' \wedge$
 $mx' \in \llbracket \text{HRB-slice } S \rrbracket_{CFG}$

note $\text{callstack}'' = \langle ms'' \neq [] \longrightarrow$

$(\exists mx'. \text{call-of-return-node } (\text{last } ms'') \ mx' \wedge mx' \notin \llbracket \text{HRB-slice } S \rrbracket_{CFG}) \rangle$

note $\text{callstack}' = \langle \forall mx \in \text{set } ms'. \exists mx'. \text{call-of-return-node } mx \ mx' \wedge$

$mx' \in \llbracket \text{HRB-slice } S \rrbracket_{CFG}$

from (*valid-edge* a) **have** *valid-node* (*targetnode* a) **by** *simp*

from (*valid-edge* a) (*intra-kind* (*kind* a))


```

have get-proc (sourcenode a) = get-proc (targetnode a) by(rule get-proc-intra)
from ⟨hd (m # ms'' @ ms) = sourcenode a⟩ have m = sourcenode a
  by simp
from ⟨valid-call-list cs m⟩ ⟨m = sourcenode a⟩
  ⟨get-proc (sourcenode a) = get-proc (targetnode a)⟩
have valid-call-list cs (targetnode a)
  apply(clarsimp simp:valid-call-list-def)
  apply(erule-tac x=cs' in allE)
  apply(erule-tac x=c in allE)
  by(auto split:list.split)
from ⟨valid-return-list rs m⟩ ⟨m = sourcenode a⟩
  ⟨get-proc (sourcenode a) = get-proc (targetnode a)⟩
have valid-return-list rs (targetnode a)
  apply(clarsimp simp:valid-return-list-def)
  apply(erule-tac x=cs' in allE) apply clarsimp
  by(case-tac cs' auto)
from ⟨msx' = targetnode a # tl (m # ms'' @ ms)⟩
have msx' = targetnode a # ms'' @ ms by simp
from IH[OF this ⟨valid-node (targetnode a)⟩ ⟨valid-call-list cs (targetnode a)⟩
  ⟨ $\forall i < \text{length } rs. rs ! i \in \text{get-return-edges } (cs ! i)$ ⟩
  ⟨valid-return-list rs (targetnode a)⟩ ⟨length rs = length cs⟩
  ⟨ms'' = targetnodes rs⟩ callstack callstack'' callstack'⟩
have same-level-path-aux cs as and upd-cs cs as = []
  and targetnode a -as→* m' and ms = ms' by simp-all
from ⟨intra-kind (kind a)⟩ ⟨same-level-path-aux cs as⟩
have same-level-path-aux cs (a # as) by(fastforce simp:intra-kind-def)
moreover
from ⟨intra-kind (kind a)⟩ ⟨upd-cs cs as = []⟩
have upd-cs cs (a # as) = [] by(fastforce simp:intra-kind-def)
moreover
from ⟨valid-edge a⟩ ⟨m = sourcenode a⟩ ⟨targetnode a -as→* m'⟩
have m -a # as→* m' by(fastforce intro:Cons-path)
ultimately show ?case using ⟨ms = ms'⟩ by simp
next
case (silent-move-call f a s s' Q r p fs a' S msx')
  note IH = ⟨ $\bigwedge m ms'' ms cs rs. \llbracket msx' = m \# ms'' @ ms; \text{valid-node } m; \text{valid-call-list } cs m; \forall i < \text{length } rs. rs ! i \in \text{get-return-edges } (cs ! i); \text{valid-return-list } rs m; \text{length } rs = \text{length } cs; ms'' = \text{targetnodes } rs; \forall mx \in \text{set } ms. \exists mx'. \text{call-of-return-node } mx mx' \wedge mx' \in \llbracket \text{HRB-slice } S \rrbracket_{CFG}; ms'' \neq [] \implies (\exists mx'. \text{call-of-return-node } (\text{last } ms'') mx' \wedge mx' \notin \llbracket \text{HRB-slice } S \rrbracket_{CFG}; \forall mx \in \text{set } ms'. \exists mx'. \text{call-of-return-node } mx mx' \wedge mx' \in \llbracket \text{HRB-slice } S \rrbracket_{CFG} \implies \text{same-level-path-aux } cs as \wedge \text{upd-cs } cs as = [] \wedge m -as \rightarrow^* m' \wedge ms = ms' \rangle$ 
  note callstack = ⟨ $\forall mx \in \text{set } ms. \exists mx'. \text{call-of-return-node } mx mx' \wedge mx' \in \llbracket \text{HRB-slice } S \rrbracket_{CFG}$ ⟩
  note callstack'' = ⟨ms'' ≠ [] ⟶ (∃ mx'. call-of-return-node (last ms'') mx' ∧ mx' ∉ [HRB-slice S]CFG)⟩

```

```

note  $callstack' = \langle \forall mx \in set\ ms'. \exists mx'. call\ of\ return\ node\ mx\ mx' \wedge$ 
 $mx' \in [HRB\ slice\ S]_{CFG} \rangle$ 
from  $\langle valid\ edge\ a \rangle$  have  $valid\ node\ (targetnode\ a)$  by simp
from  $\langle hd\ (m\ \# \ ms''\ @\ ms) =\ sourcenode\ a \rangle$  have  $m =\ sourcenode\ a$ 
by simp
from  $\langle valid\ edge\ a \rangle$   $\langle kind\ a =\ Q:r \leftrightarrow_p fs \rangle$  have  $get\ proc\ (targetnode\ a) =\ p$ 
by (rule\ get\ proc\ call)
with  $\langle valid\ call\ list\ cs\ m \rangle$   $\langle valid\ edge\ a \rangle$   $\langle kind\ a =\ Q:r \leftrightarrow_p fs \rangle$   $\langle m =\ sourcenode$ 
 $a \rangle$ 
have  $valid\ call\ list\ (a\ \# \ cs)\ (targetnode\ a)$ 
apply (clarsimp\ simp:valid\ call\ list\ def)
apply (case\ tac\ cs') apply auto
apply (erule\ tac\ x=list\ in\ allE)
by (case\ tac\ list)(auto\ simp:sourcenodes\ def)
from  $\langle \forall i < length\ rs. rs\ !\ i \in\ get\ return\ edges\ (cs\ !\ i) \rangle$   $\langle a' \in\ get\ return\ edges\ a \rangle$ 
have  $\forall i < length\ (a'\ \# \ rs). (a'\ \# \ rs)\ !\ i \in\ get\ return\ edges\ ((a\ \# \ cs)\ !\ i)$ 
by auto(case\ tac\ i,auto)
from  $\langle valid\ edge\ a \rangle$   $\langle a' \in\ get\ return\ edges\ a \rangle$  have  $valid\ edge\ a'$ 
by (rule\ get\ return\ edges\ valid)
from  $\langle valid\ edge\ a \rangle$   $\langle kind\ a =\ Q:r \leftrightarrow_p fs \rangle$   $\langle a' \in\ get\ return\ edges\ a \rangle$ 
obtain  $Q'\ f'$  where  $kind\ a' =\ Q' \leftrightarrow_{pf'} f'$  by (fastforce\ dest!:call\ return\ edges)
from  $\langle valid\ edge\ a' \rangle$   $\langle kind\ a' =\ Q' \leftrightarrow_{pf'} f' \rangle$  have  $get\ proc\ (sourcenode\ a') =\ p$ 
by (rule\ get\ proc\ return)
from  $\langle valid\ edge\ a \rangle$   $\langle a' \in\ get\ return\ edges\ a \rangle$ 
have  $get\ proc\ (sourcenode\ a) =\ get\ proc\ (targetnode\ a')$ 
by (rule\ get\ proc\ get\ return\ edge)
with  $\langle valid\ return\ list\ rs\ m \rangle$   $\langle valid\ edge\ a' \rangle$   $\langle kind\ a' =\ Q' \leftrightarrow_{pf'} f' \rangle$ 
 $\langle get\ proc\ (sourcenode\ a') =\ p \rangle$   $\langle get\ proc\ (targetnode\ a) =\ p \rangle$   $\langle m =\ sourcenode$ 
 $a \rangle$ 
have  $valid\ return\ list\ (a'\ \# \ rs)\ (targetnode\ a)$ 
apply (clarsimp\ simp:valid\ return\ list\ def)
apply (case\ tac\ cs') apply auto
apply (erule\ tac\ x=list\ in\ allE)
by (case\ tac\ list)(auto\ simp:targetnodes\ def)
from  $\langle length\ rs =\ length\ cs \rangle$  have  $length\ (a'\ \# \ rs) =\ length\ (a\ \# \ cs)$  by simp
from  $\langle ms'' =\ targetnodes\ rs \rangle$ 
have  $targetnode\ a' \ \# \ ms'' =\ targetnodes\ (a'\ \# \ rs)$  by (simp\ add:targetnodes\ def)
from  $\langle msx' =\ targetnode\ a \ \# \ targetnode\ a' \ \# \ tl\ (m\ \# \ ms''\ @\ ms) \rangle$ 
have  $msx' =\ targetnode\ a \ \# \ targetnode\ a' \ \# \ ms''\ @\ ms$  by simp
have  $\exists mx'. call\ of\ return\ node\ (last\ (targetnode\ a' \ \# \ ms''))\ mx' \wedge$ 
 $mx' \notin [HRB\ slice\ S]_{CFG}$ 
proof (cases\ ms'' =\ [])
case True
with  $\langle (\exists m \in set\ (tl\ (m\ \# \ ms''\ @\ ms))).$ 
 $\exists m'. call\ of\ return\ node\ m\ m' \wedge m' \notin [HRB\ slice\ S]_{CFG} \rangle \vee$ 
 $hd\ (m\ \# \ ms''\ @\ ms) \notin [HRB\ slice\ S]_{CFG} \rangle$   $\langle m =\ sourcenode\ a \rangle$   $callstack$ 
have  $sourcenode\ a \notin [HRB\ slice\ S]_{CFG}$  by fastforce
from  $\langle valid\ edge\ a \rangle$   $\langle a' \in\ get\ return\ edges\ a \rangle$  have  $valid\ edge\ a'$ 
by (rule\ get\ return\ edges\ valid)

```

with $\langle \text{valid-edge } a \rangle \langle a' \in \text{get-return-edges } a \rangle$
have $\text{call-of-return-node } (\text{targetnode } a') (\text{sourcenode } a)$
by $(\text{fastforce simp:call-of-return-node-def return-node-def})$
with $\langle \text{sourcenode } a \notin \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle \text{True}$ **show** $?thesis$ **by** fastforce
next
case False
with $\text{callstack}''$ **show** $?thesis$ **by** fastforce
qed
hence $\text{targetnode } a' \# \text{ms}'' \neq [] \longrightarrow$
 $(\exists mx'. \text{call-of-return-node } (\text{last } (\text{targetnode } a' \# \text{ms}'')) mx' \wedge$
 $mx' \notin \lfloor \text{HRB-slice } S \rfloor_{CFG})$ **by** simp
from $\text{IH}[\text{OF} - \langle \text{valid-node } (\text{targetnode } a) \rangle \langle \text{valid-call-list } (a \# cs) (\text{targetnode}$
 $a) \rangle$
 $\langle \forall i < \text{length } (a' \# rs). (a' \# rs) ! i \in \text{get-return-edges } ((a \# cs) ! i) \rangle$
 $\langle \text{valid-return-list } (a' \# rs) (\text{targetnode } a) \rangle \langle \text{length } (a' \# rs) = \text{length } (a \# cs) \rangle$
 $\langle \text{targetnode } a' \# \text{ms}'' = \text{targetnodes } (a' \# rs) \rangle \text{callstack this callstack}^{\wedge}$
 $\langle \text{msx}' = \text{targetnode } a \# \text{targetnode } a' \# \text{ms}'' @ \text{ms} \rangle$
have $\text{same-level-path-aux } (a \# cs) \text{ as}$ **and** $\text{upd-cs } (a \# cs) \text{ as} = []$
and $\text{targetnode } a - \text{as} \rightarrow^* m'$ **and** $\text{ms} = \text{ms}'$ **by** simp-all
from $\langle \text{kind } a = Q:r \hookrightarrow pfs \rangle \langle \text{same-level-path-aux } (a \# cs) \text{ as} \rangle$
have $\text{same-level-path-aux } cs (a \# as)$ **by** simp
moreover
from $\langle \text{kind } a = Q:r \hookrightarrow pfs \rangle \langle \text{upd-cs } (a \# cs) \text{ as} = [] \rangle$ **have** $\text{upd-cs } cs (a \# as)$
 $= []$
by simp
moreover
from $\langle \text{valid-edge } a \rangle \langle m = \text{sourcenode } a \rangle \langle \text{targetnode } a - \text{as} \rightarrow^* m' \rangle$
have $m - a \# \text{as} \rightarrow^* m'$ **by** $(\text{fastforce intro:Cons-path})$
ultimately show $?case$ **using** $\langle \text{ms} = \text{ms}' \rangle$ **by** simp
next
case $(\text{silent-move-return } f a s s' Q p f' S \text{msx}')$
note $\text{IH} = \langle \bigwedge m \text{ms}'' \text{ms } cs \text{rs}. \llbracket \text{msx}' = m \# \text{ms}'' @ \text{ms}; \text{valid-node } m;$
 $\text{valid-call-list } cs \text{ms}; \forall i < \text{length } \text{rs}. \text{rs} ! i \in \text{get-return-edges } (cs ! i);$
 $\text{valid-return-list } \text{rs } m; \text{length } \text{rs} = \text{length } cs; \text{ms}'' = \text{targetnodes } \text{rs};$
 $\forall mx \in \text{set } \text{ms}. \exists mx'. \text{call-of-return-node } mx \text{ms}' \wedge mx' \in \lfloor \text{HRB-slice } S \rfloor_{CFG};$
 $\text{ms}'' \neq [] \longrightarrow$
 $(\exists mx'. \text{call-of-return-node } (\text{last } \text{ms}'') mx' \wedge mx' \notin \lfloor \text{HRB-slice } S \rfloor_{CFG});$
 $\forall mx \in \text{set } \text{ms}'. \exists mx'. \text{call-of-return-node } mx \text{ms}' \wedge mx' \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rrbracket$
 $\implies \text{same-level-path-aux } cs \text{as} \wedge \text{upd-cs } cs \text{as} = [] \wedge m - \text{as} \rightarrow^* m' \wedge \text{ms} =$
 $\text{ms}' \rangle$
note $\text{callstack} = \langle \forall mx \in \text{set } \text{ms}. \exists mx'. \text{call-of-return-node } mx \text{ms}' \wedge$
 $mx' \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
note $\text{callstack}'' = \langle \text{ms}'' \neq [] \longrightarrow$
 $(\exists mx'. \text{call-of-return-node } (\text{last } \text{ms}'') mx' \wedge mx' \notin \lfloor \text{HRB-slice } S \rfloor_{CFG}) \rangle$
note $\text{callstack}' = \langle \forall mx \in \text{set } \text{ms}'. \exists mx'. \text{call-of-return-node } mx \text{ms}' \wedge$
 $mx' \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
have $\text{ms}'' \neq []$
proof
assume $\text{ms}'' = []$

```

with callstack
   $\langle \exists m \in \text{set } (tl (m \# ms'' @ ms)). \exists m'. \text{call-of-return-node } m \ m' \wedge m' \notin$ 
 $[HRB\text{-slice } S]_{CFG}$ 
  show False by fastforce
qed
with  $\langle hd (tl (m \# ms'' @ ms)) = \text{targetnode } a \rangle$ 
obtain xs where  $ms'' = \text{targetnode } a \# xs$  by  $(\text{cases } ms'')$  auto
with  $\langle ms'' = \text{targetnodes } rs \rangle$  obtain  $r' \ rs'$  where  $rs = r' \# rs'$ 
  and  $\text{targetnode } a = \text{targetnode } r'$  and  $xs = \text{targetnodes } rs'$ 
  by  $(\text{cases } rs)$   $(\text{auto } \text{simp}:\text{targetnodes-def})$ 
from  $\langle rs = r' \# rs' \rangle$   $\langle \text{length } rs = \text{length } cs \rangle$  obtain  $c' \ cs'$  where  $cs = c' \#$ 
 $cs'$ 
  and  $\text{length } rs' = \text{length } cs'$  by  $(\text{cases } cs)$  auto
from  $\langle \forall i < \text{length } rs. rs \ ! \ i \in \text{get-return-edges } (cs \ ! \ i) \rangle$ 
   $\langle rs = r' \# rs' \rangle$   $\langle cs = c' \# cs' \rangle$ 
have  $\langle \forall i < \text{length } rs'. rs' \ ! \ i \in \text{get-return-edges } (cs' \ ! \ i) \rangle$ 
  and  $r' \in \text{get-return-edges } c'$  by auto
from  $\langle \text{valid-edge } a \rangle$  have  $\text{valid-node } (\text{targetnode } a)$  by simp
from  $\langle hd (m \# ms'' @ ms) = \text{sourcenode } a \rangle$  have  $m = \text{sourcenode } a$ 
  by simp
from  $\langle \text{valid-call-list } cs \ m \rangle$   $\langle cs = c' \# cs' \rangle$ 
obtain  $p' \ Q' \ r \ fs'$  where  $\text{valid-edge } c'$  and  $\text{kind } c' = Q':r \hookrightarrow_p \ fs'$ 
  and  $p' = \text{get-proc } m$ 
  apply  $(\text{auto } \text{simp}:\text{valid-call-list-def})$ 
  by  $(\text{erule-tac } x = [] \text{ in } \text{allE})$  auto
from  $\langle \text{valid-edge } a \rangle$   $\langle \text{kind } a = Q \leftrightarrow_p f' \rangle$ 
have  $\text{get-proc } (\text{sourcenode } a) = p$  by  $(\text{rule } \text{get-proc-return})$ 
with  $\langle m = \text{sourcenode } a \rangle$   $\langle p' = \text{get-proc } m \rangle$  have  $[\text{simp}]:p' = p$  by simp
from  $\langle \text{valid-edge } c' \rangle$   $\langle \text{kind } c' = Q':r \hookrightarrow_p \ fs' \rangle$ 
have  $\text{get-proc } (\text{targetnode } c') = p$  by  $(\text{fastforce } \text{intro}:\text{get-proc-call})$ 
from  $\langle \text{valid-edge } c' \rangle$   $\langle r' \in \text{get-return-edges } c' \rangle$  have  $\text{valid-edge } r'$ 
  by  $(\text{rule } \text{get-return-edges-valid})$ 
from  $\langle \text{valid-edge } c' \rangle$   $\langle \text{kind } c' = Q':r \hookrightarrow_p \ fs' \rangle$   $\langle r' \in \text{get-return-edges } c' \rangle$ 
obtain  $Q'' \ f''$  where  $\text{kind } r' = Q'' \hookrightarrow_p f''$  by  $(\text{fastforce } \text{dest}!\text{:call-return-edges})$ 
with  $\langle \text{valid-edge } r' \rangle$  have  $\text{get-proc } (\text{sourcenode } r') = p$  by  $(\text{rule } \text{get-proc-return})$ 
from  $\langle \text{valid-edge } r' \rangle$   $\langle \text{kind } r' = Q'' \hookrightarrow_p f'' \rangle$  have  $\text{method-exit } (\text{sourcenode } r')$ 
  by  $(\text{fastforce } \text{simp}:\text{method-exit-def})$ 
from  $\langle \text{valid-edge } a \rangle$   $\langle \text{kind } a = Q \leftrightarrow_p f' \rangle$  have  $\text{method-exit } (\text{sourcenode } a)$ 
  by  $(\text{fastforce } \text{simp}:\text{method-exit-def})$ 
with  $\langle \text{method-exit } (\text{sourcenode } r') \rangle$   $\langle \text{get-proc } (\text{sourcenode } r') = p \rangle$ 
   $\langle \text{get-proc } (\text{sourcenode } a) = p \rangle$ 
have  $\text{sourcenode } a = \text{sourcenode } r'$  by  $(\text{fastforce } \text{intro}:\text{method-exit-unique})$ 
with  $\langle \text{valid-edge } a \rangle$   $\langle \text{valid-edge } r' \rangle$   $\langle \text{targetnode } a = \text{targetnode } r' \rangle$ 
have  $a = r'$  by  $(\text{fastforce } \text{intro}:\text{edge-det})$ 
from  $\langle \text{valid-edge } c' \rangle$   $\langle r' \in \text{get-return-edges } c' \rangle$   $\langle \text{targetnode } a = \text{targetnode } r' \rangle$ 
have  $\text{get-proc } (\text{sourcenode } c') = \text{get-proc } (\text{targetnode } a)$ 
  by  $(\text{fastforce } \text{intro}:\text{get-proc-get-return-edge})$ 
from  $\langle \text{valid-call-list } cs \ m \rangle$   $\langle cs = c' \# cs' \rangle$ 
   $\langle \text{get-proc } (\text{sourcenode } c') = \text{get-proc } (\text{targetnode } a) \rangle$ 

```

```

have valid-call-list cs' (targetnode a)
  apply (clarsimp simp:valid-call-list-def)
  apply (erule-tac x=c' # cs' in allE)
  by (case-tac cs')(auto simp:sourcenodes-def)
from ⟨valid-return-list rs m⟩ ⟨rs = r' # rs'⟩ ⟨targetnode a = targetnode r'⟩
have valid-return-list rs' (targetnode a)
  apply (clarsimp simp:valid-return-list-def)
  apply (erule-tac x=r' # cs' in allE)
  by (case-tac cs')(auto simp:targetnodes-def)
from ⟨msx' = tl (m # ms'' @ ms)⟩ ⟨ms'' = targetnode a # xs⟩
have msx' = targetnode a # xs @ ms by simp
from callstack'' ⟨ms'' = targetnode a # xs⟩
have xs ≠ [] →
  (∃ mx'. call-of-return-node (last xs) mx' ∧ mx' ∉ [HRB-slice S]CFG)
  by fastforce
from IH[OF ⟨msx' = targetnode a # xs @ ms⟩ ⟨valid-node (targetnode a)⟩
  ⟨valid-call-list cs' (targetnode a)⟩
  ⟨∀ i < length rs'. rs' ! i ∈ get-return-edges (cs' ! i)⟩
  ⟨valid-return-list rs' (targetnode a)⟩ ⟨length rs' = length cs'⟩
  ⟨xs = targetnodes rs'⟩ callstack this callstack']
have same-level-path-aux cs' as and upd-cs cs' as = []
  and targetnode a -as→* m' and ms = ms' by simp-all
from ⟨kind a = Q↔pf'⟩ ⟨same-level-path-aux cs' as⟩ ⟨cs = c' # cs'⟩
  ⟨r' ∈ get-return-edges c'⟩ ⟨a = r'⟩
have same-level-path-aux cs (a # as) by simp
moreover
from ⟨upd-cs cs' as = []⟩ ⟨kind a = Q↔pf'⟩ ⟨cs = c' # cs'⟩
have upd-cs cs (a # as) = [] by simp
moreover
from ⟨valid-edge a⟩ ⟨m = sourcenode a⟩ ⟨targetnode a -as→* m'⟩
have m -a # as→* m' by (fastforce intro:Cons-path)
ultimately show ?case using ⟨ms = ms'⟩ by simp
qed
qed

```

lemma *silent-moves-slp*:

```

[[S,f ⊢ (m#ms,s) =as⇒τ (m'#ms',s'); valid-node m;
  ∀ mx ∈ set ms. ∃ mx'. call-of-return-node mx mx' ∧ mx' ∈ [HRB-slice S]CFG;
  ∀ mx ∈ set ms'. ∃ mx'. call-of-return-node mx mx' ∧ mx' ∈ [HRB-slice S]CFG]]
⇒ m -as→sl* m' ∧ ms = ms'
by (fastforce dest!:silent-moves-slpa-path
  [of - - - [] - - - - - [] ],simplified]
  simp:targetnodes-def valid-call-list-def valid-return-list-def
  same-level-path-def slp-def)

```

lemma *slpa-silent-moves-callstacks-eq*:

```

[[same-level-path-aux cs as; S,f ⊢ (m#msx@ms,s) =as⇒τ (m'#ms',s');

```

$\text{length } ms = \text{length } ms'$; $\text{valid-call-list } cs \ m$;
 $\forall i < \text{length } rs. rs!i \in \text{get-return-edges } (cs!i)$; $\text{valid-return-list } rs \ m$;
 $\text{length } rs = \text{length } cs$; $msx = \text{targetnodes } rs$]
 $\implies ms = ms'$

proof(*induct arbitrary:m msx s rs rule:slpa-induct*)

case (*slpa-empty cs*)

from $\langle S, f \vdash (m \# msx @ ms, s) = [] \Rightarrow_{\tau} (m' \# ms', s') \rangle$

have $msx @ ms = ms'$ **by** (*fastforce elim:silent-moves.cases*)

with $\langle \text{length } ms = \text{length } ms' \rangle$ **show** $?case$ **by** *fastforce*

next

case (*slpa-intra cs a as*)

note $IH = \langle \bigwedge m \ msx \ s \ rs. \llbracket S, f \vdash (m \# msx @ ms, s) = as \Rightarrow_{\tau} (m' \# ms', s') \rrbracket$;
 $\text{length } ms = \text{length } ms'$; $\text{valid-call-list } cs \ m$;
 $\forall i < \text{length } rs. rs!i \in \text{get-return-edges } (cs!i)$;
 $\text{valid-return-list } rs \ m$; $\text{length } rs = \text{length } cs$; $msx = \text{targetnodes } rs$]
 $\implies ms = ms'$

from $\langle S, f \vdash (m \# msx @ ms, s) = a \# as \Rightarrow_{\tau} (m' \# ms', s') \rangle$ **obtain** $ms'' \ s''$

where $S, f \vdash (m \# msx @ ms, s) -a \rightarrow_{\tau} (ms'', s'')$

and $S, f \vdash (ms'', s'') = as \Rightarrow_{\tau} (m' \# ms', s')$

by (*auto elim:silent-moves.cases*)

from $\langle S, f \vdash (m \# msx @ ms, s) -a \rightarrow_{\tau} (ms'', s'') \rangle$ *intra-kind* (*kind a*)

have *valid-edge a* **and** [*simp*]: $m = \text{sourcenode } a$ $ms'' = \text{targetnode } a \# msx @$
 ms

by (*fastforce elim:silent-move.cases simp:intra-kind-def*)**+**

from $\langle \text{valid-edge } a \rangle$ *intra-kind* (*kind a*)

have $\text{get-proc } (\text{sourcenode } a) = \text{get-proc } (\text{targetnode } a)$ **by** (*rule get-proc-intra*)

from $\langle \text{valid-call-list } cs \ m \rangle$ $\langle m = \text{sourcenode } a \rangle$

$\langle \text{get-proc } (\text{sourcenode } a) = \text{get-proc } (\text{targetnode } a) \rangle$

have *valid-call-list cs* (*targetnode a*)

apply (*clarsimp simp:valid-call-list-def*)

apply (*erule-tac x=cs' in allE*)

apply (*erule-tac x=c in allE*)

by (*auto split:list.split*)

from $\langle \text{valid-return-list } rs \ m \rangle$ $\langle m = \text{sourcenode } a \rangle$

$\langle \text{get-proc } (\text{sourcenode } a) = \text{get-proc } (\text{targetnode } a) \rangle$

have *valid-return-list rs* (*targetnode a*)

apply (*clarsimp simp:valid-return-list-def*)

apply (*erule-tac x=cs' in allE*) **apply** *clarsimp*

by (*case-tac cs'*) *auto*

from $\langle S, f \vdash (ms'', s'') = as \Rightarrow_{\tau} (m' \# ms', s') \rangle$

have $S, f \vdash (\text{targetnode } a \# msx @ ms, s'') = as \Rightarrow_{\tau} (m' \# ms', s')$ **by** *simp*

from $IH[OF \text{ this } \langle \text{length } ms = \text{length } ms' \rangle \langle \text{valid-call-list } cs \ (\text{targetnode } a) \rangle$
 $\langle \forall i < \text{length } rs. rs!i \in \text{get-return-edges } (cs!i) \rangle$
 $\langle \text{valid-return-list } rs \ (\text{targetnode } a) \rangle \langle \text{length } rs = \text{length } cs \rangle$
 $\langle msx = \text{targetnodes } rs \rangle]$ **show** $?case$.

next

case (*slpa-Call cs a as Q r p fs*)

note $IH = \langle \bigwedge m \ msx \ s \ rs. \llbracket S, f \vdash (m \# msx @ ms, s) = as \Rightarrow_{\tau} (m' \# ms', s') \rrbracket$;
 $\text{length } ms = \text{length } ms'$; $\text{valid-call-list } (a \# cs) \ m$;

```

  ∀ i < length rs. rs ! i ∈ get-return-edges ((a # cs) ! i);
  valid-return-list rs m; length rs = length (a # cs);
  msx = targetnodes rs]]
  ⇒ ms = ms'
from ⟨S,f ⊢ (m # msx @ ms,s) = a # as ⇒τ (m' # ms',s')⟩ obtain ms'' s''
  where S,f ⊢ (m # msx @ ms,s) -a →τ (ms'',s'')
  and S,f ⊢ (ms'',s'') = as ⇒τ (m' # ms',s')
  by(auto elim:silent-moves.cases)
from ⟨S,f ⊢ (m # msx @ ms,s) -a →τ (ms'',s'')⟩ ⟨kind a = Q:r↔pfs⟩
obtain a' where valid-edge a and [simp]:m = sourcenode a
  and [simp]:ms'' = targetnode a # targetnode a' # msx @ ms
  and a' ∈ get-return-edges a
  by(auto elim:silent-move.cases simp:intra-kind-def)
from ⟨valid-edge a⟩ ⟨kind a = Q:r↔pfs⟩ have get-proc (targetnode a) = p
  by(rule get-proc-call)
with ⟨valid-call-list cs m⟩ ⟨valid-edge a⟩ ⟨kind a = Q:r↔pfs⟩ ⟨m = sourcenode a⟩
have valid-call-list (a # cs) (targetnode a)
  apply(clarsimp simp:valid-call-list-def)
  apply(case-tac cs') apply auto
  apply(erule-tac x=list in allE)
  by(case-tac list)(auto simp:sourcenodes-def)
from ⟨∀ i < length rs. rs ! i ∈ get-return-edges (cs ! i)⟩ ⟨a' ∈ get-return-edges a⟩
have ∀ i < length (a'#rs). (a'#rs) ! i ∈ get-return-edges ((a#cs) ! i)
  by auto(case-tac i,auto)
from ⟨valid-edge a⟩ ⟨a' ∈ get-return-edges a⟩ have valid-edge a'
  by(rule get-return-edges-valid)
from ⟨valid-edge a⟩ ⟨kind a = Q:r↔pfs⟩ ⟨a' ∈ get-return-edges a⟩
obtain Q' f' where kind a' = Q'↔pf' by(fastforce dest!:call-return-edges)
from ⟨valid-edge a'⟩ ⟨kind a' = Q'↔pf'⟩ have get-proc (sourcenode a') = p
  by(rule get-proc-return)
from ⟨valid-edge a⟩ ⟨a' ∈ get-return-edges a⟩
have get-proc (sourcenode a) = get-proc (targetnode a')
  by(rule get-proc-get-return-edge)
with ⟨valid-return-list rs m⟩ ⟨valid-edge a'⟩ ⟨kind a' = Q'↔pf'⟩
  ⟨get-proc (sourcenode a') = p⟩ ⟨get-proc (targetnode a) = p⟩ ⟨m = sourcenode a⟩
have valid-return-list (a'#rs) (targetnode a)
  apply(clarsimp simp:valid-return-list-def)
  apply(case-tac cs') apply auto
  apply(erule-tac x=list in allE)
  by(case-tac list)(auto simp:targetnodes-def)
from ⟨length rs = length cs⟩ have length (a'#rs) = length (a#cs) by simp
from ⟨msx = targetnodes rs⟩ have targetnode a' # msx = targetnodes (a' # rs)
  by(simp add:targetnodes-def)
from ⟨S,f ⊢ (ms'',s'') = as ⇒τ (m' # ms',s')⟩
have S,f ⊢ (targetnode a # (targetnode a' # msx) @ ms,s'') = as ⇒τ (m' #
ms',s')
  by simp
from IH[OF this ⟨length ms = length ms'⟩ ⟨valid-call-list (a # cs) (targetnode
a)⟩

```

$\langle \forall i < \text{length } (a' \# rs). (a' \# rs) ! i \in \text{get-return-edges } ((a \# cs) ! i) \rangle$
 $\langle \text{valid-return-list } (a' \# rs) \text{ (targetnode } a) \rangle \langle \text{length } (a' \# rs) = \text{length } (a \# cs) \rangle$
 $\langle \text{targetnode } a' \# msx = \text{targetnodes } (a' \# rs) \rangle] \text{ show ?case .}$

next

case $(\text{slpa-Return } cs \ a \ \text{as } Q \ p \ f' \ c' \ cs')$

note $IH = \langle \bigwedge m \ msx \ s \ rs. \llbracket S, f \vdash (m \# msx \ @ \ ms, s) = \text{as} \Rightarrow_{\tau} (m' \# ms', s') \rrbracket;$
 $\text{length } ms = \text{length } ms'; \text{ valid-call-list } cs' \ m;$
 $\forall i < \text{length } rs. rs ! i \in \text{get-return-edges } (cs' ! i); \text{ valid-return-list } rs \ m;$
 $\text{length } rs = \text{length } cs'; msx = \text{targetnodes } rs \rrbracket$
 $\implies ms = ms'$

from $\langle S, f \vdash (m \# msx \ @ \ ms, s) = a \# \text{as} \Rightarrow_{\tau} (m' \# ms', s') \rangle$ **obtain** $ms'' \ s''$
where $S, f \vdash (m \# msx \ @ \ ms, s) -a \rightarrow_{\tau} (ms'', s'')$
and $S, f \vdash (ms'', s'') = \text{as} \Rightarrow_{\tau} (m' \# ms', s')$
by $(\text{auto elim:silent-moves.cases})$

from $\langle S, f \vdash (m \# msx \ @ \ ms, s) -a \rightarrow_{\tau} (ms'', s'') \rangle$ $\langle \text{kind } a = Q \leftarrow pf' \rangle$
have $\text{valid-edge } a$ **and** $m = \text{sourcenode } a$ **and** $\text{hd } (msx \ @ \ ms) = \text{targetnode } a$
and $ms'' = msx \ @ \ ms$ **and** $s'' \neq []$ **and** $\text{length } s = \text{Suc}(\text{length } s'')$
and $\text{length } (m \# msx \ @ \ ms) = \text{length } s$
by $(\text{auto elim:silent-move.cases simp:intra-kind-def})$

from $\langle msx = \text{targetnodes } rs \rangle \langle \text{length } rs = \text{length } cs \rangle \langle cs = c' \# cs' \rangle$
obtain $mx' \ msx'$ **where** $msx = mx' \# msx'$
by $(\text{cases } msx)(\text{fastforce simp:targetnodes-def})+$

with $\langle \text{hd } (msx \ @ \ ms) = \text{targetnode } a \rangle$ **have** $mx' = \text{targetnode } a$ **by** simp

from $\langle \text{valid-call-list } cs \ m \rangle \langle cs = c' \# cs' \rangle$ **have** $\text{valid-edge } c'$
by $(\text{fastforce simp:valid-call-list-def})$

from $\langle \text{valid-edge } c' \rangle \langle a \in \text{get-return-edges } c' \rangle$
have $\text{get-proc } (\text{sourcenode } c') = \text{get-proc } (\text{targetnode } a)$
by $(\text{rule get-proc-get-return-edge})$

from $\langle \text{valid-call-list } cs \ m \rangle \langle cs = c' \# cs' \rangle$
 $\langle \text{get-proc } (\text{sourcenode } c') = \text{get-proc } (\text{targetnode } a) \rangle$
have $\text{valid-call-list } cs' \ (\text{targetnode } a)$
apply $(\text{clarsimp simp:valid-call-list-def})$
apply $(\text{erule-tac } x=c' \# cs' \ \text{in } \text{allE})$
by $(\text{case-tac } cs')(\text{auto simp:sourcenodes-def})$

from $\langle \text{length } rs = \text{length } cs \rangle \langle cs = c' \# cs' \rangle$ **obtain** $r' \ rs'$
where $[\text{simp}]: rs = r' \# rs'$ **and** $\text{length } rs' = \text{length } cs'$ **by** $(\text{cases } rs) \text{ auto}$

from $\langle \forall i < \text{length } rs. rs ! i \in \text{get-return-edges } (cs ! i) \rangle \langle cs = c' \# cs' \rangle$
have $\forall i < \text{length } rs'. rs' ! i \in \text{get-return-edges } (cs' ! i)$
and $r' \in \text{get-return-edges } c'$ **by** auto

with $\langle \text{valid-edge } c' \rangle \langle a \in \text{get-return-edges } c' \rangle$ **have** $[\text{simp}]: a = r'$
by $-(\text{rule get-return-edges-unique})$

with $\langle \text{valid-return-list } rs \ m \rangle$
have $\text{valid-return-list } rs' \ (\text{targetnode } a)$
apply $(\text{clarsimp simp:valid-return-list-def})$
apply $(\text{erule-tac } x=r' \# cs' \ \text{in } \text{allE})$
by $(\text{case-tac } cs')(\text{auto simp:targetnodes-def})$

from $\langle msx = \text{targetnodes } rs \rangle \langle msx = mx' \# msx' \rangle \langle rs = r' \# rs' \rangle$
have $msx' = \text{targetnodes } rs'$ **by** $(\text{simp add:targetnodes-def})$

from $\langle S, f \vdash (ms'', s'') = \text{as} \Rightarrow_{\tau} (m' \# ms', s') \rangle \langle msx = mx' \# msx' \rangle$

$\langle ms'' = msx @ ms \rangle \langle mx' = \text{targetnode } a \rangle$
have $S, f \vdash (\text{targetnode } a \# msx' @ ms, s') = as \Rightarrow_{\tau} (m' \# ms', s')$ **by** *simp*
from $IH[OF \text{ this } \langle \text{length } ms = \text{length } ms' \rangle \langle \text{valid-call-list } cs' (\text{targetnode } a) \rangle$
 $\langle \forall i < \text{length } rs'. rs' ! i \in \text{get-return-edges } (cs' ! i) \rangle$
 $\langle \text{valid-return-list } rs' (\text{targetnode } a) \rangle \langle \text{length } rs' = \text{length } cs' \rangle$
 $\langle msx' = \text{targetnodes } rs' \rangle]$ **show** *?case* .
qed

lemma *silent-moves-same-level-path*:

assumes $S, kind \vdash (m \# ms, s) = as \Rightarrow_{\tau} (m' \# ms', s')$ **and** $m - as \rightarrow_{sl^*} m'$ **shows**
 $ms = ms'$

proof –

from $\langle S, kind \vdash (m \# ms, s) = as \Rightarrow_{\tau} (m' \# ms', s') \rangle$ **obtain** *cf cfs* **where** $s = cf \# cfs$
by (*cases s*) (*auto dest:silent-moves-equal-length*)
with $\langle S, kind \vdash (m \# ms, s) = as \Rightarrow_{\tau} (m' \# ms', s') \rangle$
have *transfers* (*kinds as*) (*cf # cfs*) = s'
by (*fastforce intro:silent-moves-preds-transfers simp:kinds-def*)
with $\langle m - as \rightarrow_{sl^*} m' \rangle$ **obtain** *cf'* **where** $s' = cf' \# cfs$
by – (*drule slp-callstack-length-equal, auto*)
from $\langle S, kind \vdash (m \# ms, s) = as \Rightarrow_{\tau} (m' \# ms', s') \rangle$
have $\text{length } (m \# ms) = \text{length } s$ **and** $\text{length } (m' \# ms') = \text{length } s'$
by (*rule silent-moves-equal-length*) +
with $\langle s = cf \# cfs \rangle \langle s' = cf' \# cfs \rangle$ **have** $\text{length } ms = \text{length } ms'$ **by** *simp*
from $\langle m - as \rightarrow_{sl^*} m' \rangle$ **have** *same-level-path-aux* [] *as*
by (*simp add:slp-def same-level-path-def*)
with $\langle S, kind \vdash (m \# ms, s) = as \Rightarrow_{\tau} (m' \# ms', s') \rangle \langle \text{length } ms = \text{length } ms' \rangle$
show *?thesis* **by** (*auto elim!:slpa-silent-moves-callstacks-eq*
simp:targetnodes-def valid-call-list-def valid-return-list-def)
qed

lemma *silent-moves-call-edge*:

assumes $S, kind \vdash (m \# ms, s) = as \Rightarrow_{\tau} (m' \# ms', s')$ **and** *valid-node m*
and *callstack*: $\forall mx \in \text{set } ms. \exists mx'. \text{call-of-return-node } mx \ mx' \wedge$

$mx' \in [HRB\text{-slice } S]_{CFG}$

and *rest*: $\forall i < \text{length } rs. rs' ! i \in \text{get-return-edges } (cs' ! i)$

$ms = \text{targetnodes } rs$ *valid-return-list rs m* $\text{length } rs = \text{length } cs$

obtains $as' a as''$ **where** $as = as' @ a \# as''$ **and** $\exists Q r p fs. kind a = Q: r \hookrightarrow_p fs$

and *call-of-return-node* (*hd ms'*) (*sourcenode a*)

and *targetnode a* $- as'' \rightarrow_{sl^*} m'$

| $ms' = ms$

proof (*atomize-elim*)

from $\langle S, kind \vdash (m \# ms, s) = as \Rightarrow_{\tau} (m' \# ms', s') \rangle$

show $(\exists as' a as''. as = as' @ a \# as'' \wedge (\exists Q r p fs. kind a = Q: r \hookrightarrow_p fs) \wedge$
call-of-return-node (*hd ms'*) (*sourcenode a*) \wedge *targetnode a* $- as'' \rightarrow_{sl^*} m') \vee$
 $ms' = ms$

proof (*induct as arbitrary:m' ms' s' rule:length-induct*)

fix $as \ m' \ ms' \ s'$

assume $IH: \forall as'. \text{length } as' < \text{length } as \longrightarrow$
 $(\forall mx \ msx \ sx. S, kind \vdash (m \# ms, s) = as' \Rightarrow_{\tau} (mx \# msx, sx) \longrightarrow$
 $(\exists asx \ a \ asx'. as' = asx \ @ \ a \ \# \ asx' \wedge (\exists Q \ r \ p \ fs. kind \ a = Q: r \hookrightarrow_p fs) \wedge$
 $\text{call-of-return-node } (hd \ msx) \ (\text{sourcenode } a) \wedge \text{targetnode } a \ -asx' \rightarrow_{sl^*} mx) \vee$
 $msx = ms)$
and $S, kind \vdash (m \# ms, s) = as \Rightarrow_{\tau} (m' \# ms', s')$
show $(\exists as' \ a \ as''. as = as' \ @ \ a \ \# \ as'' \wedge (\exists Q \ r \ p \ fs. kind \ a = Q: r \hookrightarrow_p fs) \wedge$
 $\text{call-of-return-node } (hd \ ms') \ (\text{sourcenode } a) \wedge \text{targetnode } a \ -as'' \rightarrow_{sl^*} m') \vee$
 $ms' = ms$
proof $(\text{cases } as \ \text{rule: rev-cases})$
case Nil
with $\langle S, kind \vdash (m \# ms, s) = as \Rightarrow_{\tau} (m' \# ms', s') \rangle$ **have** $ms = ms'$
by $(\text{fastforce } elim: \text{silent-moves.cases})$
thus $?thesis$ **by** $simp$
next
case $(snoc \ as' \ a')$
with $\langle S, kind \vdash (m \# ms, s) = as \Rightarrow_{\tau} (m' \# ms', s') \rangle$
obtain $ms'' \ s''$ **where** $S, kind \vdash (m \# ms, s) = as' \Rightarrow_{\tau} (ms'', s'')$
and $S, kind \vdash (ms'', s'') = [a'] \Rightarrow_{\tau} (m' \# ms', s')$
by $(\text{fastforce } elim: \text{silent-moves-split})$
from $snoc$ **have** $\text{length } as' < \text{length } as$ **by** $simp$
from $\langle S, kind \vdash (ms'', s'') = [a'] \Rightarrow_{\tau} (m' \# ms', s') \rangle$
have $S, kind \vdash (ms'', s'') - a' \rightarrow_{\tau} (m' \# ms', s')$
by $(\text{fastforce } elim: \text{silent-moves.cases})$
show $?thesis$
proof $(\text{cases } kind \ a' \ \text{rule: edge-kind-cases})$
case $Intra$
with $\langle S, kind \vdash (ms'', s'') - a' \rightarrow_{\tau} (m' \# ms', s') \rangle$
have $\text{valid-edge } a'$ **and** $m' = \text{targetnode } a'$
by $(\text{auto } elim: \text{silent-move.cases } simp: \text{intra-kind-def})$
from $\langle S, kind \vdash (ms'', s'') - a' \rightarrow_{\tau} (m' \# ms', s') \rangle$ $\langle \text{intra-kind } (kind \ a') \rangle$
have $ms'' = \text{sourcenode } a' \# ms'$
by $-(\text{erule } \text{silent-move.cases}, \text{auto } simp: \text{intra-kind-def}, (\text{cases } ms'', \text{auto})+)$
with $IH \ \langle \text{length } as' < \text{length } as \rangle \ \langle S, kind \vdash (m \# ms, s) = as' \Rightarrow_{\tau} (ms'', s'') \rangle$
have $(\exists asx \ ax \ asx'. as' = asx \ @ \ ax \ \# \ asx' \wedge (\exists Q \ r \ p \ fs. kind \ ax =$
 $Q: r \hookrightarrow_p fs) \wedge$
 $\text{call-of-return-node } (hd \ ms') \ (\text{sourcenode } ax) \wedge$
 $\text{targetnode } ax \ -asx' \rightarrow_{sl^*} \text{sourcenode } a') \vee ms' = ms$
by $simp \ \text{blast}$
thus $?thesis$
proof
assume $\exists asx \ ax \ asx'. as' = asx \ @ \ ax \ \# \ asx' \wedge$
 $(\exists Q \ r \ p \ fs. kind \ ax = Q: r \hookrightarrow_p fs) \wedge$
 $\text{call-of-return-node } (hd \ ms') \ (\text{sourcenode } ax) \wedge$
 $\text{targetnode } ax \ -asx' \rightarrow_{sl^*} \text{sourcenode } a'$
then obtain $asx \ ax \ asx'$ **where** $as' = asx \ @ \ ax \ \# \ asx'$
and $\exists Q \ r \ p \ fs. kind \ ax = Q: r \hookrightarrow_p fs$
and $\text{call-of-return-node } (hd \ ms') \ (\text{sourcenode } ax)$
and $\text{targetnode } ax \ -asx' \rightarrow_{sl^*} \text{sourcenode } a'$

```

    by blast
  from ⟨as' = asx @ ax # asx'⟩ have as'@[a'] = asx @ ax # (asx' @ [a'])
    by simp
  moreover
  from ⟨targetnode ax -asx'→sl* sourcenode a'⟩ ⟨intra-kind (kind a')⟩
    ⟨m' = targetnode a'⟩ ⟨valid-edge a'⟩
  have targetnode ax -asx'@[a']→sl* m'
    by(fastforce intro:path-Append path-edge same-level-path-aux-Append
      upd-cs-Append simp:slp-def same-level-path-def intra-kind-def)
  ultimately show ?thesis using ⟨∃ Q r p fs. kind ax = Q:r↔pfs⟩
    ⟨call-of-return-node (hd ms') (sourcenode ax)⟩ snoc by blast
next
  assume ms' = ms thus ?thesis by simp
qed
next
case (Call Q r p fs)
with ⟨S,kind ⊢ (ms'',s'') -a'→τ (m'#ms',s')⟩ obtain a''
  where valid-edge a' and a'' ∈ get-return-edges a'
  and hd ms'' = sourcenode a' and m' = targetnode a'
  and ms' = (targetnode a'')#tl ms'' and length ms'' = length s''
  and pred (kind a') s''
  by(auto elim:silent-move.cases simp:intra-kind-def)
from ⟨valid-edge a'⟩ ⟨a'' ∈ get-return-edges a'⟩ have valid-edge a''
  by(rule get-return-edges-valid)
from ⟨valid-edge a''⟩ ⟨valid-edge a'⟩ ⟨a'' ∈ get-return-edges a'⟩
have return-node (targetnode a'') by(fastforce simp:return-node-def)
with ⟨valid-edge a'⟩ ⟨valid-edge a''⟩
  ⟨a'' ∈ get-return-edges a'⟩ ⟨ms' = (targetnode a'')#tl ms''⟩
have call-of-return-node (hd ms') (sourcenode a')
  by(simp add:call-of-return-node-def) blast
with snoc ⟨kind a' = Q:r↔pfs⟩ ⟨m' = targetnode a'⟩ ⟨valid-edge a'⟩
show ?thesis by(fastforce intro:empty-path simp:slp-def same-level-path-def)
next
case (Return Q p f)
with ⟨S,kind ⊢ (ms'',s'') -a'→τ (m'#ms',s')⟩
have valid-edge a' and hd ms'' = sourcenode a'
  and hd(tl ms'') = targetnode a' and m'#ms' = tl ms''
  and length ms'' = length s'' and length s'' = Suc(length s')
  and s' ≠ []
  by(auto elim:silent-move.cases simp:intra-kind-def)
hence ms'' = sourcenode a' # targetnode a' # ms' by(cases ms'') auto
with ⟨length as' < length as⟩ ⟨S,kind ⊢ (m#ms,s) = as'→τ (ms'',s'')⟩ IH
  have ⟨∃ asx ax asx'. as' = asx @ ax # asx' ∧ (∃ Q r p fs. kind ax =
Q:r↔pfs) ∧
  call-of-return-node (targetnode a') (sourcenode ax) ∧
  targetnode ax -asx'→sl* sourcenode a') ∨ ms = targetnode a' # ms'
  apply - apply(erule-tac x=as' in allE) apply clarsimp
  apply(erule-tac x=sourcenode a' in allE)
  apply(erule-tac x=targetnode a' # ms' in allE)

```

by *fastforce*
 thus *?thesis*
 proof
 assume $\exists ax\ r\ p\ fs.\ kind\ ax = Q:r \hookrightarrow pfs \wedge$
 $(\exists Q\ r\ p\ fs.\ kind\ ax = Q:r \hookrightarrow pfs) \wedge$
 $call-of-return-node\ (targetnode\ a')\ (sourcenode\ ax) \wedge$
 $targetnode\ ax -asx' \rightarrow_{sl}^* sourcenode\ a'$
 then obtain $ax\ r\ p\ fs$ where $as' = ax @ ax \# ax'$
 and $\exists Q\ r\ p\ fs.\ kind\ ax = Q:r \hookrightarrow pfs$
 and $call-of-return-node\ (targetnode\ a')\ (sourcenode\ ax)$
 and $targetnode\ ax -asx' \rightarrow_{sl}^* sourcenode\ a'$ by *blast*
 from $\langle as' = ax @ ax \# ax' \rangle$ snoc *have* $length\ ax < length\ as$ by *simp*
 moreover
 from $\langle S, kind \vdash (m \# ms, s) = as \Rightarrow_{\tau} (m' \# ms', s') \rangle$ snoc $\langle as' = ax @ ax \#$
 $asx' \rangle$
 obtain $msx\ sx$ where $S, kind \vdash (m \# ms, s) = as \Rightarrow_{\tau} (msx, sx)$
 and $S, kind \vdash (msx, sx) = ax \# asx' @ [a'] \Rightarrow_{\tau} (m' \# ms', s')$
 by *(fastforce elim:silent-moves-split)*
 from $\langle S, kind \vdash (msx, sx) = ax \# asx' @ [a'] \Rightarrow_{\tau} (m' \# ms', s') \rangle$
 obtain $xs\ x\ ys\ y$ where $S, kind \vdash (msx, sx) -ax \rightarrow_{\tau} (xs, x)$
 and $S, kind \vdash (xs, x) = asx' \Rightarrow_{\tau} (ys, y)$
 and $S, kind \vdash (ys, y) = [a'] \Rightarrow_{\tau} (m' \# ms', s')$
 apply – apply *(erule silent-moves.cases)* apply *auto*
 by *(erule silent-moves-split) auto*
 from $\langle S, kind \vdash (msx, sx) -ax \rightarrow_{\tau} (xs, x) \rangle$ $\langle \exists Q\ r\ p\ fs.\ kind\ ax = Q:r \hookrightarrow pfs \rangle$
 obtain $msx'\ ax'$ where $msx = sourcenode\ ax \# msx'$
 and $ax' \in get-return-edges\ ax$
 and $[simp]: xs = (targetnode\ ax) \# (targetnode\ ax') \# msx'$
 and $length\ x = Suc(length\ sx)$ and $length\ msx = length\ sx$
 apply – apply *(erule silent-move.cases)* apply *(auto simp:intra-kind-def)*
 by *(cases msx, auto) +*
 from $\langle S, kind \vdash (ys, y) = [a'] \Rightarrow_{\tau} (m' \# ms', s') \rangle$ obtain msy
 where $ys = sourcenode\ a' \# msy$
 apply – apply *(erule silent-moves.cases)* apply *auto*
 apply *(erule silent-move.cases)*
 by *(cases ys, auto) +*
 with $\langle S, kind \vdash (xs, x) = asx' \Rightarrow_{\tau} (ys, y) \rangle$
 $\langle targetnode\ ax -asx' \rightarrow_{sl}^* sourcenode\ a' \rangle$
 $\langle xs = (targetnode\ ax) \# (targetnode\ ax') \# msx' \rangle$
 have $(targetnode\ ax') \# msx' = msy$ apply *simp*
 by *(fastforce intro:silent-moves-same-level-path)*
 with $\langle S, kind \vdash (ys, y) = [a'] \Rightarrow_{\tau} (m' \# ms', s') \rangle$ $\langle kind\ a' = Q \leftrightarrow pf \rangle$
 $\langle ys = sourcenode\ a' \# msy \rangle$
 have $m' = targetnode\ a'$ and $msx' = ms'$
 by *(fastforce elim:silent-moves.cases silent-move.cases*
 $simp:intra-kind-def) +$
 with $\langle S, kind \vdash (m \# ms, s) = as \Rightarrow_{\tau} (msx, sx) \rangle$ $\langle msx = sourcenode\ ax \# msx' \rangle$
 have $S, kind \vdash (m \# ms, s) = as \Rightarrow_{\tau} (sourcenode\ ax \# ms', sx)$ by *simp*
 ultimately have $(\exists xs\ x\ xs'. asx = xs @ x \# xs' \wedge$

$(\exists Q r p fs. kind x = Q:r \hookrightarrow pfs) \wedge$
 $call-of-return-node (hd ms') (sourcnode x) \wedge$
 $targetnode x -xs' \rightarrow_{sl}^* sourcnode ax) \vee ms = ms' \text{ using IH}$
by simp blast
thus ?thesis
proof
assume $\exists xs x xs'. asx = xs @ x \# xs' \wedge (\exists Q r p fs. kind x = Q:r \hookrightarrow pfs) \wedge$
 $call-of-return-node (hd ms') (sourcnode x) \wedge$
 $targetnode x -xs' \rightarrow_{sl}^* sourcnode ax$
then obtain $xs x xs'$ **where** $asx = xs @ x \# xs'$
and $\exists Q r p fs. kind x = Q:r \hookrightarrow pfs$
and $call-of-return-node (hd ms') (sourcnode x)$
and $targetnode x -xs' \rightarrow_{sl}^* sourcnode ax$ **by blast**
from $\langle asx = xs @ x \# xs' \rangle \langle as' = asx @ ax \# asx' \rangle snoc$
have $as = xs @ x \# (xs' @ ax \# asx' @ [a'])$ **by simp**
from $\langle S, kind \vdash (m \# ms, s) = as \Rightarrow_{\tau} (m' \# ms', s') \rangle \langle valid-node m \rangle rest$
have $m -as \rightarrow^* m'$ **and** $valid-path-aux cs as$
by $(auto dest:silent-moves-vpa-path[of \text{-----} rs cs]$
 $simp:valid-call-list-def valid-return-list-def targetnodes-def)$
hence $m -as \rightarrow_{\sqrt{}}^* m'$
by $(fastforce intro:valid-path-aux-valid-path simp:vp-def)$
with snoc have $m -as' \rightarrow_{\sqrt{}}^* sourcnode a'$
by $(auto elim:path-split-snoc dest:valid-path-aux-split$
 $simp:vp-def valid-path-def)$
with $\langle as' = asx @ ax \# asx' \rangle$
have $valid-edge ax$ **and** $targetnode ax -asx' \rightarrow^* sourcnode a'$
by $(auto dest:path-split simp:vp-def)$
hence $sourcnode ax -ax \# asx' \rightarrow^* sourcnode a'$
by $(fastforce intro:Cons-path)$
from $\langle valid-edge a' \rangle$ **have** $sourcnode a' -[a'] \rightarrow^* targetnode a'$
by $(rule path-edge)$
with $\langle sourcnode ax -ax \# asx' \rightarrow^* sourcnode a' \rangle$
have $sourcnode ax -(ax \# asx') @ [a'] \rightarrow^* targetnode a'$
by $(rule path-Append)$
from $\langle m -as \rightarrow_{\sqrt{}}^* m' \rangle snoc \langle as' = asx @ ax \# asx' \rangle snoc$
have $valid-path-aux (\ [] @ (upd-cs \ [] asx)) (ax \# asx' @ [a'])$
by $(fastforce dest:valid-path-aux-split simp:vp-def valid-path-def)$
hence $valid-path-aux \ [] (ax \# asx' @ [a'])$
by $(rule valid-path-aux-callstack-prefix)$
with $\langle \exists Q r p fs. kind ax = Q:r \hookrightarrow pfs \rangle$
have $valid-path-aux [ax] (asx' @ [a'])$ **by fastforce**
hence $valid-path-aux (upd-cs [ax] asx') [a']$
by $(rule valid-path-aux-split)$
from $\langle targetnode ax -asx' \rightarrow_{sl}^* sourcnode a' \rangle$
have $same-level-path-aux \ [] asx'$ **and** $upd-cs \ [] asx' = \ []$
by $(simp-all add:slp-def same-level-path-def)$
hence $upd-cs (\ [] @ [ax]) asx' = \ [] @ [ax]$
by $(rule same-level-path-upd-cs-callstack-Append)$
with $\langle valid-path-aux (upd-cs [ax] asx') [a'] \rangle$

```

have valid-path-aux [ax] [a'] by (simp del:valid-path-aux.simps)
with  $\langle \exists Q r p fs. \text{kind } ax = Q:r \leftrightarrow pfs \rangle \langle \text{kind } a' = Q \leftrightarrow pf \rangle$ 
have  $a' \in \text{get-return-edges } ax$  by simp
with  $\langle \text{upd-cs } ([@ax]) \text{ asx}' = []@[ax] \rangle \langle \text{kind } a' = Q \leftrightarrow pf \rangle$ 
have upd-cs [ax] ( $\text{asx}'@[a']$ ) = [] by (fastforce intro:upd-cs-Append)
with  $\langle \exists Q r p fs. \text{kind } ax = Q:r \leftrightarrow pfs \rangle$ 
have upd-cs [] ( $ax \# \text{asx}'@[a']$ ) = [] by fastforce
from  $\langle \text{targetnode } ax - \text{asx}' \rightarrow_{sl} * \text{ sourcenode } a' \rangle$ 
have same-level-path-aux []  $\text{asx}'$  and upd-cs []  $\text{asx}' = []$ 
by (simp-all add:slp-def same-level-path-def)
hence same-level-path-aux ([@ax])  $\text{asx}'$ 
by  $-(\text{rule same-level-path-aux-callstack-Append})$ 
with  $\langle \exists Q r p fs. \text{kind } ax = Q:r \leftrightarrow pfs \rangle \langle \text{kind } a' = Q \leftrightarrow pf \rangle$ 
 $\langle a' \in \text{get-return-edges } ax \rangle \langle \text{upd-cs } ([@ax]) \text{ asx}' = []@[ax] \rangle$ 
have same-level-path-aux [] ( $(ax \# \text{asx}')@[a']$ )
by (fastforce intro:same-level-path-aux-Append)
with  $\langle \text{upd-cs } [] (ax \# \text{asx}'@[a']) = [] \rangle$ 
 $\langle \text{sourcenode } ax - (ax \# \text{asx}')@[a'] \rightarrow * \text{ targetnode } a' \rangle$ 
have sourcenode  $ax - (ax \# \text{asx}')@[a'] \rightarrow_{sl} * \text{ targetnode } a'$ 
by (simp add:slp-def same-level-path-def)
with  $\langle \text{targetnode } x - \text{xs}' \rightarrow_{sl} * \text{ sourcenode } ax \rangle$ 
have targetnode  $x - \text{xs}'@((ax \# \text{asx}')@[a']) \rightarrow_{sl} * \text{ targetnode } a'$ 
by (rule slp-Append)
with  $\langle \exists Q r p fs. \text{kind } x = Q:r \leftrightarrow pfs \rangle$ 
 $\langle \text{call-of-return-node } (hd \text{ ms}') (\text{sourcenode } x) \rangle$ 
 $\langle \text{as} = \text{xs}@x \# (\text{xs}'@ax \# \text{asx}'@[a']) \rangle \langle \text{m}' = \text{targetnode } a' \rangle$ 
show ?thesis by simp blast
next
assume  $ms = ms'$  thus ?thesis by simp
qed
next
assume  $ms = \text{targetnode } a' \# ms'$ 
from  $\langle S, \text{kind } \vdash (ms'', s'') - a' \rightarrow_{\tau} (m' \# ms', s') \rangle \langle \text{kind } a' = Q \leftrightarrow pf \rangle$ 
 $\langle ms'' = \text{sourcenode } a' \# \text{targetnode } a' \# ms' \rangle$ 
have  $\exists m \in \text{set } (\text{targetnode } a' \# ms'). \exists m'. \text{call-of-return-node } m \text{ m}' \wedge$ 
 $m' \notin \lfloor \text{HRB-slice } S \rfloor_{CFG}$ 
by (fastforce elim!:silent-move.cases simp:intra-kind-def)
with  $\langle ms = \text{targetnode } a' \# ms' \rangle$  callstack
have False by fastforce
thus ?thesis by simp
qed
qed
qed
qed
qed

```

lemma *silent-moves-called-node-in-slice1-hd-nodestack-in-slice1*:
assumes $S, \text{kind } \vdash (m \# ms, s) = \text{as} \Rightarrow_{\tau} (m' \# ms', s')$ **and** *valid-node* m

and *CFG-node* $m' \in \text{sum-SDG-slice1 } nx$
and $\forall mx \in \text{set } ms. \exists mx'. \text{call-of-return-node } mx \ mx' \wedge$
 $mx' \in \lfloor \text{HRB-slice } S \rfloor_{CFG}$
and $\forall i < \text{length } rs. rs!i \in \text{get-return-edges } (cs!i)$ **and** $ms = \text{targetnodes } rs$
and *valid-return-list* $rs \ m$ **and** $\text{length } rs = \text{length } cs$
obtains $as' \ a \ as''$ **where** $as = as' @ a \# as''$ **and** $\exists Q \ r \ p \ fs. \text{kind } a = Q:r \hookrightarrow pfs$
and *call-of-return-node* $(hd \ ms')$ (*sourcenode* a)
and *targetnode* $a - as'' \rightarrow_{sl^*} m'$ **and** *CFG-node* (*sourcenode* a) $\in \text{sum-SDG-slice1}$
 nx
 $| \ ms' = ms$
proof(*atomize-elim*)
from $\langle S, \text{kind} \vdash (m \# ms, s) = as \Rightarrow_{\tau} (m' \# ms', s') \rangle \langle \text{valid-node } m \rangle$
 $\langle \forall i < \text{length } rs. rs!i \in \text{get-return-edges } (cs!i) \rangle \langle ms = \text{targetnodes } rs \rangle$
 $\langle \text{valid-return-list } rs \ m \rangle \langle \text{length } rs = \text{length } cs \rangle$
have $m - as \rightarrow^* m'$
by(*auto dest:silent-moves-vpa-path*[*of* - - - - - $rs \ cs$]
simp:valid-call-list-def valid-return-list-def targetnodes-def)
from $\langle S, \text{kind} \vdash (m \# ms, s) = as \Rightarrow_{\tau} (m' \# ms', s') \rangle \langle \text{valid-node } m \rangle$
 $\langle \forall mx \in \text{set } ms. \exists mx'. \text{call-of-return-node } mx \ mx' \wedge mx' \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
 $\langle \forall i < \text{length } rs. rs!i \in \text{get-return-edges } (cs!i) \rangle \langle ms = \text{targetnodes } rs \rangle$
 $\langle \text{valid-return-list } rs \ m \rangle \langle \text{length } rs = \text{length } cs \rangle$
show $(\exists as' \ a \ as''. as = as' @ a \# as'' \wedge (\exists Q \ r \ p \ fs. \text{kind } a = Q:r \hookrightarrow pfs) \wedge$
 $\text{call-of-return-node } (hd \ ms') \ (\text{sourcenode } a) \wedge \text{targetnode } a - as'' \rightarrow_{sl^*} m' \wedge$
 $\text{CFG-node } (\text{sourcenode } a) \in \text{sum-SDG-slice1 } nx) \vee ms' = ms$
proof(*rule silent-moves-call-edge*)
fix $as' \ a \ as''$ **assume** $as = as' @ a \# as''$ **and** $\exists Q \ r \ p \ fs. \text{kind } a = Q:r \hookrightarrow pfs$
and *call-of-return-node* $(hd \ ms')$ (*sourcenode* a)
and *targetnode* $a - as'' \rightarrow_{sl^*} m'$
from $\langle \exists Q \ r \ p \ fs. \text{kind } a = Q:r \hookrightarrow pfs \rangle$ **obtain** $Q \ r \ p \ fs$
where $\text{kind } a = Q:r \hookrightarrow pfs$ **by** *blast*
from $\langle \text{targetnode } a - as'' \rightarrow_{sl^*} m' \rangle$ **obtain** asx **where** $\text{targetnode } a - asx \rightarrow_{sl^*}$
 m'
by $-(\text{erule same-level-path-inner-path})$
from $\langle m - as \rightarrow^* m' \rangle \langle as = as' @ a \# as'' \rangle$ **have** *valid-edge* a
by(*fastforce dest:path-split simp:vp-def*)
have $m' \neq (-Exit)$
proof
assume $m' = (-Exit)$
have *get-proc* $(-Exit) = \text{Main}$ **by**(*rule get-proc-Exit*)
from $\langle \text{targetnode } a - asx \rightarrow_{sl^*} m' \rangle$
have *get-proc* $(\text{targetnode } a) = \text{get-proc } m'$ **by**(*rule intra-path-get-procs*)
with $\langle m' = (-Exit) \rangle \langle \text{get-proc } (-Exit) = \text{Main} \rangle$
have *get-proc* $(\text{targetnode } a) = \text{Main}$ **by** *simp*
with $\langle \text{kind } a = Q:r \hookrightarrow pfs \rangle \langle \text{valid-edge } a \rangle$
have $\text{kind } a = Q:r \hookrightarrow \text{Main}fs$ **by**(*fastforce dest:get-proc-call*)
with $\langle \text{valid-edge } a \rangle$ **show** *False* **by**(*rule Main-no-call-target*)
qed
show *?thesis*
proof(*cases targetnode a = m'*)

```

case True
with  $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q:r \hookrightarrow pfs \rangle$ 
have CFG-node (sourcenode a) s-p  $\rightarrow_{\text{call}}$  CFG-node m'
  by (fastforce intro:sum-SDG-call-edge)
with  $\langle \text{CFG-node } m' \in \text{sum-SDG-slice1 } nx \rangle$ 
have CFG-node (sourcenode a)  $\in \text{sum-SDG-slice1 } nx$  by  $\neg(\text{rule call-slice1})$ 
with  $\langle as = as' @ a \# as'' \rangle \langle \exists Q r p fs. \text{kind } a = Q:r \hookrightarrow pfs \rangle$ 
   $\langle \text{call-of-return-node } (\text{hd } ms') (\text{sourcenode } a) \rangle$ 
   $\langle \text{targetnode } a -as'' \rightarrow_{sl}^* m' \rangle$  show ?thesis by blast
next
case False
with  $\langle \text{targetnode } a -asx \rightarrow_{t,*} m' \rangle \langle m' \neq (-Exit) \rangle \langle \text{valid-edge } a \rangle \langle \text{kind } a =$ 
 $Q:r \hookrightarrow pfs \rangle$ 
obtain ns where CFG-node (targetnode a) cd-ns  $\rightarrow_d^*$  CFG-node m'
  by (fastforce elim!:in-proc-cdep-SDG-path)
hence CFG-node (targetnode a) is-ns  $\rightarrow_d^*$  CFG-node m'
by (fastforce intro:intra-SDG-path-is-SDG-path cdep-SDG-path-intra-SDG-path)
with  $\langle \text{CFG-node } m' \in \text{sum-SDG-slice1 } nx \rangle$ 
have CFG-node (targetnode a)  $\in \text{sum-SDG-slice1 } nx$ 
  by  $\neg(\text{rule is-SDG-path-slice1})$ 
from  $\langle \text{valid-edge } a \rangle \langle \text{kind } a = Q:r \hookrightarrow pfs \rangle$ 
have CFG-node (sourcenode a) s-p  $\rightarrow_{\text{call}}$  CFG-node (targetnode a)
  by (fastforce intro:sum-SDG-call-edge)
with  $\langle \text{CFG-node } (\text{targetnode } a) \in \text{sum-SDG-slice1 } nx \rangle$ 
have CFG-node (sourcenode a)  $\in \text{sum-SDG-slice1 } nx$  by  $\neg(\text{rule call-slice1})$ 
with  $\langle as = as' @ a \# as'' \rangle \langle \exists Q r p fs. \text{kind } a = Q:r \hookrightarrow pfs \rangle$ 
   $\langle \text{call-of-return-node } (\text{hd } ms') (\text{sourcenode } a) \rangle$ 
   $\langle \text{targetnode } a -as'' \rightarrow_{sl}^* m' \rangle$  show ?thesis by blast
qed
next
assume ms' = ms thus ?thesis by simp
qed
qed

```

lemma *silent-moves-called-node-in-slice1-nodestack-in-slice1*:

```

 $\llbracket S, \text{kind} \vdash (m \# ms, s) = as \Rightarrow_{\tau} (m' \# ms', s') ; \text{valid-node } m ;$ 
 $\text{CFG-node } m' \in \text{sum-SDG-slice1 } nx ; nx \in S ;$ 
 $\forall mx \in \text{set } ms. \exists mx'. \text{call-of-return-node } mx \ mx' \wedge mx' \in \llbracket \text{HRB-slice } S \rrbracket_{\text{CFG}} ;$ 
 $\forall i < \text{length } rs. rs!i \in \text{get-return-edges } (cs!i) ; ms = \text{targetnodes } rs ;$ 
 $\text{valid-return-list } rs \ m ; \text{length } rs = \text{length } cs \rrbracket$ 
 $\implies \forall mx \in \text{set } ms'. \exists mx'. \text{call-of-return-node } mx \ mx' \wedge mx' \in \llbracket \text{HRB-slice}$ 
 $S \rrbracket_{\text{CFG}}$ 

```

proof (*induct* *ms'* *arbitrary:as* *m'* *s'*)

case (*Cons* *mx* *msx*)

note *IH* = $\langle \wedge as \ m' \ s'. \llbracket S, \text{kind} \vdash (m \# ms, s) = as \Rightarrow_{\tau} (m' \# msx, s') ; \text{valid-node}$

m ;

$\text{CFG-node } m' \in \text{sum-SDG-slice1 } nx ; nx \in S ;$

$\forall mx \in \text{set } ms. \exists mx'. \text{call-of-return-node } mx \ mx' \wedge mx' \in \llbracket \text{HRB-slice } S \rrbracket_{\text{CFG}} ;$

$\forall i < \text{length } rs. rs ! i \in \text{get-return-edges } (cs ! i); ms = \text{targetnodes } rs;$
 $\text{valid-return-list } rs \ m; \text{length } rs = \text{length } cs]$
 $\implies \forall mx \in \text{set } msx. \exists mx'. \text{call-of-return-node } mx \ mx' \wedge mx' \in \lfloor \text{HRB-slice } S \rfloor_{CFG}$

from $\langle S, kind \vdash (m \# ms, s) = as \Rightarrow_{\tau} (m' \# mx \# msx, s') \rangle \langle \text{valid-node } m \rangle$
 $\langle \text{CFG-node } m' \in \text{sum-SDG-slice1 } nx \rangle$
 $\langle \forall mx \in \text{set } ms. \exists mx'. \text{call-of-return-node } mx \ mx' \wedge mx' \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
 $\langle \forall i < \text{length } rs. rs ! i \in \text{get-return-edges } (cs ! i) \rangle \langle ms = \text{targetnodes } rs \rangle$
 $\langle \text{valid-return-list } rs \ m \rangle \langle \text{length } rs = \text{length } cs \rangle$

show *?case*

proof(*rule silent-moves-called-node-in-slice1-hd-nodestack-in-slice1*)

fix $as' \ a \ as''$ **assume** $as = as' @ a \# as''$ **and** $\exists Q \ r \ p \ fs. kind \ a = Q : r \hookrightarrow p \ fs$
and $\text{call-of-return-node } (\text{hd } (mx \# msx)) \ (\text{sourcenode } a)$
and $\text{CFG-node } (\text{sourcenode } a) \in \text{sum-SDG-slice1 } nx$
and $\text{targetnode } a \xrightarrow{as''}_{sl^*} m'$

from $\langle \text{CFG-node } (\text{sourcenode } a) \in \text{sum-SDG-slice1 } nx \rangle \langle nx \in S \rangle$

have $\text{sourcenode } a \in \lfloor \text{HRB-slice } S \rfloor_{CFG}$
by(*fastforce intro:combSlice-refl simp:SDG-to-CFG-set-def HRB-slice-def*)

from $\langle S, kind \vdash (m \# ms, s) = as \Rightarrow_{\tau} (m' \# mx \# msx, s') \rangle \langle as = as' @ a \# as'' \rangle$

obtain $xs \ x$ **where** $S, kind \vdash (m \# ms, s) = as' \Rightarrow_{\tau} (xs, x)$
and $S, kind \vdash (xs, x) = a \# as'' \Rightarrow_{\tau} (m' \# mx \# msx, s')$
by(*fastforce elim:silent-moves-split*)

from $\langle S, kind \vdash (xs, x) = a \# as'' \Rightarrow_{\tau} (m' \# mx \# msx, s') \rangle$

obtain $ys \ y$ **where** $S, kind \vdash (xs, x) \xrightarrow{a} (ys, y)$
and $S, kind \vdash (ys, y) = as'' \Rightarrow_{\tau} (m' \# mx \# msx, s')$
by(*fastforce elim:silent-moves.cases*)

from $\langle S, kind \vdash (xs, x) \xrightarrow{a} (ys, y) \rangle \langle \exists Q \ r \ p \ fs. kind \ a = Q : r \hookrightarrow p \ fs \rangle$

obtain $xs' \ a'$ **where** $xs = \text{sourcenode } a \# xs'$
and $ys = \text{targetnode } a \# \text{targetnode } a' \# xs'$
apply -- apply (*erule silent-move.cases*) **apply**(*auto simp:intra-kind-def*)
by(*cases xs, auto*) $+$

from $\langle S, kind \vdash (ys, y) = as'' \Rightarrow_{\tau} (m' \# mx \# msx, s') \rangle$
 $\langle ys = \text{targetnode } a \# \text{targetnode } a' \# xs' \rangle \langle \text{targetnode } a \xrightarrow{as''}_{sl^*} m' \rangle$

have $mx = \text{targetnode } a'$ **and** $xs' = msx$
by(*auto dest:silent-moves-same-level-path*)

with $\langle xs = \text{sourcenode } a \# xs' \rangle \langle S, kind \vdash (m \# ms, s) = as' \Rightarrow_{\tau} (xs, x) \rangle$

have $S, kind \vdash (m \# ms, s) = as' \Rightarrow_{\tau} (\text{sourcenode } a \# msx, x)$ **by** *simp*

from $\text{IH}[OF \langle S, kind \vdash (m \# ms, s) = as' \Rightarrow_{\tau} (\text{sourcenode } a \# msx, x) \rangle$
 $\langle \text{valid-node } m \rangle \langle \text{CFG-node } (\text{sourcenode } a) \in \text{sum-SDG-slice1 } nx \rangle \langle nx \in S \rangle$
 $\langle \forall mx \in \text{set } ms. \exists mx'. \text{call-of-return-node } mx \ mx' \wedge mx' \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
 $\langle \forall i < \text{length } rs. rs ! i \in \text{get-return-edges } (cs ! i) \rangle \langle ms = \text{targetnodes } rs \rangle$
 $\langle \text{valid-return-list } rs \ m \rangle \langle \text{length } rs = \text{length } cs \rangle]$

have $\text{callstack} : \forall mx \in \text{set } msx.$
 $\exists mx'. \text{call-of-return-node } mx \ mx' \wedge mx' \in \lfloor \text{HRB-slice } S \rfloor_{CFG} .$

with $\langle as = as' @ a \# as'' \rangle \langle \text{call-of-return-node } (\text{hd } (mx \# msx)) \ (\text{sourcenode } a) \rangle$
 $\langle \text{sourcenode } a \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$ **show** *?thesis* **by** *fastforce*

next

assume $mx \# msx = ms$

with $\langle \forall mx \in \text{set } ms. \exists mx'. \text{call-of-return-node } mx \ mx' \wedge mx' \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$

$S \rfloor_{CFG}$
show *?thesis* **by** *fastforce*
qed
qed *simp*

lemma *silent-moves-slice-intra-path*:

assumes $S, \text{slice-kind } S \vdash (m \# ms, s) = as \Rightarrow_{\tau} (m' \# ms', s')$
and $\forall mx \in \text{set } ms. \exists mx'. \text{call-of-return-node } mx \ mx' \wedge mx' \in \lfloor \text{HRB-slice } S \rfloor_{CFG}$
shows $\forall a \in \text{set } as. \text{intra-kind } (\text{kind } a)$
proof(*rule ccontr*)
assume $\neg (\forall a \in \text{set } as. \text{intra-kind } (\text{kind } a))$
hence $\exists a \in \text{set } as. \neg \text{intra-kind } (\text{kind } a)$ **by** *fastforce*
then obtain $asx \ ax \ asx'$ **where** $as = asx @ ax \# asx'$
and $\forall a \in \text{set } asx. \text{intra-kind } (\text{kind } a)$ **and** $\neg \text{intra-kind } (\text{kind } ax)$
by(*fastforce elim!:split-list-first-propE*)
from $\langle S, \text{slice-kind } S \vdash (m \# ms, s) = as \Rightarrow_{\tau} (m' \# ms', s') \rangle \langle as = asx @ ax \# asx' \rangle$
obtain $msx \ sx \ msx' \ sx'$ **where** $S, \text{slice-kind } S \vdash (m \# ms, s) = asx \Rightarrow_{\tau} (msx, sx)$
and $S, \text{slice-kind } S \vdash (msx, sx) - ax \rightarrow_{\tau} (msx', sx')$
and $S, \text{slice-kind } S \vdash (msx', sx') = asx' \Rightarrow_{\tau} (m' \# ms', s')$
by(*auto elim!:silent-moves-split elim:silent-moves.cases*)
from $\langle S, \text{slice-kind } S \vdash (msx, sx) - ax \rightarrow_{\tau} (msx', sx') \rangle$ **obtain** xs
where [*simp*]: $msx = \text{sourcenode } ax \# xs$ **by**(*cases msx*)(*auto elim:silent-move.cases*)
from $\langle S, \text{slice-kind } S \vdash (m \# ms, s) = asx \Rightarrow_{\tau} (msx, sx) \rangle \langle \forall a \in \text{set } asx. \text{intra-kind } (\text{kind } a) \rangle$
have [*simp*]: $xs = ms$ **by**(*fastforce dest:silent-moves-intra-path*)
show *False*
proof(*cases kind ax rule:edge-kind-cases*)
case *Intra* **with** $\langle \neg \text{intra-kind } (\text{kind } ax) \rangle$ **show** *False* **by** *simp*
next
case (*Call* $Q \ r \ p \ fs$)
with $\langle S, \text{slice-kind } S \vdash (msx, sx) - ax \rightarrow_{\tau} (msx', sx') \rangle$
 $\langle \forall mx \in \text{set } ms. \exists mx'. \text{call-of-return-node } mx \ mx' \wedge mx' \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
have $\text{sourcenode } ax \notin \lfloor \text{HRB-slice } S \rfloor_{CFG}$ **and** $\text{pred } (\text{slice-kind } S \ ax) \ sx$
by(*auto elim!:silent-move.cases simp:intra-kind-def*)
from $\langle \text{sourcenode } ax \notin \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle \langle \text{kind } ax = Q : r \hookrightarrow pfs \rangle$
have $\text{slice-kind } S \ ax = (\lambda cf. \text{False}) : r \hookrightarrow pfs$
by(*rule slice-kind-Call*)
with $\langle \text{pred } (\text{slice-kind } S \ ax) \ sx \rangle$ **show** *False* **by**(*cases sx*) *auto*
next
case (*Return* $Q \ p \ f$)
with $\langle S, \text{slice-kind } S \vdash (msx, sx) - ax \rightarrow_{\tau} (msx', sx') \rangle$
 $\langle \forall mx \in \text{set } ms. \exists mx'. \text{call-of-return-node } mx \ mx' \wedge mx' \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
show *False* **by**(*fastforce elim!:silent-move.cases simp:intra-kind-def*)
qed
qed

lemma *silent-moves-slice-keeps-state*:

assumes $S, \text{slice-kind } S \vdash (m \# ms, s) = as \Rightarrow_{\tau} (m' \# ms', s')$
and $\forall mx \in \text{set } ms. \exists mx'. \text{call-of-return-node } mx \ mx' \wedge mx' \in \lfloor \text{HRB-slice } S \rfloor_{CFG}$
shows $s = s'$
proof –
from *assms* **have** $\forall a \in \text{set } as. \text{intra-kind } (kind \ a)$
by (*rule silent-moves-slice-intra-path*)
with *assms* **show** *?thesis*
proof (*induct S slice-kind S m#ms s as m'#ms' s'*
arbitrary:m rule:silent-moves.induct)
case (*silent-moves-Nil sx n_c*) **thus** *?case* **by** *simp*
next
case (*silent-moves-Cons S sx a msx' sx' as s''*)
note $IH = \langle \bigwedge m. \llbracket msx' = m \# ms; \forall mx \in \text{set } ms. \exists mx'. \text{call-of-return-node } mx \ mx' \wedge mx' \in \lfloor \text{HRB-slice } S \rfloor_{CFG}; \forall a \in \text{set } as. \text{intra-kind } (kind \ a) \rrbracket \implies sx' = s'' \rangle$
note $\text{callstack} = \langle \forall mx \in \text{set } ms. \exists mx'. \text{call-of-return-node } mx \ mx' \wedge mx' \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
from $\langle \forall a \in \text{set } (a \# as). \text{intra-kind } (kind \ a) \rangle$ **have** *intra-kind (kind a)*
and $\forall a \in \text{set } as. \text{intra-kind } (kind \ a)$ **by** *simp-all*
from $\langle S, \text{slice-kind } S \vdash (m \# ms, sx) - a \rightarrow_{\tau} (msx', sx') \langle \text{intra-kind } (kind \ a) \rangle \text{callstack} \rangle$
have [*simp*]: $msx' = \text{targetnode } a \# ms$ **and** $sx' = \text{transfer } (slice-kind \ S \ a) \ sx$
and $\text{sourcenode } a \notin \lfloor \text{HRB-slice } S \rfloor_{CFG}$ **and** *valid-edge a* **and** $sx \neq []$
by (*auto elim!: silent-move.cases simp: intra-kind-def*)
from $IH[OF \langle msx' = \text{targetnode } a \# ms \rangle \text{callstack} \langle \forall a \in \text{set } as. \text{intra-kind } (kind \ a) \rangle]$
have $sx' = s''$.
from *intra-kind (kind a)*
have $sx = sx'$
proof (*cases kind a*)
case (*UpdateEdge f'*)
with $\langle \text{sourcenode } a \notin \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
have $\text{slice-kind } S \ a = \uparrow id$ **by** (*rule slice-kind-Upd*)
with $\langle sx' = \text{transfer } (slice-kind \ S \ a) \ sx \rangle \langle sx \neq [] \rangle$
show *?thesis* **by** (*cases sx*) *auto*
next
case (*PredicateEdge Q*)
with $\langle \text{sourcenode } a \notin \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle \langle \text{valid-edge } a \rangle$
obtain Q' **where** $\text{slice-kind } S \ a = (Q')_{\surd}$
by $-(\text{erule } kind\text{-Predicate-notin-slice-slice-kind-Predicate})$
with $\langle sx' = \text{transfer } (slice-kind \ S \ a) \ sx \rangle \langle sx \neq [] \rangle$
show *?thesis* **by** (*cases sx*) *auto*
qed (*auto simp: intra-kind-def*)
with $\langle sx' = s'' \rangle$ **show** *?case* **by** *simp*
qed
qed

1.14.2 Definition of *slice-edges*

definition *slice-edge* :: 'node SDG-node set \Rightarrow 'edge list \Rightarrow 'edge \Rightarrow bool
where *slice-edge* S cs $a \equiv (\forall c \in set\ cs. sourcenode\ c \in \lfloor HRB\text{-}slice\ S \rfloor_{CFG}) \wedge$
(case (kind a) of $Q \leftrightarrow pf \Rightarrow True$ | $- \Rightarrow sourcenode\ a \in \lfloor HRB\text{-}slice\ S \rfloor_{CFG}$)

lemma *silent-move-no-slice-edge*:

$\llbracket S, f \vdash (ms, s) -a \rightarrow_{\tau} (ms', s'); tl\ ms = targetnodes\ rs; length\ rs = length\ cs;$
 $\forall i < length\ cs. call\ of\ return\ node\ (tl\ ms!\ i)\ (sourcenode\ (cs!\ i)) \rrbracket$
 $\implies \neg slice\ edge\ S\ cs\ a$

proof(*induct rule:silent-move.induct*)

case (*silent-move-intra* $f\ a\ s\ s'\ ms\ S\ ms'$)

note $disj = \langle (\exists m \in set\ (tl\ ms). \exists m'. call\ of\ return\ node\ m\ m' \wedge m' \notin \lfloor HRB\text{-}slice\ S \rfloor_{CFG})$

$\vee hd\ ms \notin \lfloor HRB\text{-}slice\ S \rfloor_{CFG} \rangle$

from $\langle pred\ (f\ a)\ s \rangle \langle length\ ms = length\ s \rangle$ **obtain** $x\ xs$ **where** $ms = x\#\ xs$

by(*cases* ms) *auto*

from $\langle length\ rs = length\ cs \rangle \langle tl\ ms = targetnodes\ rs \rangle$

have $length\ (tl\ ms) = length\ cs$ **by**(*simp add:targetnodes-def*)

from $disj$ **show** $?case$

proof

assume $\exists m \in set\ (tl\ ms). \exists m'. call\ of\ return\ node\ m\ m' \wedge m' \notin \lfloor HRB\text{-}slice\ S \rfloor_{CFG}$

with $\langle \forall i < length\ cs. call\ of\ return\ node\ (tl\ ms!\ i)\ (sourcenode\ (cs!\ i)) \rangle$

$\langle length\ (tl\ ms) = length\ cs \rangle$

have $\exists c \in set\ cs. sourcenode\ c \notin \lfloor HRB\text{-}slice\ S \rfloor_{CFG}$

apply(*auto simp:in-set-conv-nth*)

by(*erule-tac x=i in alle*) *auto*

thus $?thesis$ **by**(*auto simp:slice-edge-def*)

next

assume $hd\ ms \notin \lfloor HRB\text{-}slice\ S \rfloor_{CFG}$

with $\langle hd\ ms = sourcenode\ a \rangle \langle intra\ kind\ (kind\ a) \rangle$

show $?case$ **by**(*auto simp:slice-edge-def simp:intra-kind-def*)

qed

next

case (*silent-move-call* $f\ a\ s\ s'\ Q\ r\ p\ fs\ a'\ ms\ S\ ms'$)

note $disj = \langle (\exists m \in set\ (tl\ ms). \exists m'. call\ of\ return\ node\ m\ m' \wedge m' \notin \lfloor HRB\text{-}slice\ S \rfloor_{CFG})$

$\vee hd\ ms \notin \lfloor HRB\text{-}slice\ S \rfloor_{CFG} \rangle$

from $\langle pred\ (f\ a)\ s \rangle \langle length\ ms = length\ s \rangle$ **obtain** $x\ xs$ **where** $ms = x\#\ xs$

by(*cases* ms) *auto*

from $\langle length\ rs = length\ cs \rangle \langle tl\ ms = targetnodes\ rs \rangle$

have $length\ (tl\ ms) = length\ cs$ **by**(*simp add:targetnodes-def*)

from $disj$ **show** $?case$

proof

assume $\exists m \in set\ (tl\ ms). \exists m'. call\ of\ return\ node\ m\ m' \wedge m' \notin \lfloor HRB\text{-}slice\ S \rfloor_{CFG}$

with $\langle \forall i < length\ cs. call\ of\ return\ node\ (tl\ ms!\ i)\ (sourcenode\ (cs!\ i)) \rangle$

$\langle length\ (tl\ ms) = length\ cs \rangle$

```

have  $\exists c \in \text{set } cs. \text{ sourcenode } c \notin \lfloor \text{HRB-slice } S \rfloor_{CFG}$ 
  apply(auto simp:in-set-conv-nth)
  by(erule-tac x=i in allE) auto
thus ?thesis by(auto simp:slice-edge-def)
next
  assume  $hd \ ms \notin \lfloor \text{HRB-slice } S \rfloor_{CFG}$ 
  with  $\langle hd \ ms = \text{ sourcenode } a \rangle \langle kind \ a = Q:r \hookrightarrow pfs \rangle$ 
  show ?case by(auto simp:slice-edge-def)
qed
next
case (silent-move-return f a s s' Q p f' ms S ms')
from  $\langle pred \ (f \ a) \ s \rangle \langle length \ ms = length \ s \rangle$  obtain  $x \ xs$  where  $ms = x \# \ xs$ 
  by(cases ms) auto
from  $\langle length \ rs = length \ cs \rangle \langle tl \ ms = \text{ targetnodes } rs \rangle$ 
have  $length \ (tl \ ms) = length \ cs$  by(simp add:targetnodes-def)
from  $\langle \forall i < length \ cs. \text{ call-of-return-node } (tl \ ms \ ! \ i) \ (\text{ sourcenode } (cs \ ! \ i)) \rangle$ 
   $\langle \exists m \in \text{set } (tl \ ms). \exists m'. \text{ call-of-return-node } m \ m' \wedge m' \notin \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$ 
   $\langle length \ (tl \ ms) = length \ cs \rangle$ 
have  $\exists c \in \text{set } cs. \text{ sourcenode } c \notin \lfloor \text{HRB-slice } S \rfloor_{CFG}$ 
  apply(auto simp:in-set-conv-nth)
  by(erule-tac x=i in allE) auto
thus ?case by(auto simp:slice-edge-def)
qed

```

lemma *observable-move-slice-edge*:

$\llbracket S, f \vdash (ms, s) - a \rightarrow (ms', s'); tl \ ms = \text{ targetnodes } rs; length \ rs = length \ cs;$
 $\forall i < length \ cs. \text{ call-of-return-node } (tl \ ms \ ! \ i) \ (\text{ sourcenode } (cs \ ! \ i)) \rrbracket$
 $\implies \text{ slice-edge } S \ cs \ a$

proof(*induct rule:observable-move.induct*)

case (*observable-move-intra f a s s' ms S ms'*)

from $\langle pred \ (f \ a) \ s \rangle \langle length \ ms = length \ s \rangle$ **obtain** $x \ xs$ **where** $ms = x \# \ xs$
by(*cases ms*) *auto*

from $\langle length \ rs = length \ cs \rangle \langle tl \ ms = \text{ targetnodes } rs \rangle$

have $length \ (tl \ ms) = length \ cs$ **by**(*simp add:targetnodes-def*)

with $\langle \forall m \in \text{set } (tl \ ms). \exists m'. \text{ call-of-return-node } m \ m' \wedge m' \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
 $\langle \forall i < length \ cs. \text{ call-of-return-node } (tl \ ms \ ! \ i) \ (\text{ sourcenode } (cs \ ! \ i)) \rangle$

have $\forall c \in \text{set } cs. \text{ sourcenode } c \in \lfloor \text{HRB-slice } S \rfloor_{CFG}$

apply(*auto simp:in-set-conv-nth*)

by(*erule-tac x=i in allE*) *auto*

with $\langle hd \ ms = \text{ sourcenode } a \rangle \langle hd \ ms \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle \langle \text{intra-kind } (kind \ a) \rangle$

show *?case* **by**(*auto simp:slice-edge-def simp:intra-kind-def*)

next

case (*observable-move-call f a s s' Q r p fs a' ms S ms'*)

from $\langle pred \ (f \ a) \ s \rangle \langle length \ ms = length \ s \rangle$ **obtain** $x \ xs$ **where** $ms = x \# \ xs$
by(*cases ms*) *auto*

from $\langle length \ rs = length \ cs \rangle \langle tl \ ms = \text{ targetnodes } rs \rangle$

have $length \ (tl \ ms) = length \ cs$ **by**(*simp add:targetnodes-def*)

with $\langle \forall m \in \text{set } (tl \ ms). \exists m'. \text{ call-of-return-node } m \ m' \wedge m' \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$

```

  ⟨∀i<length cs. call-of-return-node (tl ms!i) (sourcenode (cs!i))⟩
have ∀c ∈ set cs. sourcenode c ∈ [HRB-slice S]CFG
  apply(auto simp:in-set-conv-nth)
  by(erule-tac x=i in allE) auto
with ⟨hd ms = sourcenode a⟩ ⟨hd ms ∈ [HRB-slice S]CFG⟩ ⟨kind a = Q:r↔pfs⟩
show ?case by(auto simp:slice-edge-def)
next
case (observable-move-return f a s s' Q p f' ms S ms')
from ⟨pred (f a) s⟩ ⟨length ms = length s⟩ obtain x xs where ms = x#xs
  by(cases ms) auto
from ⟨length rs = length cs⟩ ⟨tl ms = targetnodes rs⟩
have length (tl ms) = length cs by(simp add:targetnodes-def)
with ⟨∀m∈set (tl ms). ∃m'. call-of-return-node m m' ∧ m' ∈ [HRB-slice S]CFG⟩
  ⟨∀i<length cs. call-of-return-node (tl ms!i) (sourcenode (cs!i))⟩
have ∀c ∈ set cs. sourcenode c ∈ [HRB-slice S]CFG
  apply(auto simp:in-set-conv-nth)
  by(erule-tac x=i in allE) auto
with ⟨kind a = Q↔pf'⟩ show ?case by(auto simp:slice-edge-def)
qed

```

```

function slice-edges :: 'node SDG-node set ⇒ 'edge list ⇒ 'edge list ⇒ 'edge list
where slice-edges S cs [] = []
  | slice-edge S cs a ⇒
    slice-edges S cs (a#as) = a#slice-edges S (upd-cs cs [a]) as
  | ¬ slice-edge S cs a ⇒
    slice-edges S cs (a#as) = slice-edges S (upd-cs cs [a]) as
by(atomize-elim)(auto,case-tac b,auto)
termination by(lexicographic-order)

```

lemma slice-edges-Append:

```

[[slice-edges S cs as = as'; slice-edges S (upd-cs cs as) asx = asx]]
⇒ slice-edges S cs (as@asx) = as'@asx'

```

proof(induct as arbitrary:cs as')

case Nil **thus** ?case **by** simp

next

case (Cons x xs)

note IH = ⟨∧cs as'. [[slice-edges S cs xs = as']

slice-edges S (upd-cs cs xs) asx = asx']

⇒ slice-edges S cs (xs @ asx) = as' @ asx'⟩

from ⟨slice-edges S (upd-cs cs (x # xs)) asx = asx'⟩

have slice-edges S (upd-cs (upd-cs cs [x]) xs) asx = asx'

by(cases kind x)(auto,cases cs,auto)

show ?case

proof(cases slice-edge S cs x)

case True

with ⟨slice-edges S cs (x # xs) = as'⟩

```

have  $x \# \text{slice-edges } S \text{ (upd-cs cs [x]) } xs = as'$  by simp
then obtain  $xs'$  where  $as' = x \# xs'$ 
  and  $\text{slice-edges } S \text{ (upd-cs cs [x]) } xs = xs'$  by  $(\text{cases } as')$  auto
from  $IH[OF \langle \text{slice-edges } S \text{ (upd-cs cs [x]) } xs = xs' \rangle$ 
   $\langle \text{slice-edges } S \text{ (upd-cs (upd-cs cs [x]) } xs) asx = asx' \rangle]$ 
have  $\text{slice-edges } S \text{ (upd-cs cs [x]) } (xs @ asx) = xs' @ asx'$  .
with  $True \langle as' = x \# xs' \rangle$  show  $?thesis$  by simp
next
  case  $False$ 
  with  $\langle \text{slice-edges } S \text{ cs } (x \# xs) = as' \rangle$ 
  have  $\text{slice-edges } S \text{ (upd-cs cs [x]) } xs = as'$  by simp
  from  $IH[OF \text{ this } \langle \text{slice-edges } S \text{ (upd-cs (upd-cs cs [x]) } xs) asx = asx' \rangle]$ 
  have  $\text{slice-edges } S \text{ (upd-cs cs [x]) } (xs @ asx) = as' @ asx'$  .
  with  $False$  show  $?thesis$  by simp
qed
qed

```

```

lemma  $\text{slice-edges-Nil-split}$ :
   $\text{slice-edges } S \text{ cs } (as @ as') = []$ 
   $\implies \text{slice-edges } S \text{ cs } as = [] \wedge \text{slice-edges } S \text{ (upd-cs cs } as) as' = []$ 
apply  $(\text{induct as arbitrary:cs})$ 
apply  $\text{clarsimp}$ 
apply  $(\text{case-tac slice-edge } S \text{ cs } a)$  apply  $\text{auto}$ 
apply  $(\text{case-tac kind } a)$  apply  $\text{auto}$ 
apply  $(\text{case-tac cs})$  apply  $\text{auto}$ 
done

```

```

lemma  $\text{slice-intra-edges-no-nodes-in-slice}$ :
   $[\text{slice-edges } S \text{ cs } as = []; \forall a \in \text{set } as. \text{intra-kind } (kind \ a);$ 
   $\forall c \in \text{set } cs. \text{sourcenode } c \in [\text{HRB-slice } S]_{CFG}]$ 
   $\implies \forall nx \in \text{set}(\text{sourcenodes } as). nx \notin [\text{HRB-slice } S]_{CFG}$ 
proof  $(\text{induct as})$ 
  case  $Nil$  thus  $?case$  by  $(\text{fastforce simp:sourcenodes-def})$ 
next
  case  $(\text{Cons } a' as')$ 
  note  $IH = \langle [\text{slice-edges } S \text{ cs } as' = []; \forall a \in \text{set } as'. \text{intra-kind } (kind \ a);$ 
   $\forall c \in \text{set } cs. \text{sourcenode } c \in [\text{HRB-slice } S]_{CFG}]$ 
   $\implies \forall nx \in \text{set}(\text{sourcenodes } as'). nx \notin [\text{HRB-slice } S]_{CFG} \rangle$ 
  from  $\langle \forall a \in \text{set} \ (a' \# as'). \text{intra-kind } (kind \ a) \rangle$ 
  have  $\text{intra-kind } (kind \ a')$  and  $\forall a \in \text{set } as'. \text{intra-kind } (kind \ a)$  by  $\text{simp-all}$ 
  from  $\langle \text{slice-edges } S \text{ cs } (a' \# as') = [] \rangle \langle \text{intra-kind } (kind \ a') \rangle$ 
   $\langle \forall c \in \text{set } cs. \text{sourcenode } c \in [\text{HRB-slice } S]_{CFG} \rangle$ 
  have  $\text{sourcenode } a' \notin [\text{HRB-slice } S]_{CFG}$  and  $\text{slice-edges } S \text{ cs } as' = []$ 
  by  $(\text{cases slice-edge } S \text{ cs } a', \text{auto simp:intra-kind-def slice-edge-def})+$ 
  from  $IH[OF \langle \text{slice-edges } S \text{ cs } as' = [] \rangle \langle \forall a \in \text{set } as'. \text{intra-kind } (kind \ a) \rangle$ 
   $\langle \forall c \in \text{set } cs. \text{sourcenode } c \in [\text{HRB-slice } S]_{CFG} \rangle]$ 
  have  $\forall nx \in \text{set}(\text{sourcenodes } as'). nx \notin [\text{HRB-slice } S]_{CFG}$  .

```

with $\langle \text{sourcenode } a' \notin [HRB\text{-slice } S]_{CFG} \rangle$ **show** $?case$ **by** $(\text{simp add:sourcenodes-def})$
qed

lemma *silent-moves-no-slice-edges*:

$\llbracket S, f \vdash (ms, s) = as \Rightarrow_{\tau} (ms', s'); \text{tl } ms = \text{targetnodes } rs; \text{length } rs = \text{length } cs;$
 $\forall i < \text{length } cs. \text{call-of-return-node } (\text{tl } ms ! i) (\text{sourcenode } (cs ! i)) \rrbracket$
 $\implies \text{slice-edges } S \text{ cs } as = [] \wedge (\exists rs'. \text{tl } ms' = \text{targetnodes } rs' \wedge$
 $\text{length } rs' = \text{length } (\text{upd-cs } cs \text{ as}) \wedge (\forall i < \text{length } (\text{upd-cs } cs \text{ as}).$
 $\text{call-of-return-node } (\text{tl } ms' ! i) (\text{sourcenode } ((\text{upd-cs } cs \text{ as}) ! i))))$

proof $(\text{induct arbitrary:rs cs rule:silent-moves.induct})$

case $(\text{silent-moves-Cons } S \text{ f } ms \text{ s } a \text{ ms}' \text{ s}' \text{ as } ms'' \text{ s}'')$

from $\langle S, f \vdash (ms, s) - a \rightarrow_{\tau} (ms', s') \rangle \langle \text{tl } ms = \text{targetnodes } rs \rangle \langle \text{length } rs = \text{length } cs \rangle$

$\langle \forall i < \text{length } cs. \text{call-of-return-node } (\text{tl } ms ! i) (\text{sourcenode } (cs ! i)) \rangle$

have $\neg \text{slice-edge } S \text{ cs } a$ **by** $(\text{rule silent-move-no-slice-edge})$

with *silent-moves-Cons* **show** $?case$

proof $(\text{induct rule:silent-move.induct})$

case $(\text{silent-move-intra } f \text{ a } s \text{ s}' \text{ ms } S \text{ ms}')$

note $IH = \langle \bigwedge rs \text{ cs}. \llbracket \text{tl } ms' = \text{targetnodes } rs; \text{length } rs = \text{length } cs;$

$\forall i < \text{length } cs. \text{call-of-return-node } (\text{tl } ms' ! i) (\text{sourcenode } (cs ! i)) \rrbracket$

$\implies \text{slice-edges } S \text{ cs } as = [] \wedge (\exists rs'. \text{tl } ms'' = \text{targetnodes } rs' \wedge$

$\text{length } rs' = \text{length } (\text{upd-cs } cs \text{ as}) \wedge (\forall i < \text{length } (\text{upd-cs } cs \text{ as}).$

$\text{call-of-return-node } (\text{tl } ms'' ! i) (\text{sourcenode } (\text{upd-cs } cs \text{ as} ! i))))$

from $\langle ms' = \text{targetnode } a \# \text{tl } ms \rangle \langle \text{tl } ms = \text{targetnodes } rs \rangle$

have $\text{tl } ms' = \text{targetnodes } rs$ **by** *simp*

from $\langle ms' = \text{targetnode } a \# \text{tl } ms \rangle \langle \text{tl } ms = \text{targetnodes } rs \rangle$

$\langle \forall i < \text{length } cs. \text{call-of-return-node } (\text{tl } ms ! i) (\text{sourcenode } (cs ! i)) \rangle$

have $\forall i < \text{length } cs. \text{call-of-return-node } (\text{tl } ms' ! i) (\text{sourcenode } (cs ! i))$

by *simp*

from $IH[OF \langle \text{tl } ms' = \text{targetnodes } rs \rangle \langle \text{length } rs = \text{length } cs \rangle \text{this}]$

have $\text{slice-edges } S \text{ cs } as = []$

and $\exists rs'. \text{tl } ms'' = \text{targetnodes } rs' \wedge \text{length } rs' = \text{length } (\text{upd-cs } cs \text{ as}) \wedge$

$(\forall i < \text{length } (\text{upd-cs } cs \text{ as}).$

$\text{call-of-return-node } (\text{tl } ms'' ! i) (\text{sourcenode } (\text{upd-cs } cs \text{ as} ! i)))$ **by** *simp-all*

with $\langle \text{intra-kind } (kind \text{ a}) \rangle \langle \neg \text{slice-edge } S \text{ cs } a \rangle$

show $?case$ **by** $(\text{fastforce simp:intra-kind-def})$

next

case $(\text{silent-move-call } f \text{ a } s \text{ s}' \text{ Q } r \text{ p } fs \text{ a}' \text{ ms } S \text{ ms}')$

note $IH = \langle \bigwedge rs \text{ cs}. \llbracket \text{tl } ms' = \text{targetnodes } rs; \text{length } rs = \text{length } cs;$

$\forall i < \text{length } cs. \text{call-of-return-node } (\text{tl } ms' ! i) (\text{sourcenode } (cs ! i)) \rrbracket$

$\implies \text{slice-edges } S \text{ cs } as = [] \wedge (\exists rs'. \text{tl } ms'' = \text{targetnodes } rs' \wedge$

$\text{length } rs' = \text{length } (\text{upd-cs } cs \text{ as}) \wedge (\forall i < \text{length } (\text{upd-cs } cs \text{ as}).$

$\text{call-of-return-node } (\text{tl } ms'' ! i) (\text{sourcenode } (\text{upd-cs } cs \text{ as} ! i))))$

from $\langle \text{tl } ms = \text{targetnodes } rs \rangle \langle ms' = \text{targetnode } a \# \text{targetnode } a' \# \text{tl } ms \rangle$

have $\text{tl } ms' = \text{targetnodes } (a' \# rs)$ **by** $(\text{simp add:targetnodes-def})$

from $\langle \text{length } rs = \text{length } cs \rangle$ **have** $\text{length } (a' \# rs) = \text{length } (a \# cs)$ **by** *simp*

from $\langle \text{valid-edge } a' \rangle \langle \text{valid-edge } a \rangle \langle a' \in \text{get-return-edges } a \rangle$

have $\text{return-node } (\text{targetnode } a')$ **by** $(\text{fastforce simp:return-node-def})$

with $\langle \text{valid-edge } a \rangle \langle \text{valid-edge } a' \rangle \langle a' \in \text{get-return-edges } a \rangle$
have $\text{call-of-return-node } (\text{targetnode } a') (\text{sourcenode } a)$
by $(\text{simp add:call-of-return-node-def}) \text{blast}$
with $\langle \forall i < \text{length } cs. \text{call-of-return-node } (\text{tl } ms ! i) (\text{sourcenode } (cs ! i)) \rangle$
 $\langle ms' = \text{targetnode } a \# \text{targetnode } a' \# \text{tl } ms \rangle$
have $\forall i < \text{length } (a \# cs).$
 $\text{call-of-return-node } (\text{tl } ms' ! i) (\text{sourcenode } ((a \# cs) ! i))$
by $\text{auto } (\text{case-tac } i, \text{auto})$
from $\text{IH}[\text{OF } \langle \text{tl } ms' = \text{targetnodes } (a' \# rs) \rangle \langle \text{length } (a' \# rs) = \text{length } (a \# cs) \rangle$
 $\text{this}]$
have $\text{slice-edges } S (a \# cs) \text{ as} = []$
and $\exists rs'. \text{tl } ms'' = \text{targetnodes } rs' \wedge$
 $\text{length } rs' = \text{length } (\text{upd-cs } (a \# cs) \text{ as}) \wedge$
 $(\forall i < \text{length } (\text{upd-cs } (a \# cs) \text{ as}).$
 $\text{call-of-return-node } (\text{tl } ms'' ! i) (\text{sourcenode } (\text{upd-cs } (a \# cs) \text{ as} ! i)))$
by simp-all
with $\langle \neg \text{slice-edge } S \text{ cs } a \rangle \langle \text{kind } a = Q : r \hookrightarrow pfs \rangle$ **show** $? \text{case by simp}$
next
case $(\text{silent-move-return } f \text{ a s s' } Q \text{ p f' ms } S \text{ ms'})$
note $\text{IH} = \langle \bigwedge rs \text{ cs}. \llbracket \text{tl } ms' = \text{targetnodes } rs; \text{length } rs = \text{length } cs;$
 $\forall i < \text{length } cs. \text{call-of-return-node } (\text{tl } ms' ! i) (\text{sourcenode } (cs ! i)) \rrbracket$
 $\implies \text{slice-edges } S \text{ cs as} = [] \wedge (\exists rs'. \text{tl } ms'' = \text{targetnodes } rs' \wedge$
 $\text{length } rs' = \text{length } (\text{upd-cs } cs \text{ as}) \wedge (\forall i < \text{length } (\text{upd-cs } cs \text{ as}).$
 $\text{call-of-return-node } (\text{tl } ms'' ! i) (\text{sourcenode } (\text{upd-cs } cs \text{ as} ! i))) \rangle$
from $\langle \text{length } s = \text{Suc } (\text{length } s') \rangle \langle s' \neq [] \rangle \langle \text{length } ms = \text{length } s \rangle \langle ms' = \text{tl } ms \rangle$
obtain $x \text{ xs}$ **where** $[\text{simp}]: ms' = x \# xs$ **by** $(\text{cases } ms)(\text{auto}, \text{case-tac } ms', \text{auto})$
from $\langle ms' = \text{tl } ms \rangle \langle \text{tl } ms = \text{targetnodes } rs \rangle$ **obtain** $r' \text{ rs'}$ **where** $rs = r' \# rs'$
and $x = \text{targetnode } r'$ **and** $\text{tl } ms' = \text{targetnodes } rs'$
by $(\text{cases } rs)(\text{auto } \text{simp:targetnodes-def})$
from $\langle \text{length } rs = \text{length } cs \rangle \langle rs = r' \# rs' \rangle$ **obtain** $c' \text{ cs'}$ **where** $cs = c' \# cs'$
and $\text{length } rs' = \text{length } cs'$ **by** $(\text{cases } cs) \text{auto}$
from $\langle \forall i < \text{length } cs. \text{call-of-return-node } (\text{tl } ms ! i) (\text{sourcenode } (cs ! i)) \rangle$
 $\langle cs = c' \# cs' \rangle \langle ms' = \text{tl } ms \rangle$
have $\forall i < \text{length } cs'. \text{call-of-return-node } (\text{tl } ms' ! i) (\text{sourcenode } (cs' ! i))$
by $\text{auto}(\text{erule-tac } x = \text{Suc } i \text{ in } \text{allE}, \text{cases } \text{tl } ms, \text{auto})$
from $\text{IH}[\text{OF } \langle \text{tl } ms' = \text{targetnodes } rs' \rangle \langle \text{length } rs' = \text{length } cs' \rangle \text{this}]$
have $\text{slice-edges } S \text{ cs' as} = []$ **and** $\exists rs'. \text{tl } ms'' = \text{targetnodes } rs' \wedge$
 $\text{length } rs' = \text{length } (\text{upd-cs } cs' \text{ as}) \wedge (\forall i < \text{length } (\text{upd-cs } cs' \text{ as}).$
 $\text{call-of-return-node } (\text{tl } ms'' ! i) (\text{sourcenode } (\text{upd-cs } cs' \text{ as} ! i)))$
by simp-all
with $\langle \neg \text{slice-edge } S \text{ cs } a \rangle \langle \text{kind } a = Q \hookleftarrow p f' \rangle \langle cs = c' \# cs' \rangle$
show $? \text{case by simp}$
qed
qed fastforce

lemma $\text{observable-moves-singular-slice-edge}$:

$\llbracket S, f \vdash (ms, s) = as \implies (ms', s); \text{tl } ms = \text{targetnodes } rs; \text{length } rs = \text{length } cs;$

$\forall i < \text{length } cs. \text{ call-of-return-node } (tl \ ms \ ! \ i) \ (\text{sourcenode } (cs \ ! \ i))$
 $\implies \text{ slice-edges } S \ cs \ as = [\text{last } as]$
proof (induct rule: observable-moves.induct)
case (observable-moves-snoc $S \ f \ ms \ s \ as \ ms' \ s' \ a \ ms'' \ s''$)
from $\langle S, f \vdash (ms, s) = as \Rightarrow_{\tau} (ms', s') \rangle \langle tl \ ms = \text{targetnodes } rs \rangle \langle \text{length } rs = \text{length } cs \rangle$
 $\langle \forall i < \text{length } cs. \text{ call-of-return-node } (tl \ ms \ ! \ i) \ (\text{sourcenode } (cs \ ! \ i)) \rangle$
obtain rs' **where** $\text{ slice-edges } S \ cs \ as = []$
and $tl \ ms' = \text{targetnodes } rs'$ **and** $\text{length } rs' = \text{length } (upd\text{-}cs \ cs \ as)$
and $\forall i < \text{length } (upd\text{-}cs \ cs \ as).$
 $\text{ call-of-return-node } (tl \ ms \ ! \ i) \ (\text{sourcenode } ((upd\text{-}cs \ cs \ as) \ ! \ i))$
by (fastforce dest!: silent-moves-no-slice-edges)
from $\langle S, f \vdash (ms', s') -a \rightarrow (ms'', s'') \rangle \text{ this}$
have $\text{ slice-edge } S \ (upd\text{-}cs \ cs \ as) \ a$ **by** $-(\text{rule } \text{observable-move-slice-edge})$
with $\langle \text{ slice-edges } S \ cs \ as = [] \rangle$ **have** $\text{ slice-edges } S \ cs \ (as \ @ \ [a]) = [] \ @ \ [a]$
by (fastforce intro: slice-edges-Append)
thus ?case **by** simp
qed

lemma *silent-moves-nonempty-nodestack-False:*

assumes $S, kind \vdash ([m], [cf]) = as \Rightarrow_{\tau} (m' \# ms', s')$ **and** *valid-node* m
and $ms' \neq []$ **and** *CFG-node* $m' \in \text{sum-SDG-slice1 } nx$ **and** $nx \in S$
shows *False*
proof –
from *assms*(1-4) **have** $\text{ slice-edges } S \ [] \ as \neq []$
proof (induct ms' arbitrary: $as \ m' \ s'$)
case (Cons $mx \ msx$)
note $IH = \langle \bigwedge as \ m' \ s'. \llbracket S, kind \vdash ([m], [cf]) = as \Rightarrow_{\tau} (m' \# msx, s') \rrbracket; \text{ valid-node } m; m;$
 $msx \neq []; \text{ CFG-node } m' \in \text{sum-SDG-slice1 } nx \rrbracket$
 $\implies \text{ slice-edges } S \ [] \ as \neq [] \rangle$
from $\langle S, kind \vdash ([m], [cf]) = as \Rightarrow_{\tau} (m' \# mx \# msx, s') \rangle \langle \text{ valid-node } m \rangle$
 $\langle \text{ CFG-node } m' \in \text{sum-SDG-slice1 } nx \rangle$
obtain $as' \ a \ as''$ **where** $as = as' \ @ \ a \ # \ as''$ **and** $\exists Q \ r \ p \ fs. \text{ kind } a = Q: r \hookrightarrow_p fs$
and $\text{ call-of-return-node } mx \ (\text{sourcenode } a)$
and $\text{ CFG-node } (\text{sourcenode } a) \in \text{sum-SDG-slice1 } nx$
and $\text{ targetnode } a -as'' \rightarrow_{s!} m'$
by (fastforce elim!: silent-moves-called-node-in-slice1-hd-nodestack-in-slice1
[of - - - - - [] []] simp: targetnodes-def valid-return-list-def)
from $\langle S, kind \vdash ([m], [cf]) = as \Rightarrow_{\tau} (m' \# mx \# msx, s') \rangle \langle as = as' \ @ \ a \ # \ as'' \rangle$
obtain $xs \ x$ **where** $S, kind \vdash ([m], [cf]) = as' \Rightarrow_{\tau} (xs, x)$
and $S, kind \vdash (xs, x) = a \ # \ as'' \Rightarrow_{\tau} (m' \# mx \ # \ msx, s')$
by (fastforce elim: silent-moves-split)
from $\langle S, kind \vdash (xs, x) = a \ # \ as'' \Rightarrow_{\tau} (m' \# mx \ # \ msx, s') \rangle$
obtain $ys \ y$ **where** $S, kind \vdash (xs, x) -a \rightarrow_{\tau} (ys, y)$
and $S, kind \vdash (ys, y) = as'' \Rightarrow_{\tau} (m' \# mx \ # \ msx, s')$
by (fastforce elim: silent-moves.cases)
from $\langle S, kind \vdash (xs, x) -a \rightarrow_{\tau} (ys, y) \rangle \langle \exists Q \ r \ p \ fs. \text{ kind } a = Q: r \hookrightarrow_p fs \rangle$

```

obtain  $xs' a'$  where  $xs = \text{sourcenode } a \# xs'$ 
and  $ys = \text{targetnode } a \# \text{targetnode } a' \# xs'$ 
apply – apply(erule silent-move.cases) apply(auto simp:intra-kind-def)
by(cases xs,auto) +
from  $\langle S, kind \vdash (ys, y) = as'' \Rightarrow_{\tau} (m' \# mx \# msx, s') \rangle$ 
 $\langle ys = \text{targetnode } a \# \text{targetnode } a' \# xs' \rangle \langle \text{targetnode } a - as'' \rightarrow_{sl} m' \rangle$ 
have  $mx = \text{targetnode } a'$  and  $xs' = msx$ 
by(auto dest:silent-moves-same-level-path)
with  $\langle xs = \text{sourcenode } a \# xs' \rangle \langle S, kind \vdash ([m], [cf]) = as' \Rightarrow_{\tau} (xs, x) \rangle$ 
have  $S, kind \vdash ([m], [cf]) = as' \Rightarrow_{\tau} (\text{sourcenode } a \# msx, x)$  by simp
show ?case
proof(cases msx = [])
  case True
from  $\langle S, kind \vdash ([m], [cf]) = as' \Rightarrow_{\tau} (\text{sourcenode } a \# msx, x) \rangle$ 
obtain  $rs'$  where  $msx = \text{targetnodes } rs' \wedge \text{length } rs' = \text{length } (\text{upd-cs } [] \text{ as}')$ 
by(fastforce dest!:silent-moves-no-slice-edges[where cs=[] and rs=[]
simp:targetnodes-def)
with True have  $\text{upd-cs } [] \text{ as}' = []$  by(cases rs')(auto simp:targetnodes-def)
with  $\langle \text{CFG-node } (\text{sourcenode } a) \in \text{sum-SDG-slice1 } nx \rangle \langle nx \in S \rangle$ 
have  $\text{slice-edge } S (\text{upd-cs } [] \text{ as}')$   $a$ 
by(cases kind a, auto intro:combSlice-refl
simp:slice-edge-def SDG-to-CFG-set-def HRB-slice-def)
hence  $\text{slice-edges } S (\text{upd-cs } [] \text{ as}')$   $(a \# as'') \neq []$  by simp
with  $\langle as = as' @ a \# as'' \rangle$  show ?thesis by(fastforce dest:slice-edges-Nil-split)
next
  case False
from  $\langle IH[OF \langle S, kind \vdash ([m], [cf]) = as' \Rightarrow_{\tau} (\text{sourcenode } a \# msx, x) \rangle$ 
 $\langle \text{valid-node } m \rangle \text{ this } \langle \text{CFG-node } (\text{sourcenode } a) \in \text{sum-SDG-slice1 } nx \rangle$ 
have  $\text{slice-edges } S [] \text{ as}' \neq []$  .
with  $\langle as = as' @ a \# as'' \rangle$  show ?thesis by(fastforce dest:slice-edges-Nil-split)
qed
qed simp
moreover
from  $\langle S, kind \vdash ([m], [cf]) = as \Rightarrow_{\tau} (m' \# ms', s') \rangle$  have  $\text{slice-edges } S [] \text{ as} = []$ 
by(fastforce dest!:silent-moves-no-slice-edges[where cs=[] and rs=[]
simp:targetnodes-def)
ultimately show False by simp
qed

```

lemma *transfers-intra-slice-kinds-slice-edges*:

$\llbracket \forall a \in \text{set } as. \text{intra-kind } (\text{kind } a); \forall c \in \text{set } cs. \text{sourcenode } c \in [\text{HRB-slice } S]_{\text{CFG}} \rrbracket$
 $\implies \text{transfers } (\text{slice-kinds } S (\text{slice-edges } S \text{ cs } as)) \text{ s} =$
 $\text{transfers } (\text{slice-kinds } S \text{ as}) \text{ s}$

proof(*induct as arbitrary:s*)

case *Nil* **thus** ?*case* **by**(*simp add:slice-kinds-def*)

next

case (*Cons a' as'*)

```

note IH = ⟨ $\wedge s. \llbracket \forall a \in \text{set } as'. \text{intra-kind } (kind\ a);$ 
 $\forall c \in \text{set } cs. \text{sourcenode } c \in \llbracket \text{HRB-slice } S \rrbracket_{CFG} \implies$ 
 $\text{transfers } (slice\ kinds\ S\ (slice\ edges\ S\ cs\ as'))\ s =$ 
 $\text{transfers } (slice\ kinds\ S\ as')\ s \rangle$ 
from ⟨ $\forall a \in \text{set } (a' \# as'). \text{intra-kind } (kind\ a) \rangle$ 
have  $\text{intra-kind } (kind\ a')$  and  $\forall a \in \text{set } as'. \text{intra-kind } (kind\ a)$ 
by simp-all
show ?case
proof(cases slice-edge S cs a')
  case True
    with ⟨ $\text{intra-kind } (kind\ a') \rangle$ 
    have  $eq:\text{transfers } (slice\ kinds\ S\ (slice\ edges\ S\ cs\ (a' \# as')))\ s$ 
      =  $\text{transfers } (slice\ kinds\ S\ (slice\ edges\ S\ cs\ as'))\ s$ 
      (transfer (slice-kind S a') s)
    by(cases kind a')(auto simp:slice-kinds-def intra-kind-def)
    have  $\text{transfers } (slice\ kinds\ S\ (a' \# as'))\ s$ 
      =  $\text{transfers } (slice\ kinds\ S\ as')\ (transfer\ (slice\ kind\ S\ a')\ s)$ 
    by(simp add:slice-kinds-def)
    with  $eq\ IH[OF\ \langle \forall a \in \text{set } as'. \text{intra-kind } (kind\ a) \rangle$ 
       $\langle \forall c \in \text{set } cs. \text{sourcenode } c \in \llbracket \text{HRB-slice } S \rrbracket_{CFG} \rangle,$ 
      of transfer (slice-kind S a') s]
    show ?thesis by simp
  next
    case False
    with ⟨ $\text{intra-kind } (kind\ a') \rangle$ 
    have  $eq:\text{transfers } (slice\ kinds\ S\ (slice\ edges\ S\ cs\ (a' \# as')))\ s$ 
      =  $\text{transfers } (slice\ kinds\ S\ (slice\ edges\ S\ cs\ as'))\ s$ 
    by(cases kind a')(auto simp:slice-kinds-def intra-kind-def)
    from False ⟨ $\text{intra-kind } (kind\ a') \rangle \langle \forall c \in \text{set } cs. \text{sourcenode } c \in \llbracket \text{HRB-slice } S \rrbracket_{CFG} \rangle$ 
    have  $\text{sourcenode } a' \notin \llbracket \text{HRB-slice } S \rrbracket_{CFG}$ 
    by(fastforce simp:slice-edge-def intra-kind-def)
    with ⟨ $\text{intra-kind } (kind\ a') \rangle$  have  $\text{transfer } (slice\ kind\ S\ a')\ s = s$ 
    by(cases s)(auto,cases kind a',
      auto simp:slice-kind-def Let-def intra-kind-def)
    hence  $\text{transfers } (slice\ kinds\ S\ (a' \# as'))\ s$ 
      =  $\text{transfers } (slice\ kinds\ S\ as')\ s$ 
    by(simp add:slice-kinds-def)
    with  $eq\ IH[OF\ \langle \forall a \in \text{set } as'. \text{intra-kind } (kind\ a) \rangle$ 
       $\langle \forall c \in \text{set } cs. \text{sourcenode } c \in \llbracket \text{HRB-slice } S \rrbracket_{CFG} \rangle, \text{of } s]$  show ?thesis by simp
qed
qed

```

lemma *exists-sliced-intra-path-preds*:

```

assumes  $m -as \rightarrow_{\iota}^* m'$  and  $\text{slice-edges } S\ cs\ as = []$ 
and  $m' \in \llbracket \text{HRB-slice } S \rrbracket_{CFG}$  and  $\forall c \in \text{set } cs. \text{sourcenode } c \in \llbracket \text{HRB-slice } S \rrbracket_{CFG}$ 
obtains  $as'$  where  $m -as' \rightarrow_{\iota}^* m'$  and  $\text{preds } (slice\ kinds\ S\ as')\ (cf \# cfs)$ 
and  $\text{slice-edges } S\ cs\ as' = []$ 

```

proof(*atomize-elim*)
from $\langle m -as \rightarrow_i^* m' \rangle$ **have** $m -as \rightarrow^* m'$ **and** $\forall a \in \text{set } as. \text{intra-kind}(\text{kind } a)$
by(*simp-all add:intra-path-def*)
from $\langle \text{slice-edges } S \text{ cs } as = [] \rangle \langle \forall a \in \text{set } as. \text{intra-kind}(\text{kind } a) \rangle$
 $\langle \forall c \in \text{set } cs. \text{sourcenode } c \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
have $\forall nx \in \text{set}(\text{sourcenodes } as). nx \notin \lfloor \text{HRB-slice } S \rfloor_{CFG}$
by(*rule slice-intra-edges-no-nodes-in-slice*)
with $\langle m -as \rightarrow_i^* m' \rangle \langle m' \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$ **have** $m' \in \text{obs-intra } m \lfloor \text{HRB-slice } S \rfloor_{CFG}$
by(*fastforce intro:obs-intra-elem*)
hence $\text{obs-intra } m \lfloor \text{HRB-slice } S \rfloor_{CFG} = \{m'\}$ **by**(*rule obs-intra-singleton-element*)
from $\langle m -as \rightarrow^* m' \rangle$ **have** *valid-node* m **and** *valid-node* m'
by(*fastforce dest:path-valid-node*)+
from $\langle m -as \rightarrow_i^* m' \rangle$ **obtain** x **where** *distance* $m \ m' \ x$ **and** $x \leq \text{length } as$
by(*erule every-path-distance*)
from $\langle \text{distance } m \ m' \ x \rangle \langle \text{obs-intra } m \lfloor \text{HRB-slice } S \rfloor_{CFG} = \{m'\} \rangle$
show $\exists as'. m -as' \rightarrow_i^* m' \wedge \text{preds } (\text{slice-kinds } S \ as') \ (\text{cf} \# \text{cfs}) \wedge$
 $\text{slice-edges } S \ \text{cs} \ as' = []$
proof(*induct x arbitrary:m rule:nat.induct*)
case *zero*
from $\langle \text{distance } m \ m' \ 0 \rangle$ **have** $m = m'$
by(*fastforce elim:distance.cases simp:intra-path-def*)
with $\langle \text{valid-node } m' \rangle$ **show** *?case*
by(*rule-tac x=[] in exI,*
auto intro:empty-path simp:slice-kinds-def intra-path-def)
next
case (*Suc x*)
note $IH = \langle \bigwedge m. \llbracket \text{distance } m \ m' \ x; \text{obs-intra } m \lfloor \text{HRB-slice } S \rfloor_{CFG} = \{m'\} \rrbracket$
 $\implies \exists as'. m -as' \rightarrow_i^* m' \wedge \text{preds } (\text{slice-kinds } S \ as') \ (\text{cf} \# \text{cfs}) \wedge$
 $\text{slice-edges } S \ \text{cs} \ as' = [] \rangle$
from $\langle \text{distance } m \ m' \ (\text{Suc } x) \rangle$ **obtain** a
where *valid-edge* a **and** $m = \text{sourcenode } a$ **and** *intra-kind*(*kind a*)
and *distance* (*targetnode a*) $m' \ x$
and *target:targetnode a* = (*SOME nx. $\exists a'. \text{sourcenode } a = \text{sourcenode } a' \wedge$*
distance (*targetnode a'*) $m' \ x \wedge$
valid-edge a' \wedge intra-kind(kind a') \wedge targetnode a' = nx)
by(*auto elim:distance-successor-distance*)
have $m \notin \lfloor \text{HRB-slice } S \rfloor_{CFG}$
proof
assume $m \in \lfloor \text{HRB-slice } S \rfloor_{CFG}$
from (*valid-edge a*) $\langle m = \text{sourcenode } a \rangle$ **have** *valid-node* m **by** *simp*
with $\langle m \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$ **have** $\text{obs-intra } m \lfloor \text{HRB-slice } S \rfloor_{CFG} = \{m\}$
by \neg (*rule n-in-obs-intra*)
with $\langle \text{obs-intra } m \lfloor \text{HRB-slice } S \rfloor_{CFG} = \{m'\} \rangle$ **have** $m = m'$ **by** *simp*
with (*valid-node m*) **have** $m -[] \rightarrow_i^* m'$
by(*fastforce intro:empty-path simp:intra-path-def*)
with $\langle \text{distance } m \ m' \ (\text{Suc } x) \rangle$ **show** *False*
by(*fastforce elim:distance.cases*)
qed

from $\langle \text{distance } (\text{targetnode } a) \ m' \ x \rangle \langle m' \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
obtain $m \ x$ **where** $m \ x \in \text{obs-intra } (\text{targetnode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG}$
by $(\text{fastforce elim:distance.cases path-ex-obs-intra})$
from $\langle \text{valid-edge } a \rangle \langle \text{intra-kind } (\text{kind } a) \rangle \langle m \notin \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle \langle m = \text{sourcenode } a \rangle$
have $\text{obs-intra } (\text{targetnode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG} \subseteq$
 $\text{obs-intra } (\text{sourcenode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG}$
by $-(\text{rule edge-obs-intra-subset,auto})$
with $\langle m \ x \in \text{obs-intra } (\text{targetnode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle \langle m = \text{sourcenode } a \rangle$
 $\langle \text{obs-intra } m \lfloor \text{HRB-slice } S \rfloor_{CFG} = \{m'\} \rangle$
have $m' \in \text{obs-intra } (\text{targetnode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG}$ **by** auto
hence $\text{obs-intra } (\text{targetnode } a) \lfloor \text{HRB-slice } S \rfloor_{CFG} = \{m'\}$
by $(\text{rule obs-intra-singleton-element})$
from $\text{IH}[\text{OF } \langle \text{distance } (\text{targetnode } a) \ m' \ x \rangle \text{ this}]$
obtain as **where** $\text{targetnode } a - as \rightarrow_i^* m'$ **and** $\text{preds } (\text{slice-kinds } S \ as) \ (cf \# \ cfs)$
and $\text{slice-edges } S \ cs \ as = []$ **by** blast
from $\langle \text{targetnode } a - as \rightarrow_i^* m' \rangle \langle \text{valid-edge } a \rangle \langle \text{intra-kind } (\text{kind } a) \rangle$
 $\langle m = \text{sourcenode } a \rangle$
have $m - a \# as \rightarrow_i^* m'$ **by** $(\text{fastforce intro:Cons-path simp:intra-path-def})$
from $\langle \forall c \in \text{set } cs. \text{sourcenode } c \in \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle \langle m \notin \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle$
 $\langle m = \text{sourcenode } a \rangle \langle \text{intra-kind } (\text{kind } a) \rangle$
have $\neg \text{slice-edge } S \ cs \ a$ **by** $(\text{fastforce simp:slice-edge-def intra-kind-def})$
with $\langle \text{slice-edges } S \ cs \ as = [] \rangle \langle \text{intra-kind } (\text{kind } a) \rangle$
have $\text{slice-edges } S \ cs \ (a \# as) = []$ **by** $(\text{fastforce simp:intra-kind-def})$
from $\langle \text{intra-kind } (\text{kind } a) \rangle$
show $?case$
proof $(\text{cases kind } a)$
case $(\text{UpdateEdge } f)$
with $\langle m \notin \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle \langle m = \text{sourcenode } a \rangle$ **have** $\text{slice-kind } S \ a = \uparrow id$
by $(\text{fastforce intro:slice-kind-Upd})$
hence $\text{transfer } (\text{slice-kind } S \ a) \ (cf \# \ cfs) = (cf \# \ cfs)$
and $\text{pred } (\text{slice-kind } S \ a) \ (cf \# \ cfs)$ **by** simp-all
with $\langle \text{preds } (\text{slice-kinds } S \ as) \ (cf \# \ cfs) \rangle$
have $\text{preds } (\text{slice-kinds } S \ (a \# as)) \ (cf \# \ cfs)$
by $(\text{simp add:slice-kinds-def})$
with $\langle m - a \# as \rightarrow_i^* m' \rangle \langle \text{slice-edges } S \ cs \ (a \# as) = [] \rangle$ **show** $?thesis$
by blast
next
case $(\text{PredicateEdge } Q)$
with $\langle m \notin \lfloor \text{HRB-slice } S \rfloor_{CFG} \rangle \langle m = \text{sourcenode } a \rangle \langle \text{distance } m \ m' \ (\text{Suc } x) \rangle$
 $\langle \text{obs-intra } m \lfloor \text{HRB-slice } S \rfloor_{CFG} = \{m'\} \rangle \langle \text{distance } (\text{targetnode } a) \ m' \ x \rangle$
 target
have $\text{slice-kind } S \ a = (\lambda s. \text{True}) \surd$
by $(\text{fastforce intro:slice-kind-Pred-obs-nearer-SOME})$
hence $\text{transfer } (\text{slice-kind } S \ a) \ (cf \# \ cfs) = (cf \# \ cfs)$
and $\text{pred } (\text{slice-kind } S \ a) \ (cf \# \ cfs)$ **by** simp-all
with $\langle \text{preds } (\text{slice-kinds } S \ as) \ (cf \# \ cfs) \rangle$
have $\text{preds } (\text{slice-kinds } S \ (a \# as)) \ (cf \# \ cfs)$
by $(\text{simp add:slice-kinds-def})$

with $\langle m - a \# as \rightarrow_l^* m' \rangle$ $\langle slice\text{-edges } S \text{ cs } (a \# as) = [] \rangle$ **show** *?thesis* **by** *blast*
qed (*auto simp:intra-kind-def*)
qed
qed

lemma *slp-to-intra-path-with-slice-edges*:

assumes $n - as \rightarrow_{sl}^* n'$ **and** *slice-edges* $S \text{ cs } as = []$
obtains as' **where** $n - as' \rightarrow_l^* n'$ **and** *slice-edges* $S \text{ cs } as' = []$

proof(*atomize-elim*)

from $\langle n - as \rightarrow_{sl}^* n' \rangle$ **have** $n - as \rightarrow^* n'$ **and** *same-level-path* as
by(*simp-all add:slp-def*)

from $\langle same\text{-level-path } as \rangle$ **have** *same-level-path-aux* $[] \text{ as}$ **and** *upd-cs* $[] \text{ as} = []$
by(*simp-all add:same-level-path-def*)

from $\langle n - as \rightarrow^* n' \rangle$ $\langle same\text{-level-path-aux } [] \text{ as} \rangle$ $\langle upd\text{-cs } [] \text{ as} = [] \rangle$
 $\langle slice\text{-edges } S \text{ cs } as = [] \rangle$

show $\exists as'. n - as' \rightarrow_l^* n' \wedge slice\text{-edges } S \text{ cs } as' = []$

proof(*induct as arbitrary:n cs rule:length-induct*)

fix $as \ n \ cs$

assume $IH:\forall as''. length \ as'' < length \ as \longrightarrow$

$(\forall n''. n'' - as'' \rightarrow^* n' \longrightarrow same\text{-level-path-aux } [] \text{ as}'' \longrightarrow$
 $upd\text{-cs } [] \text{ as}'' = [] \longrightarrow (\forall cs'. slice\text{-edges } S \text{ cs}' \text{ as}'' = [] \longrightarrow$
 $(\exists as'. n'' - as' \rightarrow_l^* n' \wedge slice\text{-edges } S \text{ cs}' \text{ as}' = [])))$

and $n - as \rightarrow^* n'$ **and** *same-level-path-aux* $[] \text{ as}$ **and** *upd-cs* $[] \text{ as} = []$
and *slice-edges* $S \text{ cs } as = []$

show $\exists as'. n - as' \rightarrow_l^* n' \wedge slice\text{-edges } S \text{ cs } as' = []$

proof(*cases as*)

case *Nil*

with $\langle n - as \rightarrow^* n' \rangle$ **show** *?thesis* **by**(*fastforce simp:intra-path-def*)

next

case (*Cons* $a' \text{ as}'$)

with $\langle n - as \rightarrow^* n' \rangle$ *Cons* **have** $n = sourcenode \ a'$ **and** *valid-edge* a'
and *targetnode* $a' - as' \rightarrow^* n'$
by(*auto intro:path-split-Cons*)

show *?thesis*

proof(*cases kind a' rule:edge-kind-cases*)

case *Intra*

with *Cons* $\langle same\text{-level-path-aux } [] \text{ as} \rangle$ **have** *same-level-path-aux* $[] \text{ as}'$
by(*fastforce simp:intra-kind-def*)

moreover

from *Intra* *Cons* $\langle upd\text{-cs } [] \text{ as} = [] \rangle$ **have** *upd-cs* $[] \text{ as}' = []$
by(*fastforce simp:intra-kind-def*)

moreover

from $\langle slice\text{-edges } S \text{ cs } as = [] \rangle$ *Cons* *Intra*

have *slice-edges* $S \text{ cs } as' = []$ **and** $\neg slice\text{-edge } S \text{ cs } a'$
by(*cases slice-edge S cs a',auto simp:intra-kind-def*)

ultimately obtain as'' **where** *targetnode* $a' - as'' \rightarrow_l^* n'$
and *slice-edges* $S \text{ cs } as'' = []$

using $IH \text{ Cons } \langle targetnode \ a' - as' \rightarrow^* n' \rangle$

```

    by(erule-tac x=as' in allE) auto
  from ⟨n = sourcenode a'⟩ ⟨valid-edge a'⟩ Intra ⟨targetnode a' -as''→l* n'⟩
  have n -a'#as''→l* n' by(fastforce intro:Cons-path simp:intra-path-def)
  moreover
  from ⟨slice-edges S cs as'' = []⟩ ⟨¬ slice-edge S cs a'⟩ Intra
  have slice-edges S cs (a'#as'') = [] by(auto simp:intra-kind-def)
  ultimately show ?thesis by blast
next
case (Call Q r p f)
with Cons ⟨same-level-path-aux [] as⟩
have same-level-path-aux [a'] as' by simp
from Call Cons ⟨upd-cs [] as = []⟩ have upd-cs [a'] as' = []
  by simp
hence as' ≠ [] by fastforce
with ⟨upd-cs [a'] as' = []⟩ obtain xs ys where as' = xs@ys and xs ≠ []
and upd-cs [a'] xs = [] and upd-cs [] ys = []
and ∀ xs' ys'. xs = xs'@ys' ∧ ys' ≠ [] → upd-cs [a'] xs' ≠ []
  by -(erule upd-cs-empty-split,auto)
from ⟨same-level-path-aux [a'] as'⟩ ⟨as' = xs@ys⟩ ⟨upd-cs [a'] xs = []⟩
have same-level-path-aux [a'] xs and same-level-path-aux [] ys
  by(rule slpa-split)+
with ⟨upd-cs [a'] xs = []⟩ have upd-cs ([a']@cs) xs = []@cs
  by(fastforce intro:same-level-path-upd-cs-callstack-Append)
from ⟨slice-edges S cs as = []⟩ Cons Call
have slice-edges S (a'#cs) as' = [] and ¬ slice-edge S cs a'
  by(cases slice-edge S cs a',auto)+
from ⟨slice-edges S (a'#cs) as' = []⟩ ⟨as' = xs@ys⟩
  ⟨upd-cs ([a']@cs) xs = []@cs⟩
have slice-edges S cs ys = []
  by(fastforce dest:slice-edges-Nil-split)
from ⟨same-level-path-aux [a'] xs⟩ ⟨upd-cs [a'] xs = []⟩
  ⟨∀ xs' ys'. xs = xs'@ys' ∧ ys' ≠ [] → upd-cs [a'] xs' ≠ []⟩
have last xs ∈ get-return-edges (last [a'])
  by(fastforce intro!:slpa-get-return-edges)
with ⟨valid-edge a'⟩ Call
obtain a where valid-edge a and sourcenode a = sourcenode a'
  and targetnode a = targetnode (last xs) and kind a = (λcf. False)√
  by -(drule call-return-node-edge,auto)
from ⟨targetnode a = targetnode (last xs)⟩ ⟨xs ≠ []⟩
have targetnode a = targetnode (last (a'#xs)) by simp
from ⟨as' = xs@ys⟩ ⟨xs ≠ []⟩ Cons have length ys < length as by simp
from ⟨targetnode a' -as'→* n'⟩ ⟨as' = xs@ys⟩ ⟨xs ≠ []⟩
have targetnode (last (a'#xs)) -ys→* n'
  by(cases xs rule:rev-cases,auto dest:path-split)
with IH ⟨length ys < length as⟩ ⟨same-level-path-aux [] ys⟩
  ⟨upd-cs [] ys = []⟩ ⟨slice-edges S cs ys = []⟩
obtain as'' where targetnode (last (a'#xs)) -as''→l* n'
  and slice-edges S cs as'' = []
  apply(erule-tac x=ys in allE) apply clarsimp

```



```

apply(erule-tac x=targetnode (last (a'#xs)) in allE)
apply clarsimp apply(erule-tac x=cs in allE)
by clarsimp
from ⟨sourcenode a = sourcenode a'⟩ ⟨n = sourcenode a'⟩
  ⟨targetnode a = targetnode (last (a'#xs))⟩ ⟨valid-edge a⟩
  ⟨kind a = (λcf. False)✓⟩ ⟨targetnode (last (a'#xs)) - as'' →t* n'⟩
have n - a#as'' →t* n'
  by(fastforce intro:Cons-path simp:intra-path-def intra-kind-def)
moreover
from ⟨kind a = (λcf. False)✓⟩ ⟨slice-edges S cs as'' = []⟩
  ⟨¬ slice-edge S cs a'⟩ ⟨sourcenode a = sourcenode a'⟩
have slice-edges S cs (a#as'') = []
  by(cases kind a')(auto simp:slice-edge-def)
ultimately show ?thesis by blast
next
case (Return Q p f)
with Cons ⟨same-level-path-aux [] as⟩ have False by simp
thus ?thesis by simp
qed
qed
qed
qed

```

1.14.3 $S, f \vdash (ms, s) = as \Rightarrow^* (ms', s')$: the reflexive transitive closure of $S, f \vdash (ms, s) = as \Rightarrow (ms', s')$

inductive trans-observable-moves ::

```

'node SDG-node set  $\Rightarrow$  ('edge  $\Rightarrow$  ('var, 'val, 'ret, 'pname) edge-kind)  $\Rightarrow$  'node list
 $\Rightarrow$ 
  (('var  $\rightarrow$  'val)  $\times$  'ret) list  $\Rightarrow$  'edge list  $\Rightarrow$  'node list  $\Rightarrow$ 
  (('var  $\rightarrow$  'val)  $\times$  'ret) list  $\Rightarrow$  bool
(-, -  $\vdash$  '(-, -)  $\Rightarrow^*$  '(-, -) [51, 50, 0, 0, 50, 0, 0] 51)

```

where tom-Nil:

```
length ms = length s  $\Rightarrow$  S, f  $\vdash$  (ms, s) = []  $\Rightarrow^*$  (ms, s)
```

| tom-Cons:

```

[[S, f  $\vdash$  (ms, s) = as  $\Rightarrow$  (ms', s'); S, f  $\vdash$  (ms', s') = as'  $\Rightarrow^*$  (ms'', s'')]
 $\Rightarrow$  S, f  $\vdash$  (ms, s) = (last as)#as'  $\Rightarrow^*$  (ms'', s'')]

```

lemma tom-split-snoc:

```

assumes S, f  $\vdash$  (ms, s) = as  $\Rightarrow^*$  (ms', s') and as  $\neq$  []
obtains asx asx' ms'' s'' where as = asx@[last asx]
and S, f  $\vdash$  (ms, s) = asx  $\Rightarrow^*$  (ms'', s'') and S, f  $\vdash$  (ms'', s'') = asx'  $\Rightarrow$  (ms', s')
proof(atomize-elim)
from assms show  $\exists$  asx asx' ms'' s''. as = asx @ [last asx]  $\wedge$ 
  S, f  $\vdash$  (ms, s) = asx  $\Rightarrow^*$  (ms'', s'')  $\wedge$  S, f  $\vdash$  (ms'', s'') = asx'  $\Rightarrow$  (ms', s')
proof(induct rule:trans-observable-moves.induct)

```

```

case (tom-Cons S f ms s as ms' s' as' ms'' s'')
note IH = ⟨as' ≠ [] ⟹ ∃ asx asx' msx sx. as' = asx @ [last asx] ∧
  S,f ⊢ (ms',s') = asx⇒* (msx,sx) ∧ S,f ⊢ (msx,sx) = asx'⇒ (ms'',s'')⟩
show ?case
proof(cases as' = [])
  case True
  with ⟨S,f ⊢ (ms',s') = as'⇒* (ms'',s'')⟩ have [simp]:ms'' = ms' s'' = s'
    by(auto elim:trans-observable-moves.cases)
  from ⟨S,f ⊢ (ms,s) = as⇒ (ms',s')⟩ have length ms = length s
    by(rule observable-moves-equal-length)
  hence S,f ⊢ (ms,s) = []⇒* (ms,s) by(rule tom-Nil)
  with ⟨S,f ⊢ (ms,s) = as⇒ (ms',s')⟩ True show ?thesis by fastforce
next
  case False
  from IH[OF this] obtain xs xs' msx sx where as' = xs @ [last xs]
    and S,f ⊢ (ms',s') = xs⇒* (msx,sx)
    and S,f ⊢ (msx,sx) = xs'⇒ (ms'',s'') by blast
  from ⟨S,f ⊢ (ms,s) = as⇒ (ms',s')⟩ ⟨S,f ⊢ (ms',s') = xs⇒* (msx,sx)⟩
  have S,f ⊢ (ms,s) = (last as)#xs⇒* (msx,sx)
    by(rule trans-observable-moves.tom-Cons)
  with ⟨S,f ⊢ (msx,sx) = xs'⇒ (ms'',s'')⟩ ⟨as' = xs @ [last xs]⟩
  show ?thesis by fastforce
qed
qed simp
qed

```

lemma tom-preserves-stacks:

```

assumes S,f ⊢ (m#ms,s) = as⇒* (m'#ms',s') and valid-node m
and valid-call-list cs m and ∀ i < length rs. rs!i ∈ get-return-edges (cs!i)
and valid-return-list rs m and length rs = length cs and ms = targetnodes rs
obtains cs' rs' where valid-node m' and valid-call-list cs' m'
and ∀ i < length rs'. rs'!i ∈ get-return-edges (cs'!i)
and valid-return-list rs' m' and length rs' = length cs'
and ms' = targetnodes rs'
proof(atomize-elim)
from assms show ∃ cs' rs'. valid-node m' ∧ valid-call-list cs' m' ∧
  (∀ i < length rs'. rs'!i ∈ get-return-edges (cs'!i)) ∧ valid-return-list rs' m' ∧
  length rs' = length cs' ∧ ms' = targetnodes rs'
proof(induct S f m#ms s as m'#ms' s' arbitrary:m ms cs rs
  rule:trans-observable-moves.induct)
  case (tom-Nil sx nc f)
  thus ?case
    apply(rule-tac x=cs in exI)
    apply(rule-tac x=rs in exI)
    by clarsimp
next
  case (tom-Cons S f sx as msx' sx' as' sx'')
  note IH = ⟨∧ m ms cs rs. [msx' = m # ms; valid-node m; valid-call-list cs m;

```

```

  ∀ i < length rs. rs ! i ∈ get-return-edges (cs ! i); valid-return-list rs m;
  length rs = length cs; ms = targetnodes rs]]
  ⇒ ∃ cs' rs'. valid-node m' ∧ valid-call-list cs' m' ∧
  (∀ i < length rs'. rs' ! i ∈ get-return-edges (cs' ! i)) ∧
  valid-return-list rs' m' ∧ length rs' = length cs' ∧
  ms' = targetnodes rs')
from ⟨S,f ⊢ (m # ms,sx) = as ⇒ (msx',sx')⟩
obtain m'' ms'' where msx' = m'' # ms''
apply(cases msx') apply(auto elim!: observable-moves.cases observable-move.cases)
  by(case-tac msaa) auto
with ⟨S,f ⊢ (m # ms,sx) = as ⇒ (msx',sx')⟩ ⟨valid-node m⟩
  ⟨valid-call-list cs m⟩ ⟨∀ i < length rs. rs ! i ∈ get-return-edges (cs ! i)⟩
  ⟨valid-return-list rs m⟩ ⟨length rs = length cs⟩ ⟨ms = targetnodes rs⟩
obtain cs'' rs'' where valid-node m'' and valid-call-list cs'' m''
  and ∀ i < length rs''. rs'' ! i ∈ get-return-edges (cs'' ! i)
  and valid-return-list rs'' m'' and length rs'' = length cs''
  and ms'' = targetnodes rs''
  by(auto elim!: observable-moves-preserves-stack)
from IH[OF ⟨msx' = m'' # ms''⟩ this(1-6)]
show ?case by fastforce
qed
qed

```

lemma *vpa-trans-observable-moves*:

```

assumes valid-path-aux cs as and m -as→* m' and preds (kinds as) s
and transfers (kinds as) s = s' and valid-call-list cs m
and ∀ i < length rs. rs ! i ∈ get-return-edges (cs ! i)
and valid-return-list rs m
and length rs = length cs and length s = Suc (length cs)
obtains ms ms'' s'' ms' as' as''
where S,kind ⊢ (m # ms,s) = slice-edges S cs as ⇒* (ms'',s'')
and S,kind ⊢ (ms'',s'') = as' ⇒τ (m' # ms',s')
and ms = targetnodes rs and length ms = length cs
and ∀ i < length cs. call-of-return-node (ms ! i) (sourcnode (cs ! i))
and slice-edges S cs as = slice-edges S cs as''
and m -as''@as'→* m' and valid-path-aux cs (as''@as')
proof(atomize-elim)
from assms show ∃ ms ms'' s'' as' ms' as''.
  S,kind ⊢ (m # ms,s) = slice-edges S cs as ⇒* (ms'',s'') ∧
  S,kind ⊢ (ms'',s'') = as' ⇒τ (m' # ms',s') ∧
  ms = targetnodes rs ∧ length ms = length cs ∧
  (∀ i < length cs. call-of-return-node (ms ! i) (sourcnode (cs ! i))) ∧
  slice-edges S cs as = slice-edges S cs as'' ∧
  m -as''@as'→* m' ∧ valid-path-aux cs (as''@as')
proof(induct arbitrary:m s rs rule:vpa-induct)
  case (vpa-empty cs)

```

```

from ⟨ $m - [] \rightarrow^* m'$ ⟩ have [simp]: $m' = m$  by fastforce
from ⟨transfers (kinds [])  $s = s'$ ⟩ ⟨length  $s = \text{Suc} (\text{length } cs)$ ⟩
have [simp]: $s' = s$  by (cases  $cs$ )(auto simp:kinds-def)
from ⟨valid-call-list  $cs\ m$ ⟩ ⟨valid-return-list  $rs\ m$ ⟩
  ⟨ $\forall i < \text{length } rs. rs\ !\ i \in \text{get-return-edges } (cs\ !\ i)$ ⟩ ⟨length  $rs = \text{length } cs$ ⟩
have  $\forall i < \text{length } cs. \text{call-of-return-node } (\text{targetnodes } rs!\ i) (\text{sourcenode } (cs!\ i))$ 
  by(rule get-return-edges-call-of-return-nodes)
with ⟨length  $s = \text{Suc} (\text{length } cs)$ ⟩ ⟨ $m - [] \rightarrow^* m'$ ⟩ ⟨length  $rs = \text{length } cs$ ⟩ show
?case
  apply(rule-tac  $x = \text{targetnodes } rs$  in  $exI$ )
  apply(rule-tac  $x = m \# \text{targetnodes } rs$  in  $exI$ )
  apply(rule-tac  $x = s$  in  $exI$ )
  apply(rule-tac  $x = []$  in  $exI$ )
  apply(rule-tac  $x = \text{targetnodes } rs$  in  $exI$ )
  apply(rule-tac  $x = []$  in  $exI$ )
  by(fastforce intro:tom-Nil silent-moves-Nil simp:targetnodes-def)
next
case (vpa-intra  $cs\ a\ as$ )
note  $IH = \langle \bigwedge m\ s\ rs. [m - as \rightarrow^* m'; \text{preds } (kinds\ as)\ s; \text{transfers } (kinds\ as)\ s = s'] \rangle$ 
  valid-call-list  $cs\ m$ ;  $\forall i < \text{length } rs. rs\ !\ i \in \text{get-return-edges } (cs\ !\ i)$ ;
  valid-return-list  $rs\ m$ ; length  $rs = \text{length } cs$ ; length  $s = \text{Suc} (\text{length } cs)$ ]
   $\implies \exists ms\ ms''\ s''\ as'\ ms'\ as''.$ 
   $S, kind \vdash (m \# ms, s) = \text{slice-edges } S\ cs\ as \Rightarrow^* (ms'', s'') \wedge$ 
   $S, kind \vdash (ms'', s'') = as' \Rightarrow_\tau (m' \# ms', s') \wedge ms = \text{targetnodes } rs \wedge$ 
  length  $ms = \text{length } cs \wedge$ 
  ( $\forall i < \text{length } cs. \text{call-of-return-node } (ms\ !\ i) (\text{sourcenode } (cs\ !\ i))$ )  $\wedge$ 
  slice-edges  $S\ cs\ as = \text{slice-edges } S\ cs\ as'' \wedge$ 
   $m - as'' @ as' \rightarrow^* m' \wedge \text{valid-path-aux } cs\ (as'' @ as')$ 
from ⟨ $m - a \# as \rightarrow^* m'$ ⟩ have  $m = \text{sourcenode } a$  and valid-edge  $a$ 
  and targetnode  $a - as \rightarrow^* m'$  by(auto elim:path-split-Cons)
from ⟨preds (kinds (a # as))  $s$ ⟩ have pred (kind  $a$ )  $s$ 
  and preds (kinds  $as$ ) (transfer (kind  $a$ )  $s$ ) by(auto simp:kinds-def)
from ⟨transfers (kinds (a # as))  $s = s'$ ⟩
have transfers (kinds  $as$ ) (transfer (kind  $a$ )  $s$ ) =  $s'$  by(fastforce simp:kinds-def)
from ⟨valid-edge  $a$ ⟩ ⟨intra-kind (kind  $a$ )⟩
have get-proc (sourcenode  $a$ ) = get-proc (targetnode  $a$ ) by(rule get-proc-intra)
from ⟨valid-call-list  $cs\ m$ ⟩ ⟨ $m = \text{sourcenode } a$ ⟩
  ⟨get-proc (sourcenode  $a$ ) = get-proc (targetnode  $a$ )⟩
have valid-call-list  $cs$  (targetnode  $a$ )
  apply(clarsimp simp:valid-call-list-def)
  apply(erule-tac  $x = cs'$  in  $allE$ )
  apply(erule-tac  $x = c$  in  $allE$ )
  by(auto split:list.split)
from ⟨intra-kind (kind  $a$ )⟩ ⟨length  $s = \text{Suc} (\text{length } cs)$ ⟩
have length (transfer (kind  $a$ )  $s$ ) =  $\text{Suc} (\text{length } cs)$ 
  by(cases  $s$ )(auto simp:intra-kind-def)
from ⟨valid-return-list  $rs\ m$ ⟩ ⟨ $m = \text{sourcenode } a$ ⟩
  ⟨get-proc (sourcenode  $a$ ) = get-proc (targetnode  $a$ )⟩

```

```

have valid-return-list rs (targetnode a)
  apply(clarsimp simp:valid-return-list-def)
  apply(erule-tac x=cs' in allE) apply clarsimp
  by(case-tac cs') auto
from IH[OF <targetnode a -as→* m'> <preds (kinds as) (transfer (kind a) s)>
  <transfers (kinds as) (transfer (kind a) s) = s'>
  <valid-call-list cs (targetnode a)>
  <∀i<length rs. rs ! i ∈ get-return-edges (cs ! i)> this <length rs = length cs>
  <length (transfer (kind a) s) = Suc (length cs)>]
obtain ms ms'' s'' as' ms' as'' where length ms = length cs
and S,kind ⊢ (targetnode a # ms,transfer (kind a) s) =slice-edges S cs as⇒*
  (ms'',s'')
  and paths:S,kind ⊢ (ms'',s'') =as'⇒τ (m' # ms',s')
  ms = targetnodes rs
  ∀i<length cs. call-of-return-node (ms ! i) (sourcenode (cs ! i))
  slice-edges S cs as = slice-edges S cs as''
  targetnode a -as'' @ as'→* m' valid-path-aux cs (as'' @ as')
  by blast
from ∀i<length cs. call-of-return-node (ms ! i) (sourcenode (cs ! i))
  <length ms = length cs>
have ∀mx ∈ set ms. return-node mx
  by(auto simp:call-of-return-node-def in-set-conv-nth)
show ?case
proof(cases (∀m ∈ set ms. ∃m'. call-of-return-node m m' ∧
  m' ∈ [HRB-slice S]CFG) ∧ m ∈ [HRB-slice S]CFG)
  case True
  with <m = sourcenode a> <length ms = length cs> <intra-kind (kind a)>
  <∀i<length cs. call-of-return-node (ms ! i) (sourcenode (cs ! i))>
  have slice-edge S cs a
  by(fastforce simp:slice-edge-def in-set-conv-nth intra-kind-def)
  with <intra-kind (kind a)>
  have slice-edges S cs (a#as) = a#slice-edges S cs as
  by(fastforce simp:intra-kind-def)
  from True <pred (kind a) s> <valid-edge a> <intra-kind (kind a)>
  <∀mx ∈ set ms. return-node mx> <length ms = length cs> <m = sourcenode
a>
  <length s = Suc (length cs)> <length (transfer (kind a) s) = Suc (length cs)>
  have S,kind ⊢ (sourcenode a#ms,s) -a→ (targetnode a#ms,transfer (kind
a) s)
  by(fastforce intro!:observable-move-intra)
  with <length ms = length cs> <length s = Suc (length cs)>
  have S,kind ⊢ (sourcenode a#ms,s) =[]@[a]⇒
  (targetnode a#ms,transfer (kind a) s)
  by(fastforce intro:observable-moves-snoc silent-moves-Nil)
  with <S,kind ⊢ (targetnode a # ms,transfer (kind a) s) =slice-edges S cs
as⇒*
  (ms'',s'')
  have S,kind ⊢ (sourcenode a#ms,s) =last [a]#slice-edges S cs as⇒* (ms'',s'')
  by(fastforce intro:tom-Cons)

```

```

with ⟨slice-edges S cs (a#as) = a#slice-edges S cs as⟩
have S,kind ⊢ (sourcenode a#ms,s) =slice-edges S cs (a#as)⇒* (ms'',s'')
  by simp
moreover
from ⟨slice-edges S cs as = slice-edges S cs as''⟩ ⟨slice-edge S cs a⟩
  ⟨intra-kind (kind a)⟩
have slice-edges S cs (a#as) = slice-edges S cs (a#as'')
  by(fastforce simp:intra-kind-def)
ultimately show ?thesis
  using paths ⟨m = sourcenode a⟩ ⟨valid-edge a⟩ ⟨intra-kind (kind a)⟩
  ⟨length ms = length cs⟩ ⟨slice-edges S cs (a#as) = a#slice-edges S cs as⟩
  apply(rule-tac x=ms in exI)
  apply(rule-tac x=ms'' in exI)
  apply(rule-tac x=s'' in exI)
  apply(rule-tac x=as' in exI)
  apply(rule-tac x=ms' in exI)
  apply(rule-tac x=a#as'' in exI)
  by(auto intro:Cons-path simp:intra-kind-def)
next
case False
with ⟨∀ mx ∈ set ms. return-node mx⟩
have disj:(∃ m ∈ set ms. ∃ m'. call-of-return-node m m' ∧
  m' ∉ [HRB-slice S]CFG) ∨ m ∉ [HRB-slice S]CFG
  by(fastforce dest:return-node-call-of-return-node)
with ⟨m = sourcenode a⟩ ⟨length ms = length cs⟩ ⟨intra-kind (kind a)⟩
  ⟨∀ i < length cs. call-of-return-node (ms ! i) (sourcenode (cs ! i))⟩
have ¬ slice-edge S cs a
  by(fastforce simp:slice-edge-def in-set-conv-nth intra-kind-def)
with ⟨intra-kind (kind a)⟩
have slice-edges S cs (a#as) = slice-edges S cs as
  by(fastforce simp:intra-kind-def)
from disj ⟨pred (kind a) s⟩ ⟨valid-edge a⟩ ⟨intra-kind (kind a)⟩
  ⟨∀ mx ∈ set ms. return-node mx⟩ ⟨length ms = length cs⟩ ⟨m = sourcenode
a)
  ⟨length s = Suc (length cs)⟩ ⟨length (transfer (kind a) s) = Suc (length cs)⟩
have S,kind ⊢ (sourcenode a#ms,s) -a→τ (targetnode a#ms,transfer (kind
a) s)
  by(fastforce intro!:silent-move-intra)
from ⟨S,kind ⊢ (targetnode a # ms,transfer (kind a) s) =slice-edges S cs
as⇒*
  (ms'',s'')⟩
show ?thesis
proof(rule trans-observable-moves.cases)
  fix msx sx nc' f
  assume targetnode a # ms = msx
  and transfer (kind a) s = sx and slice-edges S cs as = []
  and [simp]:ms'' = msx s'' = sx and length msx = length sx
from ⟨slice-edges S cs (a#as) = slice-edges S cs as⟩
  ⟨slice-edges S cs as = []⟩

```

```

have slice-edges  $S$   $cs$  ( $a\#as$ ) = [] by simp
with  $\langle$ length  $ms$  = length  $cs$  $\rangle$   $\langle$ length  $s$  = Suc (length  $cs$ ) $\rangle$ 
have  $S,kind \vdash$  (sourcenode  $a\#ms,s$ ) = slice-edges  $S$   $cs$  ( $a\#as$ ) $\Rightarrow$ *
  (sourcenode  $a\#ms,s$ )
  by(fastforce intro:tom-Nil)
moreover
from  $\langle S,kind \vdash (ms'',s'') = as' \Rightarrow_{\tau} (m'\#ms',s') \rangle$   $\langle$ targetnode  $a \# ms = msx$  $\rangle$ 
   $\langle$ transfer (kind  $a$ )  $s = sx$  $\rangle$   $\langle$ ms'' = msx $\rangle$   $\langle$ s'' = sx $\rangle$ 
   $\langle S,kind \vdash$  (sourcenode  $a\#ms,s$ )  $-a \rightarrow_{\tau}$  (targetnode  $a\#ms,transfer$  (kind
a)  $s$ ) $\rangle$ 
have  $S,kind \vdash$  (sourcenode  $a\#ms,s$ ) =  $a\#as' \Rightarrow_{\tau} (m'\#ms',s')$ 
  by(fastforce intro:silent-moves-Cons)
from this  $\langle$ valid-edge  $a$  $\rangle$   $\langle \forall i < \text{length } rs. rs ! i \in \text{get-return-edges } (cs ! i) \rangle$ 
   $\langle$ ms = targetnodes  $rs$  $\rangle$   $\langle$ valid-return-list  $rs$   $m$  $\rangle$   $\langle$ length  $rs$  = length  $cs$  $\rangle$ 
   $\langle$ length  $s$  = Suc (length  $cs$ ) $\rangle$   $\langle$ m = sourcenode  $a$  $\rangle$ 
have sourcenode  $a -a\#as' \Rightarrow$ *  $m'$  and valid-path-aux  $cs$  ( $a\#as'$ )
  by -(rule silent-moves-vpa-path,(fastforce simp:targetnodes-def))+
  ultimately show ?thesis using  $\langle$ m = sourcenode  $a$  $\rangle$   $\langle$ length  $ms$  = length
cs $\rangle$ 
   $\langle \forall i < \text{length } cs. \text{call-of-return-node } (ms ! i) \text{ (sourcenode } (cs ! i)) \rangle$ 
   $\langle$ slice-edges  $S$   $cs$  ( $a\#as$ ) = [] $\rangle$   $\langle$ intra-kind (kind  $a$ ) $\rangle$ 
   $\langle S,kind \vdash$  (sourcenode  $a\#ms,s$ ) =  $a\#as' \Rightarrow_{\tau} (m'\#ms',s')$  $\rangle$ 
   $\langle$ ms = targetnodes  $rs$  $\rangle$ 
  apply(rule-tac  $x=ms$  in  $exI$ )
  apply(rule-tac  $x=sourcenode$   $a\#ms$  in  $exI$ )
  apply(rule-tac  $x=s$  in  $exI$ )
  apply(rule-tac  $x=a\#as'$  in  $exI$ )
  apply(rule-tac  $x=ms'$  in  $exI$ )
  apply(rule-tac  $x=[]$  in  $exI$ )
  by(auto simp:intra-kind-def)
next
fix  $S' f msx sx asx msx' sx' asx' msx'' sx''$ 
assume [simp]: $S = S'$  and kind =  $f$  and targetnode  $a \# ms = msx$ 
  and transfer (kind  $a$ )  $s = sx$  and slice-edges  $S$   $cs$   $as = \text{last } asx \# asx'$ 
  and  $ms'' = msx''$  and  $s'' = sx''$ 
  and  $S',f \vdash (msx,sx) = asx \Rightarrow (msx',sx')$ 
  and  $S',f \vdash (msx',sx') = asx' \Rightarrow$ *  $(msx'',sx'')$ 
from (kind =  $f$ ) have [simp]: $f = \text{kind}$  by simp
from  $\langle S,kind \vdash$  (sourcenode  $a\#ms,s$ )  $-a \rightarrow_{\tau}$ 
  (targetnode  $a\#ms,transfer$  (kind  $a$ )  $s$ ) $\rangle$   $\langle S',f \vdash (msx,sx) = asx \Rightarrow (msx',sx') \rangle$ 
   $\langle$ transfer (kind  $a$ )  $s = sx$  $\rangle$   $\langle$ targetnode  $a \# ms = msx$  $\rangle$ 
have  $S,kind \vdash$  (sourcenode  $a\#ms,s$ ) =  $a\#asx \Rightarrow (msx',sx')$ 
  by(fastforce intro:silent-move-observable-moves)
with  $\langle S',f \vdash (msx',sx') = asx' \Rightarrow$ *  $(msx'',sx'')$  $\rangle$   $\langle$ ms'' = msx'' $\rangle$   $\langle$ s'' = sx'' $\rangle$ 
have  $S,kind \vdash$  (sourcenode  $a\#ms,s$ ) = last ( $a\#asx$ ) $\#asx' \Rightarrow$ *  $(ms'',s'')$ 
  by(fastforce intro:trans-observable-moves.tom-Cons)
moreover
from  $\langle S',f \vdash (msx,sx) = asx \Rightarrow (msx',sx') \rangle$  have  $asx \neq []$ 
  by(fastforce elim:observable-moves.cases)

```

```

with ⟨slice-edges S cs (a#as) = slice-edges S cs as⟩
  ⟨slice-edges S cs as = last asx # asx'⟩
have slice-edges S cs (a#as) = last (a#asx)#asx' by simp
moreover
from ⟨¬ slice-edge S cs a⟩ ⟨slice-edges S cs as = slice-edges S cs as''⟩
  ⟨intra-kind (kind a)⟩
have slice-edges S cs (a # as) = slice-edges S cs (a # as'')
  by(fastforce simp:intra-kind-def)
ultimately show ?thesis using paths ⟨m = sourcenode a⟩ ⟨intra-kind (kind
a)⟩
  ⟨length ms = length cs⟩ ⟨ms = targetnodes rs⟩ ⟨valid-edge a⟩
  apply(rule-tac x=ms in exI)
  apply(rule-tac x=ms'' in exI)
  apply(rule-tac x=s'' in exI)
  apply(rule-tac x=as' in exI)
  apply(rule-tac x=ms' in exI)
  apply(rule-tac x=a#as'' in exI)
  by(auto intro:Cons-path simp:intra-kind-def)
  qed
qed
next
case (vpa-Call cs a as Q r p fs)
note IH = ⟨ $\bigwedge m s rs. \llbracket m -as \rightarrow^* m'; \text{preds } (kinds\ as)\ s; \text{transfers } (kinds\ as)\ s = s';$ 
  valid-call-list (a # cs) m;
   $\forall i < \text{length } rs. rs ! i \in \text{get-return-edges } ((a \# cs) ! i)$ ;
  valid-return-list rs m; length rs = length (a # cs);
  length s = Suc (length (a # cs))
   $\implies \exists ms\ ms''\ s''\ as'\ ms'\ as''.$ 
  S,kind  $\vdash (m \# ms, s) = \text{slice-edges } S (a \# cs) as \Rightarrow^* (ms'', s'')$   $\wedge$ 
  S,kind  $\vdash (ms'', s'') = as' \Rightarrow_{\tau} (m' \# ms', s')$   $\wedge ms = \text{targetnodes } rs \wedge$ 
  length ms = length (a # cs)  $\wedge$ 
  ( $\forall i < \text{length } (a \# cs). \text{call-of-return-node } (ms ! i) (\text{sourcenode } ((a \# cs) ! i))$ )
 $\wedge$ 
  slice-edges S (a # cs) as = slice-edges S (a # cs) as''  $\wedge$ 
  m -as'' @ as' \rightarrow^* m' \wedge \text{valid-path-aux } (a \# cs) (as'' @ as')
from ⟨m -a # as \rightarrow^* m'⟩ have m = sourcenode a and valid-edge a
and targetnode a -as \rightarrow^* m' by(auto elim:path-split-Cons)
from ⟨preds (kinds (a # as)) s⟩ have pred (kind a) s
and preds (kinds as) (transfer (kind a) s) by(auto simp:kinds-def)
from ⟨transfers (kinds (a # as)) s = s'⟩
have transfers (kinds as) (transfer (kind a) s) = s' by(fastforce simp:kinds-def)
from ⟨valid-edge a⟩ ⟨kind a = Q:r \leftrightarrow pfs⟩ have get-proc (targetnode a) = p
by(rule get-proc-call)
with ⟨valid-call-list cs m⟩ ⟨valid-edge a⟩ ⟨kind a = Q:r \leftrightarrow pfs⟩ ⟨m = sourcenode
a⟩
have valid-call-list (a # cs) (targetnode a)
apply(clarsimp simp:valid-call-list-def)
apply(case-tac cs') apply auto

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    apply(erule-tac x=list in allE)
    by(case-tac list)(auto simp:sourcenodes-def)
  from ⟨valid-edge a⟩ ⟨kind a = Q:r↔pfs⟩ obtain a' where a' ∈ get-return-edges
a
  by(fastforce dest:get-return-edge-call)
with ⟨valid-edge a⟩ ⟨kind a = Q:r↔pfs⟩ obtain Q' f' where kind a' = Q'↔pf'
  by(fastforce dest!:call-return-edges)
from ⟨valid-edge a⟩ ⟨a' ∈ get-return-edges a⟩ have valid-edge a'
  by(rule get-return-edges-valid)
from ⟨valid-edge a'⟩ ⟨kind a' = Q'↔pf'⟩ have get-proc (sourcenode a') = p
  by(rule get-proc-return)
from ⟨∀ i < length rs. rs ! i ∈ get-return-edges (cs ! i)⟩ ⟨a' ∈ get-return-edges a⟩
have ∀ i < length (a'#rs). (a'#rs) ! i ∈ get-return-edges ((a#cs) ! i)
  by auto(case-tac i,auto)
from ⟨valid-edge a⟩ ⟨a' ∈ get-return-edges a⟩
have get-proc (sourcenode a) = get-proc (targetnode a')
  by(rule get-proc-get-return-edge)
with ⟨valid-return-list rs m⟩ ⟨valid-edge a'⟩ ⟨kind a' = Q'↔pf'⟩
  ⟨get-proc (sourcenode a') = p⟩ ⟨get-proc (targetnode a) = p⟩ ⟨m = sourcenode
a)
have valid-return-list (a'#rs) (targetnode a)
  apply(clarsimp simp:valid-return-list-def)
  apply(case-tac cs') apply auto
  apply(erule-tac x=list in allE)
  by(case-tac list)(auto simp:targetnodes-def)
from ⟨length rs = length cs⟩ have length (a'#rs) = length (a#cs) by simp
from ⟨length s = Suc (length cs)⟩ ⟨kind a = Q:r↔pfs⟩
have length (transfer (kind a) s) = Suc (length (a#cs))
  by(cases s) auto
from IH[OF ⟨targetnode a -as→* m'⟩ ⟨preds (kinds as) (transfer (kind a) s)⟩
  ⟨transfers (kinds as) (transfer (kind a) s) = s'⟩
  ⟨valid-call-list (a # cs) (targetnode a)⟩
  ⟨∀ i < length (a'#rs). (a'#rs) ! i ∈ get-return-edges ((a#cs) ! i)⟩
  ⟨valid-return-list (a'#rs) (targetnode a)⟩ ⟨length (a'#rs) = length (a#cs)⟩
  ⟨length (transfer (kind a) s) = Suc (length (a#cs))⟩]
obtain ms ms'' s'' as' ms' as'' where length ms = length (a#cs)
  and S,kind ⊢ (targetnode a # ms,transfer (kind a) s)
    =slice-edges S (a#cs) as⇒* (ms'',s'')
  and paths:S,kind ⊢ (ms'',s'') =as'⇒τ (m' # ms',s')
  ms = targetnodes (a'#rs)
  ∀ i < length (a#cs). call-of-return-node (ms ! i) (sourcenode ((a#cs) ! i))
  slice-edges S (a#cs) as = slice-edges S (a#cs) as''
  targetnode a -as'' @ as'→* m' valid-path-aux (a#cs) (as'' @ as')
  by blast
from ⟨ms = targetnodes (a'#rs)⟩ obtain x xs where [simp]:ms = x#xs
  and x = targetnode a' and xs = targetnodes rs
  by(cases ms)(auto simp:targetnodes-def)
from ⟨∀ i < length (a#cs). call-of-return-node (ms ! i) (sourcenode ((a#cs) !
i))⟩

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  ⟨length ms = length (a#cs)⟩
have ∀ mx ∈ set xs. return-node mx
  apply(auto simp:in-set-conv-nth) apply(case-tac i)
  apply(erule-tac x=Suc 0 in allE)
  by(auto simp:call-of-return-node-def)
show ?case
proof(cases (∀ m ∈ set xs. ∃ m'. call-of-return-node m m' ∧
  m' ∈ [HRB-slice S]CFG) ∧ sourcenode a ∈ [HRB-slice S]CFG)
  case True
  with ⟨∀ i < length (a#cs). call-of-return-node (ms ! i) (sourcenode ((a#cs) !
i))⟩
    ⟨length ms = length (a#cs)⟩ ⟨kind a = Q:r↔pfs⟩
  have slice-edge S cs a
    apply(auto simp:slice-edge-def in-set-conv-nth)
    by(erule-tac x=Suc i in allE) auto
  with ⟨kind a = Q:r↔pfs⟩
  have slice-edges S cs (a#as) = a#slice-edges S (a#cs) as by simp
  from True ⟨pred (kind a) s⟩ ⟨valid-edge a⟩ ⟨kind a = Q:r↔pfs⟩
    ⟨valid-edge a'⟩ ⟨a' ∈ get-return-edges a⟩
    ⟨∀ mx ∈ set xs. return-node mx⟩ ⟨length ms = length (a#cs)⟩ ⟨m = sourcenode
a)
    ⟨length s = Suc (length cs)⟩
    ⟨length (transfer (kind a) s) = Suc (length (a#cs))⟩
  have S,kind ⊢ (sourcenode a#xs,s) -a→
    (targetnode a#targetnode a'#xs,transfer (kind a) s)
    by -(rule-tac a'=a' in observable-move-call,fastforce+)
  with ⟨length ms = length (a#cs)⟩ ⟨length s = Suc (length cs)⟩
  have S,kind ⊢ (sourcenode a#xs,s) =[]@[a]⇒
    (targetnode a#targetnode a'#xs,transfer (kind a) s)
    by(fastforce intro:observable-moves-snoc silent-moves-Nil)
  with ⟨S,kind ⊢ (targetnode a # ms,transfer (kind a) s)
    =slice-edges S (a#cs) as⇒* (ms'',s'')⟩ ⟨x = targetnode a'⟩
  have S,kind ⊢ (sourcenode a#xs,s) =last [a]#slice-edges S (a#cs) as⇒*
    (ms'',s'')
    by -(rule tom-Cons,auto)
  with ⟨slice-edges S cs (a#as) = a#slice-edges S (a#cs) as⟩
  have S,kind ⊢ (sourcenode a#xs,s) =slice-edges S cs (a#as)⇒* (ms'',s'')
    by simp
moreover
from ⟨slice-edges S (a#cs) as = slice-edges S (a#cs) as''⟩
    ⟨slice-edge S cs a⟩ ⟨kind a = Q:r↔pfs⟩
  have slice-edges S cs (a#as) = slice-edges S cs (a#as'') by simp
ultimately show ?thesis
  using paths ⟨m = sourcenode a⟩ ⟨valid-edge a⟩ ⟨kind a = Q:r↔pfs⟩
    ⟨length ms = length (a#cs)⟩ ⟨xs = targetnodes rs⟩
    ⟨slice-edges S cs (a#as) = a#slice-edges S (a#cs) as⟩
  apply(rule-tac x=xs in exI)
  apply(rule-tac x=ms'' in exI)
  apply(rule-tac x=s'' in exI)

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apply(rule-tac x=as' in exI)
apply(rule-tac x=ms' in exI)
apply(rule-tac x=a#as'' in exI)
by(auto intro:Cons-path simp:targetnodes-def)
next
case False
with  $\langle \forall mx \in \text{set } xs. \text{return-node } mx \rangle$ 
have disj:( $\exists m \in \text{set } xs. \exists m'. \text{call-of-return-node } m \ m' \wedge$ 
 $m' \notin \lfloor \text{HRB-slice } S \rfloor_{CFG} \vee \text{sourcenode } a \notin \lfloor \text{HRB-slice } S \rfloor_{CFG}$ 
by(fastforce dest:return-node-call-of-return-node)
with  $\langle \forall i < \text{length } (a\#cs). \text{call-of-return-node } (ms \ ! \ i) \ (\text{sourcenode } ((a\#cs) \ !$ 
i)) $\rangle$ 
 $\langle \text{length } ms = \text{length } (a\#cs) \rangle \langle \text{kind } a = Q:r \hookrightarrow pfs \rangle$ 
have  $\neg \text{slice-edge } S \ cs \ a$ 
apply(auto simp:slice-edge-def in-set-conv-nth)
by(erule-tac x=Suc i in allE) auto
with  $\langle \text{kind } a = Q:r \hookrightarrow pfs \rangle$ 
have slice-edges S cs (a#as) = slice-edges S (a#cs) as by simp
from disj  $\langle \text{pred } (\text{kind } a) \ s \rangle \langle \text{valid-edge } a \rangle \langle \text{kind } a = Q:r \hookrightarrow pfs \rangle$ 
 $\langle \text{valid-edge } a' \rangle \langle a' \in \text{get-return-edges } a \rangle$ 
 $\langle \forall mx \in \text{set } xs. \text{return-node } mx \rangle \langle \text{length } ms = \text{length } (a\#cs) \rangle \langle m = \text{sourcenode}$ 
a $\rangle$ 
 $\langle \text{length } s = \text{Suc } (\text{length } cs) \rangle$ 
 $\langle \text{length } (\text{transfer } (\text{kind } a) \ s) = \text{Suc } (\text{length } (a\#cs)) \rangle$ 
have S,kind  $\vdash (\text{sourcenode } a\#xs,s) -a \rightarrow_{\tau}$ 
 $(\text{targetnode } a\#\text{targetnode } a'\#xs,\text{transfer } (\text{kind } a) \ s)$ 
by  $\neg(\text{rule-tac } a'=a' \text{ in silent-move-call,fastforce+})$ 
from S,kind  $\vdash (\text{targetnode } a \ # \ ms,\text{transfer } (\text{kind } a) \ s)$ 
 $=\text{slice-edges } S \ (a\#cs) \ as \Rightarrow^* \ (ms'',s'')$ 
show ?thesis
proof(rule trans-observable-moves.cases)
fix msx sx S' f
assume targetnode a # ms = msx
and transfer (kind a) s = sx and slice-edges S (a#cs) as = []
and [simp]:ms'' = msx s'' = sx and length msx = length sx
from slice-edges S cs (a#as) = slice-edges S (a#cs) as
 $\langle \text{slice-edges } S \ (a\#cs) \ as = [] \rangle$ 
have slice-edges S cs (a#as) = [] by simp
with  $\langle \text{length } ms = \text{length } (a\#cs) \rangle \langle \text{length } s = \text{Suc } (\text{length } cs) \rangle$ 
have S,kind  $\vdash (\text{sourcenode } a\#xs,s) =\text{slice-edges } S \ cs \ (a\#as) \Rightarrow^*$ 
 $(\text{sourcenode } a\#xs,s)$ 
by(fastforce intro:tom-Nil)
moreover
from S,kind  $\vdash (ms'',s'') = as' \Rightarrow_{\tau} (m'\#ms',s')$   $\langle \text{targetnode } a \ # \ ms = msx \rangle$ 
 $\langle \text{transfer } (\text{kind } a) \ s = sx \rangle \langle ms'' = msx \rangle \langle s'' = sx \rangle \langle x = \text{targetnode } a' \rangle$ 
 $\langle S,\text{kind } \vdash (\text{sourcenode } a\#xs,s) -a \rightarrow_{\tau}$ 
 $(\text{targetnode } a\#\text{targetnode } a'\#xs,\text{transfer } (\text{kind } a) \ s) \rangle$ 
have S,kind  $\vdash (\text{sourcenode } a\#xs,s) = a\#as' \Rightarrow_{\tau} (m'\#ms',s')$ 
by(auto intro:silent-moves-Cons)

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from this  $\langle \text{valid-edge } a \rangle$ 
   $\langle \forall i < \text{length } rs. rs ! i \in \text{get-return-edges } (cs ! i) \rangle$ 
   $\langle xs = \text{targetnodes } rs \rangle \langle \text{valid-return-list } rs \ m \rangle \langle \text{length } rs = \text{length } cs \rangle$ 
   $\langle \text{length } s = \text{Suc } (\text{length } cs) \rangle \langle m = \text{sourcenode } a \rangle$ 
have sourcenode  $a - a \# as' \Rightarrow^* m'$  and valid-path-aux  $cs (a \# as')$ 
  by  $-(\text{rule } \text{silent-moves-vpa-path}, (\text{fastforce } \text{simp} : \text{targetnodes-def})) +$ 
ultimately show ?thesis using  $\langle m = \text{sourcenode } a \rangle \langle \text{length } ms = \text{length}$ 
 $(a \# cs) \rangle$ 
   $\langle \forall i < \text{length } (a \# cs). \text{call-of-return-node } (ms ! i) (\text{sourcenode } ((a \# cs) ! i)) \rangle$ 
   $\langle \text{slice-edges } S \ cs \ (a \# as) = [] \rangle \langle \text{kind } a = Q : r \hookrightarrow pfs \rangle$ 
   $\langle S, \text{kind} \vdash (\text{sourcenode } a \# xs, s) = a \# as' \Rightarrow_{\tau} (m' \# ms', s') \rangle$ 
   $\langle xs = \text{targetnodes } rs \rangle$ 
  apply  $(\text{rule-tac } x = xs \ \text{in } exI)$ 
  apply  $(\text{rule-tac } x = \text{sourcenode } a \# xs \ \text{in } exI)$ 
  apply  $(\text{rule-tac } x = s \ \text{in } exI)$ 
  apply  $(\text{rule-tac } x = a \# as' \ \text{in } exI)$ 
  apply  $(\text{rule-tac } x = ms' \ \text{in } exI)$ 
  apply  $(\text{rule-tac } x = [] \ \text{in } exI)$ 
  by auto
next
fix  $S' \ f \ msx \ sx \ asx \ msx' \ sx' \ asx' \ msx'' \ sx''$ 
assume  $[simp]: S = S'$  and  $\text{kind} = f$  and  $\text{targetnode } a \ \# \ ms = msx$ 
  and  $\text{transfer } (\text{kind } a) \ s = sx$ 
  and  $\text{slice-edges } S \ (a \# cs) \ as = \text{last } asx \ \# \ asx'$ 
  and  $ms'' = msx''$  and  $s'' = sx''$ 
  and  $S', f \vdash (msx, sx) = asx \Rightarrow (msx', sx')$ 
  and  $S', f \vdash (msx', sx') = asx' \Rightarrow^* (msx'', sx'')$ 
from  $\langle \text{kind} = f \rangle$  have  $[simp]: f = \text{kind}$  by simp
from  $\langle S, \text{kind} \vdash (\text{sourcenode } a \# xs, s) - a \rightarrow_{\tau}$ 
 $(\text{targetnode } a \# \text{targetnode } a' \# xs, \text{transfer } (\text{kind } a) \ s) \rangle$ 
   $\langle S', f \vdash (msx, sx) = asx \Rightarrow (msx', sx') \rangle \langle x = \text{targetnode } a' \rangle$ 
   $\langle \text{transfer } (\text{kind } a) \ s = sx \rangle \langle \text{targetnode } a \ \# \ ms = msx \rangle$ 
have  $S, \text{kind} \vdash (\text{sourcenode } a \# xs, s) = a \# asx \Rightarrow (msx', sx')$ 
  by  $(\text{auto } \text{intro} : \text{silent-move-observable-moves})$ 
with  $\langle S', f \vdash (msx', sx') = asx' \Rightarrow^* (msx'', sx'') \rangle \langle ms'' = msx'' \rangle \langle s'' = sx'' \rangle$ 
have  $S, \text{kind} \vdash (\text{sourcenode } a \# xs, s) = \text{last } (a \# asx) \# asx' \Rightarrow^* (ms'', s'')$ 
  by  $(\text{fastforce } \text{intro} : \text{trans-observable-moves.tom-Cons})$ 
moreover
from  $\langle S', f \vdash (msx, sx) = asx \Rightarrow (msx', sx') \rangle$  have  $asx \neq []$ 
  by  $(\text{fastforce } \text{elim} : \text{observable-moves.cases})$ 
with  $\langle \text{slice-edges } S \ cs \ (a \# as) = \text{slice-edges } S \ (a \# cs) \ as \rangle$ 
   $\langle \text{slice-edges } S \ (a \# cs) \ as = \text{last } asx \ \# \ asx' \rangle$ 
have  $\text{slice-edges } S \ cs \ (a \# as) = \text{last } (a \# asx) \# asx'$  by simp
moreover
from  $\langle \neg \text{slice-edge } S \ cs \ a \rangle \langle \text{kind } a = Q : r \hookrightarrow pfs \rangle$ 
   $\langle \text{slice-edges } S \ (a \# cs) \ as = \text{slice-edges } S \ (a \# cs) \ as'' \rangle$ 
have  $\text{slice-edges } S \ cs \ (a \ \# \ as) = \text{slice-edges } S \ cs \ (a \ \# \ as'')$  by simp
ultimately show ?thesis using  $\text{paths } \langle m = \text{sourcenode } a \rangle \langle \text{kind } a =$ 
 $Q : r \hookrightarrow pfs \rangle$ 

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    <length ms = length (a#cs)> <xs = targetnodes rs> <valid-edge a>
    apply(rule-tac x=xs in exI)
    apply(rule-tac x=ms'' in exI)
    apply(rule-tac x=s'' in exI)
    apply(rule-tac x=as' in exI)
    apply(rule-tac x=ms' in exI)
    apply(rule-tac x=a#as'' in exI)
    by(auto intro:Cons-path simp:targetnodes-def)
  qed
next
next
case (vpa-ReturnEmpty cs a as Q p f)
from <preds (kinds (a # as)) s> <length s = Suc (length cs)> <kind a = Q↔pf>
  <cs = []>
have False by(cases s)(auto simp:kinds-def)
thus ?case by simp
next
case (vpa-ReturnCons cs a as Q p f c' cs')
note IH = <∧ m s rs. [m -as→* m'; preds (kinds as) s; transfers (kinds as)
s = s'];
  valid-call-list cs' m; ∀ i < length rs. rs ! i ∈ get-return-edges (cs' ! i);
  valid-return-list rs m; length rs = length cs'; length s = Suc (length cs')
  ⇒ ∃ ms ms'' s'' as' ms' as''.
  S,kind ⊢ (m # ms,s) = slice-edges S cs' as⇒* (ms'',s'') ∧
  S,kind ⊢ (ms'',s'') = as'⇒τ (m' # ms',s') ∧ ms = targetnodes rs ∧
  length ms = length cs' ∧
  (∀ i < length cs'. call-of-return-node (ms ! i) (sourcenode (cs' ! i))) ∧
  slice-edges S cs' as = slice-edges S cs' as'' ∧
  m -as'' @ as' →* m' ∧ valid-path-aux cs' (as'' @ as')
from <m -a # as→* m'> have m = sourcenode a and valid-edge a
  and targetnode a -as→* m' by(auto elim:path-split-Cons)
from <preds (kinds (a # as)) s> have pred (kind a) s
  and preds (kinds as) (transfer (kind a) s) by(auto simp:kinds-def)
from <transfers (kinds (a # as)) s = s'>
have transfers (kinds as) (transfer (kind a) s) = s' by(fastforce simp:kinds-def)
from <valid-call-list cs m> <cs = c' # cs'> have valid-edge c'
  by(fastforce simp:valid-call-list-def)
from <valid-edge c'> <a ∈ get-return-edges c'>
have get-proc (sourcenode c') = get-proc (targetnode a)
  by(rule get-proc-get-return-edge)
from <valid-call-list cs m> <cs = c' # cs'>
  <get-proc (sourcenode c') = get-proc (targetnode a)>
have valid-call-list cs' (targetnode a)
  apply(clarsimp simp:valid-call-list-def)
  apply(erule-tac x=c' # cs' in allE)
  by(case-tac cs')(auto simp:sourcenodes-def)
from <length rs = length cs> <cs = c' # cs'> obtain r' rs'
  where [simp]:rs = r'#rs' and length rs' = length cs' by(cases rs) auto
from <∀ i < length rs. rs ! i ∈ get-return-edges (cs ! i)> <cs = c' # cs'>

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have  $\forall i < \text{length } rs'. rs' ! i \in \text{get-return-edges } (cs' ! i)$ 
  and  $r' \in \text{get-return-edges } c'$  by auto
with  $\langle \text{valid-edge } c' \rangle \langle a \in \text{get-return-edges } c' \rangle$  have  $[simp]: a = r'$ 
  by  $-(\text{rule } \text{get-return-edges-unique})$ 
with  $\langle \text{valid-return-list } rs \ m \rangle$ 
have  $\text{valid-return-list } rs' \ (\text{targetnode } a)$ 
  apply  $(\text{clarsimp } simp:\text{valid-return-list-def})$ 
  apply  $(\text{erule-tac } x=r' \ \# \ cs' \ \text{in } \text{allE})$ 
  by  $(\text{case-tac } cs')(\text{auto } simp:\text{targetnodes-def})$ 
from  $\langle \text{length } s = \text{Suc } (\text{length } cs) \rangle \langle cs = c' \ \# \ cs' \rangle \langle \text{kind } a = Q \leftrightarrow pf \rangle$ 
have  $\text{length } (\text{transfer } (\text{kind } a) \ s) = \text{Suc } (\text{length } cs')$ 
  by  $(\text{cases } s)(\text{auto}, \text{case-tac } list, \text{auto})$ 
from  $IH[OF \ \langle \text{targetnode } a \ -as \rightarrow^* \ m' \rangle \langle \text{preds } (kinds \ as) \ (\text{transfer } (\text{kind } a) \ s) \rangle$ 
   $\langle \text{transfers } (kinds \ as) \ (\text{transfer } (\text{kind } a) \ s) = s' \rangle$ 
   $\langle \text{valid-call-list } cs' \ (\text{targetnode } a) \rangle$ 
   $\langle \forall i < \text{length } rs'. rs' ! i \in \text{get-return-edges } (cs' ! i) \rangle$ 
   $\langle \text{valid-return-list } rs' \ (\text{targetnode } a) \rangle \langle \text{length } rs' = \text{length } cs' \rangle \text{this}]$ 
obtain  $ms \ ms'' \ s'' \ as' \ ms' \ as''$  where  $\text{length } ms = \text{length } cs'$ 
  and  $S, kind \vdash (\text{targetnode } a \ \# \ ms, \text{transfer } (\text{kind } a) \ s)$ 
   $= \text{slice-edges } S \ cs' \ as \Rightarrow^* (ms'', s'')$ 
  and  $\text{paths}: S, kind \vdash (ms'', s'') = as' \Rightarrow_{\tau} (m' \ \# \ ms', s')$ 
   $ms = \text{targetnodes } rs'$ 
   $\forall i < \text{length } cs'. \text{call-of-return-node } (ms ! i) \ (\text{sourcenode } (cs' ! i))$ 
   $\text{slice-edges } S \ cs' \ as = \text{slice-edges } S \ cs' \ as''$ 
   $\text{targetnode } a \ -as'' \ @ \ as' \rightarrow^* \ m' \ \text{valid-path-aux } cs' \ (as'' \ @ \ as')$ 
  by blast
from  $\langle \forall i < \text{length } cs'. \text{call-of-return-node } (ms ! i) \ (\text{sourcenode } (cs' ! i)) \rangle$ 
   $\langle \text{length } ms = \text{length } cs' \rangle$ 
have  $\forall mx \in \text{set } ms. \text{return-node } mx$ 
  by  $(\text{auto } simp:\text{in-set-conv-nth } \text{call-of-return-node-def})$ 
from  $\langle \text{valid-edge } a \rangle \langle \text{valid-edge } c' \rangle \langle a \in \text{get-return-edges } c' \rangle$ 
have  $\text{return-node } (\text{targetnode } a)$  by  $(\text{fastforce } simp:\text{return-node-def})$ 
with  $\langle \text{valid-edge } c' \rangle \langle \text{valid-edge } a \rangle \langle a \in \text{get-return-edges } c' \rangle$ 
have  $\text{call-of-return-node } (\text{targetnode } a) \ (\text{sourcenode } c')$ 
  by  $(\text{simp } add:\text{call-of-return-node-def}) \ \text{blast}$ 
show ?case
proof  $(\text{cases } (\forall m \in \text{set } (\text{targetnode } a \ \# \ ms). \exists m'. \text{call-of-return-node } m \ m' \wedge$ 
   $m' \in \lfloor \text{HRB-slice } S \rfloor_{CFG}))$ 
  case True
  then obtain  $x$  where  $\text{call-of-return-node } (\text{targetnode } a) \ x$ 
  and  $x \in \lfloor \text{HRB-slice } S \rfloor_{CFG}$  by fastforce
  with  $\langle \text{call-of-return-node } (\text{targetnode } a) \ (\text{sourcenode } c') \rangle$ 
  have  $\text{sourcenode } c' \in \lfloor \text{HRB-slice } S \rfloor_{CFG}$  by fastforce
  with True  $\langle \forall i < \text{length } cs'. \text{call-of-return-node } (ms ! i) \ (\text{sourcenode } (cs' ! i)) \rangle$ 
   $\langle \text{length } ms = \text{length } cs' \rangle \langle cs = c' \ \# \ cs' \rangle \langle \text{kind } a = Q \leftrightarrow pf \rangle$ 
  have  $\text{slice-edge } S \ cs \ a$ 
  apply  $(\text{auto } simp:\text{slice-edge-def } \text{in-set-conv-nth})$ 
  by  $(\text{erule-tac } x=i \ \text{in } \text{allE}) \ \text{auto}$ 
  with  $\langle \text{kind } a = Q \leftrightarrow pf \rangle \langle cs = c' \ \# \ cs' \rangle$ 

```

```

have slice-edges  $S$   $cs$  ( $a\#as$ ) =  $a\#$ slice-edges  $S$   $cs'$   $as$  by simp
from True  $\langle$ pred ( $kind$   $a$ )  $s$  $\rangle$   $\langle$ valid-edge  $a$  $\rangle$   $\langle$ kind  $a = Q\leftrightarrow pf$  $\rangle$ 
   $\langle$  $\forall mx \in set$   $ms$ . return-node  $mx$  $\rangle$   $\langle$ length  $ms = length$   $cs'$  $\rangle$ 
   $\langle$ length  $s = Suc$  (length  $cs$ ) $\rangle$   $\langle$  $m = sourcenode$   $a$  $\rangle$ 
   $\langle$ length (transfer ( $kind$   $a$ )  $s$ ) =  $Suc$  (length  $cs'$ ) $\rangle$ 
   $\langle$ return-node (targetnode  $a$ ) $\rangle$   $\langle$  $cs = c' \# cs'$  $\rangle$ 
have  $S, kind \vdash$  (sourcenode  $a\#$ targetnode  $a\#ms, s$ )  $-a \rightarrow$ 
  (targetnode  $a\#ms, transfer$  ( $kind$   $a$ )  $s$ )
  by (auto intro!: observable-move-return)
with  $\langle$ length  $ms = length$   $cs'$  $\rangle$   $\langle$ length  $s = Suc$  (length  $cs$ ) $\rangle$   $\langle$  $cs = c' \# cs'$  $\rangle$ 
have  $S, kind \vdash$  (sourcenode  $a\#$ targetnode  $a\#ms, s$ ) =  $\llbracket @ [a] \rrbracket \Rightarrow$ 
  (targetnode  $a\#ms, transfer$  ( $kind$   $a$ )  $s$ )
  by (fastforce intro: observable-moves-snoc silent-moves-Nil)
with  $\langle$  $S, kind \vdash$  (targetnode  $a \# ms, transfer$  ( $kind$   $a$ )  $s$ )
  = slice-edges  $S$   $cs'$   $as \Rightarrow^*$  ( $ms'', s''$ ) $\rangle$ 
have  $S, kind \vdash$  (sourcenode  $a\#$ targetnode  $a\#ms, s$ )
  = last  $[a]\#$ slice-edges  $S$   $cs'$   $as \Rightarrow^*$  ( $ms'', s''$ )
  by  $-(rule$  tom-Cons, auto)
with  $\langle$ slice-edges  $S$   $cs$  ( $a\#as$ ) =  $a\#$ slice-edges  $S$   $cs'$   $as$  $\rangle$ 
have  $S, kind \vdash$  (sourcenode  $a\#$ targetnode  $a\#ms, s$ ) = slice-edges  $S$   $cs$  ( $a\#as$ )  $\Rightarrow^*$ 

  ( $ms'', s''$ ) by simp
moreover
from  $\langle$ slice-edges  $S$   $cs'$   $as = slice$ -edges  $S$   $cs'$   $as''$  $\rangle$ 
   $\langle$ slice-edge  $S$   $cs$   $a$  $\rangle$   $\langle$ kind  $a = Q\leftrightarrow pf$  $\rangle$   $\langle$  $cs = c' \# cs'$  $\rangle$ 
have slice-edges  $S$   $cs$  ( $a\#as$ ) = slice-edges  $S$   $cs$  ( $a\#as''$ ) by simp
ultimately show ?thesis
  using paths  $\langle$  $m = sourcenode$   $a$  $\rangle$   $\langle$ valid-edge  $a$  $\rangle$   $\langle$ kind  $a = Q\leftrightarrow pf$  $\rangle$ 
   $\langle$ length  $ms = length$   $cs'$  $\rangle$   $\langle$  $ms = targetnodes$   $rs'$  $\rangle$   $\langle$  $cs = c' \# cs'$  $\rangle$ 
   $\langle$ slice-edges  $S$   $cs$  ( $a\#as$ ) =  $a\#$ slice-edges  $S$   $cs'$   $as$  $\rangle$ 
   $\langle$  $a \in get$ -return-edges  $c'$  $\rangle$ 
   $\langle$ call-of-return-node (targetnode  $a$ ) (sourcenode  $c'$ ) $\rangle$ 
  apply (rule-tac  $x=targetnode$   $a\#ms$  in  $exI$ )
  apply (rule-tac  $x=ms''$  in  $exI$ )
  apply (rule-tac  $x=s''$  in  $exI$ )
  apply (rule-tac  $x=as'$  in  $exI$ )
  apply (rule-tac  $x=ms'$  in  $exI$ )
  apply (rule-tac  $x=a\#as''$  in  $exI$ )
  apply (auto intro: Cons-path simp: targetnodes-def)
  by (case-tac  $i$ ) auto
next
case False
with  $\langle$  $\forall mx \in set$   $ms$ . return-node  $mx$  $\rangle$   $\langle$ return-node (targetnode  $a$ ) $\rangle$ 
have  $\exists m \in set$  (targetnode  $a \# ms$ ).  $\exists m'$ . call-of-return-node  $m$   $m' \wedge$ 
   $m' \notin [HRB$ -slice  $S]_{CFG}$ 
  by (fastforce dest: return-node-call-of-return-node)
with  $\langle$  $\forall i < length$   $cs'$ . call-of-return-node ( $ms ! i$ ) (sourcenode ( $cs' ! i$ )) $\rangle$ 
   $\langle$ length  $ms = length$   $cs'$  $\rangle$   $\langle$  $cs = c' \# cs'$  $\rangle$   $\langle$ kind  $a = Q\leftrightarrow pf$  $\rangle$ 
   $\langle$ call-of-return-node (targetnode  $a$ ) (sourcenode  $c'$ ) $\rangle$ 

```

```

have  $\neg$  slice-edge  $S$   $cs$   $a$ 
  apply(auto simp:slice-edge-def in-set-conv-nth)
  by(erule-tac  $x=i$  in allE) auto
with  $\langle kind\ a = Q \leftrightarrow pf \rangle \langle cs = c' \# cs' \rangle$ 
have slice-edges  $S$   $cs$   $(a\#as) = slice\text{-edges}\ S\ cs'\ as$  by simp
from  $\langle pred\ (kind\ a)\ s \rangle \langle valid\text{-edge}\ a \rangle \langle kind\ a = Q \leftrightarrow pf \rangle$ 
   $\langle \forall mx \in set\ ms.\ return\text{-node}\ mx \rangle \langle length\ ms = length\ cs' \rangle$ 
   $\langle length\ s = Suc\ (length\ cs) \rangle \langle m = sourcenode\ a \rangle$ 
   $\langle length\ (transfer\ (kind\ a)\ s) = Suc\ (length\ cs') \rangle$ 
   $\langle return\text{-node}\ (targetnode\ a) \rangle \langle cs = c' \# cs' \rangle$ 
   $\langle \exists m \in set\ (targetnode\ a\ \# ms).\ \exists m'. call\text{-of}\text{-return}\text{-node}\ m\ m' \wedge$ 
   $m' \notin \lfloor HRB\text{-slice}\ S \rfloor_{CFG} \rangle$ 
have  $S, kind \vdash (sourcenode\ a\ \# targetnode\ a\ \# ms, s) -a \rightarrow_{\tau}$ 
   $(targetnode\ a\ \# ms, transfer\ (kind\ a)\ s)$ 
  by(auto intro!:silent-move-return)
from  $\langle S, kind \vdash (targetnode\ a\ \# ms, transfer\ (kind\ a)\ s)$ 
   $= slice\text{-edges}\ S\ cs'\ as \Rightarrow * (ms'', s'') \rangle$ 
show ?thesis
proof(rule trans-observable-moves.cases)
  fix  $msx\ sx\ S'\ f'$ 
  assume  $targetnode\ a\ \# ms = msx$ 
  and  $transfer\ (kind\ a)\ s = sx$  and  $slice\text{-edges}\ S\ cs'\ as = []$ 
  and  $[simp]:ms'' = msx\ s'' = sx$  and  $length\ msx = length\ sx$ 
from  $\langle slice\text{-edges}\ S\ cs\ (a\#as) = slice\text{-edges}\ S\ cs'\ as \rangle$ 
   $\langle slice\text{-edges}\ S\ cs'\ as = [] \rangle$ 
have  $slice\text{-edges}\ S\ cs\ (a\#as) = []$  by simp
with  $\langle length\ ms = length\ cs' \rangle \langle length\ s = Suc\ (length\ cs) \rangle \langle cs = c' \# cs' \rangle$ 
  have  $S, kind \vdash (sourcenode\ a\ \# targetnode\ a\ \# ms, s) = slice\text{-edges}\ S\ cs$ 
   $(a\#as) \Rightarrow *$ 
   $(sourcenode\ a\ \# targetnode\ a\ \# ms, s)$ 
  by(fastforce intro:tom-Nil)
moreover
from  $\langle S, kind \vdash (ms'', s'') = as' \Rightarrow_{\tau} (m' \# ms', s') \rangle \langle targetnode\ a\ \# ms = msx \rangle$ 
   $\langle transfer\ (kind\ a)\ s = sx \rangle \langle ms'' = msx \rangle \langle s'' = sx \rangle$ 
   $\langle S, kind \vdash (sourcenode\ a\ \# targetnode\ a\ \# ms, s) -a \rightarrow_{\tau}$ 
   $(targetnode\ a\ \# ms, transfer\ (kind\ a)\ s) \rangle$ 
have  $S, kind \vdash (sourcenode\ a\ \# targetnode\ a\ \# ms, s) = a\#as' \Rightarrow_{\tau} (m' \# ms', s')$ 
  by(auto intro:silent-moves-Cons)
from this  $\langle valid\text{-edge}\ a \rangle$ 
   $\langle \forall i < length\ rs.\ rs\ !\ i \in get\text{-return}\text{-edges}\ (cs\ !\ i) \rangle$ 
   $\langle valid\text{-return}\text{-list}\ rs\ m \rangle \langle length\ rs = length\ cs \rangle$ 
   $\langle length\ s = Suc\ (length\ cs) \rangle \langle m = sourcenode\ a \rangle$ 
   $\langle ms = targetnodes\ rs' \rangle \langle rs = r' \# rs' \rangle \langle cs = c' \# cs' \rangle$ 
have  $sourcenode\ a -a\#as' \rightarrow * m'$  and  $valid\text{-path}\text{-aux}\ cs\ (a\#as')$ 
  by  $\neg$ (rule silent-moves-vpa-path, (fastforce simp:targetnodes-def))+
ultimately show ?thesis using  $\langle m = sourcenode\ a \rangle \langle length\ ms = length$ 
 $cs' \rangle$ 
   $\langle \forall i < length\ cs'. call\text{-of}\text{-return}\text{-node}\ (ms\ !\ i)\ (sourcenode\ (cs'\ !\ i)) \rangle$ 
   $\langle slice\text{-edges}\ S\ cs\ (a\#as) = [] \rangle \langle kind\ a = Q \leftrightarrow pf \rangle$ 

```



```

⟨S, kind ⊢ (sourcenode a # targetnode a # ms, s) = a # as' ⇒τ (m' # ms', s')⟩
⟨ms = targetnodes rs'⟩ ⟨rs = r' # rs'⟩ ⟨cs = c' # cs'⟩
⟨call-of-return-node (targetnode a) (sourcenode c')⟩
apply(rule-tac x=targetnode a # ms in exI)
apply(rule-tac x=sourcenode a # targetnode a # ms in exI)
apply(rule-tac x=s in exI)
apply(rule-tac x=a # as' in exI)
apply(rule-tac x=ms' in exI)
apply(rule-tac x=[] in exI)
apply(auto simp:targetnodes-def)
by(case-tac i) auto
next
fix S' f' msx sx asx msx' sx' asx' msx'' sx''
assume [simp]:S = S' and kind = f' and targetnode a # ms = msx
and transfer (kind a) s = sx
and slice-edges S cs' as = last asx # asx'
and ms'' = msx'' and s'' = sx''
and S', f' ⊢ (msx, sx) = asx ⇒ (msx', sx')
and S', f' ⊢ (msx', sx') = asx' ⇒* (msx'', sx'')
from ⟨kind = f'⟩ have [simp]:f' = kind by simp
from ⟨S, kind ⊢ (sourcenode a # targetnode a # ms, s) - a →τ
(targetnode a # ms, transfer (kind a) s)⟩
⟨S', f' ⊢ (msx, sx) = asx ⇒ (msx', sx')⟩
⟨transfer (kind a) s = sx⟩ ⟨targetnode a # ms = msx⟩
have S, kind ⊢ (sourcenode a # targetnode a # ms, s) = a # asx ⇒ (msx', sx')
by(auto intro:silent-move-observable-moves)
with ⟨S', f' ⊢ (msx', sx') = asx' ⇒* (msx'', sx'')⟩ ⟨ms'' = msx''⟩ ⟨s'' = sx''⟩
have S, kind ⊢ (sourcenode a # targetnode a # ms, s) = last (a # asx) # asx' ⇒*
(ms'', s'')
by(fastforce intro:trans-observable-moves.tom-Cons)
moreover
from ⟨S', f' ⊢ (msx, sx) = asx ⇒ (msx', sx')⟩ have asx ≠ []
by(fastforce elim:observable-moves.cases)
with ⟨slice-edges S cs (a # as) = slice-edges S cs' as⟩
⟨slice-edges S cs' as = last asx # asx'⟩
have slice-edges S cs (a # as) = last (a # asx) # asx' by simp
moreover
from ⟨¬ slice-edge S cs a⟩ ⟨kind a = Q↔pf⟩
⟨slice-edges S cs' as = slice-edges S cs' as''⟩ ⟨cs = c' # cs'⟩
have slice-edges S cs (a # as) = slice-edges S cs (a # as'') by simp
ultimately show ?thesis using paths ⟨m = sourcenode a⟩ ⟨kind a = Q↔pf⟩
⟨length ms = length cs'⟩ ⟨ms = targetnodes rs'⟩ ⟨valid-edge a⟩
⟨rs = r' # rs'⟩ ⟨cs = c' # cs'⟩ ⟨r' ∈ get-return-edges c'⟩
⟨call-of-return-node (targetnode a) (sourcenode c')⟩
apply(rule-tac x=targetnode a # ms in exI)
apply(rule-tac x=ms'' in exI)
apply(rule-tac x=s'' in exI)
apply(rule-tac x=as' in exI)
apply(rule-tac x=ms' in exI)

```

```

    apply(rule-tac x=a#as'' in exI)
    apply(auto intro:Cons-path simp:targetnodes-def)
    by(case-tac i) auto
  qed
  qed
  qed
  qed

```

lemma *valid-path-trans-observable-moves*:

```

  assumes  $m -as \rightarrow_{\sqrt{*}} m'$  and  $\text{preds } (\text{kinds } as) [cf] = s'$ 
  and  $\text{transfers } (\text{kinds } as) [cf] = s'$ 
  obtains  $ms'' s'' ms' as' as''$ 
  where  $S, kind \vdash ([m], [cf]) = \text{slice-edges } S \ [] \ as \Rightarrow^* (ms'', s'')$ 
  and  $S, kind \vdash (ms'', s'') = as' \Rightarrow_{\tau} (m' \# ms', s')$ 
  and  $\text{slice-edges } S \ [] \ as = \text{slice-edges } S \ [] \ as''$ 
  and  $m -as'' @ as' \rightarrow_{\sqrt{*}} m'$ 
  proof(atomize-elim)
    from  $\langle m -as \rightarrow_{\sqrt{*}} m' \rangle$  have  $\text{valid-path-aux } \ [] \ as$  and  $m -as \rightarrow^* m'$ 
    by(simp-all add:vp-def valid-path-def)
    from this  $\langle \text{preds } (\text{kinds } as) [cf] \rangle \langle \text{transfers } (\text{kinds } as) [cf] = s' \rangle$ 
    show  $\exists ms'' s'' as' ms' as''$ .
       $S, kind \vdash ([m], [cf]) = \text{slice-edges } S \ [] \ as \Rightarrow^* (ms'', s'') \wedge$ 
       $S, kind \vdash (ms'', s'') = as' \Rightarrow_{\tau} (m' \# ms', s') \wedge$ 
       $\text{slice-edges } S \ [] \ as = \text{slice-edges } S \ [] \ as'' \wedge m -as'' @ as' \rightarrow_{\sqrt{*}} m'$ 
    by  $-(\text{erule vpa-trans-observable-moves}[\text{of } \text{-----} \ [] \ S],$ 
       $\text{auto simp:valid-call-list-def valid-return-list-def vp-def valid-path-def})$ 
  qed

```

lemma *WS-weak-sim-trans*:

```

  assumes  $((ms_1, s_1), (ms_2, s_2)) \in WS S$ 
  and  $S, kind \vdash (ms_1, s_1) = as \Rightarrow^* (ms_1', s_1')$  and  $as \neq []$ 
  shows  $((ms_1', s_1'), (ms_1', \text{transfers } (\text{slice-kinds } S as) s_2)) \in WS S \wedge$ 
   $S, \text{slice-kind } S \vdash (ms_2, s_2) = as \Rightarrow^* (ms_1', \text{transfers } (\text{slice-kinds } S as) s_2)$ 
  proof -
    obtain  $f$  where  $f = \text{kind}$  by simp
    with  $\langle S, kind \vdash (ms_1, s_1) = as \Rightarrow^* (ms_1', s_1') \rangle$ 
    have  $S, f \vdash (ms_1, s_1) = as \Rightarrow^* (ms_1', s_1')$  by simp
    from  $\langle S, f \vdash (ms_1, s_1) = as \Rightarrow^* (ms_1', s_1') \rangle \langle ((ms_1, s_1), (ms_2, s_2)) \in WS S \rangle$ 
     $\langle as \neq [] \rangle \langle f = \text{kind} \rangle$ 
    show ?thesis
    proof(induct arbitrary:ms2 s2 rule:trans-observable-moves.induct)
      case tom-Nil thus ?case by simp
    next
      case (tom-Cons S f ms s as ms' s' as' ms'' s'')
      note IH =  $\langle \bigwedge ms_2 s_2. \llbracket ((ms', s'), (ms_2, s_2)) \in WS S; as' \neq []; f = \text{kind} \rrbracket$ 
       $\implies ((ms'', s''), (ms'', \text{transfers } (\text{slice-kinds } S as') s_2)) \in WS S \wedge$ 

```

$S, \text{slice-kind } S \vdash (ms_2, s_2) = as' \Rightarrow^* (ms'', \text{transfers } (\text{slice-kinds } S \text{ } as') \ s_2)$
from $\langle S, f \vdash (ms, s) = as \Rightarrow (ms', s') \rangle$ **have** $s' \neq []$
by (*fastforce elim: observable-moves.cases observable-move.cases*)
from $\langle S, f \vdash (ms, s) = as \Rightarrow (ms', s') \rangle$
obtain $asx \ ax \ msx \ sx$ **where** $S, f \vdash (ms, s) = asx \Rightarrow_\tau (msx, sx)$
and $S, f \vdash (msx, sx) - ax \rightarrow (ms', s')$ **and** $as = asx @ [ax]$
by (*fastforce elim: observable-moves.cases*)
from $\langle S, f \vdash (ms, s) = asx \Rightarrow_\tau (msx, sx) \rangle \langle ((ms, s), (ms_2, s_2)) \in WS \ S \rangle \langle f = kind \rangle$
have $((msx, sx), (ms_2, s_2)) \in WS \ S$ **by** (*fastforce intro: WS-silent-moves*)
from $\langle ((msx, sx), (ms_2, s_2)) \in WS \ S \rangle \langle S, f \vdash (msx, sx) - ax \rightarrow (ms', s') \rangle \langle s' \neq [] \rangle$
 $\langle f = kind \rangle$
obtain asx' **where** $((ms', s'), (ms', \text{transfer } (\text{slice-kind } S \ ax) \ s_2)) \in WS \ S$
and $S, \text{slice-kind } S \vdash (ms_2, s_2) = asx' @ [ax] \Rightarrow$
 $(ms', \text{transfer } (\text{slice-kind } S \ ax) \ s_2)$
by (*fastforce elim: WS-observable-move*)
show *?case*
proof (*cases as' = []*)
case *True*
with $\langle S, f \vdash (ms', s') = as' \Rightarrow^* (ms'', s'') \rangle$ **have** $ms' = ms'' \wedge s' = s''$
by (*fastforce elim: trans-observable-moves.cases dest: observable-move-notempty*)
from $\langle ((ms', s'), (ms', \text{transfer } (\text{slice-kind } S \ ax) \ s_2)) \in WS \ S \rangle$
have $\text{length } ms' = \text{length } (\text{transfer } (\text{slice-kind } S \ ax) \ s_2)$
by (*fastforce elim: WS.cases*)
with $\langle S, \text{slice-kind } S \vdash (ms_2, s_2) = asx' @ [ax] \Rightarrow$
 $(ms', \text{transfer } (\text{slice-kind } S \ ax) \ s_2) \rangle$
have $S, \text{slice-kind } S \vdash (ms_2, s_2) = (\text{last } (asx' @ [ax])) \# [] \Rightarrow^*$
 $(ms', \text{transfer } (\text{slice-kind } S \ ax) \ s_2)$
by (*fastforce intro: trans-observable-moves.intros*)
with $\langle ((ms', s'), (ms', \text{transfer } (\text{slice-kind } S \ ax) \ s_2)) \in WS \ S \rangle \langle as = asx @ [ax] \rangle$
 $\langle ms' = ms'' \wedge s' = s'' \rangle$ *True*
show *?thesis* **by** (*fastforce simp: slice-kinds-def*)
next
case *False*
from *IH[OF* $\langle ((ms', s'), (ms', \text{transfer } (\text{slice-kind } S \ ax) \ s_2)) \in WS \ S \rangle$ *this*
 $\langle f = kind \rangle]$
have $((ms'', s''), (ms'', \text{transfers } (\text{slice-kinds } S \ as') \ s_2)) \in WS \ S$
and $S, \text{slice-kind } S \vdash (ms', \text{transfer } (\text{slice-kind } S \ ax) \ s_2) = as' \Rightarrow^*$
 $(ms'', \text{transfers } (\text{slice-kinds } S \ as') \ (\text{transfer } (\text{slice-kind } S \ ax) \ s_2))$
by *simp-all*
with $\langle S, \text{slice-kind } S \vdash (ms_2, s_2) = asx' @ [ax] \Rightarrow$
 $(ms', \text{transfer } (\text{slice-kind } S \ ax) \ s_2) \rangle$
have $S, \text{slice-kind } S \vdash (ms_2, s_2) = (\text{last } (asx' @ [ax])) \# as' \Rightarrow^*$
 $(ms'', \text{transfers } (\text{slice-kinds } S \ as') \ (\text{transfer } (\text{slice-kind } S \ ax) \ s_2))$
by (*fastforce intro: trans-observable-moves.tom-Cons*)
with $\langle ((ms'', s''), (ms'', \text{transfers } (\text{slice-kinds } S \ as') \ s_2)) \in WS \ S \rangle$
 $\langle \text{transfer } (\text{slice-kind } S \ ax) \ s_2 \rangle \rangle \in WS \ S$ *False* $\langle as = asx @ [ax] \rangle$
show *?thesis* **by** (*fastforce simp: slice-kinds-def*)
qed

qed
qed

lemma *stacks-rewrite*:

assumes *valid-call-list cs m* **and** *valid-return-list rs m*
and $\forall i < \text{length } rs. rs!i \in \text{get-return-edges } (cs!i)$
and $\text{length } rs = \text{length } cs$ **and** $ms = \text{targetnodes } rs$
shows $\forall i < \text{length } cs. \text{call-of-return-node } (ms!i) (\text{sourcenode } (cs!i))$

proof

fix i **show** $i < \text{length } cs \longrightarrow$
 $\text{call-of-return-node } (ms!i) (\text{sourcenode } (cs!i))$

proof

assume $i < \text{length } cs$
with $\langle \forall i < \text{length } rs. rs!i \in \text{get-return-edges } (cs!i) \rangle \langle \text{length } rs = \text{length } cs \rangle$
have $rs!i \in \text{get-return-edges } (cs!i)$ **by** *fastforce*
from $\langle \text{valid-return-list } rs \ m \rangle$ **have** $\forall r \in \text{set } rs. \text{valid-edge } r$
by *(rule valid-return-list-valid-edges)*
with $\langle i < \text{length } cs \rangle \langle \text{length } rs = \text{length } cs \rangle$
have *valid-edge* $(rs!i)$ **by** *(simp add:all-set-conv-all-nth)*
from $\langle \text{valid-call-list } cs \ m \rangle$ **have** $\forall c \in \text{set } cs. \text{valid-edge } c$
by *(rule valid-call-list-valid-edges)*
with $\langle i < \text{length } cs \rangle$ **have** *valid-edge* $(cs!i)$ **by** *(simp add:all-set-conv-all-nth)*
with $\langle \text{valid-edge } (rs!i) \rangle \langle rs!i \in \text{get-return-edges } (cs!i) \rangle \langle ms = \text{targetnodes } rs \rangle$
 $\langle i < \text{length } cs \rangle \langle \text{length } rs = \text{length } cs \rangle$
show $\text{call-of-return-node } (ms!i) (\text{sourcenode } (cs!i))$
by *(fastforce simp:call-of-return-node-def return-node-def targetnodes-def)*

qed
qed

lemma *slice-tom-preds-vp*:

assumes $S, \text{slice-kind } S \vdash (m \# ms, s) = as \Rightarrow * (m' \# ms', s')$ **and** *valid-node* m
and *valid-call-list* $cs \ m$ **and** $\forall i < \text{length } rs. rs!i \in \text{get-return-edges } (cs!i)$
and *valid-return-list* $rs \ m$ **and** $\text{length } rs = \text{length } cs$ **and** $ms = \text{targetnodes } rs$
and $\forall mx \in \text{set } ms. \exists mx'. \text{call-of-return-node } mx \ mx' \wedge mx' \in \lfloor \text{HRB-slice } S \rfloor_{CFG}$
obtains $as' \ cs' \ rs'$ **where** *preds* $(\text{slice-kinds } S \ as') \ s$
and *slice-edges* $S \ cs \ as' = as$ **and** $m -as' \rightarrow * m'$ **and** *valid-path-aux* $cs \ as'$
and *upd-cs* $cs \ as' = cs'$ **and** *valid-node* m' **and** *valid-call-list* $cs' \ m'$
and $\forall i < \text{length } rs'. rs'!i \in \text{get-return-edges } (cs'!i)$
and *valid-return-list* $rs' \ m'$ **and** $\text{length } rs' = \text{length } cs'$
and $ms' = \text{targetnodes } rs'$ **and** *transfers* $(\text{slice-kinds } S \ as') \ s \neq \square$
and *transfers* $(\text{slice-kinds } S \ (\text{slice-edges } S \ cs \ as')) \ s =$
 $\text{transfers } (\text{slice-kinds } S \ as') \ s$

proof *(atomize-elim)*

from *assms* **show** $\exists as' \ cs' \ rs'. \text{preds } (\text{slice-kinds } S \ as') \ s \wedge$
 $\text{slice-edges } S \ cs \ as' = as \wedge m -as' \rightarrow * m' \wedge \text{valid-path-aux } cs \ as' \wedge$
 $\text{upd-cs } cs \ as' = cs' \wedge \text{valid-node } m' \wedge \text{valid-call-list } cs' \ m' \wedge$
 $(\forall i < \text{length } rs'. rs'!i \in \text{get-return-edges } (cs'!i)) \wedge \text{valid-return-list } rs' \ m' \wedge$

```

length rs' = length cs' ∧ ms' = targetnodes rs' ∧
transfers (slice-kinds S as') s ≠ [] ∧
transfers (slice-kinds S (slice-edges S cs as')) s =
transfers (slice-kinds S as') s
proof(induct S slice-kind S m#ms s as m'#ms' s'
arbitrary:m ms cs rs rule:trans-observable-moves.induct)
case (tom-Nil s nc)
from ⟨length (m' # ms') = length s⟩ have s ≠ [] by(cases s) auto
have preds (slice-kinds S []) s by(fastforce simp:slice-kinds-def)
moreover
have slice-edges S cs [] = [] by simp
moreover
from ⟨valid-node m'⟩ have m' -[]→* m' by(fastforce intro:empty-path)
moreover
have valid-path-aux cs [] by simp
moreover
have upd-cs cs [] = cs by simp
ultimately show ?case using ⟨valid-call-list cs m'⟩ ⟨valid-return-list rs m'⟩
⟨∀i<length rs. rs ! i ∈ get-return-edges (cs ! i)⟩ ⟨length rs = length cs⟩
⟨ms' = targetnodes rs⟩ ⟨s ≠ []⟩ ⟨valid-node m'⟩
apply(rule-tac x=[] in exI)
apply(rule-tac x=cs in exI)
apply(rule-tac x=rs in exI)
by(clarsimp simp:slice-kinds-def)
next
case (tom-Cons S s as msx' s' as' sx'')
note IH = ⟨∧m ms cs rs. [msx' = m # ms; valid-node m; valid-call-list cs m;
∀i<length rs. rs ! i ∈ get-return-edges (cs ! i); valid-return-list rs m;
length rs = length cs; ms = targetnodes rs;
∀mx∈set ms. ∃mx'. call-of-return-node mx mx' ∧ mx' ∈ [HRB-slice S] CFG]
⇒ ∃as'' cs' rs'. preds (slice-kinds S as'') s' ∧
slice-edges S cs as'' = as' ∧ m -as''→* m' ∧ valid-path-aux cs as'' ∧
upd-cs cs as'' = cs' ∧ valid-node m' ∧ valid-call-list cs' m' ∧
(∀i<length rs'. rs' ! i ∈ get-return-edges (cs' ! i)) ∧
valid-return-list rs' m' ∧ length rs' = length cs' ∧ ms' = targetnodes rs' ∧
transfers (slice-kinds S as'') s' ≠ [] ∧
transfers (slice-kinds S (slice-edges S cs as'')) s' =
transfers (slice-kinds S as'') s'⟩
note callstack = ⟨∀mx∈set ms.
∃mx'. call-of-return-node mx mx' ∧ mx' ∈ [HRB-slice S] CFG⟩
from ⟨S,slice-kind S ⊢ (m # ms,s) =as⇒ (msx',s')⟩
obtain asx ax xs s'' where as = asx@[ax]
and S,slice-kind S ⊢ (m#ms,s) =asx⇒τ (xs,s'')
and S,slice-kind S ⊢ (xs,s'') -ax→ (msx',s')
by(fastforce elim:observable-moves.cases)
from ⟨S,slice-kind S ⊢ (xs,s'') -ax→ (msx',s')⟩
obtain xs' ms'' where [simp]:xs = sourcenode ax#xs' msx' = targetnode
ax#ms''
by(cases xs)(auto elim!:observable-move.cases,case-tac list,auto)

```

from $\langle S, \text{slice-kind } S \vdash (m \# ms, s) = as \Rightarrow (msx', s') \rangle$ *tom-Cons*
obtain $cs'' \ rs''$ **where** *results:valid-node (targetnode ax)*
valid-call-list cs'' (targetnode ax)
 $\forall i < \text{length } rs''. \ rs''!i \in \text{get-return-edges } (cs''!i)$
valid-return-list rs'' (targetnode ax) length rs'' = length cs''
 $ms'' = \text{targetnodes } rs''$ **and** *upd-cs cs as = cs''*
by(*auto elim!:observable-moves-preserves-stack*)
from $\langle S, \text{slice-kind } S \vdash (m \# ms, s) = asx \Rightarrow_{\tau} (xs, s'') \rangle$ *callstack*
have $\forall a \in \text{set } asx. \ \text{intra-kind } (kind \ a)$
by *simp(rule silent-moves-slice-intra-path)*
with $\langle S, \text{slice-kind } S \vdash (m \# ms, s) = asx \Rightarrow_{\tau} (xs, s'') \rangle$
have [*simp*]: $xs' = ms$ **by**(*fastforce dest:silent-moves-intra-path*)
from $\langle S, \text{slice-kind } S \vdash (xs, s'') - ax \rightarrow (msx', s') \rangle$
have $\forall mx \in \text{set } ms''. \ \exists mx'. \ \text{call-of-return-node } mx \ mx' \wedge mx' \in \lfloor \text{HRB-slice } S \rfloor_{CFG}$
by(*fastforce dest:observable-set-stack-in-slice*)
from *IH[OF $\langle msx' = \text{targetnode } ax \# ms'' \rangle$ results this]*
obtain $asx' \ cs' \ rs'$ **where** *preds (slice-kinds S asx') s'*
and *slice-edges S cs'' asx' = as' and targetnode ax - asx' \rightarrow^* m'*
and *valid-path-aux cs'' asx' and upd-cs cs'' asx' = cs'*
and *valid-node m' and valid-call-list cs' m'*
and $\forall i < \text{length } rs'. \ rs'!i \in \text{get-return-edges } (cs'!i)$
and *valid-return-list rs' m' and length rs' = length cs'*
and $ms' = \text{targetnodes } rs'$ **and** *transfers (slice-kinds S asx') s' \neq []*
and *trans-eq:transfers (slice-kinds S (slice-edges S cs'' asx')) s' =*
transfers (slice-kinds S asx') s'
by *blast*
from $\langle S, \text{slice-kind } S \vdash (m \# ms, s) = asx \Rightarrow_{\tau} (xs, s'') \rangle$
have *preds (slice-kinds S asx) s and transfers (slice-kinds S asx) s = s''*
by(*auto intro:silent-moves-preds-transfers simp:slice-kinds-def*)
from $\langle S, \text{slice-kind } S \vdash (xs, s'') - ax \rightarrow (msx', s') \rangle$
have *pred (slice-kind S ax) s'' and transfer (slice-kind S ax) s'' = s'*
by(*auto elim:observable-move.cases*)
with $\langle \text{preds (slice-kinds S asx) s} \rangle \langle as = asx@[ax] \rangle$
 $\langle \text{transfers (slice-kinds S asx) s} = s'' \rangle$
have *preds (slice-kinds S as) s by (simp add:preds-split slice-kinds-def)*
from $\langle \text{transfers (slice-kinds S asx) s} = s'' \rangle$
 $\langle \text{transfer (slice-kind S ax) s''} = s' \rangle \langle as = asx@[ax] \rangle$
have *transfers (slice-kinds S as) s = s'*
by(*simp add:transfers-split slice-kinds-def*)
with $\langle \text{preds (slice-kinds S asx') s'} \rangle \langle \text{preds (slice-kinds S as) s} \rangle$
have *preds (slice-kinds S (as@asx')) s by (simp add:preds-split slice-kinds-def)*
moreover
from $\langle \text{valid-call-list } cs \ m \rangle \langle \text{valid-return-list } rs \ m \rangle$
 $\langle \forall i < \text{length } rs. \ rs!i \in \text{get-return-edges } (cs!i) \rangle \langle \text{length } rs = \text{length } cs \rangle$
 $\langle ms = \text{targetnodes } rs \rangle$
have $\forall i < \text{length } cs. \ \text{call-of-return-node } (ms!i) \ (\text{sourcenode } (cs!i))$
by(*rule stacks-rewrite*)
with $\langle S, \text{slice-kind } S \vdash (m \# ms, s) = as \Rightarrow (msx', s') \rangle \langle ms = \text{targetnodes } rs \rangle$

$\langle \text{length } rs = \text{length } cs \rangle$
have $\text{slice-edges } S \text{ cs as} = [\text{last as}]$
by ($\text{fastforce elim:observable-moves-singular-slice-edge}$)
with $\langle \text{slice-edges } S \text{ cs'' asx}' = \text{as}' \rangle \langle \text{upd-cs cs as} = \text{cs}'' \rangle$
have $\text{slice-edges } S \text{ cs (as@asx}')$ $= [\text{last as}]@as'$
by ($\text{fastforce intro:slice-edges-Append}$)
moreover
from $\langle S, \text{slice-kind } S \vdash (m \# ms, s) = as \Rightarrow_{\tau} (xs, s'') \rangle \langle \text{valid-node } m \rangle$
 $\langle \text{valid-call-list cs } m \rangle \langle \forall i < \text{length } rs. rs ! i \in \text{get-return-edges } (cs ! i) \rangle$
 $\langle \text{valid-return-list rs } m \rangle \langle \text{length } rs = \text{length } cs \rangle \langle ms = \text{targetnodes } rs \rangle$
have $m - as \rightarrow^* \text{sourcenode } ax$ **by** ($\text{fastforce intro:silent-moves-vpa-path}$)
from $\langle S, \text{slice-kind } S \vdash (xs, s'') - ax \rightarrow (msx', s') \rangle$ **have** $\text{valid-edge } ax$
by ($\text{fastforce elim:observable-move.cases}$)
hence $\text{sourcenode } ax - [ax] \rightarrow^* \text{targetnode } ax$ **by** (rule path-edge)
with $\langle m - as \rightarrow^* \text{sourcenode } ax \rangle \langle as = asx@[ax] \rangle$
have $m - as \rightarrow^* \text{targetnode } ax$ **by** ($\text{fastforce intro:path-Append}$)
with $\langle \text{targetnode } ax - asx' \rightarrow^* m' \rangle$ **have** $m - as@asx' \rightarrow^* m'$
by $-(\text{rule path-Append})$
moreover
from $\langle \forall a \in \text{set } asx. \text{intra-kind } (kind \ a) \rangle$ **have** $\text{valid-path-aux cs asx}$
by ($\text{rule valid-path-aux-intra-path}$)
from $\langle \forall a \in \text{set } asx. \text{intra-kind } (kind \ a) \rangle$ **have** $\text{upd-cs cs asx} = cs$
by ($\text{rule upd-cs-intra-path}$)
from $\langle m - asx \rightarrow^* \text{sourcenode } ax \rangle \langle \forall a \in \text{set } asx. \text{intra-kind } (kind \ a) \rangle$
have $\text{get-proc } m = \text{get-proc } (\text{sourcenode } ax)$
by ($\text{fastforce intro:intra-path-get-procs simp:intra-path-def}$)
with $\langle \text{valid-return-list rs } m \rangle$ **have** $\text{valid-return-list rs } (\text{sourcenode } ax)$
apply ($\text{clarsimp simp:valid-return-list-def}$)
apply ($\text{erule-tac } x = cs' \text{ in allE}$) **apply** clarsimp
by ($\text{case-tac } cs'$) auto
with $\langle S, \text{slice-kind } S \vdash (xs, s'') - ax \rightarrow (msx', s') \rangle \langle \text{valid-edge } ax \rangle$
 $\langle \forall i < \text{length } rs. rs ! i \in \text{get-return-edges } (cs ! i) \rangle \langle ms = \text{targetnodes } rs \rangle$
 $\langle \text{length } rs = \text{length } cs \rangle$
have $\text{valid-path-aux cs } [ax]$
by ($\text{auto intro!:observable-move-vpa-path simp del:valid-path-aux.simps}$)
with $\langle \text{valid-path-aux cs asx} \rangle \langle \text{upd-cs cs asx} = cs \rangle \langle as = asx@[ax] \rangle$
have $\text{valid-path-aux cs as}$ **by** ($\text{fastforce intro:valid-path-aux-Append}$)
with $\langle \text{upd-cs cs as} = \text{cs}'' \rangle \langle \text{valid-path-aux cs'' asx}' \rangle$
have $\text{valid-path-aux cs (as@asx}')$ **by** ($\text{fastforce intro:valid-path-aux-Append}$)
moreover
from $\langle \text{upd-cs cs as} = \text{cs}'' \rangle \langle \text{upd-cs cs'' asx}' = \text{cs}' \rangle$
have $\text{upd-cs cs (as@asx}') = \text{cs}'$ **by** ($\text{rule upd-cs-Append}$)
moreover
from $\langle \text{transfers } (\text{slice-kinds } S \ as) \ s = s' \rangle$
 $\langle \text{transfers } (\text{slice-kinds } S \ asx') \ s' \neq [] \rangle$
have $\text{transfers } (\text{slice-kinds } S \ (as@asx')) \ s \neq []$
by ($\text{simp add:slice-kinds-def transfers-split}$)
moreover
from $\langle S, \text{slice-kind } S \vdash (m \# ms, s) = as \Rightarrow (msx', s') \rangle$

```

have transfers (map (slice-kind S) as) s = s'
  by simp(rule observable-moves-preds-transfers)
from ⟨S, slice-kind S ⊢ (m # ms, s) = as ⇒ (msx', s')⟩ ⟨ms = targetnodes rs⟩
  ⟨length rs = length cs⟩ ⟨∀ i < length rs. rs ! i ∈ get-return-edges (cs ! i)⟩
  ⟨valid-call-list cs m⟩ ⟨valid-return-list rs m⟩
have slice-edges S cs as = [last as]
  by(fastforce intro!: observable-moves-singular-slice-edge
    [OF - - stacks-rewrite])
from ⟨S, slice-kind S ⊢ (m # ms, s) = asx ⇒τ (xs, s'')⟩ callstack
have s = s'' by(fastforce intro: silent-moves-slice-keeps-state)
with ⟨S, slice-kind S ⊢ (xs, s'') - ax → (msx', s')⟩
have transfer (slice-kind S ax) s = s' by(fastforce elim: observable-move.cases)
with ⟨slice-edges S cs as = [last as]⟩ ⟨as = asx@[ax]⟩
have s' = transfers (slice-kinds S (slice-edges S cs as)) s
  by(simp add: slice-kinds-def)
from ⟨upd-cs cs as = cs''⟩
have slice-edges S cs (as @ asx') =
  (slice-edges S cs as)@(slice-edges S cs'' asx')
  by(fastforce intro: slice-edges-Append)
hence trans-eq': transfers (slice-kinds S (slice-edges S cs (as @ asx'))) s =
  transfers (slice-kinds S (slice-edges S cs'' asx'))
  (transfers (slice-kinds S (slice-edges S cs as)) s)
  by(simp add: slice-kinds-def transfers-split)
from ⟨s' = transfers (slice-kinds S (slice-edges S cs as)) s⟩
  ⟨transfers (map (slice-kind S) as) s = s'⟩
have transfers (map (slice-kind S) (slice-edges S cs as)) s =
  transfers (map (slice-kind S) as) s
  by(simp add: slice-kinds-def)
with trans-eq trans-eq'
  ⟨s' = transfers (slice-kinds S (slice-edges S cs as)) s⟩
have transfers (slice-kinds S (slice-edges S cs (as @ asx'))) s =
  transfers (slice-kinds S (as @ asx')) s
  by(simp add: slice-kinds-def transfers-split)
ultimately show ?case
  using ⟨valid-node m'⟩ ⟨valid-call-list cs' m'⟩
  ⟨∀ i < length rs'. rs' ! i ∈ get-return-edges (cs' ! i)⟩
  ⟨valid-return-list rs' m'⟩ ⟨length rs' = length cs'⟩ ⟨ms' = targetnodes rs'⟩
  apply(rule-tac x=as@asx' in exI)
  apply(rule-tac x=cs' in exI)
  apply(rule-tac x=rs' in exI)
  by clarsimp
qed
qed

```

1.14.4 The fundamental property of static interprocedural slicing

theorem *fundamental-property-of-static-slicing*:

assumes $m -as \rightarrow_{\sqrt{*}} m'$ **and** $\text{preds}(\text{kinds } as)$ [cf] **and** CFG-node $m' \in S$

obtains as' **where** $\text{preds (slice-kinds } S \text{ } as') [cf]$
and $\forall V \in \text{Use } m'. \text{state-val (transfers (slice-kinds } S \text{ } as') [cf]) } V =$
 $\text{state-val (transfers (kinds } as) [cf]) } V$
and $\text{slice-edges } S \ [] \text{ } as = \text{slice-edges } S \ [] \text{ } as'$
and $\text{transfers (kinds } as) [cf] \neq []$ **and** $m - as' \rightarrow_{\sqrt{*}} m'$
proof(*atomize-elim*)
from $\langle m - as \rightarrow_{\sqrt{*}} m' \rangle \langle \text{preds (kinds } as) [cf] \rangle$ **obtain** $ms'' \ s'' \ ms' \ as' \ as''$
where $S, kind \vdash ([m], [cf]) = \text{slice-edges } S \ [] \text{ } as \Rightarrow *$
 (ms'', s'')
and $S, kind \vdash (ms'', s'') = as' \Rightarrow_{\tau} (m' \# ms', \text{transfers (kinds } as) [cf])$
and $\text{slice-edges } S \ [] \text{ } as = \text{slice-edges } S \ [] \text{ } as''$
and $m - as'' @ as' \rightarrow_{\sqrt{*}} m'$
by(*auto elim:valid-path-trans-observable-moves[of - - - - S]*)
from $\langle m - as \rightarrow_{\sqrt{*}} m' \rangle$ **have** *valid-node* m **and** *valid-node* m'
by(*auto intro:path-valid-node simp:vp-def*)
with $\langle \text{CFG-node } m' \in S \rangle$ **have** $\text{CFG-node } m' \in \text{HRB-slice } S$
by $-(\text{rule HRB-slice-refl})$
from $\langle \text{valid-node } m \rangle \langle \text{CFG-node } m' \in S \rangle$ **have** $(([m], [cf]), ([m], [cf])) \in \text{WS } S$
by(*fastforce intro:WSI*)
{ fix V **assume** $V \in \text{Use } m'$
with $\langle \text{valid-node } m' \rangle$ **have** $V \in \text{Use}_{SDG} (\text{CFG-node } m')$
by(*fastforce intro:CFG-Use-SDG-Use*)
moreover
from $\langle \text{valid-node } m' \rangle$
have $\text{parent-node } (\text{CFG-node } m') - [] \rightarrow_{\iota^*} \text{parent-node } (\text{CFG-node } m')$
by(*fastforce intro:empty-path simp:intra-path-def*)
ultimately have $V \in \text{rv } S (\text{CFG-node } m')$
using $\langle \text{CFG-node } m' \in \text{HRB-slice } S \rangle \langle \text{CFG-node } m' \in S \rangle$
by(*fastforce intro:rvI simp:sourcenodes-def*) **}**
hence $\forall V \in \text{Use } m'. V \in \text{rv } S (\text{CFG-node } m')$ **by** *simp*
show $\exists as'. \text{preds (slice-kinds } S \text{ } as') [cf] \wedge$
 $(\forall V \in \text{Use } m'. \text{state-val (transfers (slice-kinds } S \text{ } as') [cf]) } V =$
 $\text{state-val (transfers (kinds } as) [cf]) } V) \wedge$
 $\text{slice-edges } S \ [] \text{ } as = \text{slice-edges } S \ [] \text{ } as' \wedge$
 $\text{transfers (kinds } as) [cf] \neq [] \wedge m - as' \rightarrow_{\sqrt{*}} m'$
proof(*cases slice-edges* $S \ [] \text{ } as = []$)
case *True*
hence $\text{preds (slice-kinds } S \ []) [cf]$
and $\text{slice-edges } S \ [] \ [] = \text{slice-edges } S \ [] \text{ } as$
by(*simp-all add:slice-kinds-def*)
with $\langle S, kind \vdash ([m], [cf]) = \text{slice-edges } S \ [] \text{ } as \Rightarrow * (ms'', s'') \rangle$
have $[simp]: ms'' = [m] \ s'' = [cf]$ **by**(*auto elim:trans-observable-moves.cases*)
with $\langle S, kind \vdash (ms'', s'') = as' \Rightarrow_{\tau} (m' \# ms', \text{transfers (kinds } as) [cf]) \rangle$
have $S, kind \vdash ([m], [cf]) = as' \Rightarrow_{\tau} (m' \# ms', \text{transfers (kinds } as) [cf])$
by *simp*
with $\langle \text{valid-node } m \rangle$ **have** $m - as' \rightarrow_{\sqrt{*}} m'$ **and** *valid-path-aux* $[] \text{ } as'$
by(*auto intro:silent-moves-vpa-path[of - - - - - - - - - - []]*)
 $\text{simp:targetnodes-def valid-return-list-def}$
hence $m - as' \rightarrow_{\sqrt{*}} m'$ **by**(*simp add:vp-def valid-path-def*)

from $\langle S, kind \vdash ([m], [cf]) = as' \Rightarrow_{\tau} (m' \# ms', transfers (kinds as) [cf]) \rangle$
have $slice\text{-}edges\ S \ []\ as' = []$
by (*fastforce dest:silent-moves-no-slice-edges* [**where** $cs=[]$ **and** $rs=[]$]
simp:targetnodes-def)
from $\langle S, kind \vdash ([m], [cf]) = as' \Rightarrow_{\tau} (m' \# ms', transfers (kinds as) [cf]) \rangle$
 $\langle valid\text{-}node\ m \rangle \langle valid\text{-}node\ m' \rangle \langle CFG\text{-}node\ m' \in S \rangle$
have $returns: \forall mx \in set\ ms'$
 $\exists mx'. call\text{-}of\text{-}return\text{-}node\ mx\ mx' \wedge mx' \in [HRB\text{-}slice\ S]_{CFG}$
by $-(erule\ silent\text{-}moves\text{-}called\text{-}node\text{-}in\text{-}slice1\text{-}nodestack\text{-}in\text{-}slice1$
 $[of\ \text{-----}\ []\ []],$
auto intro:refl-slice1 simp:targetnodes-def valid-return-list-def)
from $\langle S, kind \vdash ([m], [cf]) = as' \Rightarrow_{\tau} (m' \# ms', transfers (kinds as) [cf]) \rangle$
 $\langle ([m], [cf]), ([m], [cf]) \rangle \in WS\ S$
have $WS: ((m' \# ms', transfers (kinds as) [cf]), ([m], [cf])) \in WS\ S$
by (*rule WS-silent-moves*)
hence $transfers (kinds as) [cf] \neq []$ **by** (*auto elim!: WS.cases*)
with $WS\ returns\ \langle transfers (kinds as) [cf] \neq [] \rangle$
have $\forall V \in rv\ S\ (CFG\text{-}node\ m')$
 $state\text{-}val\ (transfers (kinds as) [cf])\ V = fst\ cf\ V$
apply $-$ **apply** (*erule WS.cases*) **apply** *clarsimp*
by (*case-tac msx*) (*auto simp:hd-conv-nth*)
with $\langle \forall V \in Use\ m'. V \in rv\ S\ (CFG\text{-}node\ m') \rangle$
have $Uses: \forall V \in Use\ m'. state\text{-}val\ (transfers (kinds as) [cf])\ V = fst\ cf\ V$
by *simp*
have $[simp]: ms' = []$
proof (*rule ccontr*)
assume $ms' \neq []$
with $\langle S, kind \vdash ([m], [cf]) = as' \Rightarrow_{\tau} (m' \# ms', transfers (kinds as) [cf]) \rangle$
 $\langle valid\text{-}node\ m \rangle \langle valid\text{-}node\ m' \rangle \langle CFG\text{-}node\ m' \in S \rangle$
show *False*
by (*fastforce elim:silent-moves-nonempty-nodestack-False intro:refl-slice1*)
qed
with $\langle S, kind \vdash ([m], [cf]) = as' \Rightarrow_{\tau} (m' \# ms', transfers (kinds as) [cf]) \rangle$
have $S, kind \vdash ([m], [cf]) = as' \Rightarrow_{\tau} ([m'], transfers (kinds as) [cf])$
by *simp*
with $\langle valid\text{-}node\ m \rangle$ **have** $m - as' \rightarrow_{sl}^* m'$ **by** (*fastforce dest:silent-moves-slp*)
from *this* $\langle slice\text{-}edges\ S \ []\ as' = [] \rangle$
obtain asx **where** $m - asx \rightarrow_{\iota}^* m'$ **and** $slice\text{-}edges\ S \ []\ asx = []$
by (*erule slp-to-intra-path-with-slice-edges*)
with $\langle CFG\text{-}node\ m' \in HRB\text{-}slice\ S \rangle$
obtain asx' **where** $m - asx' \rightarrow_{\iota}^* m'$
and $preds (slice\text{-}kinds\ S\ asx') [cf]$
and $slice\text{-}edges\ S \ []\ asx' = []$
by $-(erule\ exists\text{-}sliced\text{-}intra\text{-}path\text{-}preds, auto\ simp:SDG\text{-}to\text{-}CFG\text{-}set\text{-}def)$
from $\langle m - asx' \rightarrow_{\iota}^* m' \rangle$ **have** $m - asx' \rightarrow_{\surd}^* m'$ **by** (*rule intra-path-vp*)
from $Uses\ \langle slice\text{-}edges\ S \ []\ asx' = [] \rangle$
have $hd (transfers (slice\text{-}kinds\ S$
 $(slice\text{-}edges\ S \ []\ asx')) [cf]) = cf$ **by** (*simp add:slice-kinds-def*)
from $\langle m - asx' \rightarrow_{\iota}^* m' \rangle \langle CFG\text{-}node\ m' \in S \rangle$

```

have transfers (slice-kinds S (slice-edges S [] asx')) [cf] =
  transfers (slice-kinds S asx') [cf]
  by(fastforce intro:transfers-intra-slice-kinds-slice-edges simp:intra-path-def)
with ⟨hd (transfers (slice-kinds S (slice-edges S [] asx')) [cf]) = cf⟩
have hd (transfers (slice-kinds S asx') [cf]) = cf by simp
with Uses have  $\forall V \in \text{Use } m'. \text{state-val } (\text{transfers } (\text{slice-kinds } S \text{ asx}') [cf]) V$ 
=
  state-val (transfers (kinds as) [cf]) V by simp
with ⟨m - asx'  $\rightarrow_{\sqrt{*}}$  m' ⟨preds (slice-kinds S asx') [cf])
  ⟨slice-edges S [] asx' = []⟩ ⟨transfers (kinds as) [cf]  $\neq$  []⟩ True
show ?thesis by fastforce
next
case False
with ⟨(([m],[cf]),([m],[cf]))  $\in$  WS S⟩
  ⟨S,kind  $\vdash$  ([m],[cf]) = slice-edges S [] as  $\Rightarrow^*$  (ms'',s'')⟩
have WS:((ms'',s''),(ms'',transfers (slice-kinds S (slice-edges S [] as)) [cf]))
   $\in$  WS S
  and tom:S,slice-kind S  $\vdash$  ([m],[cf]) = slice-edges S [] as  $\Rightarrow^*$ 
  (ms'',transfers (slice-kinds S (slice-edges S [] as)) [cf])
  by(fastforce dest:WS-weak-sim-trans)+
from WS obtain mx msx where [simp]:msx = mx#msx and valid-node mx
  by -(erule WS.cases,cases msx,auto)
from ⟨S,kind  $\vdash$  (ms'',s'') = as  $\Rightarrow_{\tau}$  (m'#ms',transfers (kinds as) [cf])⟩ WS
have WS':((m'#ms',transfers (kinds as) [cf]),
  (mx#msx,transfers (slice-kinds S (slice-edges S [] as)) [cf]))  $\in$  WS S
  by simp(rule WS-silent-moves)
from tom ⟨valid-node m⟩
obtain asx csx rsx where preds (slice-kinds S asx) [cf]
  and slice-edges S [] asx = slice-edges S [] as
  and m - asx  $\rightarrow_{\sqrt{*}}$  mx and transfers (slice-kinds S asx) [cf]  $\neq$  []
  and upd-cs [] asx = csx and stack:valid-node mx valid-call-list csx mx
   $\forall i < \text{length } rsx. rsx!i \in \text{get-return-edges } (csx!i)$ 
  valid-return-list rsx mx length rsx = length csx
  msx = targetnodes rsx
  and trans-eq:transfers (slice-kinds S
  (slice-edges S [] asx)) [cf] =
  transfers (slice-kinds S asx) [cf]
  by(auto elim:slice-tom-preds-vp[of - - - - - [] []]
  simp:valid-call-list-def valid-return-list-def targetnodes-def
  vp-def valid-path-def)
from ⟨transfers (slice-kinds S asx) [cf]  $\neq$  []⟩
obtain cf' cfs' where eq:transfers (slice-kinds S asx) [cf] =
  cf'#cfs' by(cases transfers (slice-kinds S asx) [cf]) auto
from WS' have callstack: $\forall mx \in \text{set } msx. \exists mx'. \text{call-of-return-node } mx mx' \wedge$ 
  mx'  $\in$  [HRB-slice S] CFG
  by(fastforce elim:WS.cases)
with ⟨S,kind  $\vdash$  (ms'',s'') = as  $\Rightarrow_{\tau}$  (m'#ms',transfers (kinds as) [cf])
  ⟨valid-node m'⟩ stack ⟨CFG-node m' ∈ S⟩

```

have $callstack' : \forall mx \in set\ ms'. \exists mx'. call\ of\ return\ node\ mx\ mx' \wedge$
 $mx' \in [HRB\ slice\ S]_{CFG}$
by $simp(erule\ silent\ moves\ called\ node\ in\ slice1\ no\ dest\ stack\ in\ slice1$
 $[of\ \dots\ rsx\ csx], auto\ intro: refl\ slice1)$
with $\langle S, kind \vdash (ms'', s'') = as' \Rightarrow_{\tau} (m' \# ms', transfers\ (kinds\ as)\ [cf]) \rangle$
 $stack\ callstack$
have $mx - as' \rightarrow_{sl^*} m'$ **and** $msx = ms'$ **by** $(auto\ dest!: silent\ moves\ slp)$
from $\langle S, kind \vdash (ms'', s'') = as' \Rightarrow_{\tau} (m' \# ms', transfers\ (kinds\ as)\ [cf]) \rangle$
 $stack$
have $slice\ edges\ S\ csx\ as' = []$
by $(auto\ dest!: silent\ moves\ no\ slice\ edges\ [OF\ \dots\ stacks\ rewrite])$
with $\langle mx - as' \rightarrow_{sl^*} m' \rangle$ **obtain** asx'' **where** $mx - asx'' \rightarrow_{l^*} m'$
and $slice\ edges\ S\ csx\ asx'' = []$
by $(erule\ slp\ to\ intra\ path\ with\ slice\ edges)$
from $stack$ **have** $\forall i < length\ csx. call\ of\ return\ node\ (msx!i)\ (sourcenode\ (csx!i))$
by $-(rule\ stacks\ rewrite)$
with $callstack\ \langle msx = targetnodes\ rsx \rangle\ \langle length\ rsx = length\ csx \rangle$
have $\forall c \in set\ csx. sourcenode\ c \in [HRB\ slice\ S]_{CFG}$
by $(auto\ simp: all\ set\ conv\ all\ nth\ targetnodes\ def)$
with $\langle mx - asx'' \rightarrow_{l^*} m' \rangle\ \langle slice\ edges\ S\ csx\ asx'' = [] \rangle\ \langle valid\ node\ m' \rangle$
 $eq\ \langle CFG\ node\ m' \in S \rangle$
obtain asx' **where** $mx - asx' \rightarrow_{l^*} m'$
and $preds\ (slice\ kinds\ S\ asx')\ (cf' \# cfs')$
and $slice\ edges\ S\ csx\ asx' = []$
by $-(erule\ exists\ sliced\ intra\ path\ preds,$
 $auto\ intro: HRB\ slice\ refl\ simp: SDG\ to\ CFG\ set\ def)$
with eq **have** $preds\ (slice\ kinds\ S\ asx')$
 $(transfers\ (slice\ kinds\ S\ asx)\ [cf])$ **by** $simp$
with $\langle preds\ (slice\ kinds\ S\ asx)\ [cf] \rangle$
have $preds\ (slice\ kinds\ S\ (asx @ asx'))\ [cf]$
by $(simp\ add: slice\ kinds\ def\ preds\ split)$
from $\langle m - asx \rightarrow_{\sqrt{*}} mx \rangle\ \langle mx - asx' \rightarrow_{l^*} m' \rangle$ **have** $m - asx @ asx' \rightarrow_{\sqrt{*}} m'$
by $(fastforce\ elim: vp\ slp\ Append\ intra\ path\ slp)$
from $\langle upd\ cs\ []\ asx = csx \rangle\ \langle slice\ edges\ S\ csx\ asx' = [] \rangle$
have $slice\ edges\ S\ []\ (asx @ asx') =$
 $(slice\ edges\ S\ []\ asx) @ []$
by $(fastforce\ intro: slice\ edges\ Append)$
from $\langle mx - asx' \rightarrow_{l^*} m' \rangle\ \langle \forall c \in set\ csx. sourcenode\ c \in [HRB\ slice\ S]_{CFG} \rangle$
have $trans\ eq': transfers\ (slice\ kinds\ S\ (slice\ edges\ S\ csx\ asx'))$
 $(transfers\ (slice\ kinds\ S\ asx)\ [cf]) =$
 $transfers\ (slice\ kinds\ S\ asx')\ (transfers\ (slice\ kinds\ S\ asx)\ [cf])$
by $(fastforce\ intro: transfers\ intra\ slice\ kinds\ slice\ edges\ simp: intra\ path\ def)$
from $\langle upd\ cs\ []\ asx = csx \rangle$
have $slice\ edges\ S\ []\ (asx @ asx') =$
 $(slice\ edges\ S\ []\ asx) @ (slice\ edges\ S\ csx\ asx')$
by $(fastforce\ intro: slice\ edges\ Append)$
hence $transfers\ (slice\ kinds\ S\ (slice\ edges\ S\ []\ (asx @ asx')))\ [cf] =$
 $transfers\ (slice\ kinds\ S\ (slice\ edges\ S\ csx\ asx'))$
 $(transfers\ (slice\ kinds\ S\ (slice\ edges\ S\ []\ asx))\ [cf])$

```

    by(simp add:slice-kinds-def transfers-split)
  with trans-eq have transfers (slice-kinds S (slice-edges S [] (asx@asx'))) [cf]
=
  transfers (slice-kinds S (slice-edges S csx asx'))
  (transfers (slice-kinds S asx) [cf]) by simp
with trans-eq' have trans-eq'':
  transfers (slice-kinds S (slice-edges S [] (asx@asx'))) [cf] =
  transfers (slice-kinds S (asx@asx')) [cf]
  by(simp add:slice-kinds-def transfers-split)
from WS' obtain x xs where m'#ms' = xs@x#msx
and xs ≠ [] → (∃ mx'. call-of-return-node x mx' ∧
mx' ∉ [HRB-slice S]CFG)
and rest:∀ i < length (mx#msx). ∀ V ∈ rv S (CFG-node ((x#msx)!i)).
(fst ((transfers (kinds as) [cf])!(length xs + i))) V =
(fst ((transfers (slice-kinds S
(slice-edges S [] as)) [cf])!i)) V
transfers (kinds as) [cf] ≠ []
transfers (slice-kinds S
(slice-edges S [] as)) [cf] ≠ []
by(fastforce elim:WS.cases)
from ⟨m'#ms' = xs@x#msx⟩ ⟨xs ≠ [] → (∃ mx'. call-of-return-node x mx' ∧
mx' ∉ [HRB-slice S]CFG)⟩ callstack'
have [simp]:xs = [] x = m' ms' = msx by(cases xs,auto)+
from rest have ∀ V ∈ rv S (CFG-node m').
state-val (transfers (kinds as) [cf]) V =
state-val (transfers (slice-kinds S (slice-edges S [] as)) [cf]) V
by(fastforce dest:hd-conv-nth)
with ⟨∀ V ∈ Use m'. V ∈ rv S (CFG-node m')⟩
⟨slice-edges S [] asx = slice-edges S [] as⟩
have ∀ V ∈ Use m'. state-val (transfers (kinds as) [cf]) V =
state-val (transfers (slice-kinds S (slice-edges S [] asx)) [cf]) V
by simp
with ⟨slice-edges S [] (asx@asx') = (slice-edges S [] asx)@[]⟩
have ∀ V ∈ Use m'. state-val (transfers (kinds as) [cf]) V =
state-val (transfers (slice-kinds S (slice-edges S [] (asx@asx'))) [cf]) V
by simp
with trans-eq'' have ∀ V ∈ Use m'. state-val (transfers (kinds as) [cf]) V =
state-val (transfers (slice-kinds S (asx@asx')) [cf]) V
by simp
with ⟨preds (slice-kinds S (asx@asx')) [cf]⟩
⟨m -asx@asx'→√* m'⟩ ⟨slice-edges S [] (asx@asx') =
(slice-edges S [] asx)@[]⟩ ⟨transfers (kinds as) [cf] ≠ []⟩
⟨slice-edges S [] asx = slice-edges S [] as⟩
show ?thesis by fastforce
qed
qed
end

```

1.14.5 The fundamental property of static interprocedural slicing related to the semantics

```

locale SemanticsProperty = SDG sourcenode targetnode kind valid-edge Entry
  get-proc get-return-edges procs Main Exit Def Use ParamDefs ParamUses +
  CFG-semantics-wf sourcenode targetnode kind valid-edge Entry
  get-proc get-return-edges procs Main sem identifies
for sourcenode :: 'edge  $\Rightarrow$  'node and targetnode :: 'edge  $\Rightarrow$  'node
and kind :: 'edge  $\Rightarrow$  ('var,'val,'ret,'pname) edge-kind
and valid-edge :: 'edge  $\Rightarrow$  bool
and Entry :: 'node (('Entry'-)) and get-proc :: 'node  $\Rightarrow$  'pname
and get-return-edges :: 'edge  $\Rightarrow$  'edge set
and procs :: ('pname  $\times$  'var list  $\times$  'var list) list and Main :: 'pname
and Exit::'node (('Exit'-))
and Def :: 'node  $\Rightarrow$  'var set and Use :: 'node  $\Rightarrow$  'var set
and ParamDefs :: 'node  $\Rightarrow$  'var list and ParamUses :: 'node  $\Rightarrow$  'var set list
and sem :: 'com  $\Rightarrow$  ('var  $\rightarrow$  'val) list  $\Rightarrow$  'com  $\Rightarrow$  ('var  $\rightarrow$  'val) list  $\Rightarrow$  bool
  (((1<-,-)  $\Rightarrow$  / (1<-,-)) [0,0,0,0] 81)
and identifies :: 'node  $\Rightarrow$  'com  $\Rightarrow$  bool (-  $\triangleq$  - [51,0] 80)
begin

```

theorem *fundamental-property-of-path-slicing-semantically:*

```

assumes  $m \triangleq c$  and  $\langle c, [cf] \rangle \Rightarrow \langle c', s' \rangle$ 
obtains  $m'$  as  $cfs'$  where  $m -as \rightarrow_{\sqrt{*}} m'$  and  $m' \triangleq c'$ 
and  $preds$  ( $slice\text{-kinds} \{CFG\text{-node } m'\}$   $as$ ) [( $cf, undefined$ )]
and  $\forall V \in Use\ m'$ .
   $state\text{-val} (transfers (slice\text{-kinds} \{CFG\text{-node } m'\}) as) [(cf, undefined)]) V =$ 
   $state\text{-val } cfs' V$  and  $map\ fst\ cfs' = s'$ 

```

proof(*atomize-elim*)

```

from  $\langle m \triangleq c \rangle \langle c, [cf] \rangle \Rightarrow \langle c', s' \rangle$  obtain  $m'$  as  $cfs'$  where  $m -as \rightarrow_{\sqrt{*}} m'$ 
and  $transfers (kinds as) [(cf, undefined)] = cfs'$ 
and  $preds (kinds as) [(cf, undefined)]$  and  $m' \triangleq c'$  and  $map\ fst\ cfs' = s'$ 
by(fastforce dest:fundamental-property)
from  $\langle m -as \rightarrow_{\sqrt{*}} m' \rangle \langle preds (kinds as) [(cf, undefined)] \rangle$  obtain  $as'$ 
where  $preds (slice\text{-kinds} \{CFG\text{-node } m'\}) as' [(cf, undefined)]$ 
and  $vals: \forall V \in Use\ m'. state\text{-val} (transfers (slice\text{-kinds} \{CFG\text{-node } m'\}) as') [(cf, undefined)]) V =$ 
 $state\text{-val} (transfers (kinds as) [(cf, undefined)]) V$ 
and  $m -as' \rightarrow_{\sqrt{*}} m'$ 
by -(erule fundamental-property-of-static-slicing, auto)
from  $\langle transfers (kinds as) [(cf, undefined)] = cfs' \rangle vals$  have  $\forall V \in Use\ m'$ .
   $state\text{-val} (transfers (slice\text{-kinds} \{CFG\text{-node } m'\}) as') [(cf, undefined)]) V =$ 
   $state\text{-val } cfs' V$  by simp
with  $\langle preds (slice\text{-kinds} \{CFG\text{-node } m'\}) as' [(cf, undefined)] \rangle \langle m -as' \rightarrow_{\sqrt{*}} m' \rangle$ 
   $\langle m' \triangleq c' \rangle \langle map\ fst\ cfs' = s' \rangle$ 
show  $\exists as\ m' cfs'. m -as \rightarrow_{\sqrt{*}} m' \wedge m' \triangleq c' \wedge$ 
   $preds (slice\text{-kinds} \{CFG\text{-node } m'\}) as [(cf, undefined)] \wedge$ 
   $(\forall V \in Use\ m'. state\text{-val} (transfers (slice\text{-kinds} \{CFG\text{-node } m'\}) as)$ 
   $[(cf, undefined)]) V = state\text{-val } cfs' V) \wedge map\ fst\ cfs' = s'$ 

```

by *blast*
qed
end
end

Chapter 2

Instantiating the Framework with a simple While-Language using procedures

2.1 Commands

```
theory Com imports ../StaticInter/BasicDefs begin
```

2.1.1 Variables and Values

```
type-synonym vname = string — names for variables
```

```
type-synonym pname = string — names for procedures
```

```
datatype val  
  = Bool bool      — Boolean value  
  | Intg int       — integer value
```

```
abbreviation true == Bool True
```

```
abbreviation false == Bool False
```

2.1.2 Expressions

```
datatype bop = Eq | And | Less | Add | Sub — names of binary operations
```

```
datatype expr  
  = Val val — value  
  | Var vname — local variable  
  | BinOp expr bop expr (- «->» - [80,0,81] 80) — binary operation
```

```
fun binop :: bop ⇒ val ⇒ val ⇒ val option
```



```

where binop Eq v1 v2 = Some(Bool(v1 = v2))
| binop And (Bool b1) (Bool b2) = Some(Bool(b1 ∧ b2))
| binop Less (Intg i1) (Intg i2) = Some(Bool(i1 < i2))
| binop Add (Intg i1) (Intg i2) = Some(Intg(i1 + i2))
| binop Sub (Intg i1) (Intg i2) = Some(Intg(i1 - i2))
| binop bop v1 v2 = None

```

2.1.3 Commands

```

datatype cmd
= Skip
| LAss vname expr (· := - [70,70] 70) — local assignment
| Seq cmd cmd (· ;; / - [60,61] 60)
| Cond expr cmd cmd (if '(-) - / else - [80,79,79] 70)
| While expr cmd (while '(-) - [80,79] 70)
| Call pname expr list vname list
— Call needs procedure, actual parameters and variables for return values

```

```

fun num-inner-nodes :: cmd ⇒ nat (#:-)
where #:Skip = 1
| #:(V:=e) = 2
| #:(c1;;c2) = #:c1 + #:c2
| #:(if (b) c1 else c2) = #:c1 + #:c2 + 1
| #:(while (b) c) = #:c + 2
| #:(Call p es rets) = 2

```

```

lemma num-inner-nodes-gr-0 [simp]:#:c > 0
by(induct c) auto

```

```

lemma [dest]:#:c = 0 ⇒ False
by(induct c) auto

```

end

2.2 The state

```

theory ProcState imports Com begin

```

```

fun interpret :: expr ⇒ (vname ↦ val) ⇒ val option
where Val: interpret (Val v) cf = Some v
| Var: interpret (Var V) cf = cf V
| BinOp: interpret (e1◀bop▶e2) cf =
(case interpret e1 cf of None ⇒ None
| Some v1 ⇒ (case interpret e2 cf of None ⇒ None

```

| *Some* $v_2 \Rightarrow$ (
case binop bop $v_1 v_2$ of *None* \Rightarrow *None* | *Some* $v \Rightarrow$ *Some* v)))

abbreviation *update* :: (*vname* \rightarrow *val*) \Rightarrow *vname* \Rightarrow *expr* \Rightarrow (*vname* \rightarrow *val*)
where *update cf* $V e \equiv cf(V := (interpret e cf))$

abbreviation *state-check* :: (*vname* \rightarrow *val*) \Rightarrow *expr* \Rightarrow *val option* \Rightarrow *bool*
where *state-check cf* $b v \equiv (interpret b cf = v)$

end

2.3 Definition of the CFG

theory *PCFG* **imports** *ProcState* **begin**

definition *Main* :: *pname*
where *Main* = "Main"

datatype *label* = *Label* *nat* | *Entry* | *Exit*

2.3.1 The CFG for every procedure

Definition of \oplus

fun *label-incr* :: *label* \Rightarrow *nat* \Rightarrow *label* ($- \oplus -$ 60)
where (*Label* l) $\oplus i =$ *Label* ($l + i$)
| *Entry* $\oplus i =$ *Entry*
| *Exit* $\oplus i =$ *Exit*

lemma *Exit-label-incr* [*dest*]: *Exit* = $n \oplus i \Longrightarrow n =$ *Exit*
by(*cases n,auto*)

lemma *label-incr-Exit* [*dest*]: $n \oplus i =$ *Exit* $\Longrightarrow n =$ *Exit*
by(*cases n,auto*)

lemma *Entry-label-incr* [*dest*]: *Entry* = $n \oplus i \Longrightarrow n =$ *Entry*
by(*cases n,auto*)

lemma *label-incr-Entry* [*dest*]: $n \oplus i =$ *Entry* $\Longrightarrow n =$ *Entry*
by(*cases n,auto*)

lemma *label-incr-inj*:
 $n \oplus c = n' \oplus c \Longrightarrow n = n'$
by(*cases n*)(*cases n',auto*)+

lemma *label-incr-simp*: $n \oplus i = m \oplus (i + j) \Longrightarrow n = m \oplus j$
by(*cases n,auto,cases m,auto*)

lemma *label-incr-simp-rev*: $m \oplus (j + i) = n \oplus i \implies m \oplus j = n$
by(*cases n, auto, cases m, auto*)

lemma *label-incr-start-Node-smaller*:
 $Label\ l = n \oplus i \implies n = Label\ (l - i)$
by(*cases n, auto*)

lemma *label-incr-start-Node-smaller-rev*:
 $n \oplus i = Label\ l \implies n = Label\ (l - i)$
by(*cases n, auto*)

lemma *label-incr-ge*: $Label\ l = n \oplus i \implies l \geq i$
by(*cases n*) *auto*

lemma *label-incr-0* [*dest*]:
 $\llbracket Label\ 0 = n \oplus i; i > 0 \rrbracket \implies False$
by(*cases n*) *auto*

lemma *label-incr-0-rev* [*dest*]:
 $\llbracket n \oplus i = Label\ 0; i > 0 \rrbracket \implies False$
by(*cases n*) *auto*

The edges of the procedure CFG

Control flow information in this language is the node, to which we return after the calles procedure is finished.

datatype *p-edge-kind* =
 $IEdge\ (vname, val, pname \times label, pname)\ edge\text{-}kind$
 $| CEdge\ pname \times expr\ list \times vname\ list$

type-synonym *p-edge* = ($label \times p\text{-}edge\text{-}kind \times label$)

inductive *Proc-CFG* :: $cmd \Rightarrow label \Rightarrow p\text{-}edge\text{-}kind \Rightarrow label \Rightarrow bool$
 $(- \vdash - \dashrightarrow_p -)$

where

Proc-CFG-Entry-Exit:
 $prog \vdash Entry - IEdge\ (\lambda s. False)_{\surd \rightarrow_p} Exit$

| *Proc-CFG-Entry*:
 $prog \vdash Entry - IEdge\ (\lambda s. True)_{\surd \rightarrow_p} Label\ 0$

| *Proc-CFG-Skip*:
 $Skip \vdash Label\ 0 - IEdge\ \uparrow id \rightarrow_p Exit$

| *Proc-CFG-LAss*:

- $V := e \vdash \text{Label } 0 \text{ -IEdge } \uparrow(\lambda cf. \text{ update cf } V e) \rightarrow_p \text{Label } 1$
- | *Proc-CFG-LAssSkip*:
 $V := e \vdash \text{Label } 1 \text{ -IEdge } \uparrow id \rightarrow_p \text{Exit}$
- | *Proc-CFG-SeqFirst*:
 $\llbracket c_1 \vdash n \text{ -et} \rightarrow_p n'; n' \neq \text{Exit} \rrbracket \implies c_1;; c_2 \vdash n \text{ -et} \rightarrow_p n'$
- | *Proc-CFG-SeqConnect*:
 $\llbracket c_1 \vdash n \text{ -et} \rightarrow_p \text{Exit}; n \neq \text{Entry} \rrbracket \implies c_1;; c_2 \vdash n \text{ -et} \rightarrow_p \text{Label } \#:c_1$
- | *Proc-CFG-SeqSecond*:
 $\llbracket c_2 \vdash n \text{ -et} \rightarrow_p n'; n \neq \text{Entry} \rrbracket \implies c_1;; c_2 \vdash n \oplus \#:c_1 \text{ -et} \rightarrow_p n' \oplus \#:c_1$
- | *Proc-CFG-CondTrue*:
 $\text{if } (b) \ c_1 \text{ else } c_2 \vdash \text{Label } 0$
 $\text{-IEdge } (\lambda cf. \text{ state-check cf } b \text{ (Some true)})_{\surd} \rightarrow_p \text{Label } 1$
- | *Proc-CFG-CondFalse*:
 $\text{if } (b) \ c_1 \text{ else } c_2 \vdash \text{Label } 0 \text{ -IEdge } (\lambda cf. \text{ state-check cf } b \text{ (Some false)})_{\surd} \rightarrow_p$
 $\text{Label } (\#:c_1 + 1)$
- | *Proc-CFG-CondThen*:
 $\llbracket c_1 \vdash n \text{ -et} \rightarrow_p n'; n \neq \text{Entry} \rrbracket \implies \text{if } (b) \ c_1 \text{ else } c_2 \vdash n \oplus 1 \text{ -et} \rightarrow_p n' \oplus 1$
- | *Proc-CFG-CondElse*:
 $\llbracket c_2 \vdash n \text{ -et} \rightarrow_p n'; n \neq \text{Entry} \rrbracket$
 $\implies \text{if } (b) \ c_1 \text{ else } c_2 \vdash n \oplus (\#:c_1 + 1) \text{ -et} \rightarrow_p n' \oplus (\#:c_1 + 1)$
- | *Proc-CFG-WhileTrue*:
 $\text{while } (b) \ c' \vdash \text{Label } 0 \text{ -IEdge } (\lambda cf. \text{ state-check cf } b \text{ (Some true)})_{\surd} \rightarrow_p \text{Label } 2$
- | *Proc-CFG-WhileFalse*:
 $\text{while } (b) \ c' \vdash \text{Label } 0 \text{ -IEdge } (\lambda cf. \text{ state-check cf } b \text{ (Some false)})_{\surd} \rightarrow_p \text{Label } 1$
- | *Proc-CFG-WhileFalseSkip*:
 $\text{while } (b) \ c' \vdash \text{Label } 1 \text{ -IEdge } \uparrow id \rightarrow_p \text{Exit}$
- | *Proc-CFG-WhileBody*:
 $\llbracket c' \vdash n \text{ -et} \rightarrow_p n'; n \neq \text{Entry}; n' \neq \text{Exit} \rrbracket$
 $\implies \text{while } (b) \ c' \vdash n \oplus 2 \text{ -et} \rightarrow_p n' \oplus 2$
- | *Proc-CFG-WhileBodyExit*:
 $\llbracket c' \vdash n \text{ -et} \rightarrow_p \text{Exit}; n \neq \text{Entry} \rrbracket \implies \text{while } (b) \ c' \vdash n \oplus 2 \text{ -et} \rightarrow_p \text{Label } 0$
- | *Proc-CFG-Call*:
 $\text{Call } p \text{ es } \text{rets} \vdash \text{Label } 0 \text{ -CEdge } (p, \text{es}, \text{rets}) \rightarrow_p \text{Label } 1$
- | *Proc-CFG-CallSkip*:

Call p es $rets \vdash Label\ l - IEdge \uparrow id \rightarrow_p Exit$

Some lemmas about the procedure CFG

lemma *Proc-CFG-Exit-no-sourcenode* [*dest*]:
 $prog \vdash Exit -et \rightarrow_p n' \implies False$
by(*induct prog n \equiv Exit et n' rule:Proc-CFG.induct,auto*)

lemma *Proc-CFG-Entry-no-targetnode* [*dest*]:
 $prog \vdash n -et \rightarrow_p Entry \implies False$
by(*induct prog n et n' \equiv Entry rule:Proc-CFG.induct,auto*)

lemma *Proc-CFG-IEdge-intra-kind*:
 $prog \vdash n -IEdge et \rightarrow_p n' \implies intra\ kind\ et$
by(*induct prog n x \equiv IEdge et n' rule:Proc-CFG.induct,auto simp:intra-kind-def*)

lemma [*dest*]: $prog \vdash n -IEdge (Q:r \leftrightarrow_p fs) \rightarrow_p n' \implies False$
by(*fastforce dest:Proc-CFG-IEdge-intra-kind simp:intra-kind-def*)

lemma [*dest*]: $prog \vdash n -IEdge (Q \leftrightarrow_p f) \rightarrow_p n' \implies False$
by(*fastforce dest:Proc-CFG-IEdge-intra-kind simp:intra-kind-def*)

lemma *Proc-CFG-sourcelabel-less-num-nodes*:
 $prog \vdash Label\ l -et \rightarrow_p n' \implies l < \#:prog$
proof(*induct prog Label l et n' arbitrary:l rule:Proc-CFG.induct*)
case (*Proc-CFG-SeqFirst c₁ et n' c₂ l*)
thus *?case by simp*
next
case (*Proc-CFG-SeqConnect c₁ et c₂ l*)
thus *?case by simp*
next
case (*Proc-CFG-SeqSecond c₂ n et n' c₁ l*)
note $n = \langle n \oplus \#:c_1 = Label\ l \rangle$
note $IH = \langle \bigwedge l. n = Label\ l \implies l < \#:c_2 \rangle$
from n **obtain** l' **where** $l':n = Label\ l'$ **by**(*cases n*) **auto**
from IH [*OF this*] **have** $l' < \#:c_2$.
with $n\ l'$ **show** *?case by simp*
next
case (*Proc-CFG-CondThen c₁ n et n' b c₂ l*)
note $n = \langle n \oplus 1 = Label\ l \rangle$
note $IH = \langle \bigwedge l. n = Label\ l \implies l < \#:c_1 \rangle$
from n **obtain** l' **where** $l':n = Label\ l'$ **by**(*cases n*) **auto**
from IH [*OF this*] **have** $l' < \#:c_1$.
with $n\ l'$ **show** *?case by simp*
next

case (*Proc-CFG-CondElse* c_2 n *et* n' b c_1 l)
note $n = \langle n \oplus (\# : c_1 + 1) = \text{Label } l \rangle$
note $IH = \langle \bigwedge l. n = \text{Label } l \implies l < \# : c_2 \rangle$
from n **obtain** l' **where** $l' : n = \text{Label } l'$ **by**(*cases* n) *auto*
from IH [*OF this*] **have** $l' < \# : c_2$.
with n l' **show** ?*case* **by** *simp*
next
case (*Proc-CFG-WhileBody* c' n *et* n' b l)
note $n = \langle n \oplus 2 = \text{Label } l \rangle$
note $IH = \langle \bigwedge l. n = \text{Label } l \implies l < \# : c' \rangle$
from n **obtain** l' **where** $l' : n = \text{Label } l'$ **by**(*cases* n) *auto*
from IH [*OF this*] **have** $l' < \# : c'$.
with n l' **show** ?*case* **by** *simp*
next
case (*Proc-CFG-WhileBodyExit* c' n *et* b l)
note $n = \langle n \oplus 2 = \text{Label } l \rangle$
note $IH = \langle \bigwedge l. n = \text{Label } l \implies l < \# : c' \rangle$
from n **obtain** l' **where** $l' : n = \text{Label } l'$ **by**(*cases* n) *auto*
from IH [*OF this*] **have** $l' < \# : c'$.
with n l' **show** ?*case* **by** *simp*
qed (*auto simp:num-inner-nodes-gr-0*)

lemma *Proc-CFG-targetlabel-less-num-nodes*:
 $\text{prog} \vdash n \text{ --et--}_p \text{Label } l \implies l < \# : \text{prog}$
proof(*induct prog n et Label l arbitrary:l rule:Proc-CFG.induct*)
case (*Proc-CFG-SeqFirst* c_1 n *et* c_2 l)
thus ?*case* **by** *simp*
next
case (*Proc-CFG-SeqSecond* c_2 n *et* n' c_1 l)
note $n' = \langle n' \oplus \# : c_1 = \text{Label } l \rangle$
note $IH = \langle \bigwedge l. n' = \text{Label } l \implies l < \# : c_2 \rangle$
from n' **obtain** l' **where** $l' : n' = \text{Label } l'$ **by**(*cases* n') *auto*
from IH [*OF this*] **have** $l' < \# : c_2$.
with n' l' **show** ?*case* **by** *simp*
next
case (*Proc-CFG-CondThen* c_1 n *et* n' b c_2 l)
note $n' = \langle n' \oplus 1 = \text{Label } l \rangle$
note $IH = \langle \bigwedge l. n' = \text{Label } l \implies l < \# : c_1 \rangle$
from n' **obtain** l' **where** $l' : n' = \text{Label } l'$ **by**(*cases* n') *auto*
from IH [*OF this*] **have** $l' < \# : c_1$.
with n' l' **show** ?*case* **by** *simp*
next
case (*Proc-CFG-CondElse* c_2 n *et* n' b c_1 l)
note $n' = \langle n' \oplus (\# : c_1 + 1) = \text{Label } l \rangle$
note $IH = \langle \bigwedge l. n' = \text{Label } l \implies l < \# : c_2 \rangle$
from n' **obtain** l' **where** $l' : n' = \text{Label } l'$ **by**(*cases* n') *auto*
from IH [*OF this*] **have** $l' < \# : c_2$.
with n' l' **show** ?*case* **by** *simp*

next
 case (*Proc-CFG-WhileBody* $c' n et n' b l$)
 note $n' = \langle n' \oplus 2 = \text{Label } b \rangle$
 note $IH = \langle \wedge l. n' = \text{Label } l \implies l < \# : c' \rangle$
 from n' obtain l' where $l':n' = \text{Label } l'$ by (*cases* n') *auto*
 from IH [OF *this*] have $l' < \# : c'$.
 with $n' l'$ show ?*case* by *simp*
qed (*auto simp:num-inner-nodes-gr-0*)

lemma *Proc-CFG-EntryD*:
 $prog \vdash \text{Entry } -et \rightarrow_p n'$
 $\implies (n' = \text{Exit} \wedge et = \text{IEdge}(\lambda s. \text{False})_{\surd}) \vee (n' = \text{Label } 0 \wedge et = \text{IEdge}(\lambda s. \text{True})_{\surd})$
by (*induct prog n \equiv Entry et n' rule:Proc-CFG.induct,auto*)

lemma *Proc-CFG-Exit-edge*:
 obtains $l et$ where $prog \vdash \text{Label } l -\text{IEdge } et \rightarrow_p \text{Exit}$ and $l \leq \# : prog$
proof (*atomize-elim*)
 show $\exists l et. prog \vdash \text{Label } l -\text{IEdge } et \rightarrow_p \text{Exit} \wedge l \leq \# : prog$
proof (*induct prog*)
 case *Skip*
 have $\text{Skip} \vdash \text{Label } 0 -\text{IEdge } \uparrow id \rightarrow_p \text{Exit}$ by (*rule Proc-CFG-Skip*)
 thus ?*case* by *fastforce*
next
 case (*LAss* $V e$)
 have $V := e \vdash \text{Label } 1 -\text{IEdge } \uparrow id \rightarrow_p \text{Exit}$ by (*rule Proc-CFG-LAssSkip*)
 thus ?*case* by *fastforce*
next
 case (*Seq* $c_1 c_2$)
 from $\langle \exists l et. c_2 \vdash \text{Label } l -\text{IEdge } et \rightarrow_p \text{Exit} \wedge l \leq \# : c_2 \rangle$
 obtain $l et$ where $c_2 \vdash \text{Label } l -\text{IEdge } et \rightarrow_p \text{Exit}$ and $l \leq \# : c_2$ by *blast*
 hence $c_1 ;; c_2 \vdash \text{Label } l \oplus \# : c_1 -\text{IEdge } et \rightarrow_p \text{Exit} \oplus \# : c_1$
 by (*fastforce intro:Proc-CFG-SeqSecond*)
 with $\langle l \leq \# : c_2 \rangle$ show ?*case* by *fastforce*
next
 case (*Cond* $b c_1 c_2$)
 from $\langle \exists l et. c_1 \vdash \text{Label } l -\text{IEdge } et \rightarrow_p \text{Exit} \wedge l \leq \# : c_1 \rangle$
 obtain $l et$ where $c_1 \vdash \text{Label } l -\text{IEdge } et \rightarrow_p \text{Exit}$ and $l \leq \# : c_1$ by *blast*
 hence if (b) c_1 else $c_2 \vdash \text{Label } l \oplus 1 -\text{IEdge } et \rightarrow_p \text{Exit} \oplus 1$
 by (*fastforce intro:Proc-CFG-CondThen*)
 with $\langle l \leq \# : c_1 \rangle$ show ?*case* by *fastforce*
next
 case (*While* $b c'$)
 have while (b) $c' \vdash \text{Label } 1 -\text{IEdge } \uparrow id \rightarrow_p \text{Exit}$ by (*rule Proc-CFG-WhileFalseSkip*)
 thus ?*case* by *fastforce*
next
 case (*Call* $p es rets$)

have $Call\ p\ es\ rets \vdash Label\ 1 - IEdge \uparrow id \rightarrow_p Exit$ **by** $(rule\ Proc-CFG-CallSkip)$
thus $?case$ **by** $fastforce$
qed
qed

Lots of lemmas for call edges ...

lemma *Proc-CFG-Call-Labels*:

$prog \vdash n - CEdge\ (p, es, rets) \rightarrow_p n' \implies \exists l. n = Label\ l \wedge n' = Label\ (Suc\ l)$
by $(induct\ prog\ n\ et \equiv CEdge\ (p, es, rets)\ n' rule: Proc-CFG.induct, auto)$

lemma *Proc-CFG-Call-target-0*:

$prog \vdash n - CEdge\ (p, es, rets) \rightarrow_p Label\ 0 \implies n = Entry$
by $(induct\ prog\ n\ et \equiv CEdge\ (p, es, rets)\ n' \equiv Label\ 0 rule: Proc-CFG.induct)$
 $(auto\ dest: Proc-CFG-Call-Labels)$

lemma *Proc-CFG-Call-Intra-edge-not-same-source*:

$\llbracket prog \vdash n - CEdge\ (p, es, rets) \rightarrow_p n'; prog \vdash n - IEdge\ et \rightarrow_p n'' \rrbracket \implies False$
proof $(induct\ prog\ n\ CEdge\ (p, es, rets)\ n' arbitrary: n'' rule: Proc-CFG.induct)$

case $(Proc-CFG-SeqFirst\ c_1\ n\ n'\ c_2)$

note $IH = \langle \bigwedge n''. c_1 \vdash n - IEdge\ et \rightarrow_p n'' \implies False \rangle$

from $\langle c_1; c_2 \vdash n - IEdge\ et \rightarrow_p n'' \rangle \langle c_1 \vdash n - CEdge\ (p, es, rets) \rightarrow_p n' \rangle$
 $\langle n' \neq Exit \rangle$

obtain nx **where** $c_1 \vdash n - IEdge\ et \rightarrow_p nx$

apply $-$ **apply** $(erule\ Proc-CFG.cases)$

apply $(auto\ intro: Proc-CFG-Entry-Exit\ Proc-CFG-Entry)$

by $(case-tac\ n)(auto\ dest: Proc-CFG-sourcelabel-less-num-nodes)$

then show $?case$ **by** $(rule\ IH)$

next

case $(Proc-CFG-SeqConnect\ c_1\ n\ c_2)$

from $\langle c_1 \vdash n - CEdge\ (p, es, rets) \rightarrow_p Exit \rangle$

show $?case$ **by** $(fastforce\ dest: Proc-CFG-Call-Labels)$

next

case $(Proc-CFG-SeqSecond\ c_2\ n\ n'\ c_1)$

note $IH = \langle \bigwedge n''. c_2 \vdash n - IEdge\ et \rightarrow_p n'' \implies False \rangle$

from $\langle c_1; c_2 \vdash n \oplus \#: c_1 - IEdge\ et \rightarrow_p n'' \rangle \langle c_2 \vdash n - CEdge\ (p, es, rets) \rightarrow_p n' \rangle$
 $\langle n \neq Entry \rangle$

obtain nx **where** $c_2 \vdash n - IEdge\ et \rightarrow_p nx$

apply $-$ **apply** $(erule\ Proc-CFG.cases, auto)$

apply $(cases\ n)$ **apply** $(auto\ dest: Proc-CFG-sourcelabel-less-num-nodes)$

apply $(cases\ n)$ **apply** $(auto\ dest: Proc-CFG-sourcelabel-less-num-nodes)$

by $(cases\ n, auto, case-tac\ n, auto)$

then show $?case$ **by** $(rule\ IH)$

next

case $(Proc-CFG-CondThen\ c_1\ n\ n'\ b\ c_2)$

note $IH = \langle \bigwedge n''. c_1 \vdash n - IEdge\ et \rightarrow_p n'' \implies False \rangle$

from $\langle if\ (b)\ c_1\ else\ c_2 \vdash n \oplus 1 - IEdge\ et \rightarrow_p n'' \rangle \langle c_1 \vdash n - CEdge\ (p, es, rets) \rightarrow_p n' \rangle$


```

  ⟨n ≠ Entry⟩
obtain nx where c1 ⊢ n -IEdge et→p nx
  apply - apply(erule Proc-CFG.cases,auto)
  apply(cases n) apply auto apply(case-tac n) apply auto
  apply(cases n) apply auto
  by(case-tac n)(auto dest:Proc-CFG-sourcelabel-less-num-nodes)
then show ?case by (rule IH)
next
  case (Proc-CFG-CondElse c2 n n' b c1)
  note IH = ⟨∧n''. c2 ⊢ n -IEdge et→p n'' ⇒ False⟩
  from ⟨if (b) c1 else c2 ⊢ n ⊕ #:c1 + 1 -IEdge et→p n''⟩ ⟨c2 ⊢ n -CEdge (p,
  es, rets)→p n'⟩
  ⟨n ≠ Entry⟩
  obtain nx where c2 ⊢ n -IEdge et→p nx
  apply - apply(erule Proc-CFG.cases,auto)
  apply(cases n) apply auto
  apply(case-tac n) apply(auto dest:Proc-CFG-sourcelabel-less-num-nodes)
  by(cases n,auto,case-tac n,auto)
  then show ?case by (rule IH)
next
  case (Proc-CFG-WhileBody c' n n' b)
  note IH = ⟨∧n''. c' ⊢ n -IEdge et→p n'' ⇒ False⟩
  from ⟨while (b) c' ⊢ n ⊕ 2 -IEdge et→p n''⟩ ⟨c' ⊢ n -CEdge (p, es, rets)→p
  n'⟩
  ⟨n ≠ Entry⟩ ⟨n' ≠ Exit⟩
  obtain nx where c' ⊢ n -IEdge et→p nx
  apply - apply(erule Proc-CFG.cases,auto)
  apply(drule label-incr-ge[OF sym]) apply simp
  apply(cases n) apply auto apply(case-tac n) apply auto
  by(cases n,auto,case-tac n,auto)
  then show ?case by (rule IH)
next
  case (Proc-CFG-WhileBodyExit c' n b)
  from ⟨c' ⊢ n -CEdge (p, es, rets)→p Exit⟩
  show ?case by(fastforce dest:Proc-CFG-Call-Labels)
next
  case Proc-CFG-Call
  from ⟨Call p es rets ⊢ Label 0 -IEdge et→p n''⟩
  show ?case by(fastforce elim:Proc-CFG.cases)
qed

```

lemma Proc-CFG-Call-Intra-edge-not-same-target:

```

  [[prog ⊢ n -CEdge (p,es,rets)→p n'; prog ⊢ n'' -IEdge et→p n'']] ⇒ False
proof(induct prog n CEdge (p,es,rets) n' arbitrary:n'' rule:Proc-CFG.induct)
  case (Proc-CFG-SeqFirst c1 n n' c2)
  note IH = ⟨∧n''. c1 ⊢ n'' -IEdge et→p n' ⇒ False⟩
  from ⟨c1;c2 ⊢ n'' -IEdge et→p n'⟩ ⟨c1 ⊢ n -CEdge (p, es, rets)→p n'⟩
  ⟨n' ≠ Exit⟩

```

```

have  $c_1 \vdash n'' - IEdge\ et \rightarrow_p\ n'$ 
  apply – apply(erule Proc-CFG.cases)
  apply(auto intro:Proc-CFG-Entry dest:Proc-CFG-targetlabel-less-num-nodes)
  by(case-tac n')(auto dest:Proc-CFG-targetlabel-less-num-nodes)
then show ?case by (rule IH)
next
  case (Proc-CFG-SeqConnect c1 n c2)
  from  $\langle c_1 \vdash n - CEdge\ (p, es, rets) \rightarrow_p\ Exit \rangle$ 
  show ?case by(fastforce dest:Proc-CFG-Call-Labels)
next
  case (Proc-CFG-SeqSecond c2 n n' c1)
  note  $IH = \langle \bigwedge n''. c_2 \vdash n'' - IEdge\ et \rightarrow_p\ n' \implies False \rangle$ 
  from  $\langle c_1;;c_2 \vdash n'' - IEdge\ et \rightarrow_p\ n' \oplus \#:c_1 \rangle \langle c_2 \vdash n - CEdge\ (p, es, rets) \rightarrow_p\ n' \rangle$ 

   $\langle n \neq Entry \rangle$ 
  obtain nx where  $c_2 \vdash nx - IEdge\ et \rightarrow_p\ n'$ 
  apply – apply(erule Proc-CFG.cases, auto)
  apply(fastforce intro:Proc-CFG-Entry-Exit)
  apply(cases n') apply(auto dest:Proc-CFG-targetlabel-less-num-nodes)
  apply(cases n') apply(auto dest:Proc-CFG-Call-target-0)
  apply(cases n') apply(auto dest:Proc-CFG-Call-Labels)
  by(case-tac n') auto
  then show ?case by (rule IH)
next
  case (Proc-CFG-CondThen c1 n n' b c2)
  note  $IH = \langle \bigwedge n''. c_1 \vdash n'' - IEdge\ et \rightarrow_p\ n' \implies False \rangle$ 
  from  $\langle \text{if } (b) c_1 \text{ else } c_2 \vdash n'' - IEdge\ et \rightarrow_p\ n' \oplus 1 \rangle \langle c_1 \vdash n - CEdge\ (p, es,$ 
  rets)  $\rightarrow_p\ n' \rangle$ 
   $\langle n \neq Entry \rangle$ 
  obtain nx where  $c_1 \vdash nx - IEdge\ et \rightarrow_p\ n'$ 
  apply – apply(erule Proc-CFG.cases, auto)
  apply(cases n') apply(auto intro:Proc-CFG-Entry-Exit)
  apply(cases n') apply(auto dest:Proc-CFG-Call-target-0)
  apply(cases n') apply(auto dest:Proc-CFG-targetlabel-less-num-nodes)
  apply(cases n') apply auto apply(case-tac n') apply auto
  apply(cases n') apply auto
  apply(case-tac n') apply(auto dest:Proc-CFG-targetlabel-less-num-nodes)
  by(case-tac n')(auto dest:Proc-CFG-Call-Labels)
  then show ?case by (rule IH)
next
  case (Proc-CFG-CondElse c2 n n' b c1)
  note  $IH = \langle \bigwedge n''. c_2 \vdash n'' - IEdge\ et \rightarrow_p\ n' \implies False \rangle$ 
  from  $\langle \text{if } (b) c_1 \text{ else } c_2 \vdash n'' - IEdge\ et \rightarrow_p\ n' \oplus \#:c_1 + 1 \rangle \langle c_2 \vdash n - CEdge\ (p,$ 
  es, rets)  $\rightarrow_p\ n' \rangle$ 
   $\langle n \neq Entry \rangle$ 
  obtain nx where  $c_2 \vdash nx - IEdge\ et \rightarrow_p\ n'$ 
  apply – apply(erule Proc-CFG.cases, auto)
  apply(cases n') apply(auto intro:Proc-CFG-Entry-Exit)
  apply(cases n') apply(auto dest:Proc-CFG-Call-target-0)

```

```

    apply(cases n') apply(auto dest:Proc-CFG-Call-target-0)
  apply(cases n') apply auto
  apply(case-tac n') apply(auto dest:Proc-CFG-targetlabel-less-num-nodes)
  apply(case-tac n') apply(auto dest:Proc-CFG-Call-Labels)
  by(cases n',auto,case-tac n',auto)
then show ?case by (rule IH)
next
case (Proc-CFG-WhileBody c' n n' b)
note IH = ⟨ $\bigwedge n'' . c' \vdash n'' - IEdge \text{ et} \rightarrow_p n' \implies \text{False}$ ⟩
from ⟨while (b) c'  $\vdash$  n'' - IEdge et  $\rightarrow_p$  n'  $\oplus$  2⟩ ⟨c'  $\vdash$  n - CEdge (p, es, rets)  $\rightarrow_p$  n'⟩
  ⟨n  $\neq$  Entry⟩ ⟨n'  $\neq$  Exit⟩
obtain nx where c'  $\vdash$  nx - IEdge et  $\rightarrow_p$  n'
  apply - apply(erule Proc-CFG.cases,auto)
  apply(cases n') apply(auto dest:Proc-CFG-Call-target-0)
  apply(cases n') apply auto
  by(cases n',auto,case-tac n',auto)
then show ?case by (rule IH)
next
case (Proc-CFG-WhileBodyExit c' n b)
from ⟨c'  $\vdash$  n - CEdge (p, es, rets)  $\rightarrow_p$  Exit⟩
show ?case by(fastforce dest:Proc-CFG-Call-Labels)
next
case Proc-CFG-Call
from ⟨Call p es rets  $\vdash$  n'' - IEdge et  $\rightarrow_p$  Label 1⟩
show ?case by(fastforce elim:Proc-CFG.cases)
qed

```

lemma Proc-CFG-Call-nodes-eq:

```

[[prog  $\vdash$  n - CEdge (p,es,rets)  $\rightarrow_p$  n'; prog  $\vdash$  n - CEdge (p',es',rets')  $\rightarrow_p$  n']]
 $\implies n' = n'' \wedge p = p' \wedge es = es' \wedge rets = rets'$ 
proof(induct prog n CEdge (p,es,rets) n' arbitrary:n'' rule:Proc-CFG.induct)
  case (Proc-CFG-SeqFirst c1 n n' c2)
  note IH = ⟨ $\bigwedge n'' . c_1 \vdash n - CEdge (p',es',rets') \rightarrow_p n''$ ⟩
   $\implies n' = n'' \wedge p = p' \wedge es = es' \wedge rets = rets'$ 
  from ⟨c1;; c2  $\vdash$  n - CEdge (p',es',rets')  $\rightarrow_p$  n''⟩ ⟨c1  $\vdash$  n - CEdge (p,es,rets)  $\rightarrow_p$  n'⟩
  have c1  $\vdash$  n - CEdge (p',es',rets')  $\rightarrow_p$  n''
  apply - apply(erule Proc-CFG.cases,auto)
  apply(fastforce dest:Proc-CFG-Call-Labels)
  by(case-tac n,(fastforce dest:Proc-CFG-sourcelabel-less-num-nodes)+)
  then show ?case by (rule IH)
next
case (Proc-CFG-SeqConnect c1 n c2)
from ⟨c1  $\vdash$  n - CEdge (p,es,rets)  $\rightarrow_p$  Exit⟩ have False
  by(fastforce dest:Proc-CFG-Call-Labels)
  thus ?case by simp
next

```

```

case (Proc-CFG-SeqSecond  $c_2$   $n$   $n'$   $c_1$ )
note  $IH = \langle \bigwedge n''. c_2 \vdash n - CEdge(p', es', rets') \rightarrow_p n'' \rangle$ 
 $\implies n' = n'' \wedge p = p' \wedge es = es' \wedge rets = rets'$ 
from  $\langle c_1;; c_2 \vdash n \oplus \#:c_1 - CEdge(p', es', rets') \rightarrow_p n'' \rangle \langle n \neq Entry \rangle$ 
obtain  $nx$  where  $edge:c_2 \vdash n - CEdge(p', es', rets') \rightarrow_p nx$  and  $nx: nx \oplus \#:c_1 = n''$ 
apply – apply(erule Proc-CFG.cases, auto)
by(cases n, auto dest:Proc-CFG-sourcelabel-less-num-nodes label-incr-inj) +
from  $edge$  have  $n' = nx \wedge p = p' \wedge es = es' \wedge rets = rets'$  by (rule IH)
with  $nx$  show ?case by auto
next
case (Proc-CFG-CondThen  $c_1$   $n$   $n'$   $b$   $c_2$ )
note  $IH = \langle \bigwedge n''. c_1 \vdash n - CEdge(p', es', rets') \rightarrow_p n'' \rangle$ 
 $\implies n' = n'' \wedge p = p' \wedge es = es' \wedge rets = rets'$ 
from  $\langle if(b) c_1 else c_2 \vdash n \oplus 1 - CEdge(p', es', rets') \rightarrow_p n'' \rangle$ 
obtain  $nx$  where  $c_1 \vdash n - CEdge(p', es', rets') \rightarrow_p nx \wedge nx \oplus 1 = n''$ 
proof(rule Proc-CFG.cases)
fix  $c_2'$   $nx$  etx  $nx'$  bx  $c_1'$ 
assume  $if(b) c_1 else c_2 = if(bx) c_1' else c_2'$ 
and  $n \oplus 1 = nx \oplus \#:c_1' + 1$  and  $nx \neq Entry$ 
with  $\langle c_1 \vdash n - CEdge(p, es, rets) \rightarrow_p n' \rangle$  obtain  $l$  where  $n = Label\ l$  and  $l \geq \#:c_1$ 
by(cases n, auto, cases nx, auto)
with  $\langle c_1 \vdash n - CEdge(p, es, rets) \rightarrow_p n' \rangle$  have False
by(fastforce dest:Proc-CFG-sourcelabel-less-num-nodes)
thus ?thesis by simp
qed (auto dest:label-incr-inj)
then obtain  $nx$  where  $edge:c_1 \vdash n - CEdge(p', es', rets') \rightarrow_p nx$ 
and  $nx: nx \oplus 1 = n''$  by blast
from  $IH[OF\ edge]$   $nx$  show ?case by simp
next
case (Proc-CFG-CondElse  $c_2$   $n$   $n'$   $b$   $c_1$ )
note  $IH = \langle \bigwedge n''. c_2 \vdash n - CEdge(p', es', rets') \rightarrow_p n'' \rangle$ 
 $\implies n' = n'' \wedge p = p' \wedge es = es' \wedge rets = rets'$ 
from  $\langle if(b) c_1 else c_2 \vdash n \oplus \#:c_1 + 1 - CEdge(p', es', rets') \rightarrow_p n'' \rangle$ 
obtain  $nx$  where  $c_2 \vdash n - CEdge(p', es', rets') \rightarrow_p nx \wedge nx \oplus \#:c_1 + 1 = n''$ 
proof(rule Proc-CFG.cases)
fix  $c_1'$   $nx$  etx  $nx'$  bx  $c_2'$ 
assume  $ifs:if(b) c_1 else c_2 = if(bx) c_1' else c_2'$ 
and  $n \oplus \#:c_1 + 1 = nx \oplus 1$  and  $nx \neq Entry$ 
and  $edge:c_1' \vdash nx - etx \rightarrow_p nx'$ 
then obtain  $l$  where  $nx = Label\ l$  and  $l \geq \#:c_1$ 
by(cases n, auto, cases nx, auto)
with  $edge$  ifs have False
by(fastforce dest:Proc-CFG-sourcelabel-less-num-nodes)
thus ?thesis by simp
qed (auto dest:label-incr-inj)
then obtain  $nx$  where  $edge:c_2 \vdash n - CEdge(p', es', rets') \rightarrow_p nx$ 
and  $nx: nx \oplus \#:c_1 + 1 = n''$ 

```

by *blast*
 from $IH[OF\ edge] \ nx \ show \ ?case \ by \ simp$
 next
 case (*Proc-CFG-WhileBody* $c' \ n \ n' \ b$)
 note $IH = \langle \bigwedge n''. \ c' \vdash \ n - CEdge \ (p', es', rets') \rightarrow_p \ n''$
 $\implies \ n' = n'' \wedge \ p = p' \wedge \ es = es' \wedge \ rets = rets' \rangle$
 from $\langle while \ (b) \ c' \vdash \ n \oplus \ 2 - CEdge \ (p', es', rets') \rightarrow_p \ n'' \rangle$
 obtain $nx \ where \ c' \vdash \ n - CEdge \ (p', es', rets') \rightarrow_p \ nx \wedge \ nx \oplus \ 2 = n''$
 by (*rule Proc-CFG.cases, auto dest:label-incr-inj Proc-CFG-Call-Labels*)
 then obtain $nx \ where \ edge: c' \vdash \ n - CEdge \ (p', es', rets') \rightarrow_p \ nx$
 and $nx: nx \oplus \ 2 = n''$ by *blast*
 from $IH[OF\ edge] \ nx \ show \ ?case \ by \ simp$
 next
 case (*Proc-CFG-WhileBodyExit* $c' \ n \ b$)
 from $\langle c' \vdash \ n - CEdge \ (p, es, rets) \rightarrow_p \ Exit \rangle \ have \ False$
 by (*fastforce dest:Proc-CFG-Call-Labels*)
 thus *?case by simp*
 next
 case *Proc-CFG-Call*
 from $\langle Call \ p \ es \ rets \vdash \ Label \ 0 - CEdge \ (p', es', rets') \rightarrow_p \ n'' \rangle$
 have $p = p' \wedge \ es = es' \wedge \ rets = rets' \wedge \ n'' = Label \ 1$
 by (*auto elim:Proc-CFG.cases*)
 then show *?case by simp*
 qed

lemma *Proc-CFG-Call-nodes-eq'*:
 $\llbracket prog \vdash \ n - CEdge \ (p, es, rets) \rightarrow_p \ n'; \ prog \vdash \ n'' - CEdge \ (p', es', rets') \rightarrow_p \ n'' \rrbracket$
 $\implies \ n = n'' \wedge \ p = p' \wedge \ es = es' \wedge \ rets = rets'$
proof (*induct prog n CEdge (p, es, rets) n' arbitrary:n'' rule:Proc-CFG.induct*)
 case (*Proc-CFG-SeqFirst* $c_1 \ n \ n' \ c_2$)
 note $IH = \langle \bigwedge n''. \ c_1 \vdash \ n'' - CEdge \ (p', es', rets') \rightarrow_p \ n'$
 $\implies \ n = n'' \wedge \ p = p' \wedge \ es = es' \wedge \ rets = rets' \rangle$
 from $\langle c_1; c_2 \vdash \ n'' - CEdge \ (p', es', rets') \rightarrow_p \ n' \rangle \langle c_1 \vdash \ n - CEdge \ (p, es, rets) \rightarrow_p \ n' \rangle$
 have $c_1 \vdash \ n'' - CEdge \ (p', es', rets') \rightarrow_p \ n'$
 apply – apply (*erule Proc-CFG.cases, auto*)
 apply (*fastforce dest:Proc-CFG-Call-Labels*)
 by (*case-tac n', auto dest:Proc-CFG-targetlabel-less-num-nodes Proc-CFG-Call-Labels*)
 then show *?case by (rule IH)*
 next
 case (*Proc-CFG-SeqConnect* $c_1 \ n \ c_2$)
 from $\langle c_1 \vdash \ n - CEdge \ (p, es, rets) \rightarrow_p \ Exit \rangle \ have \ False$
 by (*fastforce dest:Proc-CFG-Call-Labels*)
 thus *?case by simp*
 next
 case (*Proc-CFG-SeqSecond* $c_2 \ n \ n' \ c_1$)
 note $IH = \langle \bigwedge n''. \ c_2 \vdash \ n'' - CEdge \ (p', es', rets') \rightarrow_p \ n'$
 $\implies \ n = n'' \wedge \ p = p' \wedge \ es = es' \wedge \ rets = rets' \rangle$

```

from  $\langle c_1;;c_2 \vdash n'' - CEdge (p',es',rets') \rightarrow_p n' \oplus \#:c_1 \rangle$ 
obtain  $nx$  where  $edge:c_2 \vdash nx - CEdge (p',es',rets') \rightarrow_p n'$  and  $nx:nx \oplus \#:c_1 = n''$ 
apply – apply(erule Proc-CFG.cases,auto)
by(cases  $n'$ ,
  auto dest:Proc-CFG-targetlabel-less-num-nodes Proc-CFG-Call-Labels
  label-incr-inj)
from  $edge$  have  $n = nx \wedge p = p' \wedge es = es' \wedge rets = rets'$  by (rule IH)
with  $nx$  show ?case by auto
next
case (Proc-CFG-CondThen  $c_1 n n' b c_2$ )
note  $IH = \langle \bigwedge n''. c_1 \vdash n'' - CEdge (p',es',rets') \rightarrow_p n' \rangle$ 
 $\implies n = n'' \wedge p = p' \wedge es = es' \wedge rets = rets'$ 
from  $\langle if (b) c_1 else c_2 \vdash n'' - CEdge (p',es',rets') \rightarrow_p n' \oplus 1 \rangle$ 
obtain  $nx$  where  $c_1 \vdash nx - CEdge (p',es',rets') \rightarrow_p n' \wedge nx \oplus 1 = n''$ 
proof(cases)
  case (Proc-CFG-CondElse  $nx nx'$ )
  from  $\langle n' \oplus 1 = nx' \oplus \#:c_1 + 1 \rangle$ 
   $\langle c_1 \vdash n - CEdge (p,es,rets) \rightarrow_p n' \rangle$ 
  obtain  $l$  where  $n' = Label l$  and  $l \geq \#:c_1$ 
  by(cases  $n'$ , auto dest:Proc-CFG-Call-Labels,cases  $nx'$ ,auto)
  with  $\langle c_1 \vdash n - CEdge (p,es,rets) \rightarrow_p n' \rangle$  have False
  by(fastforce dest:Proc-CFG-targetlabel-less-num-nodes)
  thus ?thesis by simp
qed (auto dest:label-incr-inj)
then obtain  $nx$  where  $edge:c_1 \vdash nx - CEdge (p',es',rets') \rightarrow_p n'$ 
  and  $nx:nx \oplus 1 = n''$ 
  by blast
from IH[OF edge]  $nx$  show ?case by simp
next
case (Proc-CFG-CondElse  $c_2 n n' b c_1$ )
note  $IH = \langle \bigwedge n''. c_2 \vdash n'' - CEdge (p',es',rets') \rightarrow_p n' \rangle$ 
 $\implies n = n'' \wedge p = p' \wedge es = es' \wedge rets = rets'$ 
from  $\langle if (b) c_1 else c_2 \vdash n'' - CEdge (p',es',rets') \rightarrow_p n' \oplus \#:c_1 + 1 \rangle$ 
obtain  $nx$  where  $c_2 \vdash nx - CEdge (p',es',rets') \rightarrow_p n' \wedge nx \oplus \#:c_1 + 1 = n''$ 
proof(cases)
  case (Proc-CFG-CondThen  $nx nx'$ )
  from  $\langle n' \oplus \#:c_1 + 1 = nx' \oplus 1 \rangle$ 
   $\langle c_1 \vdash nx - CEdge (p',es',rets') \rightarrow_p nx' \rangle$ 
  obtain  $l$  where  $nx' = Label l$  and  $l \geq \#:c_1$ 
  by(cases  $n'$ ,auto,cases  $nx'$ ,auto dest:Proc-CFG-Call-Labels)
  with  $\langle c_1 \vdash nx - CEdge (p',es',rets') \rightarrow_p nx' \rangle$ 
  have False by(fastforce dest:Proc-CFG-targetlabel-less-num-nodes)
  thus ?thesis by simp
qed (auto dest:label-incr-inj)
then obtain  $nx$  where  $edge:c_2 \vdash nx - CEdge (p',es',rets') \rightarrow_p n'$ 
  and  $nx:nx \oplus \#:c_1 + 1 = n''$ 
  by blast
from IH[OF edge]  $nx$  show ?case by simp

```

next
case (*Proc-CFG-WhileBody* $c' n n' b$)
note $IH = \langle \bigwedge n''. c' \vdash n'' - CEdge (p', es', rets') \rightarrow_p n' \implies n = n'' \wedge p = p' \wedge es = es' \wedge rets = rets' \rangle$
from $\langle while (b) c' \vdash n'' - CEdge (p', es', rets') \rightarrow_p n' \oplus 2 \rangle$
obtain nx **where** $edge: c' \vdash nx - CEdge (p', es', rets') \rightarrow_p n'$ **and** $nx: nx \oplus 2 = n''$
by(*rule Proc-CFG.cases, auto dest: label-incr-inj*)
from $IH[OF edge]$ nx **show** $?case$ **by** *simp*
next
case (*Proc-CFG-WhileBodyExit* $c' n b$)
from $\langle c' \vdash n - CEdge (p, es, rets) \rightarrow_p Exit \rangle$
have *False* **by**(*fastforce dest: Proc-CFG-Call-Labels*)
thus $?case$ **by** *simp*
next
case *Proc-CFG-Call*
from $\langle Call p es rets \vdash n'' - CEdge (p', es', rets') \rightarrow_p Label 1 \rangle$
have $p = p' \wedge es = es' \wedge rets = rets' \wedge n'' = Label 0$
by(*auto elim: Proc-CFG.cases*)
then show $?case$ **by** *simp*
qed

lemma *Proc-CFG-Call-targetnode-no-Call-sourcenode:*

$\llbracket prog \vdash n - CEdge (p, es, rets) \rightarrow_p n'; prog \vdash n' - CEdge (p', es', rets') \rightarrow_p n'' \rrbracket$
 $\implies False$

proof(*induct prog n CEdge (p, es, rets) n' arbitrary: n'' rule: Proc-CFG.induct*)

case (*Proc-CFG-SeqFirst* $c_1 n n' c_2$)

note $IH = \langle \bigwedge n''. c_1 \vdash n' - CEdge (p', es', rets') \rightarrow_p n'' \implies False \rangle$

from $\langle c_1;; c_2 \vdash n' - CEdge (p', es', rets') \rightarrow_p n'' \rangle \langle c_1 \vdash n - CEdge (p, es, rets) \rightarrow_p n' \rangle$

have $c_1 \vdash n' - CEdge (p', es', rets') \rightarrow_p n''$

apply – **apply**(*erule Proc-CFG.cases, auto*)

apply(*fastforce dest: Proc-CFG-Call-Labels*)

by(*case-tac n*)(*auto dest: Proc-CFG-targetlabel-less-num-nodes*)

then show $?case$ **by** (*rule IH*)

next

case (*Proc-CFG-SeqConnect* $c_1 n c_2$)

from $\langle c_1 \vdash n - CEdge (p, es, rets) \rightarrow_p Exit \rangle$ **have** *False*

by(*fastforce dest: Proc-CFG-Call-Labels*)

thus $?case$ **by** *simp*

next

case (*Proc-CFG-SeqSecond* $c_2 n n' c_1$)

note $IH = \langle \bigwedge n''. c_2 \vdash n' - CEdge (p', es', rets') \rightarrow_p n'' \implies False \rangle$

from $\langle c_1;; c_2 \vdash n' \oplus \#: c_1 - CEdge (p', es', rets') \rightarrow_p n'' \rangle \langle c_2 \vdash n - CEdge (p, es, rets) \rightarrow_p n' \rangle$

obtain nx **where** $c_2 \vdash n' - CEdge (p', es', rets') \rightarrow_p nx$

apply – **apply**(*erule Proc-CFG.cases, auto*)

apply(*cases n'*) **apply**(*auto dest: Proc-CFG-sourcelabel-less-num-nodes*)

```

    apply(fastforce dest:Proc-CFG-Call-Labels)
  by(cases n', auto, case-tac n, auto)
then show ?case by (rule IH)
next
case (Proc-CFG-CondThen c1 n n' b c2)
note IH = ⟨ $\bigwedge n''.$   $c_1 \vdash n' - CEdge (p', es', rets') \rightarrow_p n'' \implies False$ ⟩
from ⟨if (b)  $c_1$  else  $c_2 \vdash n' \oplus 1 - CEdge (p', es', rets') \rightarrow_p n''$ ⟩ ⟨ $c_1 \vdash n - CEdge$ 

$(p, es, rets) \rightarrow_p n'$ ⟩
obtain nx where  $c_1 \vdash n' - CEdge (p', es', rets') \rightarrow_p nx$ 
  apply – apply(erule Proc-CFG.cases, auto)
  apply(cases n') apply auto apply(case-tac n) apply auto
  apply(cases n') apply auto
  by(case-tac n)(auto dest:Proc-CFG-targetlabel-less-num-nodes)
then show ?case by (rule IH)
next
case (Proc-CFG-CondElse c2 n n' b c1)
note IH = ⟨ $\bigwedge n''.$   $c_2 \vdash n' - CEdge (p', es', rets') \rightarrow_p n'' \implies False$ ⟩
from ⟨if (b)  $c_1$  else  $c_2 \vdash n' \oplus \#:c_1 + 1 - CEdge (p', es', rets') \rightarrow_p n''$ ⟩
  ⟨ $c_2 \vdash n - CEdge (p, es, rets) \rightarrow_p n'$ ⟩
obtain nx where  $c_2 \vdash n' - CEdge (p', es', rets') \rightarrow_p nx$ 
  apply – apply(erule Proc-CFG.cases, auto)
  apply(cases n') apply auto
  apply(case-tac n) apply(auto dest:Proc-CFG-sourcelabel-less-num-nodes)
  by(cases n', auto, case-tac n, auto)
then show ?case by (rule IH)
next
case (Proc-CFG-WhileBody c' n n' b)
note IH = ⟨ $\bigwedge n''.$   $c' \vdash n' - CEdge (p', es', rets') \rightarrow_p n'' \implies False$ ⟩
from ⟨while (b)  $c' \vdash n' \oplus 2 - CEdge (p', es', rets') \rightarrow_p n''$ ⟩ ⟨ $c' \vdash n - CEdge$ 

$(p, es, rets) \rightarrow_p n'$ ⟩
obtain nx where  $c' \vdash n' - CEdge (p', es', rets') \rightarrow_p nx$ 
  apply – apply(erule Proc-CFG.cases, auto)
  by(cases n', auto, case-tac n, auto)+
then show ?case by (rule IH)
next
case (Proc-CFG-WhileBodyExit c' n b)
from ⟨ $c' \vdash n - CEdge (p, es, rets) \rightarrow_p Exit$ ⟩
show ?case by(fastforce dest:Proc-CFG-Call-Labels)
next
case Proc-CFG-Call
from ⟨Call p es rets  $\vdash Label 1 - CEdge (p', es', rets') \rightarrow_p n''$ ⟩
show ?case by(fastforce elim:Proc-CFG.cases)
qed


```

lemma Proc-CFG-Call-follows-id-edge:

```

[[prog  $\vdash n - CEdge (p, es, rets) \rightarrow_p n'$ ; prog  $\vdash n' - IEdge et \rightarrow_p n''$ ]]  $\implies et = \uparrow id$ 
proof(induct prog n CEdge (p, es, rets) n' arbitrary:n'' rule:Proc-CFG.induct)
  case (Proc-CFG-SeqFirst c1 n n' c2)

```



```

note  $IH = \langle \bigwedge n''. c_1 \vdash n' - IEdge\ et \rightarrow_p\ n'' \implies et = \uparrow id \rangle$ 
from  $\langle c_1;;c_2 \vdash n' - IEdge\ et \rightarrow_p\ n'' \rangle \langle c_1 \vdash n - CEdge\ (p,es,rets) \rightarrow_p\ n' \rangle \langle n' \neq$ 
Exit
obtain  $nx$  where  $c_1 \vdash n' - IEdge\ et \rightarrow_p\ nx$ 
apply – apply(erule Proc-CFG.cases,auto)
by(case-tac n)(auto dest:Proc-CFG-targetlabel-less-num-nodes)
then show ?case by (rule IH)
next
case (Proc-CFG-SeqConnect c_1 n c_2)
from  $\langle c_1 \vdash n - CEdge\ (p, es, rets) \rightarrow_p\ Exit \rangle$ 
show ?case by(fastforce dest:Proc-CFG-Call-Labels)
next
case (Proc-CFG-SeqSecond c_2 n' c_1)
note  $IH = \langle \bigwedge n''. c_2 \vdash n' - IEdge\ et \rightarrow_p\ n'' \implies et = \uparrow id \rangle$ 
from  $\langle c_1;;c_2 \vdash n' \oplus \#:c_1 - IEdge\ et \rightarrow_p\ n'' \rangle \langle c_2 \vdash n - CEdge\ (p,es,rets) \rightarrow_p\ n' \rangle$ 
obtain  $nx$  where  $c_2 \vdash n' - IEdge\ et \rightarrow_p\ nx$ 
apply – apply(erule Proc-CFG.cases,auto)
apply(cases n') apply(auto dest:Proc-CFG-sourcelabel-less-num-nodes)
apply(cases n') apply(auto dest:Proc-CFG-sourcelabel-less-num-nodes)
by(cases n',auto,case-tac n,auto)
then show ?case by (rule IH)
next
case (Proc-CFG-CondThen c_1 n n' b c_2)
note  $IH = \langle \bigwedge n''. c_1 \vdash n' - IEdge\ et \rightarrow_p\ n'' \implies et = \uparrow id \rangle$ 
from  $\langle if\ (b)\ c_1\ else\ c_2 \vdash n' \oplus 1 - IEdge\ et \rightarrow_p\ n'' \rangle \langle c_1 \vdash n - CEdge\ (p,es,rets) \rightarrow_p$ 
 $n' \rangle$ 
 $\langle n \neq Entry \rangle$ 
obtain  $nx$  where  $c_1 \vdash n' - IEdge\ et \rightarrow_p\ nx$ 
apply – apply(erule Proc-CFG.cases,auto)
apply(cases n') apply auto apply(case-tac n) apply auto
apply(cases n') apply auto
by(case-tac n)(auto dest:Proc-CFG-targetlabel-less-num-nodes)
then show ?case by (rule IH)
next
case (Proc-CFG-CondElse c_2 n n' b c_1)
note  $IH = \langle \bigwedge n''. c_2 \vdash n' - IEdge\ et \rightarrow_p\ n'' \implies et = \uparrow id \rangle$ 
from  $\langle if\ (b)\ c_1\ else\ c_2 \vdash n' \oplus \#:c_1 + 1 - IEdge\ et \rightarrow_p\ n'' \rangle \langle c_2 \vdash n - CEdge$ 
 $(p,es,rets) \rightarrow_p\ n' \rangle$ 
obtain  $nx$  where  $c_2 \vdash n' - IEdge\ et \rightarrow_p\ nx$ 
apply – apply(erule Proc-CFG.cases,auto)
apply(cases n') apply auto
apply(case-tac n) apply(auto dest:Proc-CFG-sourcelabel-less-num-nodes)
by(cases n',auto,case-tac n,auto)
then show ?case by (rule IH)
next
case (Proc-CFG-WhileBody c' n n' b)
note  $IH = \langle \bigwedge n''. c' \vdash n' - IEdge\ et \rightarrow_p\ n'' \implies et = \uparrow id \rangle$ 
from  $\langle while\ (b)\ c' \vdash n' \oplus 2 - IEdge\ et \rightarrow_p\ n'' \rangle \langle c' \vdash n - CEdge\ (p,es,rets) \rightarrow_p$ 
 $n' \rangle$ 

```

```

obtain  $nx$  where  $c' \vdash n' - IEdge\ et \rightarrow_p\ nx$ 
  apply – apply( $erule\ Proc-CFG.cases, auto$ )
    apply( $cases\ n'$ ) apply  $auto$ 
    apply( $cases\ n'$ ) apply  $auto$  apply( $case-tac\ n$ ) apply  $auto$ 
    by( $cases\ n', auto, case-tac\ n, auto$ )
then show  $?case$  by ( $rule\ IH$ )
next
  case ( $Proc-CFG-WhileBodyExit\ c'\ n\ et'\ b$ )
  from  $\langle c' \vdash n - CEdge\ (p, es, rets) \rightarrow_p\ Exit \rangle$ 
  show  $?case$  by( $fastforce\ dest:Proc-CFG-Call-Labels$ )
next
  case  $Proc-CFG-Call$ 
  from  $\langle Call\ p\ es\ rets \vdash Label\ l - IEdge\ et \rightarrow_p\ n'' \rangle$  show  $?case$ 
    by( $fastforce\ elim:Proc-CFG.cases$ )
qed

```

lemma $Proc-CFG-edge-det$:

```

 $\llbracket prog \vdash n - et \rightarrow_p\ n'; prog \vdash n - et' \rightarrow_p\ n' \rrbracket \implies et = et'$ 
proof( $induct\ rule:Proc-CFG.induct$ )
  case  $Proc-CFG-Entry-Exit$  thus  $?case$  by( $fastforce\ dest:Proc-CFG-EntryD$ )
next
  case  $Proc-CFG-Entry$  thus  $?case$  by( $fastforce\ dest:Proc-CFG-EntryD$ )
next
  case  $Proc-CFG-Skip$  thus  $?case$  by( $fastforce\ elim:Proc-CFG.cases$ )
next
  case  $Proc-CFG-LAss$  thus  $?case$  by( $fastforce\ elim:Proc-CFG.cases$ )
next
  case  $Proc-CFG-LAssSkip$  thus  $?case$  by( $fastforce\ elim:Proc-CFG.cases$ )
next
  case ( $Proc-CFG-SeqFirst\ c_1\ n\ et\ n'\ c_2$ )
  note  $edge = \langle c_1 \vdash n - et \rightarrow_p\ n' \rangle$ 
  note  $IH = \langle c_1 \vdash n - et' \rightarrow_p\ n' \implies et = et' \rangle$ 
  from  $edge\ \langle n' \neq Exit \rangle$  obtain  $l$  where  $l:n' = Label\ l$  by ( $cases\ n'$ )  $auto$ 
  with  $edge$  have  $l < \#:c_1$  by( $fastforce\ intro:Proc-CFG-targetlabel-less-num-nodes$ )
  with  $\langle c_1;;c_2 \vdash n - et' \rightarrow_p\ n' \rangle\ l$  have  $c_1 \vdash n - et' \rightarrow_p\ n'$ 
    by( $fastforce\ elim:Proc-CFG.cases\ intro:Proc-CFG.intros\ dest:label-incr-ge$ )
  from  $IH[OF\ this]$  show  $?case$  .
next
  case ( $Proc-CFG-SeqConnect\ c_1\ n\ et\ c_2$ )
  note  $edge = \langle c_1 \vdash n - et \rightarrow_p\ Exit \rangle$ 
  note  $IH = \langle c_1 \vdash n - et' \rightarrow_p\ Exit \implies et = et' \rangle$ 
  from  $edge\ \langle n \neq Entry \rangle$  obtain  $l$  where  $l:n = Label\ l$  by ( $cases\ n$ )  $auto$ 
  with  $edge$  have  $l < \#:c_1$  by( $fastforce\ intro:Proc-CFG-sourcelabel-less-num-nodes$ )
  with  $\langle c_1;;c_2 \vdash n - et' \rightarrow_p\ Label\ \#:c_1 \rangle\ l$  have  $c_1 \vdash n - et' \rightarrow_p\ Exit$ 
    by( $fastforce\ elim:Proc-CFG.cases$ 
       $dest:Proc-CFG-targetlabel-less-num-nodes\ label-incr-ge$ )
  from  $IH[OF\ this]$  show  $?case$  .
next

```

```

case (Proc-CFG-SeqSecond  $c_2$   $n$   $et$   $n'$   $c_1$ )
note  $edge = \langle c_2 \vdash n -et \rightarrow_p n' \rangle$ 
note  $IH = \langle c_2 \vdash n -et' \rightarrow_p n' \implies et = et' \rangle$ 
from  $edge \langle n \neq Entry \rangle$  obtain  $l$  where  $l:n = Label\ l$  by (cases  $n$ ) auto
with  $edge$  have  $l < \#:c_2$  by(fastforce intro:Proc-CFG-sourcelabel-less-num-nodes)
with  $\langle c_1;;c_2 \vdash n \oplus \#:c_1 -et' \rightarrow_p n' \oplus \#:c_1 \rangle$   $l$  have  $c_2 \vdash n -et' \rightarrow_p n'$ 
by  $-(erule\ Proc-CFG.cases,$ 
  (fastforce dest:Proc-CFG-sourcelabel-less-num-nodes label-incr-ge
    dest!:label-incr-inj)+)
from  $IH[OF\ this]$  show  $?case$  .
next
case Proc-CFG-CondTrue thus  $?case$  by(fastforce elim:Proc-CFG.cases)
next
case Proc-CFG-CondFalse thus  $?case$  by(fastforce elim:Proc-CFG.cases)
next
case (Proc-CFG-CondThen  $c_1$   $n$   $et$   $n'$   $b$   $c_2$ )
note  $edge = \langle c_1 \vdash n -et \rightarrow_p n' \rangle$ 
note  $IH = \langle c_1 \vdash n -et' \rightarrow_p n' \implies et = et' \rangle$ 
from  $edge \langle n \neq Entry \rangle$  obtain  $l$  where  $l:n = Label\ l$  by (cases  $n$ ) auto
with  $edge$  have  $l < \#:c_1$  by(fastforce intro:Proc-CFG-sourcelabel-less-num-nodes)
with  $\langle if\ (b)\ c_1\ else\ c_2 \vdash n \oplus 1 -et' \rightarrow_p n' \oplus 1 \rangle$   $l$  have  $c_1 \vdash n -et' \rightarrow_p n'$ 
by  $-(erule\ Proc-CFG.cases,(fastforce\ dest:label-incr-ge\ label-incr-inj)+)$ 
from  $IH[OF\ this]$  show  $?case$  .
next
case (Proc-CFG-CondElse  $c_2$   $n$   $et$   $n'$   $b$   $c_1$ )
note  $edge = \langle c_2 \vdash n -et \rightarrow_p n' \rangle$ 
note  $IH = \langle c_2 \vdash n -et' \rightarrow_p n' \implies et = et' \rangle$ 
from  $edge \langle n \neq Entry \rangle$  obtain  $l$  where  $l:n = Label\ l$  by (cases  $n$ ) auto
with  $edge$  have  $l < \#:c_2$  by(fastforce intro:Proc-CFG-sourcelabel-less-num-nodes)
with  $\langle if\ (b)\ c_1\ else\ c_2 \vdash n \oplus (\#:c_1 + 1) -et' \rightarrow_p n' \oplus (\#:c_1 + 1) \rangle$   $l$ 
have  $c_2 \vdash n -et' \rightarrow_p n'$ 
by  $-(erule\ Proc-CFG.cases,(fastforce\ dest:Proc-CFG-sourcelabel-less-num-nodes$ 
  label-incr-inj label-incr-ge label-incr-simp-rev)+)
from  $IH[OF\ this]$  show  $?case$  .
next
case Proc-CFG-WhileTrue thus  $?case$  by(fastforce elim:Proc-CFG.cases)
next
case Proc-CFG-WhileFalse thus  $?case$  by(fastforce elim:Proc-CFG.cases)
next
case Proc-CFG-WhileFalseSkip thus  $?case$  by(fastforce elim:Proc-CFG.cases)
next
case (Proc-CFG-WhileBody  $c'$   $n$   $et$   $n'$   $b$ )
note  $edge = \langle c' \vdash n -et \rightarrow_p n' \rangle$ 
note  $IH = \langle c' \vdash n -et' \rightarrow_p n' \implies et = et' \rangle$ 
from  $edge \langle n \neq Entry \rangle$  obtain  $l$  where  $l:n = Label\ l$  by (cases  $n$ ) auto
with  $edge$  have  $less:l < \#:c'$ 
by(fastforce intro:Proc-CFG-sourcelabel-less-num-nodes)
from  $edge \langle n' \neq Exit \rangle$  obtain  $l'$  where  $l':n' = Label\ l'$  by (cases  $n'$ ) auto

```

with *edge* **have** $l' < \# : c'$ **by** (*fastforce* *intro:Proc-CFG-targetlabel-less-num-nodes*)
with $\langle \text{while } (b) \ c' \vdash n \oplus 2 \ -et' \rightarrow_p \ n' \oplus 2 \rangle \ l \ \text{less } l'$ **have** $c' \vdash n \ -et' \rightarrow_p \ n'$
by (*fastforce* *elim:Proc-CFG.cases* *dest:label-incr-start-Node-smaller*)
from *IH[OF this]* **show** *?case* .
next
case (*Proc-CFG-WhileBodyExit* $c' \ n \ et \ b$)
note $\langle \text{edge} = \langle c' \vdash n \ -et \rightarrow_p \ \text{Exit} \rangle$
note $\langle \text{IH} = \langle c' \vdash n \ -et' \rightarrow_p \ \text{Exit} \implies et = et' \rangle$
from $\langle \text{edge} \ \langle n \neq \text{Entry} \rangle$ **obtain** l **where** $l : n = \text{Label } l$ **by** (*cases* n) *auto*
with $\langle \text{edge} \ \text{have } l < \# : c' \ \text{by} \ (\text{fastforce} \ \text{intro:Proc-CFG-sourcelabel-less-num-nodes})$
with $\langle \text{while } (b) \ c' \vdash n \oplus 2 \ -et' \rightarrow_p \ \text{Label } 0 \rangle \ l$ **have** $c' \vdash n \ -et' \rightarrow_p \ \text{Exit}$
by $\langle \text{erule } \text{Proc-CFG.cases,auto} \ \text{dest:label-incr-start-Node-smaller} \rangle$
from *IH[OF this]* **show** *?case* .
next
case *Proc-CFG-Call* **thus** *?case* **by** (*fastforce* *elim:Proc-CFG.cases*)
next
case *Proc-CFG-CallSkip* **thus** *?case* **by** (*fastforce* *elim:Proc-CFG.cases*)
qed

lemma *WCFG-deterministic*:

$\llbracket \text{prog} \vdash n_1 \ -et_1 \rightarrow_p \ n_1' ; \text{prog} \vdash n_2 \ -et_2 \rightarrow_p \ n_2' ; n_1 = n_2 ; n_1' \neq n_2' \rrbracket$
 $\implies \exists Q \ Q' . et_1 = \text{IEdge } (Q)_{\surd} \wedge et_2 = \text{IEdge } (Q')_{\surd} \wedge$
 $(\forall s . (Q \ s \longrightarrow \neg Q' \ s) \wedge (Q' \ s \longrightarrow \neg Q \ s))$

proof (*induct* *arbitrary:n₂ n₂'* *rule:Proc-CFG.induct*)

case (*Proc-CFG-Entry-Exit* *prog*)
from $\langle \text{prog} \vdash n_2 \ -et_2 \rightarrow_p \ n_2' \rangle \ \langle \text{Entry} = n_2 \rangle \ \langle \text{Exit} \neq n_2' \rangle$
have $et_2 = \text{IEdge } (\lambda s . \text{True})_{\surd}$ **by** (*fastforce* *dest:Proc-CFG-EntryD*)
thus *?case* **by** *simp*

next

case (*Proc-CFG-Entry* *prog*)
from $\langle \text{prog} \vdash n_2 \ -et_2 \rightarrow_p \ n_2' \rangle \ \langle \text{Entry} = n_2 \rangle \ \langle \text{Label } 0 \neq n_2' \rangle$
have $et_2 = \text{IEdge } (\lambda s . \text{False})_{\surd}$ **by** (*fastforce* *dest:Proc-CFG-EntryD*)
thus *?case* **by** *simp*

next

case *Proc-CFG-Skip*
from $\langle \text{Skip} \vdash n_2 \ -et_2 \rightarrow_p \ n_2' \rangle \ \langle \text{Label } 0 = n_2 \rangle \ \langle \text{Exit} \neq n_2' \rangle$
have *False* **by** (*fastforce* *elim:Proc-CFG.cases*)
thus *?case* **by** *simp*

next

case (*Proc-CFG-LAss* $V \ e$)
from $\langle V := e \vdash n_2 \ -et_2 \rightarrow_p \ n_2' \rangle \ \langle \text{Label } 0 = n_2 \rangle \ \langle \text{Label } 1 \neq n_2' \rangle$
have *False* **by** $\langle \text{erule } \text{Proc-CFG.cases,auto} \rangle$
thus *?case* **by** *simp*

next

case (*Proc-CFG-LAssSkip* $V \ e$)
from $\langle V := e \vdash n_2 \ -et_2 \rightarrow_p \ n_2' \rangle \ \langle \text{Label } 1 = n_2 \rangle \ \langle \text{Exit} \neq n_2' \rangle$
have *False* **by** $\langle \text{erule } \text{Proc-CFG.cases,auto} \rangle$
thus *?case* **by** *simp*

next
case (*Proc-CFG-SeqFirst* c_1 n et n' c_2)
note $IH = \langle \bigwedge n_2 n_2'. \llbracket c_1 \vdash n_2 -et_2 \rightarrow_p n_2'; n = n_2; n' \neq n_2' \rrbracket$
 $\implies \exists Q Q'. et = IEdge(Q)_{\surd} \wedge et_2 = IEdge(Q')_{\surd} \wedge$
 $(\forall s. (Q s \longrightarrow \neg Q' s) \wedge (Q' s \longrightarrow \neg Q s)) \rangle$
from $\langle c_1;; c_2 \vdash n_2 -et_2 \rightarrow_p n_2' \rangle \langle c_1 \vdash n -et \rightarrow_p n' \rangle \langle n = n_2 \rangle \langle n' \neq n_2' \rangle$
have $c_1 \vdash n_2 -et_2 \rightarrow_p n_2' \vee (c_1 \vdash n_2 -et_2 \rightarrow_p Exit \wedge n_2' = Label \# : c_1)$
apply – **apply**(*erule Proc-CFG.cases*)
apply(*auto intro:Proc-CFG.intros*)
by(*case-tac n, auto dest:Proc-CFG-sourcelabel-less-num-nodes*) +
thus ?*case*
proof
assume $c_1 \vdash n_2 -et_2 \rightarrow_p n_2'$
from $IH[OF \text{ this } \langle n = n_2 \rangle \langle n' \neq n_2' \rangle]$ **show** ?*case* .
next
assume $c_1 \vdash n_2 -et_2 \rightarrow_p Exit \wedge n_2' = Label \# : c_1$
hence *edge*: $c_1 \vdash n_2 -et_2 \rightarrow_p Exit$ **and** $n_2' : n_2' = Label \# : c_1$ **by** *simp-all*
from $IH[OF \text{ edge } \langle n = n_2 \rangle \langle n' \neq Exit \rangle]$ **show** ?*case* .
qed
next
case (*Proc-CFG-SeqConnect* c_1 n et c_2)
note $IH = \langle \bigwedge n_2 n_2'. \llbracket c_1 \vdash n_2 -et_2 \rightarrow_p n_2'; n = n_2; Exit \neq n_2' \rrbracket$
 $\implies \exists Q Q'. et = IEdge(Q)_{\surd} \wedge et_2 = IEdge(Q')_{\surd} \wedge$
 $(\forall s. (Q s \longrightarrow \neg Q' s) \wedge (Q' s \longrightarrow \neg Q s)) \rangle$
from $\langle c_1;; c_2 \vdash n_2 -et_2 \rightarrow_p n_2' \rangle \langle c_1 \vdash n -et \rightarrow_p Exit \rangle \langle n = n_2 \rangle \langle n \neq Entry \rangle$
 $\langle Label \# : c_1 \neq n_2' \rangle$ **have** $c_1 \vdash n_2 -et_2 \rightarrow_p n_2' \wedge Exit \neq n_2'$
apply – **apply**(*erule Proc-CFG.cases*)
apply(*auto intro:Proc-CFG.intros*)
by(*case-tac n, auto dest:Proc-CFG-sourcelabel-less-num-nodes*) +
from $IH[OF \text{ this } [THEN \text{ conjunct1}] \langle n = n_2 \rangle \text{ this } [THEN \text{ conjunct2}]]$
show ?*case* .
next
case (*Proc-CFG-SeqSecond* c_2 n et n' c_1)
note $IH = \langle \bigwedge n_2 n_2'. \llbracket c_2 \vdash n_2 -et_2 \rightarrow_p n_2'; n = n_2; n' \neq n_2' \rrbracket$
 $\implies \exists Q Q'. et = IEdge(Q)_{\surd} \wedge et_2 = IEdge(Q')_{\surd} \wedge$
 $(\forall s. (Q s \longrightarrow \neg Q' s) \wedge (Q' s \longrightarrow \neg Q s)) \rangle$
from $\langle c_1;; c_2 \vdash n_2 -et_2 \rightarrow_p n_2' \rangle \langle c_2 \vdash n -et \rightarrow_p n' \rangle \langle n \oplus \# : c_1 = n_2 \rangle$
 $\langle n' \oplus \# : c_1 \neq n_2' \rangle \langle n \neq Entry \rangle$
obtain nx **where** $c_2 \vdash n -et_2 \rightarrow_p nx \wedge nx \oplus \# : c_1 = n_2'$
apply – **apply**(*erule Proc-CFG.cases*)
apply(*auto intro:Proc-CFG.intros*)
apply(*cases n, auto dest:Proc-CFG-sourcelabel-less-num-nodes*)
apply(*cases n, auto dest:Proc-CFG-sourcelabel-less-num-nodes*)
by(*fastforce dest:label-incr-inj*)
with $\langle n' \oplus \# : c_1 \neq n_2' \rangle$ **have** *edge*: $c_2 \vdash n -et_2 \rightarrow_p nx$ **and** *neq*: $n' \neq nx$
by *auto*
from $IH[OF \text{ edge - neq}]$ **show** ?*case* **by** *simp*
next
case (*Proc-CFG-CondTrue* b c_1 c_2)

```

from ⟨if (b) c1 else c2 ⊢ n2 -et2→p n2'⟩ ⟨Label 0 = n2⟩ ⟨Label 1 ≠ n2'⟩
show ?case by -(erule Proc-CFG.cases,auto)
next
case (Proc-CFG-CondFalse b c1 c2)
from ⟨if (b) c1 else c2 ⊢ n2 -et2→p n2'⟩ ⟨Label 0 = n2⟩ ⟨Label (#:c1 + 1) ≠
n2'⟩
show ?case by -(erule Proc-CFG.cases,auto)
next
case (Proc-CFG-CondThen c1 n et n' b c2)
note IH = ⟨∧ n2 n2'. ⟦c1 ⊢ n2 -et2→p n2'; n = n2; n' ≠ n2'⟧
⇒ ∃ Q Q'. et = IEdge (Q)✓ ∧ et2 = IEdge (Q')✓ ∧
(∀ s. (Q s → ¬ Q' s) ∧ (Q' s → ¬ Q s))
from ⟨if (b) c1 else c2 ⊢ n2 -et2→p n2'⟩ ⟨c1 ⊢ n -et→p n'⟩ ⟨n ≠ Entry⟩
⟨n ⊕ 1 = n2⟩ ⟨n' ⊕ 1 ≠ n2'⟩
obtain nx where c1 ⊢ n -et2→p nx ∧ n' ≠ nx
apply - apply (erule Proc-CFG.cases)
apply (auto intro:Proc-CFG.intros simp del:One-nat-def)
apply (drule label-incr-inj) apply (auto simp del:One-nat-def)
apply (drule label-incr-simp-rev[OF sym])
by (case-tac na,auto dest:Proc-CFG-sourcelabel-less-num-nodes)
from IH[OF this[THEN conjunct1]] - this[THEN conjunct2]] show ?case by
simp
next
case (Proc-CFG-CondElse c2 n et n' b c1)
note IH = ⟨∧ n2 n2'. ⟦c2 ⊢ n2 -et2→p n2'; n = n2; n' ≠ n2'⟧
⇒ ∃ Q Q'. et = IEdge (Q)✓ ∧ et2 = IEdge (Q')✓ ∧
(∀ s. (Q s → ¬ Q' s) ∧ (Q' s → ¬ Q s))
from ⟨if (b) c1 else c2 ⊢ n2 -et2→p n2'⟩ ⟨c2 ⊢ n -et→p n'⟩ ⟨n ≠ Entry⟩
⟨n ⊕ #:c1 + 1 = n2⟩ ⟨n' ⊕ #:c1 + 1 ≠ n2'⟩
obtain nx where c2 ⊢ n -et2→p nx ∧ n' ≠ nx
apply - apply (erule Proc-CFG.cases)
apply (auto intro:Proc-CFG.intros simp del:One-nat-def)
apply (drule label-incr-simp-rev)
apply (case-tac na,auto,cases n,auto dest:Proc-CFG-sourcelabel-less-num-nodes)
by (fastforce dest:label-incr-inj)
from IH[OF this[THEN conjunct1]] - this[THEN conjunct2]] show ?case by
simp
next
case (Proc-CFG-WhileTrue b c')
from ⟨while (b) c' ⊢ n2 -et2→p n2'⟩ ⟨Label 0 = n2⟩ ⟨Label 2 ≠ n2'⟩
show ?case by -(erule Proc-CFG.cases,auto)
next
case (Proc-CFG-WhileFalse b c')
from ⟨while (b) c' ⊢ n2 -et2→p n2'⟩ ⟨Label 0 = n2⟩ ⟨Label 1 ≠ n2'⟩
show ?case by -(erule Proc-CFG.cases,auto)
next
case (Proc-CFG-WhileFalseSkip b c')
from ⟨while (b) c' ⊢ n2 -et2→p n2'⟩ ⟨Label 1 = n2⟩ ⟨Exit ≠ n2'⟩
show ?case by -(erule Proc-CFG.cases,auto dest:label-incr-ge)

```

```

next
  case (Proc-CFG-WhileBody  $c' n et n' b$ )
  note  $IH = \langle \bigwedge n_2 n_2'. \llbracket c' \vdash n_2 -et_2 \rightarrow_p n_2'; n = n_2; n' \neq n_2 \rrbracket$ 
     $\implies \exists Q Q'. et = IEdge(Q)_{\surd} \wedge et_2 = IEdge(Q')_{\surd} \wedge$ 
     $(\forall s. (Q s \longrightarrow \neg Q' s) \wedge (Q' s \longrightarrow \neg Q s)) \rangle$ 
  from  $\langle while(b) c' \vdash n_2 -et_2 \rightarrow_p n_2' \rangle \langle c' \vdash n -et \rightarrow_p n' \rangle \langle n \neq Entry \rangle$ 
   $\langle n' \neq Exit \rangle \langle n \oplus 2 = n_2 \rangle \langle n' \oplus 2 \neq n_2' \rangle$ 
  obtain  $nx$  where  $c' \vdash n -et_2 \rightarrow_p nx \wedge n' \neq nx$ 
  apply – apply(erule Proc-CFG.cases)
  apply(auto intro:Proc-CFG.intros)
  apply(fastforce dest:label-incr-ge[OF sym])
  apply(fastforce dest:label-incr-inj)
  by(fastforce dest:label-incr-inj)
  from  $IH[OF this[THEN conjunct1]] - this[THEN conjunct2]$  show ?case by
simp
next
  case (Proc-CFG-WhileBodyExit  $c' n et b$ )
  note  $IH = \langle \bigwedge n_2 n_2'. \llbracket c' \vdash n_2 -et_2 \rightarrow_p n_2'; n = n_2; Exit \neq n_2' \rrbracket$ 
     $\implies \exists Q Q'. et = IEdge(Q)_{\surd} \wedge et_2 = IEdge(Q')_{\surd} \wedge$ 
     $(\forall s. (Q s \longrightarrow \neg Q' s) \wedge (Q' s \longrightarrow \neg Q s)) \rangle$ 
  from  $\langle while(b) c' \vdash n_2 -et_2 \rightarrow_p n_2' \rangle \langle c' \vdash n -et \rightarrow_p Exit \rangle \langle n \neq Entry \rangle$ 
   $\langle n \oplus 2 = n_2 \rangle \langle Label\ 0 \neq n_2' \rangle$ 
  obtain  $nx$  where  $c' \vdash n -et_2 \rightarrow_p nx \wedge Exit \neq nx$ 
  apply – apply(erule Proc-CFG.cases)
  apply(auto intro:Proc-CFG.intros)
  apply(fastforce dest:label-incr-ge[OF sym])
  by(fastforce dest:label-incr-inj)
  from  $IH[OF this[THEN conjunct1]] - this[THEN conjunct2]$  show ?case by
simp
next
  case Proc-CFG-Call thus ?case by –(erule Proc-CFG.cases,auto)
next
  case Proc-CFG-CallSkip thus ?case by –(erule Proc-CFG.cases,auto)
qed

```

2.3.2 And now: the interprocedural CFG

Statements containing calls

A procedure is a tuple composed of its name, its input and output variables and its method body

type-synonym $proc = (pname \times vname\ list \times vname\ list \times cmd)$

type-synonym $procs = proc\ list$

containsCall guarantees that a call to procedure p is in a certain statement.

declare *conj-cong*[*fundef-cong*]

function *containsCall* ::

$procs \Rightarrow cmd \Rightarrow pname\ list \Rightarrow pname \Rightarrow bool$

where $\text{containsCall procs Skip ps } p \longleftrightarrow \text{False}$
 $\mid \text{containsCall procs } (V := e) \text{ ps } p \longleftrightarrow \text{False}$
 $\mid \text{containsCall procs } (c_1 ;; c_2) \text{ ps } p \longleftrightarrow$
 $\quad \text{containsCall procs } c_1 \text{ ps } p \vee \text{containsCall procs } c_2 \text{ ps } p$
 $\mid \text{containsCall procs } (\text{if } (b) \text{ } c_1 \text{ else } c_2) \text{ ps } p \longleftrightarrow$
 $\quad \text{containsCall procs } c_1 \text{ ps } p \vee \text{containsCall procs } c_2 \text{ ps } p$
 $\mid \text{containsCall procs } (\text{while } (b) \text{ } c) \text{ ps } p \longleftrightarrow$
 $\quad \text{containsCall procs } c \text{ ps } p$
 $\mid \text{containsCall procs } (\text{Call } q \text{ es' rets'}) \text{ ps } p \longleftrightarrow p = q \wedge \text{ps} = [] \vee$
 $\quad (\exists \text{ins outs } c \text{ ps}'. \text{ps} = q \# \text{ps}' \wedge (q, \text{ins}, \text{outs}, c) \in \text{set procs} \wedge$
 $\quad \text{containsCall procs } c \text{ ps}' p)$

by *pat-completeness auto*

termination *containsCall*

by (*relation measures* $[\lambda(\text{procs}, c, \text{ps}, p). \text{length } \text{ps},$
 $\lambda(\text{procs}, c, \text{ps}, p). \text{size } c])$ *auto*

lemmas $\text{containsCall-induct}[\text{case-names Skip LAss Seq Cond While Call}] =$
 $\text{containsCall.induct}$

lemma *containsCallcases:*

$\text{containsCall procs prog ps } p$
 $\implies \text{ps} = [] \wedge \text{containsCall procs prog ps } p \vee$
 $(\exists q \text{ ins outs } c \text{ ps}'. \text{ps} = \text{ps}' @ [q] \wedge (q, \text{ins}, \text{outs}, c) \in \text{set procs} \wedge$
 $\text{containsCall procs } c [] p \wedge \text{containsCall procs prog ps}' q)$

proof (*induct procs prog ps p rule:containsCall-induct*)

case ($\text{Call procs } q \text{ es' rets}' \text{ ps } p$)

note $IH = \langle \bigwedge x \ y \ z \ \text{ps}'. [\text{ps} = q \# \text{ps}'; (q, x, y, z) \in \text{set procs};$
 $\text{containsCall procs } z \ \text{ps}' \ p]$

$\implies \text{ps}' = [] \wedge \text{containsCall procs } z \ \text{ps}' \ p \vee$
 $(\exists qx \ \text{ins} \ \text{outs} \ c \ \text{psx}. \text{ps}' = \text{psx} @ [qx] \wedge (qx, \text{ins}, \text{outs}, c) \in \text{set procs} \wedge$
 $\text{containsCall procs } c [] p \wedge$
 $\text{containsCall procs } z \ \text{psx} \ qx)$

from $\langle \text{containsCall procs } (\text{Call } q \text{ es' rets}') \text{ ps } p$

have $p = q \wedge \text{ps} = [] \vee$

$(\exists \text{ins} \ \text{outs} \ c \ \text{ps}'. \text{ps} = q \# \text{ps}' \wedge (q, \text{ins}, \text{outs}, c) \in \text{set procs} \wedge$
 $\text{containsCall procs } c \ \text{ps}' \ p)$ **by** *simp*

thus *?case*

proof

assume $\text{assms}: p = q \wedge \text{ps} = []$

hence $\text{containsCall procs } (\text{Call } q \text{ es' rets}') \text{ ps } p$ **by** *simp*

with assms **show** *?thesis* **by** *simp*

next

assume $\exists \text{ins} \ \text{outs} \ c \ \text{ps}'. \text{ps} = q \# \text{ps}' \wedge (q, \text{ins}, \text{outs}, c) \in \text{set procs} \wedge$
 $\text{containsCall procs } c \ \text{ps}' \ p$

then obtain $\text{ins} \ \text{outs} \ c \ \text{ps}'$ **where** $\text{ps} = q \# \text{ps}'$ **and** $(q, \text{ins}, \text{outs}, c) \in \text{set procs}$

and $\text{containsCall procs } c \ \text{ps}' \ p$ **by** *blast*

from $IH[OF \ \text{this}]$ **have** $\text{ps}' = [] \wedge \text{containsCall procs } c \ \text{ps}' \ p \vee$

$(\exists qx\ insx\ outsx\ cx\ psx.$
 $ps' = psx @ [qx] \wedge (qx, insx, outsx, cx) \in set\ procs \wedge$
 $containsCall\ procs\ cx \ []\ p \wedge containsCall\ procs\ c\ psx\ qx) .$
thus *?thesis*
proof
assume $assms: ps' = [] \wedge containsCall\ procs\ c\ ps'\ p$
have $containsCall\ procs\ (Call\ q\ es'\ rets') \ []\ q$ **by** *simp*
with $assms\ \langle ps = q\#\ ps' \rangle \langle (q, ins, outs, c) \in set\ procs \rangle$ **show** *?thesis* **by** *fastforce*
next
assume $\exists qx\ insx\ outsx\ cx\ psx.$
 $ps' = psx @ [qx] \wedge (qx, insx, outsx, cx) \in set\ procs \wedge$
 $containsCall\ procs\ cx \ []\ p \wedge containsCall\ procs\ c\ psx\ qx$
then obtain $qx\ insx\ outsx\ cx\ psx$
where $ps' = psx @ [qx]$ **and** $(qx, insx, outsx, cx) \in set\ procs$
and $containsCall\ procs\ cx \ []\ p$
and $containsCall\ procs\ c\ psx\ qx$ **by** *blast*
from $\langle (q, ins, outs, c) \in set\ procs \rangle \langle containsCall\ procs\ c\ psx\ qx \rangle$
have $containsCall\ procs\ (Call\ q\ es'\ rets')\ (q\#\ psx)\ qx$ **by** *fastforce*
with $\langle ps' = psx @ [qx] \rangle \langle ps = q\#\ ps' \rangle \langle (qx, insx, outsx, cx) \in set\ procs \rangle$
 $\langle containsCall\ procs\ cx \ []\ p \rangle$ **show** *?thesis* **by** *fastforce*
qed
qed
qed *auto*

lemma *containsCallE*:

$\llbracket containsCall\ procs\ prog\ ps\ p \rrbracket$
 $\llbracket ps = [] ; containsCall\ procs\ prog\ ps\ p \rrbracket \implies P\ procs\ prog\ ps\ p ;$
 $\wedge q\ ins\ outs\ c\ es'\ rets'\ ps'. \llbracket ps = ps' @ [q] ; (q, ins, outs, c) \in set\ procs ;$
 $containsCall\ procs\ c \ []\ p ; containsCall\ procs\ prog\ ps'\ q \rrbracket$
 $\implies P\ procs\ prog\ ps\ p \rrbracket \implies P\ procs\ prog\ ps\ p$
by (*auto dest:containsCallcases*)

lemma *containsCall-in-proc*:

$\llbracket containsCall\ procs\ prog\ qs\ q ; (q, ins, outs, c) \in set\ procs ;$
 $containsCall\ procs\ c \ []\ p \rrbracket$
 $\implies containsCall\ procs\ prog\ (qs @ [q])\ p$
proof (*induct procs prog qs q rule:containsCall-induct*)
case $(Call\ procs\ qx\ esx\ retsx\ ps\ p')$
note $IH = \langle \wedge x\ y\ z\ psx. \llbracket ps = qx\#\ psx ; (qx, x, y, z) \in set\ procs ;$
 $containsCall\ procs\ z\ psx\ p' ; (p', ins, outs, c) \in set\ procs ;$
 $containsCall\ procs\ c \ []\ p \rrbracket \implies containsCall\ procs\ z\ (psx @ [p'])\ p \rangle$
from $\langle containsCall\ procs\ (Call\ qx\ esx\ retsx)\ ps\ p' \rangle$
have $p' = qx \wedge ps = [] \vee$
 $(\exists insx\ outsx\ cx\ psx. ps = qx\#\ psx \wedge (qx, insx, outsx, cx) \in set\ procs \wedge$
 $containsCall\ procs\ cx\ psx\ p')$ **by** *simp*
thus *?case*

proof
assume $assms:p' = qx \wedge ps = []$
with $\langle(p', ins, outs, c) \in set\ procs\rangle \langle containsCall\ procs\ c\ []\ p\rangle$
have $containsCall\ procs\ (Call\ qx\ esx\ retsx)\ [p']\ p$ **by** *fastforce*
with *assms* **show** *?thesis* **by** *simp*
next
assume $\exists insx\ outsx\ cx\ psx. ps = qx\#\psx \wedge (qx,insx,outsx,cx) \in set\ procs \wedge$
 $containsCall\ procs\ cx\ psx\ p'$
then obtain $insx\ outsx\ cx\ psx$ **where** $ps = qx\#\psx$
and $(qx,insx,outsx,cx) \in set\ procs$
and $containsCall\ procs\ cx\ psx\ p'$ **by** *blast*
from $IH[OF\ this\ \langle(p', ins, outs, c) \in set\ procs\rangle$
 $\langle containsCall\ procs\ c\ []\ p\rangle]$
have $containsCall\ procs\ cx\ (psx\ @\ [p'])\ p$.
with $\langle ps = qx\#\psx\rangle \langle (qx,insx,outsx,cx) \in set\ procs\rangle$
show *?thesis* **by** *fastforce*
qed
qed *auto*

lemma *containsCall-indirection:*

$\llbracket containsCall\ procs\ prog\ qs\ q; containsCall\ procs\ c\ ps\ p;$
 $(q,ins,outs,c) \in set\ procs \rrbracket$
 $\implies containsCall\ procs\ prog\ (qs@q\#\ps)\ p$
proof(*induct procs prog qs q rule:containsCall-induct*)
case $(Call\ procs\ px\ esx\ retsx\ ps'\ p')$
note $IH = \langle \bigwedge x\ y\ z\ psx. \llbracket ps' = px\ \#\ psx; (px, x, y, z) \in set\ procs;$
 $containsCall\ procs\ z\ psx\ p'; containsCall\ procs\ c\ ps\ p;$
 $(p', ins, outs, c) \in set\ procs \rrbracket$
 $\implies containsCall\ procs\ z\ (psx\ @\ p'\ \#\ ps)\ p)$
from $\langle containsCall\ procs\ (Call\ px\ esx\ retsx)\ ps'\ p'\rangle$
have $p' = px \wedge ps' = [] \vee$
 $(\exists insx\ outsx\ cx\ psx. ps' = px\#\psx \wedge (px,insx,outsx,cx) \in set\ procs \wedge$
 $containsCall\ procs\ cx\ psx\ p')$ **by** *simp*
thus *?case*
proof
assume $p' = px \wedge ps' = []$
with $\langle containsCall\ procs\ c\ ps\ p\rangle \langle(p', ins, outs, c) \in set\ procs\rangle$
show *?thesis* **by** *fastforce*
next
assume $\exists insx\ outsx\ cx\ psx. ps' = px\#\psx \wedge (px,insx,outsx,cx) \in set\ procs \wedge$
 $containsCall\ procs\ cx\ psx\ p'$
then obtain $insx\ outsx\ cx\ psx$ **where** $ps' = px\#\psx$
and $(px,insx,outsx,cx) \in set\ procs$
and $containsCall\ procs\ cx\ psx\ p'$ **by** *blast*
from $IH[OF\ this\ \langle containsCall\ procs\ c\ ps\ p\rangle$
 $\langle(p', ins, outs, c) \in set\ procs\rangle]$
have $containsCall\ procs\ cx\ (psx\ @\ p'\ \#\ ps)\ p$.
with $\langle ps' = px\#\psx\rangle \langle (px,insx,outsx,cx) \in set\ procs\rangle$

```

  show ?thesis by fastforce
qed
qed auto

```

```

lemma Proc-CFG-Call-containsCall:
  prog ⊢ n -CEdge (p,es,rets)→p n' ⇒ containsCall procs prog [] p
by(induct prog n et≡CEdge (p,es,rets) n' rule:Proc-CFG.induct,auto)

```

```

lemma containsCall-empty-Proc-CFG-Call-edge:
  assumes containsCall procs prog [] p
  obtains l es rets l' where prog ⊢ Label l -CEdge (p,es,rets)→p Label l'
proof(atomize-elim)
  from ⟨containsCall procs prog [] p⟩
  show ∃ l es rets l'. prog ⊢ Label l -CEdge (p,es,rets)→p Label l'
  proof(induct procs prog ps≡[]::pname list p rule:containsCall-induct)
    case Seq thus ?case
    by auto(fastforce dest:Proc-CFG-SeqFirst,fastforce dest:Proc-CFG-SeqSecond)
  next
    case Cond thus ?case
    by auto(fastforce dest:Proc-CFG-CondThen,fastforce dest:Proc-CFG-CondElse)
  next
    case While thus ?case by(fastforce dest:Proc-CFG-WhileBody)
  next
    case Call thus ?case by(fastforce intro:Proc-CFG-Call)
  qed auto
qed

```

The edges of the combined CFG

```

type-synonym node = (pname × label)
type-synonym edge = (node × (vname,val,node,pname) edge-kind × node)

```

```

fun get-proc :: node ⇒ pname
  where get-proc (p,l) = p

```

```

inductive PCFG ::
  cmd ⇒ procs ⇒ node ⇒ (vname,val,node,pname) edge-kind ⇒ node ⇒ bool
  (-, - ⊢ - ->- [51,51,0,0,0] 81)
for prog::cmd and procs::procs
where

```

```

  Main:
  prog ⊢ n -IEdge et→p n' ⇒ prog,procs ⊢ (Main,n) -et→ (Main,n')

```

```

| Proc:
  [(p,ins,outs,c) ∈ set procs; c ⊢ n -IEdge et→p n'];

```

$\text{containsCall } \text{procs } \text{prog } \text{ps } p \llbracket$
 $\implies \text{prog}, \text{procs} \vdash (p, n) \text{ --et--} \rightarrow (p, n')$

| *MainCall*:

$\llbracket \text{prog} \vdash \text{Label } l \text{ --CEdge } (p, \text{es}, \text{rets}) \rightarrow_p n'; (p, \text{ins}, \text{outs}, c) \in \text{set } \text{procs} \rrbracket$
 $\implies \text{prog}, \text{procs} \vdash (\text{Main}, \text{Label } l)$
 $\quad \text{--}(\lambda s. \text{True}):(\text{Main}, n') \hookrightarrow_p \text{map } (\lambda e \text{ cf. } \text{interpret } e \text{ cf}) \text{ es} \rightarrow (p, \text{Entry})$

| *ProcCall*:

$\llbracket (p, \text{ins}, \text{outs}, c) \in \text{set } \text{procs}; c \vdash \text{Label } l \text{ --CEdge } (p', \text{es}', \text{rets}') \rightarrow_p \text{Label } l';$
 $\quad (p', \text{ins}', \text{outs}', c') \in \text{set } \text{procs}; \text{containsCall } \text{procs } \text{prog } \text{ps } p \rrbracket$
 $\implies \text{prog}, \text{procs} \vdash (p, \text{Label } l)$
 $\quad \text{--}(\lambda s. \text{True}): (p, \text{Label } l') \hookrightarrow_p \text{map } (\lambda e \text{ cf. } \text{interpret } e \text{ cf}) \text{ es}' \rightarrow (p', \text{Entry})$

| *MainReturn*:

$\llbracket \text{prog} \vdash \text{Label } l \text{ --CEdge } (p, \text{es}, \text{rets}) \rightarrow_p \text{Label } l'; (p, \text{ins}, \text{outs}, c) \in \text{set } \text{procs} \rrbracket$
 $\implies \text{prog}, \text{procs} \vdash (p, \text{Exit}) \text{ --}(\lambda \text{cf. } \text{snd } \text{cf} = (\text{Main}, \text{Label } l')) \hookrightarrow_p$
 $\quad (\lambda \text{cf } \text{cf}'. \text{cf}'(\text{rets } [:=] \text{map } \text{cf } \text{outs})) \rightarrow (\text{Main}, \text{Label } l')$

| *ProcReturn*:

$\llbracket (p, \text{ins}, \text{outs}, c) \in \text{set } \text{procs}; c \vdash \text{Label } l \text{ --CEdge } (p', \text{es}', \text{rets}') \rightarrow_p \text{Label } l';$
 $\quad (p', \text{ins}', \text{outs}', c') \in \text{set } \text{procs}; \text{containsCall } \text{procs } \text{prog } \text{ps } p \rrbracket$
 $\implies \text{prog}, \text{procs} \vdash (p', \text{Exit}) \text{ --}(\lambda \text{cf. } \text{snd } \text{cf} = (p, \text{Label } l')) \hookrightarrow_{p'}$
 $\quad (\lambda \text{cf } \text{cf}'. \text{cf}'(\text{rets}' [:=] \text{map } \text{cf } \text{outs}')) \rightarrow (p, \text{Label } l')$

| *MainCallReturn*:

$\text{prog} \vdash n \text{ --CEdge } (p, \text{es}, \text{rets}) \rightarrow_p n'$
 $\implies \text{prog}, \text{procs} \vdash (\text{Main}, n) \text{ --}(\lambda s. \text{False})_{\surd} \rightarrow (\text{Main}, n')$

| *ProcCallReturn*:

$\llbracket (p, \text{ins}, \text{outs}, c) \in \text{set } \text{procs}; c \vdash n \text{ --CEdge } (p', \text{es}', \text{rets}') \rightarrow_p n';$
 $\quad \text{containsCall } \text{procs } \text{prog } \text{ps } p \rrbracket$
 $\implies \text{prog}, \text{procs} \vdash (p, n) \text{ --}(\lambda s. \text{False})_{\surd} \rightarrow (p, n')$

end

2.4 Well-formedness of programs

theory *WellFormProgs* imports *PCFG* begin

2.4.1 Well-formedness of procedure lists.

definition *wf-proc* :: *proc* \Rightarrow *bool*

where *wf-proc* *x* \equiv *let* (*p*, *ins*, *outs*, *c*) = *x* *in*
p \neq *Main* \wedge *distinct ins* \wedge *distinct outs*

definition *well-formed* :: *procs* \Rightarrow *bool*
where *well-formed procs* \equiv *distinct-fst procs* \wedge
 $(\forall (p,ins,outs,c) \in \text{set procs}. \text{wf-proc } (p,ins,outs,c))$

lemma [*dest*]: $\llbracket \text{well-formed procs}; (Main,ins,outs,c) \in \text{set procs} \rrbracket \Longrightarrow \text{False}$
by(*fastforce simp:well-formed-def wf-proc-def*)

lemma *well-formed-same-procs* [*dest*]:
 $\llbracket \text{well-formed procs}; (p,ins,outs,c) \in \text{set procs}; (p,ins',outs',c') \in \text{set procs} \rrbracket$
 $\Longrightarrow ins = ins' \wedge outs = outs' \wedge c = c'$
apply(*auto simp:well-formed-def distinct-fst-def distinct-map inj-on-def*)
by(*erule-tac x=(p,ins,outs,c) in ballE,auto*) $+$

lemma *PCFG-sourcelabel-None-less-num-nodes*:
 $\llbracket \text{prog,procs} \vdash (Main,Label\ l) -et \rightarrow n'; \text{well-formed procs} \rrbracket \Longrightarrow l < \#: \text{prog}$
proof(*induct (Main,Label\ l) et n'*
arbitrary:l rule:PCFG.induct)
case (*Main et n'*)
from $\langle \text{prog} \vdash Label\ l -IEdge\ et \rightarrow_p\ n' \rangle$
show $?case$ **by**(*fastforce elim:Proc-CFG-sourcelabel-less-num-nodes*)
next
case (*MainCall l p es rets n' ins outs c*)
from $\langle \text{prog} \vdash Label\ l -CEdge\ (p,es,rets) \rightarrow_p\ n' \rangle$
show $?case$ **by**(*fastforce elim:Proc-CFG-sourcelabel-less-num-nodes*)
next
case (*MainCallReturn p es rets n' l*)
from $\langle \text{prog} \vdash Label\ l -CEdge\ (p, es, rets) \rightarrow_p\ n' \rangle$
show $?case$ **by**(*fastforce elim:Proc-CFG-sourcelabel-less-num-nodes*)
qed *auto*

lemma *Proc-CFG-sourcelabel-Some-less-num-nodes*:
 $\llbracket \text{prog,procs} \vdash (p,Label\ l) -et \rightarrow n'; (p,ins,outs,c) \in \text{set procs};$
well-formed procs $\rrbracket \Longrightarrow l < \#:c$
proof(*induct (p,Label\ l) et n' arbitrary:l rule:PCFG.induct*)
case (*Proc ins' outs' c' et n'*)
from $\langle c' \vdash Label\ l -IEdge\ et \rightarrow_p\ n' \rangle$ **have** $l < \#:c'$
by(*fastforce intro:Proc-CFG-sourcelabel-less-num-nodes*)
with $\langle \text{well-formed procs} \rangle \langle (p,ins,outs,c) \in \text{set procs} \rangle$
 $\langle (p,ins',outs',c') \in \text{set procs} \rangle$
show $?case$ **by** *fastforce*
next
case (*ProcCall ins' outs' c' l' p' es rets l'' ins'' outs'' c'' ps*)
from $\langle c' \vdash Label\ l' -CEdge\ (p',es,rets) \rightarrow_p\ Label\ l'' \rangle$ **have** $l' < \#:c'$
by(*fastforce intro:Proc-CFG-sourcelabel-less-num-nodes*)
with $\langle \text{well-formed procs} \rangle \langle (p,ins,outs,c) \in \text{set procs} \rangle$
 $\langle (p, ins', outs', c') \in \text{set procs} \rangle$
show $?case$ **by** *fastforce*
next

case (*ProcCallReturn* $ins' outs' c' p' es rets n'$)
from $\langle c' \vdash Label\ l - CEdge\ (p', es, rets) \rightarrow_p n' \rangle$ **have** $l < \#:c'$
by(*fastforce intro:Proc-CFG-sourcelabel-less-num-nodes*)
with $\langle well\text{-formed}\ procs \rangle \langle (p, ins, outs, c) \in set\ procs \rangle$
 $\langle (p, ins', outs', c') \in set\ procs \rangle$
show ?*case* **by** *fastforce*
qed *auto*

lemma *Proc-CFG-targetlabel-Main-less-num-nodes*:
 $\llbracket prog, procs \vdash n - et \rightarrow (Main, Label\ l); well\text{-formed}\ procs \rrbracket \implies l < \#:prog$
proof(*induct n et (Main, Label l)*)
arbitrary:l rule:PCFG.induct
case (*Main n et*)
from $\langle prog \vdash n - IEdge\ et \rightarrow_p Label\ l \rangle$
show ?*case* **by**(*fastforce elim:Proc-CFG-targetlabel-less-num-nodes*)
next
case (*MainReturn l' p es rets l'' ins outs c*)
from $\langle prog \vdash Label\ l' - CEdge\ (p, es, rets) \rightarrow_p Label\ l'' \rangle$
show ?*case* **by**(*fastforce elim:Proc-CFG-targetlabel-less-num-nodes*)
next
case (*MainCallReturn n p es rets*)
from $\langle prog \vdash n - CEdge\ (p, es, rets) \rightarrow_p Label\ l \rangle$
show ?*case* **by**(*fastforce elim:Proc-CFG-targetlabel-less-num-nodes*)
qed *auto*

lemma *Proc-CFG-targetlabel-Some-less-num-nodes*:
 $\llbracket prog, procs \vdash n - et \rightarrow (p, Label\ l); (p, ins, outs, c) \in set\ procs; well\text{-formed}\ procs \rrbracket \implies l < \#:c$
proof(*induct n et (p, Label l) arbitrary:l rule:PCFG.induct*)
case (*Proc ins' outs' c' n et*)
from $\langle c' \vdash n - IEdge\ et \rightarrow_p Label\ l \rangle$ **have** $l < \#:c'$
by(*fastforce intro:Proc-CFG-targetlabel-less-num-nodes*)
with $\langle well\text{-formed}\ procs \rangle \langle (p, ins, outs, c) \in set\ procs \rangle$
 $\langle (p, ins', outs', c') \in set\ procs \rangle$
show ?*case* **by** *fastforce*
next
case (*ProcReturn ins' outs' c' l' p' es rets l ins'' outs'' c'' ps*)
from $\langle c' \vdash Label\ l' - CEdge\ (p', es, rets) \rightarrow_p Label\ l \rangle$ **have** $l < \#:c'$
by(*fastforce intro:Proc-CFG-targetlabel-less-num-nodes*)
with $\langle well\text{-formed}\ procs \rangle \langle (p, ins, outs, c) \in set\ procs \rangle$
 $\langle (p, ins', outs', c') \in set\ procs \rangle$
show ?*case* **by** *fastforce*
next
case (*ProcCallReturn ins' outs' c' n p'' es rets*)
from $\langle c' \vdash n - CEdge\ (p'', es, rets) \rightarrow_p Label\ l \rangle$ **have** $l < \#:c'$
by(*fastforce intro:Proc-CFG-targetlabel-less-num-nodes*)
with $\langle well\text{-formed}\ procs \rangle \langle (p, ins, outs, c) \in set\ procs \rangle$

$\langle (p, ins', outs', c') \in set\ procs \rangle$
show $?case$ **by** *fastforce*
qed *auto*

lemma *Proc-CFG-edge-det:*

$\llbracket prog, procs \vdash n -et \rightarrow n'; prog, procs \vdash n -et' \rightarrow n'; well\text{-formed}\ procs \rrbracket$
 $\implies et = et'$

proof(*induct rule:PCFG.induct*)

case *Main* **thus** $?case$ **by**(*auto elim:PCFG.cases dest:Proc-CFG-edge-det*)

next

case *Proc* **thus** $?case$ **by**(*auto elim:PCFG.cases dest:Proc-CFG-edge-det*)

next

case (*MainCall* $l\ p\ es\ rets\ n'\ ins\ outs\ c$)

from $\langle prog, procs \vdash (Main, Label\ l) -et' \rightarrow (p, Entry) \rangle \langle well\text{-formed}\ procs \rangle$

obtain $es'\ rets'\ n''\ ins'\ outs'\ c'$

where $prog \vdash Label\ l -CEdge\ (p, es', rets') \rightarrow_p\ n''$

and $\langle (p, ins', outs', c') \in set\ procs \rangle$

and $et' = (\lambda s. True): (Main, n'') \hookrightarrow_p\ map\ (\lambda e\ cf. interpret\ e\ cf)\ es'$

by(*auto elim:PCFG.cases*)

from $\langle (p, ins, outs, c) \in set\ procs \rangle \langle (p, ins', outs', c') \in set\ procs \rangle$

$\langle well\text{-formed}\ procs \rangle$

have $ins = ins'$ **by** *fastforce*

from $\langle prog \vdash Label\ l -CEdge\ (p, es, rets) \rightarrow_p\ n' \rangle$

$\langle prog \vdash Label\ l -CEdge\ (p, es', rets') \rightarrow_p\ n'' \rangle$

have $es = es'$ **and** $n' = n''$ **by**(*auto dest:Proc-CFG-Call-nodes-eq*)

with $\langle et' = (\lambda s. True): (Main, n'') \hookrightarrow_p\ map\ (\lambda e\ cf. interpret\ e\ cf)\ es' \rangle \langle ins = ins' \rangle$

show $?case$ **by** *simp*

next

case (*ProcCall* $p\ ins\ outs\ c\ l\ p'\ es'\ rets'\ l'\ ins'\ outs'\ c'\ ps$)

from $\langle prog, procs \vdash (p, Label\ l) -et' \rightarrow (p', Entry) \rangle \langle (p', ins', outs', c') \in set\ procs \rangle$

$\langle (p, ins, outs, c) \in set\ procs \rangle \langle well\text{-formed}\ procs \rangle$

$\langle c \vdash Label\ l -CEdge\ (p', es', rets') \rightarrow_p\ Label\ l' \rangle$

show $?case$

proof(*induct (p, Label l) et' (p', Entry) rule:PCFG.induct*)

case (*ProcCall* $insx\ outsx\ cx\ es'x\ rets'x\ l'x\ ins'x\ outs'x\ c'x\ ps$)

from $\langle well\text{-formed}\ procs \rangle \langle (p, insx, outsx, cx) \in set\ procs \rangle$

$\langle (p, ins, outs, c) \in set\ procs \rangle$

have [*simp*]: $cx = c$ **by** *auto*

from $\langle cx \vdash Label\ l -CEdge\ (p', es'x, rets'x) \rightarrow_p\ Label\ l'x \rangle$

$\langle c \vdash Label\ l -CEdge\ (p', es', rets') \rightarrow_p\ Label\ l' \rangle$

have [*simp*]: $es'x = es'\ l'x = l'$ **by**(*auto dest:Proc-CFG-Call-nodes-eq*)

show $?case$ **by** *simp*

qed *auto*

next

case *MainReturn*

thus $?case$ **by** $-(erule\ PCFG.cases, auto\ dest:Proc-CFG-Call-nodes-eq')$

next

case (*ProcReturn* $p\ ins\ outs\ c\ l\ p'\ es'\ rets'\ l'\ ins'\ outs'\ c'\ ps$)

```

from ⟨prog,procs ⊢ (p',Exit) -et'→ (p, Label l')⟩
  ⟨(p, ins, outs, c) ∈ set procs⟩ ⟨(p', ins', outs', c') ∈ set procs⟩
  ⟨c ⊢ Label l -CEdge (p', es', rets')→p Label l'⟩
  ⟨containsCall procs prog ps p⟩ ⟨well-formed procs⟩
show ?case
proof(induct (p',Exit) et' (p,Label l') rule:PCFG.induct)
  case (ProcReturn insx outsx cx lx es'x rets'x ins'x outs'x c'x psx)
  from ⟨(p', ins'x, outs'x, c'x) ∈ set procs⟩
    ⟨(p', ins', outs', c') ∈ set procs⟩ ⟨well-formed procs⟩
  have [simp]:outs'x = outs' by fastforce
  from ⟨(p, insx, outsx, cx) ∈ set procs⟩ ⟨(p, ins, outs, c) ∈ set procs⟩
    ⟨well-formed procs⟩
  have [simp]:cx = c by auto
  from ⟨cx ⊢ Label lx -CEdge (p', es'x, rets'x)→p Label l'⟩
    ⟨c ⊢ Label l -CEdge (p', es', rets')→p Label l'⟩
  have [simp]:rets'x = rets' by(fastforce dest:Proc-CFG-Call-nodes-eq')
  show ?case by simp
qed auto
next
case MainCallReturn thus ?case by(auto elim:PCFG.cases dest:Proc-CFG-edge-det)
next
case ProcCallReturn thus ?case by(auto elim:PCFG.cases dest:Proc-CFG-edge-det)
qed

```

lemma Proc-CFG-deterministic:

```

[[prog,procs ⊢ n1 -et1→ n1'; prog,procs ⊢ n2 -et2→ n2'; n1 = n2; n1' ≠ n2';
  intra-kind et1; intra-kind et2; well-formed procs]]
⇒ ∃ Q Q'. et1 = (Q)✓ ∧ et2 = (Q')✓ ∧
  (∀ s. (Q s → ¬ Q' s) ∧ (Q' s → ¬ Q s))

```

proof(induct arbitrary:n₂ n₂' rule:PCFG.induct)

case (Main n et n')

from ⟨prog,procs ⊢ n₂ -et₂→ n₂'⟩ ⟨(Main,n) = n₂⟩

⟨intra-kind et₂⟩ ⟨well-formed procs⟩

obtain m m' **where** (Main,m) = n₂ **and** (Main,m') = n₂'

and disj:prog ⊢ m -IEdge et₂→_p m' ∨

(∃ p es rets. prog ⊢ m -CEdge (p,es,rets)→_p m' ∧ et₂ = (λs. False)_✓)

by(induct rule:PCFG.induct)(fastforce simp:intra-kind-def)+

from disj **show** ?case

proof

assume prog ⊢ m -IEdge et₂→_p m'

with ⟨(Main,m) = n₂⟩ ⟨(Main,m') = n₂'⟩

⟨prog ⊢ n -IEdge et→_p n'⟩ ⟨(Main,n) = n₂⟩ ⟨(Main,n') ≠ n₂'⟩

show ?thesis **by**(auto dest:WCFG-deterministic)

next

assume ∃ p es rets. prog ⊢ m -CEdge (p, es, rets)→_p m' ∧ et₂ = (λs. False)_✓

with ⟨(Main,m) = n₂⟩ ⟨(Main,m') = n₂'⟩

⟨prog ⊢ n -IEdge et→_p n'⟩ ⟨(Main,n) = n₂⟩ ⟨(Main,n') ≠ n₂'⟩

have False **by**(fastforce dest:Proc-CFG-Call-Intra-edge-not-same-source)


```

    thus ?thesis by simp
  qed
next
case (Proc p ins outs c n et n')
from ⟨prog,procs ⊢ n2 -et2→ n2'⟩ ⟨(p,n) = n2⟩ ⟨intra-kind et2⟩
  ⟨(p,ins,outs,c) ∈ set procs⟩ ⟨well-formed procs⟩
obtain m m' where (p,m) = n2 and (p,m') = n2'
  and disj:c ⊢ m -IEdge et2→p m' ∨
  (∃ p' es' rets'. c ⊢ m -CEdge (p',es',rets')→p m' ∧ et2 = (λs. False)√)
  by(induct rule:PCFG.induct)(fastforce simp:intra-kind-def)+
from disj show ?case
proof
  assume c ⊢ m -IEdge et2→p m'
  with ⟨(p,m) = n2⟩ ⟨(p,m') = n2'⟩
    ⟨c ⊢ n -IEdge et→p n'⟩ ⟨(p,n) = n2⟩ ⟨(p,n') ≠ n2'⟩
  show ?thesis by(auto dest:WCFG-deterministic)
next
  assume ∃ p' es' rets'. c ⊢ m -CEdge (p', es', rets')→p m' ∧ et2 = (λs. False)√
  with ⟨(p,m) = n2⟩ ⟨(p,m') = n2'⟩
    ⟨c ⊢ n -IEdge et→p n'⟩ ⟨(p,n) = n2⟩ ⟨(p,n') ≠ n2'⟩
  have False by(fastforce dest:Proc-CFG-Call-Intra-edge-not-same-source)
  thus ?thesis by simp
qed
next
case (MainCallReturn n p es rets n' n2 n2')
from ⟨prog,procs ⊢ n2 -et2→ n2'⟩ ⟨(Main,n) = n2⟩
  ⟨intra-kind et2⟩ ⟨well-formed procs⟩
obtain m m' where (Main,m) = n2 and (Main,m') = n2'
  and disj:prog ⊢ m -IEdge et2→p m' ∨
  (∃ p es rets. prog ⊢ m -CEdge (p,es,rets)→p m' ∧ et2 = (λs. False)√)
  by(induct rule:PCFG.induct)(fastforce simp:intra-kind-def)+
from disj show ?case
proof
  assume prog ⊢ m -IEdge et2→p m'
  with ⟨(Main,m) = n2⟩ ⟨(Main,m') = n2'⟩ ⟨prog ⊢ n -CEdge (p, es, rets)→p
n'⟩
    ⟨(Main, n) = n2⟩ ⟨(Main, n') ≠ n2'⟩
  have False by(fastforce dest:Proc-CFG-Call-Intra-edge-not-same-source)
  thus ?thesis by simp
next
  assume ∃ p es rets. prog ⊢ m -CEdge (p,es,rets)→p m' ∧ et2 = (λs. False)√
  with ⟨(Main,m) = n2⟩ ⟨(Main,m') = n2'⟩ ⟨prog ⊢ n -CEdge (p, es, rets)→p
n'⟩
    ⟨(Main, n) = n2⟩ ⟨(Main, n') ≠ n2'⟩
  show ?thesis by(fastforce dest:Proc-CFG-Call-nodes-eq)
qed
next
case (ProcCallReturn p ins outs c n p' es rets n' ps n2 n2')
from ⟨prog,procs ⊢ n2 -et2→ n2'⟩ ⟨(p,n) = n2⟩ ⟨intra-kind et2⟩

```

$\langle (p, ins, outs, c) \in set\ procs \rangle \langle well\text{-}formed\ procs \rangle$
obtain $m\ m'$ **where** $\langle (p, m) = n_2 \rangle$ **and** $\langle (p, m') = n_2' \rangle$
and $disj : c \vdash m - IEdge\ et_2 \rightarrow_p\ m' \vee$
 $(\exists p'\ es'\ rets'. c \vdash m - CEdge\ (p', es', rets') \rightarrow_p\ m' \wedge et_2 = (\lambda s. False)_{\checkmark})$
by $(induct\ rule : PCFG.induct)(fastforce\ simp : intra\text{-}kind\text{-}def) +$
from $disj$ **show** $?case$
proof
assume $c \vdash m - IEdge\ et_2 \rightarrow_p\ m'$
with $\langle (p, m) = n_2 \rangle \langle (p, m') = n_2' \rangle$
 $\langle c \vdash n - CEdge\ (p', es, rets) \rightarrow_p\ n' \rangle \langle (p, n) = n_2 \rangle \langle (p, n') \neq n_2' \rangle$
have $False$ **by** $(fastforce\ dest : Proc\text{-}CFG\text{-}Call\text{-}Intra\text{-}edge\text{-}not\text{-}same\text{-}source)$
thus $?thesis$ **by** $simp$
next
assume $\exists p'\ es'\ rets'. c \vdash m - CEdge\ (p', es', rets') \rightarrow_p\ m' \wedge et_2 = (\lambda s. False)_{\checkmark}$
with $\langle (p, m) = n_2 \rangle \langle (p, m') = n_2' \rangle$
 $\langle c \vdash n - CEdge\ (p', es, rets) \rightarrow_p\ n' \rangle \langle (p, n) = n_2 \rangle \langle (p, n') \neq n_2' \rangle$
show $?thesis$ **by** $(fastforce\ dest : Proc\text{-}CFG\text{-}Call\text{-}nodes\text{-}eq)$
qed
qed $(auto\ simp : intra\text{-}kind\text{-}def)$

2.4.2 Well-formedness of programs in combination with a procedure list.

definition $wf :: cmd \Rightarrow procs \Rightarrow bool$
where $wf\ prog\ procs \equiv well\text{-}formed\ procs \wedge$
 $(\forall ps\ p. containsCall\ procs\ prog\ ps\ p \longrightarrow (\exists ins\ outs\ c. (p, ins, outs, c) \in set\ procs$
 \wedge
 $(\forall c'\ n\ n'\ es\ rets. c' \vdash n - CEdge\ (p, es, rets) \rightarrow_p\ n' \longrightarrow$
 $distinct\ rets \wedge length\ rets = length\ outs \wedge length\ es = length\ ins)))$

lemma $wf\text{-}well\text{-}formed$ $[intro] : wf\ prog\ procs \Longrightarrow well\text{-}formed\ procs$
by $(simp\ add : wf\text{-}def)$

lemma $wf\text{-}distinct\text{-}rets$ $[intro] :$
 $\llbracket wf\ prog\ procs ; containsCall\ procs\ prog\ ps\ p ; (p, ins, outs, c) \in set\ procs ;$
 $c' \vdash n - CEdge\ (p, es, rets) \rightarrow_p\ n' \rrbracket \Longrightarrow distinct\ rets$
by $(fastforce\ simp : wf\text{-}def)$

lemma
assumes $wf\ prog\ procs$ **and** $containsCall\ procs\ prog\ ps\ p$
and $(p, ins, outs, c) \in set\ procs$ **and** $c' \vdash n - CEdge\ (p, es, rets) \rightarrow_p\ n'$
shows $wf\text{-}length\text{-}retsI$ $[intro] : length\ rets = length\ outs$
and $wf\text{-}length\text{-}esI$ $[intro] : length\ es = length\ ins$
proof –
from $\langle wf\ prog\ procs \rangle$ **have** $well\text{-}formed\ procs$ **by** $fastforce$
from $assms$

```

obtain ins' outs' c' where  $(p, ins', outs', c') \in set\ procs$ 
  and lengths: length rets = length outs' length es = length ins'
  by(simp add:wf-def) blast
from  $\langle (p, ins, outs, c) \in set\ procs \rangle \langle (p, ins', outs', c') \in set\ procs \rangle$ 
   $\langle well\ formed\ procs \rangle$ 
have ins' = ins outs' = outs c' = c by auto
with lengths show length rets = length outs length es = length ins
  by simp-all
qed

```

2.4.3 Type of well-formed programs

definition *wf-prog* = $\{(prog, procs). wf\ prog\ procs\}$

```

typedef wf-prog = wf-prog
  unfolding wf-prog-def
  apply (rule-tac x=(Skip,[])) in exI
  apply (simp add:wf-def well-formed-def)
  done

```

lemma *wf-wf-prog*: *Rep-wf-prog wfp* = $(prog, procs) \implies wf\ prog\ procs$
using *Rep-wf-prog*[*of wfp*] **by**(*simp* *add*:*wf-prog-def*)

lemma *wfp-Seq1*: **assumes** *Rep-wf-prog wfp* = $(c_1;; c_2, procs)$
obtains *wfp'* **where** *Rep-wf-prog wfp'* = $(c_1, procs)$
using $\langle Rep-wf-prog\ wfp = (c_1;; c_2, procs) \rangle$
apply(*cases* *wfp*) **apply**(*auto* *simp*:*Abs-wf-prog-inverse* *wf-prog-def* *wf-def*)
apply(*erule-tac* *x*=*Abs-wf-prog* $(c_1, procs)$) **in** *meta-allE*
by(*auto* *elim*:*meta-mp* *simp*:*Abs-wf-prog-inverse* *wf-prog-def* *wf-def*)

lemma *wfp-Seq2*: **assumes** *Rep-wf-prog wfp* = $(c_1;; c_2, procs)$
obtains *wfp'* **where** *Rep-wf-prog wfp'* = $(c_2, procs)$
using $\langle Rep-wf-prog\ wfp = (c_1;; c_2, procs) \rangle$
apply(*cases* *wfp*) **apply**(*auto* *simp*:*Abs-wf-prog-inverse* *wf-prog-def* *wf-def*)
apply(*erule-tac* *x*=*Abs-wf-prog* $(c_2, procs)$) **in** *meta-allE*
by(*auto* *elim*:*meta-mp* *simp*:*Abs-wf-prog-inverse* *wf-prog-def* *wf-def*)

lemma *wfp-CondTrue*: **assumes** *Rep-wf-prog wfp* = $(if\ (b)\ c_1\ else\ c_2, procs)$
obtains *wfp'* **where** *Rep-wf-prog wfp'* = $(c_1, procs)$
using $\langle Rep-wf-prog\ wfp = (if\ (b)\ c_1\ else\ c_2, procs) \rangle$
apply(*cases* *wfp*) **apply**(*auto* *simp*:*Abs-wf-prog-inverse* *wf-prog-def* *wf-def*)
apply(*erule-tac* *x*=*Abs-wf-prog* $(c_1, procs)$) **in** *meta-allE*
by(*auto* *elim*:*meta-mp* *simp*:*Abs-wf-prog-inverse* *wf-prog-def* *wf-def*)

lemma *wfp-CondFalse*: **assumes** *Rep-wf-prog wfp* = $(if\ (b)\ c_1\ else\ c_2, procs)$
obtains *wfp'* **where** *Rep-wf-prog wfp'* = $(c_2, procs)$
using $\langle Rep-wf-prog\ wfp = (if\ (b)\ c_1\ else\ c_2, procs) \rangle$
apply(*cases* *wfp*) **apply**(*auto* *simp*:*Abs-wf-prog-inverse* *wf-prog-def* *wf-def*)

```

apply(erule-tac x=Abs-wf-prog (c2, procs) in meta-allE)
by(auto elim:meta-mp simp:Abs-wf-prog-inverse wf-prog-def wf-def)

lemma wfp-WhileBody: assumes Rep-wf-prog wfp = (while (b) c', procs)
  obtains wfp' where Rep-wf-prog wfp' = (c', procs)
using ⟨Rep-wf-prog wfp = (while (b) c', procs)⟩
apply(cases wfp) apply(auto simp:Abs-wf-prog-inverse wf-prog-def wf-def)
apply(erule-tac x=Abs-wf-prog (c', procs) in meta-allE)
by(auto elim:meta-mp simp:Abs-wf-prog-inverse wf-prog-def wf-def)

lemma wfp-Call: assumes Rep-wf-prog wfp = (prog,procs)
  and (p,ins,outs,c) ∈ set procs and containsCall procs prog ps p
  obtains wfp' where Rep-wf-prog wfp' = (c,procs)
using assms
apply(cases wfp) apply(auto simp:Abs-wf-prog-inverse wf-prog-def wf-def)
apply(erule-tac x=Abs-wf-prog (c, procs) in meta-allE)
apply(erule meta-mp) apply(rule Abs-wf-prog-inverse)
by(auto dest:containsCall-indirection simp:wf-prog-def wf-def)

end

```

2.5 Instantiate CFG locales with Proc CFG

```

theory Interpretation imports WellFormProgs ../StaticInter/CFGExit begin

```

2.5.1 Lifting of the basic definitions

```

abbreviation sourcenode :: edge ⇒ node
  where sourcenode e ≡ fst e

```

```

abbreviation targetnode :: edge ⇒ node
  where targetnode e ≡ snd(snd e)

```

```

abbreviation kind :: edge ⇒ (vname,val,node,pname) edge-kind
  where kind e ≡ fst(snd e)

```

```

definition valid-edge :: wf-prog ⇒ edge ⇒ bool
  where valid-edge wfp a ≡ let (prog,procs) = Rep-wf-prog wfp in
    prog,procs ⊢ sourcenode a -kind a → targetnode a

```

```

definition get-return-edges :: wf-prog ⇒ edge ⇒ edge set
  where get-return-edges wfp a ≡
    case kind a of Q:r↔_pfs ⇒ {a'. valid-edge wfp a' ∧ (∃ Q' f'. kind a' = Q'↔_pf')}
  ∧

```

$$|- \Rightarrow \{ \text{targetnode } a' = r \}$$

lemma *get-return-edges-non-call-empty*:

$\forall Q \ r \ p \ fs. \text{kind } a \neq Q:r \hookrightarrow_p fs \implies \text{get-return-edges wfp } a = \{ \}$
by(cases kind a, auto simp:get-return-edges-def)

lemma *call-has-return-edge*:

assumes *valid-edge wfp a* **and** *kind a = Q:r \hookrightarrow_p fs*
obtains *a'* **where** *valid-edge wfp a'* **and** $\exists Q' \ f'. \text{kind } a' = Q' \hookrightarrow_p f'$
and *targetnode a' = r*
proof(atomize-elim)
from $\langle \text{valid-edge wfp } a \rangle \langle \text{kind } a = Q:r \hookrightarrow_p fs \rangle$
obtain *prog procs* **where** *Rep-wf-prog wfp = (prog, procs)*
and $\text{prog, procs} \vdash \text{sourcenode } a - Q:r \hookrightarrow_p fs \rightarrow \text{targetnode } a$
by(fastforce simp:valid-edge-def)
from $\langle \text{prog, procs} \vdash \text{sourcenode } a - Q:r \hookrightarrow_p fs \rightarrow \text{targetnode } a \rangle$
show $\exists a'. \text{valid-edge wfp } a' \wedge (\exists Q' \ f'. \text{kind } a' = Q' \hookrightarrow_p f') \wedge \text{targetnode } a' = r$
proof(induct sourcenode a Q:r \hookrightarrow_p fs targetnode a rule:PCFG.induct)
case (MainCall l es rets n' ins outs c)
from $\langle \text{prog} \vdash \text{Label } l - \text{CEdge } (p, es, rets) \rightarrow_p n' \rangle$ **obtain** *l'*
where [simp]: $n' = \text{Label } l'$
by(fastforce dest:Proc-CFG-Call-Labels)
from *MainCall*
have $\text{prog, procs} \vdash (p, \text{Exit}) - (\lambda cf. \text{snd } cf = (\text{Main}, \text{Label } l')) \hookrightarrow_p$
 $(\lambda cf \ cf'. cf'(\text{rets} \text{[:]=} \text{map } cf \ \text{outs})) \rightarrow (\text{Main}, \text{Label } l')$
by(fastforce intro:MainReturn)
with $\langle \text{Rep-wf-prog wfp} = (\text{prog, procs}) \rangle \langle (\text{Main}, n') = r \rangle$ **show** ?thesis
by(fastforce simp:valid-edge-def)
next
case (ProcCall px ins outs c l es' rets' l' ins' outs' c' ps)
from *ProcCall* **have** $\text{prog, procs} \vdash (p, \text{Exit}) - (\lambda cf. \text{snd } cf = (px, \text{Label } l')) \hookrightarrow_p$
 $(\lambda cf \ cf'. cf'(\text{rets}' \text{[:]=} \text{map } cf \ \text{outs}')) \rightarrow (px, \text{Label } l')$
by(fastforce intro:ProcReturn)
with $\langle \text{Rep-wf-prog wfp} = (\text{prog, procs}) \rangle \langle (px, \text{Label } l') = r \rangle$ **show** ?thesis
by(fastforce simp:valid-edge-def)
qed auto
qed

lemma *get-return-edges-call-nonempty*:

$\llbracket \text{valid-edge wfp } a; \text{kind } a = Q:r \hookrightarrow_p fs \rrbracket \implies \text{get-return-edges wfp } a \neq \{ \}$
by $-$ (erule call-has-return-edge, (fastforce simp:get-return-edges-def)+)

lemma *only-return-edges-in-get-return-edges*:

$\llbracket \text{valid-edge wfp } a; \text{kind } a = Q:r \hookrightarrow_p fs; a' \in \text{get-return-edges wfp } a \rrbracket$

$\implies \exists Q' f'. \text{kind } a' = Q' \leftrightarrow pf'$
by(cases kind a, auto simp:get-return-edges-def)

abbreviation lift-procs :: wf-prog \implies (pname \times vname list \times vname list) list
where lift-procs wfp \equiv let (prog,procs) = Rep-wf-prog wfp in
 map ($\lambda x. (\text{fst } x, \text{fst}(\text{snd } x), \text{fst}(\text{snd}(\text{snd } x)))$) procs

2.5.2 Instatiation of the CFG locale

interpretation ProcCFG:

CFG sourcenode targetnode kind valid-edge wfp (Main, Entry)
 get-proc get-return-edges wfp lift-procs wfp Main
for wfp

proof –

from Rep-wf-prog[of wfp]

obtain prog procs **where** [simp]:Rep-wf-prog wfp = (prog,procs)

by(fastforce simp:wf-prog-def)

hence wf:well-formed procs **by**(fastforce intro:wf-wf-prog)

show CFG sourcenode targetnode kind (valid-edge wfp) (Main, Entry)

get-proc (get-return-edges wfp) (lift-procs wfp) Main

proof

fix a **assume** valid-edge wfp a **and** targetnode a = (Main, Entry)

from this wf **show** False **by**(auto elim:PCFG.cases simp:valid-edge-def)

next

show get-proc (Main, Entry) = Main **by** simp

next

fix a Q r p fs

assume valid-edge wfp a **and** kind a = Q:r \leftrightarrow pf

and sourcenode a = (Main, Entry)

thus False **by**(auto elim:PCFG.cases simp:valid-edge-def)

next

fix a a'

assume valid-edge wfp a **and** valid-edge wfp a'

and sourcenode a = sourcenode a' **and** targetnode a = targetnode a'

with wf **show** a = a'

by(cases a,cases a', auto dest:Proc-CFG-edge-det simp:valid-edge-def)

next

fix a Q r f

assume valid-edge wfp a **and** kind a = Q:r \leftrightarrow Mainf

from this wf **show** False **by**(auto elim:PCFG.cases simp:valid-edge-def)

next

fix a Q' f'

assume valid-edge wfp a **and** kind a = Q' \leftrightarrow Mainf'

from this wf **show** False **by**(auto elim:PCFG.cases simp:valid-edge-def)

next

fix a Q r p fs

assume valid-edge wfp a **and** kind a = Q:r \leftrightarrow pf

thus $\exists \text{ins outs. } (p, \text{ins}, \text{outs}) \in \text{set } (\text{lift-procs } wfp)$

```

apply(auto simp:valid-edge-def) apply(erule PCFG.cases) apply auto
  apply(fastforce dest:Proc-CFG-IEdge-intra-kind simp:intra-kind-def)
  apply(fastforce dest:Proc-CFG-IEdge-intra-kind simp:intra-kind-def)
  apply(rule-tac x=ins in exI) apply(rule-tac x=outs in exI)
  apply(rule-tac x=(p,ins,outs,c) in image-eqI) apply auto
  apply(rule-tac x=ins' in exI) apply(rule-tac x=outs' in exI)
  apply(rule-tac x=(p,ins',outs',c') in image-eqI) by(auto simp:set-conv-nth)
next
  fix a assume valid-edge wfp a and intra-kind (kind a)
  thus get-proc (sourcenode a) = get-proc (targetnode a)
    by(auto elim:PCFG.cases simp:valid-edge-def intra-kind-def)
next
  fix a Q r p fs
  assume valid-edge wfp a and kind a = Q:r↔pfs
  thus get-proc (targetnode a) = p by(auto elim:PCFG.cases simp:valid-edge-def)

next
  fix a Q' p f'
  assume valid-edge wfp a and kind a = Q'↔pf'
  thus get-proc (sourcenode a) = p by(auto elim:PCFG.cases simp:valid-edge-def)

next
  fix a Q r p fs
  assume valid-edge wfp a and kind a = Q:r↔pfs
  hence prog,procs ⊢ sourcenode a -kind a → targetnode a
    by(simp add:valid-edge-def)
  from this (kind a = Q:r↔pfs)
  show  $\forall a'. \text{valid-edge wfp } a' \wedge \text{targetnode } a' = \text{targetnode } a \longrightarrow$ 
     $(\exists Qx \text{ rx fsx. } \text{kind } a' = Qx:\text{rx}\leftrightarrow_p\text{fsx})$ 
  proof(induct sourcenode a kind a targetnode a rule:PCFG.induct)
    case (MainCall l p' es rets n' ins outs c)
    from  $\langle \lambda s. \text{True}:(\text{Main}, n')\leftrightarrow_p\text{map interpret es} = \text{kind } a \rangle \langle \text{kind } a = Q:r\leftrightarrow_p\text{fs} \rangle$ 
    have  $[\text{simp}]:p' = p$  by simp
    { fix a' assume valid-edge wfp a' and targetnode a' = (p', Entry)
      hence  $\exists Qx \text{ rx fsx. } \text{kind } a' = Qx:\text{rx}\leftrightarrow_p\text{fsx}$ 
      by(auto elim:PCFG.cases simp:valid-edge-def) }
    with  $\langle (p', \text{Entry}) = \text{targetnode } a \rangle$  show ?case by simp
  next
    case (ProcCall px ins outs c l p' es rets l' ins' outs' c' ps)
    from  $\langle \lambda s. \text{True}:(\text{px}, \text{Label } l')\leftrightarrow_p\text{map interpret es} = \text{kind } a \rangle \langle \text{kind } a =$ 
 $Q:r\leftrightarrow_p\text{fs} \rangle$ 
    have  $[\text{simp}]:p' = p$  by simp
    { fix a' assume valid-edge wfp a' and targetnode a' = (p', Entry)
      hence  $\exists Qx \text{ rx fsx. } \text{kind } a' = Qx:\text{rx}\leftrightarrow_p\text{fsx}$ 
      by(auto elim:PCFG.cases simp:valid-edge-def) }
    with  $\langle (p', \text{Entry}) = \text{targetnode } a \rangle$  show ?case by simp
  qed auto
next
  fix a Q' p f'

```

```

assume valid-edge wfp a and kind a = Q'↔pf'
hence prog,procs ⊢ sourcenode a -kind a → targetnode a
  by(simp add:valid-edge-def)
from this (kind a = Q'↔pf')
show  $\forall a'. \text{valid-edge wfp } a' \wedge \text{sourcenode } a' = \text{sourcenode } a \longrightarrow$ 
   $(\exists Qx \text{ } fx. \text{kind } a' = Qx \leftrightarrow_p fx)$ 
proof(induct sourcenode a kind a targetnode a rule:PCFG.induct)
  case (MainReturn l p' es rets l' ins outs c)
  from  $\langle \lambda cf. \text{snd } cf = (\text{Main}, \text{Label } l') \leftrightarrow_p \lambda cf \text{ } cf'. \text{cf}'(\text{rets } [:=] \text{map } cf \text{ } \text{outs}) =$ 
    kind a  $\langle \text{kind } a = Q' \leftrightarrow_p f' \rangle$  have [simp]:p' = p by simp
  { fix a' assume valid-edge wfp a' and sourcenode a' = (p', Exit)
    hence  $\exists Qx \text{ } fx. \text{kind } a' = Qx \leftrightarrow_p fx$ 
    by(auto elim:PCFG.cases simp:valid-edge-def) }
  with  $\langle (p', \text{Exit}) = \text{sourcenode } a \rangle$  show ?case by simp
next
  case (ProcReturn px ins outs c l p' es rets l' ins' outs' c' ps)
  from  $\langle \lambda cf. \text{snd } cf = (px, \text{Label } l') \leftrightarrow_p \lambda cf \text{ } cf'. \text{cf}'(\text{rets } [:=] \text{map } cf \text{ } \text{outs}') =$ 
    kind a  $\langle \text{kind } a = Q' \leftrightarrow_p f' \rangle$  have [simp]:p' = p by simp
  { fix a' assume valid-edge wfp a' and sourcenode a' = (p', Exit)
    hence  $\exists Qx \text{ } fx. \text{kind } a' = Qx \leftrightarrow_p fx$ 
    by(auto elim:PCFG.cases simp:valid-edge-def) }
  with  $\langle (p', \text{Exit}) = \text{sourcenode } a \rangle$  show ?case by simp
qed auto
next
  fix a Q r p fs
  assume valid-edge wfp a and kind a = Q:r↔pfs
  thus get-return-edges wfp a ≠ {} by(rule get-return-edges-call-nonempty)
next
  fix a a'
  assume valid-edge wfp a and a' ∈ get-return-edges wfp a
  thus valid-edge wfp a'
  by(cases kind a,auto simp:get-return-edges-def)
next
  fix a a'
  assume valid-edge wfp a and a' ∈ get-return-edges wfp a
  thus  $\exists Q r p fs. \text{kind } a = Q:r \leftrightarrow_p fs$ 
  by(cases kind a)(auto simp:get-return-edges-def)
next
  fix a Q r p fs a'
  assume valid-edge wfp a and kind a = Q:r↔pfs
  and a' ∈ get-return-edges wfp a
  thus  $\exists Q' f'. \text{kind } a' = Q' \leftrightarrow_p f'$  by(rule only-return-edges-in-get-return-edges)
next
  fix a Q' p f'
  assume valid-edge wfp a and kind a = Q'↔pf'
  hence prog,procs ⊢ sourcenode a -kind a → targetnode a
  by(simp add:valid-edge-def)
from this (kind a = Q'↔pf')
show  $\exists !a'. \text{valid-edge wfp } a' \wedge (\exists Q r fs. \text{kind } a' = Q:r \leftrightarrow_p fs) \wedge$ 

```


$a \in \text{get-return-edges wfp } a'$
proof(*induct sourcenode a kind a targetnode a rule:PCFG.induct*)
case (*MainReturn l px es rets l' ins outs c*)
from $\langle \lambda cf. \text{snd } cf = (\text{Main}, \text{Label } l') \leftrightarrow_{px} \lambda cf. cf'. cf'(\text{rets } [:=] \text{map } cf \text{ outs}) = \text{kind } a \rangle \langle \text{kind } a = Q' \leftrightarrow_{pf} \rangle$ **have** [*simp*]: $px = p$ **by** *simp*
from $\langle \text{prog} \vdash \text{Label } l - \text{CEdge } (px, es, rets) \rightarrow_p \text{Label } l' \rangle$ **have** $l' = \text{Suc } l$
by(*fastforce dest:Proc-CFG-Call-Labels*)
from $\langle \text{prog} \vdash \text{Label } l - \text{CEdge } (px, es, rets) \rightarrow_p \text{Label } l' \rangle$
have *containsCall procs prog [] px* **by**(*rule Proc-CFG-Call-containsCall*)
with $\langle \text{prog} \vdash \text{Label } l - \text{CEdge } (px, es, rets) \rightarrow_p \text{Label } l' \rangle$
 $\langle (px, ins, outs, c) \in \text{set procs} \rangle$
have $\text{prog}, \text{procs} \vdash (p, \text{Exit}) - (\lambda cf. \text{snd } cf = (\text{Main}, \text{Label } l') \leftrightarrow_p (\lambda cf. cf'. cf'(\text{rets } [:=] \text{map } cf \text{ outs})) \rightarrow (\text{Main}, \text{Label } l'))$
by(*fastforce intro:PCFG.MainReturn*)
with $\langle (px, \text{Exit}) = \text{sourcenode } a \rangle \langle (\text{Main}, \text{Label } l') = \text{targetnode } a \rangle$
 $\langle \lambda cf. \text{snd } cf = (\text{Main}, \text{Label } l') \leftrightarrow_{px} \lambda cf. cf'. cf'(\text{rets } [:=] \text{map } cf \text{ outs}) = \text{kind } a \rangle$
have $\text{edge}: \text{prog}, \text{procs} \vdash \text{sourcenode } a - \text{kind } a \rightarrow \text{targetnode } a$ **by** *simp*
from $\langle \text{prog} \vdash \text{Label } l - \text{CEdge } (px, es, rets) \rightarrow_p \text{Label } l' \rangle$
 $\langle (px, ins, outs, c) \in \text{set procs} \rangle$
have $\text{edge}': \text{prog}, \text{procs} \vdash (\text{Main}, \text{Label } l)$
 $- (\lambda s. \text{True}): (\text{Main}, \text{Label } l') \leftrightarrow_p \text{map } (\lambda e. cf. \text{interpret } e \text{ cf}) \text{ es} \rightarrow (p, \text{Entry})$
by(*fastforce intro:MainCall*)
show *?case*
proof(*rule ex-ex1I*)
from $\text{edge } \text{edge}' \langle (\text{Main}, \text{Label } l') = \text{targetnode } a \rangle$
 $\langle l' = \text{Suc } l \rangle \langle \text{kind } a = Q' \leftrightarrow_{pf} \rangle$
show $\exists a'. \text{valid-edge wfp } a' \wedge$
 $(\exists Q \text{ r fs. } \text{kind } a' = Q: r \leftrightarrow_p \text{fs}) \wedge a \in \text{get-return-edges wfp } a'$
by(*fastforce simp:valid-edge-def get-return-edges-def*)
next
fix $a' a''$
assume $\text{valid-edge wfp } a' \wedge$
 $(\exists Q \text{ r fs. } \text{kind } a' = Q: r \leftrightarrow_p \text{fs}) \wedge a \in \text{get-return-edges wfp } a'$
and $\text{valid-edge wfp } a'' \wedge$
 $(\exists Q \text{ r fs. } \text{kind } a'' = Q: r \leftrightarrow_p \text{fs}) \wedge a \in \text{get-return-edges wfp } a''$
then obtain $Q \text{ r fs } Q' \text{ r}' \text{ fs}'$ **where** $\text{valid-edge wfp } a'$
and $\text{kind } a' = Q: r \leftrightarrow_p \text{fs}$ **and** $a \in \text{get-return-edges wfp } a'$
and $\text{valid-edge wfp } a''$ **and** $\text{kind } a'' = Q': r' \leftrightarrow_p \text{fs}'$
and $a \in \text{get-return-edges wfp } a''$ **by** *blast*
from $\langle \text{valid-edge wfp } a' \rangle \langle \text{kind } a' = Q: r \leftrightarrow_p \text{fs} \rangle$ [*THEN sym*] $\text{edge wf } \langle l' = \text{Suc } l \rangle$
 $\langle a \in \text{get-return-edges wfp } a' \rangle \langle (\text{Main}, \text{Label } l') = \text{targetnode } a \rangle$
have $\text{nodes}: \text{sourcenode } a' = (\text{Main}, \text{Label } l) \wedge \text{targetnode } a' = (p, \text{Entry})$
apply(*auto simp:valid-edge-def get-return-edges-def*)
by(*erule PCFG.cases, auto dest:Proc-CFG-Call-Labels*)
from $\langle \text{valid-edge wfp } a'' \rangle \langle \text{kind } a'' = Q': r' \leftrightarrow_p \text{fs}' \rangle$ [*THEN sym*] $\langle l' = \text{Suc } l \rangle$
 $\langle a \in \text{get-return-edges wfp } a'' \rangle \langle (\text{Main}, \text{Label } l') = \text{targetnode } a \rangle \text{wf } \text{edge}'$
have $\text{nodes}': \text{sourcenode } a'' = (\text{Main}, \text{Label } l) \wedge \text{targetnode } a'' = (p, \text{Entry})$

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    apply(auto simp:valid-edge-def get-return-edges-def)
    by(erule PCFG.cases,auto dest:Proc-CFG-Call-Labels)+
  with nodes ⟨valid-edge wfp a'⟩ ⟨valid-edge wfp a''⟩ wf
  have kind a' = kind a''
    by(fastforce dest:Proc-CFG-edge-det simp:valid-edge-def)
  with nodes nodes' show a' = a'' by(cases a',cases a'',auto)
qed
next
case (ProcReturn p' ins outs c l px esx retsx l' ins' outs' c' ps)
from ⟨λcf. snd cf = (p', Label l')↔pxλcf cf'. cf'(retsx [:=] map cf outs') =
  kind a⟩ ⟨kind a = Q'↔pf⟩ have [simp]:px = p by simp
from ⟨c ⊢ Label l -CEdge (px, esx, retsx)→p Label l'⟩ have l' = Suc l
  by(fastforce dest:Proc-CFG-Call-Labels)
from ⟨(p',ins,outs,c) ∈ set procs⟩
  ⟨c ⊢ Label l -CEdge (px, esx, retsx)→p Label l'⟩
  ⟨(px, ins', outs', c') ∈ set procs⟩ ⟨containsCall procs prog ps p'⟩
have prog,procs ⊢ (p,Exit) -⟨λcf. snd cf = (p',Label l')↔p
  (λcf cf'. cf'(retsx [:=] map cf outs'))→ (p',Label l')
  by(fastforce intro:PCFG.ProcReturn)
with ⟨(px, Exit) = sourcenode a⟩ ⟨(p', Label l') = targetnode a⟩
  ⟨λcf. snd cf = (p', Label l')↔pxλcf cf'. cf'(retsx [:=] map cf outs') =
  kind a⟩ have edge:prog,procs ⊢ sourcenode a -kind a→ targetnode a by
simp
from ⟨(p',ins,outs,c) ∈ set procs⟩
  ⟨c ⊢ Label l -CEdge (px, esx, retsx)→p Label l'⟩
  ⟨(px, ins', outs', c') ∈ set procs⟩ ⟨containsCall procs prog ps p'⟩
have edge':prog,procs ⊢ (p',Label l)
  -⟨λs. True⟩:(p',Label l')↔pmap (λe cf. interpret e cf) esx→ (p,Entry)
  by(fastforce intro:ProcCall)
show ?case
proof(rule ex-ex1I)
  from edge edge' ⟨(p', Label l') = targetnode a⟩ ⟨l' = Suc l⟩
    ⟨(p', ins, outs, c) ∈ set procs⟩ ⟨kind a = Q'↔pf⟩
  show ∃ a'. valid-edge wfp a' ∧
    (∃ Q r fs. kind a' = Q:r↔pfs) ∧ a ∈ get-return-edges wfp a'
    by(fastforce simp:valid-edge-def get-return-edges-def)
next
fix a' a''
assume valid-edge wfp a' ∧
  (∃ Q r fs. kind a' = Q:r↔pfs) ∧ a ∈ get-return-edges wfp a'
  and valid-edge wfp a'' ∧
  (∃ Q r fs. kind a'' = Q:r↔pfs) ∧ a ∈ get-return-edges wfp a''
then obtain Q r fs Q' r' fs' where valid-edge wfp a'
  and kind a' = Q:r↔pfs and a ∈ get-return-edges wfp a'
  and valid-edge wfp a'' and kind a'' = Q':r'↔pfs'
  and a ∈ get-return-edges wfp a'' by blast
from ⟨valid-edge wfp a'⟩ ⟨kind a' = Q:r↔pfs⟩ [THEN sym]
  ⟨a ∈ get-return-edges wfp a'⟩ edge ⟨(p', Label l') = targetnode a⟩ wf
  ⟨(p', ins, outs, c) ∈ set procs⟩ ⟨l' = Suc l⟩

```

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have nodes:sourcenode  $a' = (p', \text{Label } l) \wedge \text{targetnode } a' = (p, \text{Entry})$ 
  apply(auto simp:valid-edge-def get-return-edges-def)
  by(erule PCFG.cases,auto dest:Proc-CFG-Call-Labels)+
from  $\langle \text{valid-edge wfp } a'' \rangle \langle \text{kind } a'' = Q':r' \hookrightarrow_p fs' \rangle [\text{THEN sym}]$ 
   $\langle a \in \text{get-return-edges wfp } a'' \rangle \text{edge } \langle (p', \text{Label } l') = \text{targetnode } a \rangle \text{wf}$ 
   $\langle (p', \text{ins}, \text{outs}, c) \in \text{set procs} \rangle \langle l' = \text{Suc } l \rangle$ 
have nodes':sourcenode  $a'' = (p', \text{Label } l) \wedge \text{targetnode } a'' = (p, \text{Entry})$ 
  apply(auto simp:valid-edge-def get-return-edges-def)
  by(erule PCFG.cases,auto dest:Proc-CFG-Call-Labels)+
with nodes  $\langle \text{valid-edge wfp } a' \rangle \langle \text{valid-edge wfp } a'' \rangle \text{wf}$ 
have kind  $a' = \text{kind } a''$ 
  by(fastforce dest:Proc-CFG-edge-det simp:valid-edge-def)
with nodes nodes' show  $a' = a''$  by(cases  $a'$ ,cases  $a''$ ,auto)
qed
qed auto
next
fix  $a \ a'$ 
assume  $\text{valid-edge wfp } a$  and  $a' \in \text{get-return-edges wfp } a$ 
then obtain  $Q \ r \ p \ fs \ l'$ 
  where  $\text{kind } a = Q:r \hookrightarrow_p fs$  and  $\text{valid-edge wfp } a'$ 
  by(cases kind  $a$ )(fastforce simp:valid-edge-def get-return-edges-def)+
from  $\langle \text{valid-edge wfp } a \rangle \langle \text{kind } a = Q:r \hookrightarrow_p fs \rangle \langle a' \in \text{get-return-edges wfp } a \rangle$ 
obtain  $Q' \ f'$  where  $\text{kind } a' = Q' \hookrightarrow_p f'$ 
  by(fastforce dest!:only-return-edges-in-get-return-edges)
with  $\langle \text{valid-edge wfp } a' \rangle$  have sourcenode  $a' = (p, \text{Exit})$ 
  by(auto elim:PCFG.cases simp:valid-edge-def)
from  $\langle \text{valid-edge wfp } a \rangle \langle \text{kind } a = Q:r \hookrightarrow_p fs \rangle$ 
have  $\text{prog}, \text{procs} \vdash \text{sourcenode } a - Q:r \hookrightarrow_p fs \rightarrow \text{targetnode } a$ 
  by(simp add:valid-edge-def)
thus  $\exists a''.$   $\text{valid-edge wfp } a'' \wedge \text{sourcenode } a'' = \text{targetnode } a \wedge$ 
   $\text{targetnode } a'' = \text{sourcenode } a' \wedge \text{kind } a'' = (\lambda cf. \text{False})_{\surd}$ 
proof(induct sourcenode  $a \ Q:r \hookrightarrow_p fs \ \text{targetnode } a$  rule:PCFG.induct)
  case (MainCall  $l \ es \ \text{rets } n' \ \text{ins} \ \text{outs} \ c$ )
  have  $c \vdash \text{Entry} - \text{IEdge } (\lambda s. \text{False})_{\surd} \rightarrow_p \text{Exit}$  by(rule Proc-CFG-Entry-Exit)
  moreover
  from  $\langle \text{prog} \vdash \text{Label } l - \text{CEdge } (p, \text{es}, \text{rets}) \rightarrow_p n' \rangle$ 
  have  $\text{containsCall procs prog } \square \ p$  by(rule Proc-CFG-Call-containsCall)
  ultimately have  $\text{prog}, \text{procs} \vdash (p, \text{Entry}) - (\lambda s. \text{False})_{\surd} \rightarrow (p, \text{Exit})$ 
  using  $\langle (p, \text{ins}, \text{outs}, c) \in \text{set procs} \rangle$  by(fastforce intro:Proc)
  with  $\langle \text{sourcenode } a' = (p, \text{Exit}) \rangle \langle (p, \text{Entry}) = \text{targetnode } a \rangle [\text{THEN sym}]$ 
  show ?case by(fastforce simp:valid-edge-def)
next
  case (ProcCall  $px \ \text{ins} \ \text{outs} \ c \ l \ es' \ \text{rets}' \ l' \ \text{ins}' \ \text{outs}' \ c' \ ps$ )
  have  $c' \vdash \text{Entry} - \text{IEdge } (\lambda s. \text{False})_{\surd} \rightarrow_p \text{Exit}$  by(rule Proc-CFG-Entry-Exit)
  moreover
  from  $\langle c \vdash \text{Label } l - \text{CEdge } (p, \text{es}', \text{rets}') \rightarrow_p \text{Label } l' \rangle$ 
  have  $\text{containsCall procs } c \ \square \ p$  by(rule Proc-CFG-Call-containsCall)
  with  $\langle \text{containsCall procs prog } ps \ px \rangle \langle (px, \text{ins}, \text{outs}, c) \in \text{set procs} \rangle$ 
  have  $\text{containsCall procs prog } (ps@[px]) \ p$ 

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    by(rule containsCall-in-proc)
  ultimately have prog,procs ⊢ (p,Entry) −(λs. False)✓→ (p,Exit)
    using ⟨(p, ins', outs', c') ∈ set procs⟩ by(fastforce intro:Proc)
  with ⟨sourcnode a' = (p,Exit)⟩ ⟨(p, Entry) = targetnode a⟩[THEN sym]
  show ?case by(fastforce simp:valid-edge-def)
qed auto
next
fix a a'
assume valid-edge wfp a and a' ∈ get-return-edges wfp a
then obtain Q r p fs l'
  where kind a = Q:r↔pfs and valid-edge wfp a'
  by(cases kind a)(fastforce simp:valid-edge-def get-return-edges-def)+
from ⟨valid-edge wfp a⟩ ⟨kind a = Q:r↔pfs⟩ ⟨a' ∈ get-return-edges wfp a⟩
obtain Q' f' where kind a' = Q'↔pf' and targetnode a' = r
  by(auto simp:get-return-edges-def)
from ⟨valid-edge wfp a⟩ ⟨kind a = Q:r↔pfs⟩
have prog,procs ⊢ sourcnode a −Q:r↔pfs→ targetnode a
  by(simp add:valid-edge-def)
thus ∃ a''. valid-edge wfp a'' ∧ sourcnode a'' = sourcnode a ∧
  targetnode a'' = targetnode a' ∧ kind a'' = (λcf. False)✓
proof(induct sourcnode a Q:r↔pfs targetnode a rule:PCFG.induct)
  case (MainCall l es rets n' ins outs c)
  from ⟨prog ⊢ Label l −CEdge (p, es, rets)→p n'⟩
  have prog,procs ⊢ (Main,Label l) −(λs. False)✓→ (Main,n')
    by(rule MainCallReturn)
  with ⟨(Main, Label l) = sourcnode a⟩[THEN sym] ⟨targetnode a' = r⟩
  ⟨(Main, n') = r⟩[THEN sym]
  show ?case by(auto simp:valid-edge-def)
next
  case (ProcCall px ins outs c l es' rets' l' ins' outs' c' ps)
  from ⟨(px,ins,outs,c) ∈ set procs⟩ ⟨containsCall procs prog ps px⟩
  ⟨c ⊢ Label l −CEdge (p, es', rets')→p Label l'⟩
  have prog,procs ⊢ (px,Label l) −(λs. False)✓→ (px,Label l')
    by(fastforce intro:ProcCallReturn)
  with ⟨(px, Label l) = sourcnode a⟩[THEN sym] ⟨targetnode a' = r⟩
  ⟨(px, Label l') = r⟩[THEN sym]
  show ?case by(auto simp:valid-edge-def)
qed auto
next
fix a Q r p fs
assume valid-edge wfp a and kind a = Q:r↔pfs
hence prog,procs ⊢ sourcnode a −kind a→ targetnode a
  by(simp add:valid-edge-def)
from this ⟨kind a = Q:r↔pfs⟩
show ∃!a'. valid-edge wfp a' ∧
  sourcnode a' = sourcnode a ∧ intra-kind (kind a')
proof(induct sourcnode a kind a targetnode a rule:PCFG.induct)
  case (MainCall l p' es rets n' ins outs c)
  show ?thesis

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proof(rule ex-ex1I)
  from ⟨prog ⊢ Label l - CEdge (p', es, rets) →p n'⟩
  have prog,procs ⊢ (Main,Label l) - (λs. False) √ → (Main,n')
    by(rule MainCallReturn)
  with ⟨(Main, Label l) = sourcenode a⟩[THEN sym]
  show ∃ a'. valid-edge wfp a' ∧
    sourcenode a' = sourcenode a ∧ intra-kind (kind a')
    by(fastforce simp:valid-edge-def intra-kind-def)
next
  fix a' a''
  assume valid-edge wfp a' ∧ sourcenode a' = sourcenode a ∧
    intra-kind (kind a') and valid-edge wfp a'' ∧
    sourcenode a'' = sourcenode a ∧ intra-kind (kind a'')
  hence valid-edge wfp a' and sourcenode a' = sourcenode a
    and intra-kind (kind a') and valid-edge wfp a''
    and sourcenode a'' = sourcenode a and intra-kind (kind a'') by simp-all
  from ⟨valid-edge wfp a'⟩ ⟨sourcenode a' = sourcenode a⟩
    ⟨intra-kind (kind a')⟩ ⟨prog ⊢ Label l - CEdge (p', es, rets) →p n'⟩
    ⟨(Main, Label l) = sourcenode a⟩ wf
  have targetnode a' = (Main,Label (Suc l))
  by(auto elim!:PCFG.cases dest:Proc-CFG-Call-Intra-edge-not-same-source

    Proc-CFG-Call-Labels simp:intra-kind-def valid-edge-def)
  from ⟨valid-edge wfp a''⟩ ⟨sourcenode a'' = sourcenode a⟩
    ⟨intra-kind (kind a'')⟩ ⟨prog ⊢ Label l - CEdge (p', es, rets) →p n'⟩
    ⟨(Main, Label l) = sourcenode a⟩ wf
  have targetnode a'' = (Main,Label (Suc l))
  by(auto elim!:PCFG.cases dest:Proc-CFG-Call-Intra-edge-not-same-source

    Proc-CFG-Call-Labels simp:intra-kind-def valid-edge-def)
  with ⟨valid-edge wfp a'⟩ ⟨sourcenode a' = sourcenode a⟩
    ⟨valid-edge wfp a''⟩ ⟨sourcenode a'' = sourcenode a⟩
    ⟨targetnode a' = (Main,Label (Suc l))⟩ wf
  show a' = a'' by(cases a',cases a'')
    (auto dest:Proc-CFG-edge-det simp:valid-edge-def)
qed
next
case (ProcCall px ins outs c l p' es' rets' l' ins' outs' c' ps)
show ?thesis
proof(rule ex-ex1I)
  from ⟨(px, ins, outs, c) ∈ set procs⟩ ⟨containsCall procs prog ps px⟩
    ⟨c ⊢ Label l - CEdge (p', es', rets') →p Label l'⟩
  have prog,procs ⊢ (px,Label l) - (λs. False) √ → (px,Label l')
    by -(rule ProcCallReturn)
  with ⟨(px, Label l) = sourcenode a⟩[THEN sym]
  show ∃ a'. valid-edge wfp a' ∧ sourcenode a' = sourcenode a ∧
    intra-kind (kind a')
    by(fastforce simp:valid-edge-def intra-kind-def)
next

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```

fix a' a''
assume valid-edge wfp a'  $\wedge$  sourcenode a' = sourcenode a  $\wedge$ 
  intra-kind (kind a') and valid-edge wfp a''  $\wedge$ 
  sourcenode a'' = sourcenode a  $\wedge$  intra-kind (kind a'')
hence valid-edge wfp a' and sourcenode a' = sourcenode a
and intra-kind (kind a') and valid-edge wfp a''
and sourcenode a'' = sourcenode a and intra-kind (kind a'') by simp-all
from  $\langle$ valid-edge wfp a'  $\rangle$   $\langle$ sourcenode a' = sourcenode a  $\rangle$ 
   $\langle$ intra-kind (kind a')  $\rangle$   $\langle$ (px, ins, outs, c)  $\in$  set procs  $\rangle$ 
   $\langle$ c  $\vdash$  Label l - CEdge (p', es', rets')  $\rightarrow_p$  Label l'  $\rangle$ 
   $\langle$ (p', ins', outs', c')  $\in$  set procs  $\rangle$  wf
   $\langle$ containsCall procs prog ps px  $\rangle$   $\langle$ (px, Label l) = sourcenode a  $\rangle$ 
have targetnode a' = (px, Label (Suc l))
apply(auto simp:valid-edge-def) apply(erule PCFG.cases)
by(auto dest:Proc-CFG-Call-Intra-edge-not-same-source
  Proc-CFG-Call-nodes-eq Proc-CFG-Call-Labels simp:intra-kind-def)
from  $\langle$ valid-edge wfp a''  $\rangle$   $\langle$ sourcenode a'' = sourcenode a  $\rangle$ 
   $\langle$ intra-kind (kind a'')  $\rangle$   $\langle$ (px, ins, outs, c)  $\in$  set procs  $\rangle$ 
   $\langle$ c  $\vdash$  Label l - CEdge (p', es', rets')  $\rightarrow_p$  Label l'  $\rangle$ 
   $\langle$ (p', ins', outs', c')  $\in$  set procs  $\rangle$  wf
   $\langle$ containsCall procs prog ps px  $\rangle$   $\langle$ (px, Label l) = sourcenode a  $\rangle$ 
have targetnode a'' = (px, Label (Suc l))
apply(auto simp:valid-edge-def) apply(erule PCFG.cases)
by(auto dest:Proc-CFG-Call-Intra-edge-not-same-source
  Proc-CFG-Call-nodes-eq Proc-CFG-Call-Labels simp:intra-kind-def)
with  $\langle$ valid-edge wfp a'  $\rangle$   $\langle$ sourcenode a' = sourcenode a  $\rangle$ 
   $\langle$ valid-edge wfp a''  $\rangle$   $\langle$ sourcenode a'' = sourcenode a  $\rangle$ 
   $\langle$ targetnode a' = (px, Label (Suc l))  $\rangle$  wf
show a' = a'' by(cases a', cases a'')
  (auto dest:Proc-CFG-edge-det simp:valid-edge-def)
qed
qed auto
next
fix a Q' p f'
assume valid-edge wfp a and kind a = Q'  $\leftrightarrow_p$  f'
hence prog, procs  $\vdash$  sourcenode a -kind a  $\rightarrow$  targetnode a
by(simp add:valid-edge-def)
from this  $\langle$ kind a = Q'  $\leftrightarrow_p$  f'  $\rangle$ 
show  $\exists!$ a'. valid-edge wfp a'  $\wedge$ 
  targetnode a' = targetnode a  $\wedge$  intra-kind (kind a')
proof(induct sourcenode a kind a targetnode a rule:PCFG.induct)
case (MainReturn l p' es rets l' ins outs c)
show ?thesis
proof(rule ex-ex1I)
from  $\langle$ prog  $\vdash$  Label l - CEdge (p', es, rets)  $\rightarrow_p$  Label l'  $\rangle$ 
have prog, procs  $\vdash$  (Main, Label l) -( $\lambda$ s. False)  $\surd$   $\rightarrow$ 
  (Main, Label l') by(rule MainCallReturn)
with  $\langle$ (Main, Label l') = targetnode a  $\rangle$  [THEN sym]
show  $\exists$  a'. valid-edge wfp a'  $\wedge$ 

```

$targetnode\ a' = targetnode\ a \wedge intra\text{-}kind\ (kind\ a')$
by(*fastforce simp:valid-edge-def intra-kind-def*)

next
fix $a'\ a''$
assume $valid\text{-}edge\ wfp\ a' \wedge targetnode\ a' = targetnode\ a \wedge$
 $intra\text{-}kind\ (kind\ a')$ **and** $valid\text{-}edge\ wfp\ a'' \wedge$
 $targetnode\ a'' = targetnode\ a \wedge intra\text{-}kind\ (kind\ a'')$
hence $valid\text{-}edge\ wfp\ a'$ **and** $targetnode\ a' = targetnode\ a$
and $intra\text{-}kind\ (kind\ a')$ **and** $valid\text{-}edge\ wfp\ a''$
and $targetnode\ a'' = targetnode\ a$ **and** $intra\text{-}kind\ (kind\ a'')$ **by** *simp-all*
from $\langle valid\text{-}edge\ wfp\ a' \rangle \langle targetnode\ a' = targetnode\ a \rangle$
 $\langle intra\text{-}kind\ (kind\ a') \rangle \langle prog \vdash Label\ l - CEdge\ (p', es, rets) \rightarrow_p\ Label\ l' \rangle$
 $\langle (Main, Label\ l') = targetnode\ a \rangle\ wf$
have $sourcenode\ a' = (Main, Label\ l)$
apply(*auto elim!:PCFG.cases dest:Proc-CFG-Call-Intra-edge-not-same-target*)

simp:valid-edge-def intra-kind-def)

by(*fastforce dest:Proc-CFG-Call-nodes-eq' Proc-CFG-Call-Labels*)
from $\langle valid\text{-}edge\ wfp\ a'' \rangle \langle targetnode\ a'' = targetnode\ a \rangle$
 $\langle intra\text{-}kind\ (kind\ a'') \rangle \langle prog \vdash Label\ l - CEdge\ (p', es, rets) \rightarrow_p\ Label\ l' \rangle$
 $\langle (Main, Label\ l') = targetnode\ a \rangle\ wf$
have $sourcenode\ a'' = (Main, Label\ l)$
apply(*auto elim!:PCFG.cases dest:Proc-CFG-Call-Intra-edge-not-same-target*)

simp:valid-edge-def intra-kind-def)

by(*fastforce dest:Proc-CFG-Call-nodes-eq' Proc-CFG-Call-Labels*)
with $\langle valid\text{-}edge\ wfp\ a' \rangle \langle targetnode\ a' = targetnode\ a \rangle$
 $\langle valid\text{-}edge\ wfp\ a'' \rangle \langle targetnode\ a'' = targetnode\ a \rangle$
 $\langle sourcenode\ a' = (Main, Label\ l) \rangle\ wf$
show $a' = a''$ **by**(*cases a',cases a''*)
(*auto dest:Proc-CFG-edge-det simp:valid-edge-def*)

qed
next
case (*ProcReturn px ins outs c l p' es' rets' l' ins' outs' c' ps*)
show *?thesis*
proof(*rule ex-ex1I*)
from $\langle (px, ins, outs, c) \in set\ procs \rangle \langle containsCall\ procs\ prog\ ps\ px \rangle$
 $\langle c \vdash Label\ l - CEdge\ (p', es', rets') \rightarrow_p\ Label\ l' \rangle$
have $prog, procs \vdash (px, Label\ l) - (\lambda s. False) \checkmark \rightarrow (px, Label\ l')$
by $-(rule\ ProcCallReturn)$
with $\langle (px, Label\ l') = targetnode\ a \rangle [THEN\ sym]$
show $\exists a'. valid\text{-}edge\ wfp\ a' \wedge$
 $targetnode\ a' = targetnode\ a \wedge intra\text{-}kind\ (kind\ a')$
by(*fastforce simp:valid-edge-def intra-kind-def*)

next
fix $a'\ a''$
assume $valid\text{-}edge\ wfp\ a' \wedge targetnode\ a' = targetnode\ a \wedge$
 $intra\text{-}kind\ (kind\ a')$ **and** $valid\text{-}edge\ wfp\ a'' \wedge$
 $targetnode\ a'' = targetnode\ a \wedge intra\text{-}kind\ (kind\ a'')$

```

hence valid-edge wfp a' and targetnode a' = targetnode a
  and intra-kind (kind a') and valid-edge wfp a''
  and targetnode a'' = targetnode a and intra-kind (kind a'') by simp-all
from ⟨valid-edge wfp a'⟩ ⟨targetnode a' = targetnode a⟩
  ⟨intra-kind (kind a')⟩ ⟨(px, ins, outs, c) ∈ set procs⟩
  ⟨(p', ins', outs', c') ∈ set procs⟩ wf
  ⟨c ⊢ Label l - CEdge (p', es', rets') →p Label l'⟩
  ⟨containsCall procs prog ps px⟩ ⟨(px, Label l') = targetnode a⟩
have sourcenode a' = (px, Label l)
  apply(auto simp:valid-edge-def) apply(erule PCFG.cases)
  by(auto dest:Proc-CFG-Call-Intra-edge-not-same-target
    Proc-CFG-Call-nodes-eq' simp:intra-kind-def)
from ⟨valid-edge wfp a''⟩ ⟨targetnode a'' = targetnode a⟩
  ⟨intra-kind (kind a'')⟩ ⟨(px, ins, outs, c) ∈ set procs⟩
  ⟨(p', ins', outs', c') ∈ set procs⟩ wf
  ⟨c ⊢ Label l - CEdge (p', es', rets') →p Label l'⟩
  ⟨containsCall procs prog ps px⟩ ⟨(px, Label l') = targetnode a⟩
have sourcenode a'' = (px, Label l)
  apply(auto simp:valid-edge-def) apply(erule PCFG.cases)
  by(auto dest:Proc-CFG-Call-Intra-edge-not-same-target
    Proc-CFG-Call-nodes-eq' simp:intra-kind-def)
with ⟨valid-edge wfp a'⟩ ⟨targetnode a' = targetnode a⟩
  ⟨valid-edge wfp a''⟩ ⟨targetnode a'' = targetnode a⟩
  ⟨sourcenode a' = (px, Label l)⟩ wf
show a' = a'' by(cases a', cases a'')
  (auto dest:Proc-CFG-edge-det simp:valid-edge-def)
qed
qed auto
next
  fix a a' Q1 r1 p fs1 Q2 r2 fs2
  assume valid-edge wfp a and valid-edge wfp a'
    and kind a = Q1:r1↔pfs1 and kind a' = Q2:r2↔pfs2
  thus targetnode a = targetnode a' by(auto elim!:PCFG.cases simp:valid-edge-def)
next
  from wf show distinct-fst (lift-procs wfp)
    by(fastforce simp:well-formed-def distinct-fst-def o-def)
next
  fix p ins outs assume (p, ins, outs) ∈ set (lift-procs wfp)
  from ⟨(p, ins, outs) ∈ set (lift-procs wfp)⟩ wf
  show distinct ins by(fastforce simp:well-formed-def wf-proc-def)
next
  fix p ins outs assume (p, ins, outs) ∈ set (lift-procs wfp)
  from ⟨(p, ins, outs) ∈ set (lift-procs wfp)⟩ wf
  show distinct outs by(fastforce simp:well-formed-def wf-proc-def)
qed
qed

```


2.5.3 Instatiation of the *CFGExit* locale

interpretation *ProcCFGExit*:

CFGExit sourcenode targetnode kind valid-edge wfp (Main,Entry)
get-proc get-return-edges wfp lift-procs wfp Main (Main,Exit)
for *wfp*

proof –

from *Rep-wf-prog[of wfp]*

obtain *prog procs* **where** [*simp*]:*Rep-wf-prog wfp = (prog,procs)*

by(*fastforce simp:wf-prog-def*)

hence *wf:well-formed procs* **by**(*fastforce intro:wf-wf-prog*)

show *CFGExit sourcenode targetnode kind (valid-edge wfp) (Main, Entry)*
get-proc (get-return-edges wfp) (lift-procs wfp) Main (Main, Exit)

proof

fix *a* **assume** *valid-edge wfp a* **and** *sourcenode a = (Main, Exit)*

with *wf* **show** *False* **by**(*auto elim:PCFG.cases simp:valid-edge-def*)

next

show *get-proc (Main, Exit) = Main* **by** *simp*

next

fix *a Q p f*

assume *valid-edge wfp a* **and** *kind a = Q \leftrightarrow pf*

and *targetnode a = (Main, Exit)*

thus *False* **by**(*auto elim:PCFG.cases simp:valid-edge-def*)

next

have *prog,procs* \vdash *(Main,Entry) \rightarrow (Main,Exit)*

by(*fastforce intro:Main Proc-CFG-Entry-Exit*)

thus $\exists a.$ *valid-edge wfp a* \wedge

sourcenode a = (Main, Entry) \wedge

targetnode a = (Main, Exit) \wedge kind a = (Main, Exit) \rightarrow

by(*fastforce simp:valid-edge-def*)

qed

qed

end

2.6 Labels

theory *Labels* **imports** *Com* **begin**

Labels describe a mapping from the inner node label to the matching command

inductive *labels* :: *cmd* \Rightarrow *nat* \Rightarrow *cmd* \Rightarrow *bool*

where

Labels-Base:

labels c 0 c

| *Labels-LAss*:

```

labels (V:=e) 1 Skip

| Labels-Seq1:
labels c1 l c  $\implies$  labels (c1;;c2) l (c;;c2)

| Labels-Seq2:
labels c2 l c  $\implies$  labels (c1;;c2) (l + #:c1) c

| Labels-CondTrue:
labels c1 l c  $\implies$  labels (if (b) c1 else c2) (l + 1) c

| Labels-CondFalse:
labels c2 l c  $\implies$  labels (if (b) c1 else c2) (l + #:c1 + 1) c

| Labels-WhileBody:
labels c' l c  $\implies$  labels (while(b) c') (l + 2) (c;;while(b) c')

| Labels-WhileExit:
labels (while(b) c') 1 Skip

| Labels-Call:
labels (Call p es rets) 1 Skip

lemma label-less-num-inner-nodes:
labels c l c'  $\implies$  l < #:c
proof(induct c arbitrary:l c')
  case Skip
  from ⟨labels Skip l c'⟩ show ?case by(fastforce elim:labels.cases)
next
  case (LAss V e)
  from ⟨labels (V:=e) l c'⟩ show ?case by(fastforce elim:labels.cases)
next
  case (Seq c1 c2)
  note IH1 = ⟨ $\bigwedge$ l c'. labels c1 l c'  $\implies$  l < #:c1⟩
  note IH2 = ⟨ $\bigwedge$ l c'. labels c2 l c'  $\implies$  l < #:c2⟩
  from ⟨labels (c1;;c2) l c'⟩ IH1 IH2 show ?case
  by simp(erule labels.cases,auto,force)
next
  case (Cond b c1 c2)
  note IH1 = ⟨ $\bigwedge$ l c'. labels c1 l c'  $\implies$  l < #:c1⟩
  note IH2 = ⟨ $\bigwedge$ l c'. labels c2 l c'  $\implies$  l < #:c2⟩
  from ⟨labels (if (b) c1 else c2) l c'⟩ IH1 IH2 show ?case
  by simp(erule labels.cases,auto,force)
next
  case (While b c)
  note IH = ⟨ $\bigwedge$ l c'. labels c l c'  $\implies$  l < #:c⟩
  from ⟨labels (while (b) c) l c'⟩ IH show ?case
  by simp(erule labels.cases,fastforce+)

```

```

next
  case (Call p es rets)
  thus ?case by simp(erule labels.cases.fastforce+)
qed

declare One-nat-def [simp del]

lemma less-num-inner-nodes-label:
  assumes  $l < \# : c$  obtains  $c'$  where labels  $c$   $l$   $c'$ 
proof(atomize-elim)
  from  $\langle l < \# : c \rangle$  show  $\exists c'. \text{labels } c \ l \ c'$ 
proof(induct c arbitrary:l)
  case Skip
  from  $\langle l < \# : \text{Skip} \rangle$  have  $l = 0$  by simp
  thus ?case by(fastforce intro:Labels-Base)
next
  case (LAss V e)
  from  $\langle l < \# : (V := e) \rangle$  have  $l = 0 \vee l = 1$  by auto
  thus ?case by(auto intro:Labels-Base Labels-LAss)
next
  case (Seq c1 c2)
  note IH1 =  $\langle \bigwedge l. l < \# : c_1 \implies \exists c'. \text{labels } c_1 \ l \ c' \rangle$ 
  note IH2 =  $\langle \bigwedge l. l < \# : c_2 \implies \exists c'. \text{labels } c_2 \ l \ c' \rangle$ 
  show ?case
  proof(cases  $l < \# : c_1$ )
    case True
    from IH1[OF this] obtain  $c'$  where labels  $c_1 \ l \ c'$  by auto
    hence labels  $(c_1;;c_2) \ l \ (c';;c_2)$  by(fastforce intro:Labels-Seq1)
    thus ?thesis by auto
  next
    case False
    hence  $\# : c_1 \leq l$  by simp
    then obtain  $l'$  where  $l = l' + \# : c_1$  and  $l' = l - \# : c_1$  by simp
    from  $\langle l = l' + \# : c_1 \rangle \langle l < \# : c_1;;c_2 \rangle$  have  $l' < \# : c_2$  by simp
    from IH2[OF this] obtain  $c'$  where labels  $c_2 \ l' \ c'$  by auto
    with  $\langle l = l' + \# : c_1 \rangle$  have labels  $(c_1;;c_2) \ l \ c'$ 
      by(fastforce intro:Labels-Seq2)
    thus ?thesis by auto
  qed
next
  case (Cond b c1 c2)
  note IH1 =  $\langle \bigwedge l. l < \# : c_1 \implies \exists c'. \text{labels } c_1 \ l \ c' \rangle$ 
  note IH2 =  $\langle \bigwedge l. l < \# : c_2 \implies \exists c'. \text{labels } c_2 \ l \ c' \rangle$ 
  show ?case
  proof(cases  $l = 0$ )
    case True
    thus ?thesis by(fastforce intro:Labels-Base)
  next

```

```

case False
hence  $0 < l$  by simp
then obtain  $l'$  where  $l = l' + 1$  and  $l' = l - 1$  by simp
thus ?thesis
proof(cases  $l' < \# : c_1$ )
  case True
  from IH1[OF this] obtain  $c'$  where labels  $c_1$   $l'$   $c'$  by auto
  with  $\langle l = l' + 1 \rangle$  have labels (if (b)  $c_1$  else  $c_2$ )  $l$   $c'$ 
    by(fastforce dest:Labels-CondTrue)
  thus ?thesis by auto
next
case False
hence  $\# : c_1 \leq l'$  by simp
then obtain  $l''$  where  $l' = l'' + \# : c_1$  and  $l'' = l' - \# : c_1$  by simp
from  $\langle l' = l'' + \# : c_1 \rangle \langle l = l' + 1 \rangle \langle l < \# : \text{if (b) } c_1 \text{ else } c_2 \rangle$ 
have  $l'' < \# : c_2$  by simp
from IH2[OF this] obtain  $c'$  where labels  $c_2$   $l''$   $c'$  by auto
with  $\langle l' = l'' + \# : c_1 \rangle \langle l = l' + 1 \rangle$  have labels (if (b)  $c_1$  else  $c_2$ )  $l$   $c'$ 
  by(fastforce dest:Labels-CondFalse)
thus ?thesis by auto
qed
qed
next
case (While b c')
note IH =  $\langle \bigwedge l. l < \# : c' \implies \exists c''. \text{labels } c' \ l \ c'' \rangle$ 
show ?case
proof(cases  $l < 1$ )
  case True
  hence  $l = 0$  by simp
  thus ?thesis by(fastforce intro:Labels-Base)
next
case False
show ?thesis
proof(cases  $l < 2$ )
  case True
  with  $\langle \neg l < 1 \rangle$  have  $l = 1$  by simp
  thus ?thesis by(fastforce intro:Labels-WhileExit)
next
case False
with  $\langle \neg l < 1 \rangle$  have  $2 \leq l$  by simp
then obtain  $l'$  where  $l = l' + 2$  and  $l' = l - 2$ 
  by(simp del:add-2-eq-Suc')
from  $\langle l = l' + 2 \rangle \langle l < \# : \text{while (b) } c' \rangle$  have  $l' < \# : c'$  by simp
from IH[OF this] obtain  $c''$  where labels  $c' \ l' \ c''$  by auto
with  $\langle l = l' + 2 \rangle$  have labels (while (b)  $c'$ )  $l$  ( $c'' ; \text{while (b) } c'$ )
  by(fastforce dest:Labels-WhileBody)
thus ?thesis by auto
qed
qed

```

```

next
  case (Call p es rets)
  show ?case
  proof(cases l < 1)
    case True
    hence l = 0 by simp
    thus ?thesis by(fastforce intro:Labels-Base)
  next
  case False
  with ⟨l < #:Call p es rets⟩ have l = 1 by simp
  thus ?thesis by(fastforce intro:Labels-Call)
qed
qed
qed

```

lemma labels-det:

```

labels c l c' ⇒ (∧c''. labels c l c'' ⇒ c' = c'')
proof(induct rule:labels.induct)
  case (Labels-Base c c'')
  from ⟨labels c 0 c'⟩ obtain l where labels c l c'' and l = 0 by auto
  thus ?case by(induct rule:labels.induct,auto)
next
  case (Labels-Seq1 c1 l c c2)
  note IH = ⟨∧c''. labels c1 l c'' ⇒ c = c''⟩
  from ⟨labels c1 l c⟩ have l < #:c1 by(fastforce intro:label-less-num-inner-nodes)
  with ⟨labels (c1;;c2) l c'⟩ obtain cx where c'' = cx;;c2 ∧ labels c1 l cx
  by(fastforce elim:labels.cases intro:Labels-Base)
  hence [simp]:c'' = cx;;c2 and labels c1 l cx by simp-all
  from IH[OF ⟨labels c1 l cx⟩] show ?case by simp
next
  case (Labels-Seq2 c2 l c c1)
  note IH = ⟨∧c''. labels c2 l c'' ⇒ c = c''⟩
  from ⟨labels (c1;;c2) (l + #:c1) c'⟩ ⟨labels c2 l c⟩ have labels c2 l c''
  by(auto elim:labels.cases dest:label-less-num-inner-nodes)
  from IH[OF this] show ?case .
next
  case (Labels-CondTrue c1 l c b c2)
  note IH = ⟨∧c''. labels c1 l c'' ⇒ c = c''⟩
  from ⟨labels (if (b) c1 else c2) (l + 1) c'⟩ ⟨labels c1 l c⟩ have labels c1 l c''
  by(fastforce elim:labels.cases dest:label-less-num-inner-nodes)
  from IH[OF this] show ?case .
next
  case (Labels-CondFalse c2 l c b c1)
  note IH = ⟨∧c''. labels c2 l c'' ⇒ c = c''⟩
  from ⟨labels (if (b) c1 else c2) (l + #:c1 + 1) c'⟩ ⟨labels c2 l c⟩
  have labels c2 l c''
  by(fastforce elim:labels.cases dest:label-less-num-inner-nodes)
  from IH[OF this] show ?case .

```

```

next
  case (Labels-WhileBody c' l c b)
  note IH = ⟨ $\bigwedge c''$ . labels c' l c''  $\implies$  c = c''⟩
  from ⟨labels (while (b) c') (l + 2) c'⟩ ⟨labels c' l c⟩
  obtain cx where c'' = cx;;while (b) c'  $\wedge$  labels c' l cx
  by -(erule labels.cases,auto)
  hence [simp]:c'' = cx;;while (b) c' and labels c' l cx by simp-all
  from IH[OF ⟨labels c' l cx⟩] show ?case by simp
qed (fastforce elim:labels.cases)+

```

```

definition label :: cmd  $\Rightarrow$  nat  $\Rightarrow$  cmd
  where label c n  $\equiv$  (THE c'. labels c n c')

```

```

lemma labels-THE:
  labels c l c'  $\implies$  (THE c'. labels c l c') = c'
by(fastforce intro:the-equality dest:labels-det)

```

```

lemma labels-label:labels c l c'  $\implies$  label c l = c'
by(fastforce intro:labels-THE simp:label-def)

```

end

2.7 Instantiate well-formedness locales with Proc CFG

```

theory WellFormed imports Interpretation Labels ../StaticInter/CFGExit-wf begin

```

2.7.1 Determining the first atomic command

```

fun fst-cmd :: cmd  $\Rightarrow$  cmd
  where fst-cmd (c1;;c2) = fst-cmd c1
    | fst-cmd c = c

```

```

lemma Proc-CFG-Call-target-fst-cmd-Skip:
  [[labels prog l' c; prog  $\vdash$  n -CEdge (p,es,rets) $\rightarrow_p$  Label l']]
 $\implies$  fst-cmd c = Skip

```

```

proof(induct arbitrary:n rule:labels.induct)
  case (Labels-Seq1 c1 l c c2)
  note IH = ⟨ $\bigwedge n$ . c1  $\vdash$  n -CEdge (p, es, rets) $\rightarrow_p$  Label l  $\implies$  fst-cmd c = Skip⟩
  from ⟨c1;; c2  $\vdash$  n -CEdge (p, es, rets) $\rightarrow_p$  Label l⟩ ⟨labels c1 l c⟩
  have c1  $\vdash$  n -CEdge (p, es, rets) $\rightarrow_p$  Label l
  apply - apply(erule Proc-CFG.cases,auto dest:Proc-CFG-Call-Labels)

```

```

  by(case-tac n')(auto dest:label-less-num-inner-nodes)
from IH[OF this] show ?case by simp
next
case (Labels-Seq2 c2 l c c1)
note IH = ⟨ $\bigwedge n. c_2 \vdash n - CEdge(p, es, rets) \rightarrow_p Label\ l \implies fst-cmd\ c = Skip$ ⟩
from ⟨ $c_1;; c_2 \vdash n - CEdge(p, es, rets) \rightarrow_p Label(l + \#:c_1)$ ⟩ ⟨labels c2 l c⟩
obtain nx where  $c_2 \vdash nx - CEdge(p, es, rets) \rightarrow_p Label\ l$ 
  apply - apply(erule Proc-CFG.cases)
  apply(auto dest:Proc-CFG-targetlabel-less-num-nodes Proc-CFG-Call-Labels)
  by(case-tac n') auto
from IH[OF this] show ?case by simp
next
case (Labels-CondTrue c1 l c b c2)
note IH = ⟨ $\bigwedge n. c_1 \vdash n - CEdge(p, es, rets) \rightarrow_p Label\ l \implies fst-cmd\ c = Skip$ ⟩
from ⟨if (b) c1 else c2  $\vdash n - CEdge(p, es, rets) \rightarrow_p Label(l + 1)$ ⟩ ⟨labels c1 l c⟩
obtain nx where  $c_1 \vdash nx - CEdge(p, es, rets) \rightarrow_p Label\ l$ 
  apply - apply(erule Proc-CFG.cases,auto)
  apply(case-tac n') apply auto
  by(case-tac n')(auto dest:label-less-num-inner-nodes)
from IH[OF this] show ?case by simp
next
case (Labels-CondFalse c2 l c b c1)
note IH = ⟨ $\bigwedge n. c_2 \vdash n - CEdge(p, es, rets) \rightarrow_p Label\ l \implies fst-cmd\ c = Skip$ ⟩
from ⟨if (b) c1 else c2  $\vdash n - CEdge(p, es, rets) \rightarrow_p Label(l + \#:c_1 + 1)$ ⟩
  ⟨labels c2 l c⟩
obtain nx where  $c_2 \vdash nx - CEdge(p, es, rets) \rightarrow_p Label\ l$ 
  apply - apply(erule Proc-CFG.cases,auto)
  apply(case-tac n') apply(auto dest:Proc-CFG-targetlabel-less-num-nodes)
  by(case-tac n') auto
from IH[OF this] show ?case by simp
next
case (Labels-WhileBody c' l c b)
note IH = ⟨ $\bigwedge n. c' \vdash n - CEdge(p, es, rets) \rightarrow_p Label\ l \implies fst-cmd\ c = Skip$ ⟩
from ⟨while (b) c'  $\vdash n - CEdge(p, es, rets) \rightarrow_p Label(l + 2)$ ⟩ ⟨labels c' l c⟩
obtain nx where  $c' \vdash nx - CEdge(p, es, rets) \rightarrow_p Label\ l$ 
  apply - apply(erule Proc-CFG.cases,auto)
  by(case-tac n') auto
from IH[OF this] show ?case by simp
next
case (Labels-Call px esx retsx)
from ⟨Call px esx retsx  $\vdash n - CEdge(p, es, rets) \rightarrow_p Label\ 1$ ⟩
show ?case by(fastforce elim:Proc-CFG.cases)
qed(auto dest:Proc-CFG-Call-Labels)

```

lemma Proc-CFG-Call-source-fst-cmd-Call:
 $\llbracket labels\ prog\ l\ c; prog \vdash Label\ l - CEdge(p, es, rets) \rightarrow_p n \rrbracket$
 $\implies \exists p\ es\ rets. fst-cmd\ c = Call\ p\ es\ rets$
proof(induct arbitrary:n' rule:labels.induct)

```

case (Labels-Base  $c\ n'$ )
from  $\langle c \vdash \text{Label } 0 - \text{CEdge } (p, es, rets) \rightarrow_p n' \rangle$  show ?case
  by(induct  $c$  Label 0 CEdge  $(p, es, rets)$   $n'$  rule:Proc-CFG.induct) auto
next
case (Labels-LAss  $V\ e\ n'$ )
from  $\langle V := e \vdash \text{Label } 1 - \text{CEdge } (p, es, rets) \rightarrow_p n' \rangle$  show ?case
  by(fastforce elim:Proc-CFG.cases)
next
case (Labels-Seq1  $c_1\ l\ c\ c_2$ )
note  $IH = \langle \bigwedge n'. c_1 \vdash \text{Label } l - \text{CEdge } (p, es, rets) \rightarrow_p n' \rangle$ 
   $\implies \exists p\ es\ rets. \text{fst-cmd } c = \text{Call } p\ es\ rets$ 
from  $\langle c_1;; c_2 \vdash \text{Label } l - \text{CEdge } (p, es, rets) \rightarrow_p n' \rangle$   $\langle \text{labels } c_1\ l\ c \rangle$ 
have  $c_1 \vdash \text{Label } l - \text{CEdge } (p, es, rets) \rightarrow_p n'$ 
  apply – apply(erule Proc-CFG.cases,auto dest:Proc-CFG-Call-Labels)
  by(case-tac n)(auto dest:label-less-num-inner-nodes)
from  $IH[OF\ this]$  show ?case by simp
next
case (Labels-Seq2  $c_2\ l\ c\ c_1$ )
note  $IH = \langle \bigwedge n'. c_2 \vdash \text{Label } l - \text{CEdge } (p, es, rets) \rightarrow_p n' \rangle$ 
   $\implies \exists p\ es\ rets. \text{fst-cmd } c = \text{Call } p\ es\ rets$ 
from  $\langle c_1;; c_2 \vdash \text{Label } (l + \# : c_1) - \text{CEdge } (p, es, rets) \rightarrow_p n' \rangle$   $\langle \text{labels } c_2\ l\ c \rangle$ 
obtain  $nx$  where  $c_2 \vdash \text{Label } l - \text{CEdge } (p, es, rets) \rightarrow_p nx$ 
  apply – apply(erule Proc-CFG.cases)
  apply(auto dest:Proc-CFG-sourcelabel-less-num-nodes Proc-CFG-Call-Labels)
  by(case-tac n) auto
from  $IH[OF\ this]$  show ?case by simp
next
case (Labels-CondTrue  $c_1\ l\ c\ b\ c_2$ )
note  $IH = \langle \bigwedge n'. c_1 \vdash \text{Label } l - \text{CEdge } (p, es, rets) \rightarrow_p n' \rangle$ 
   $\implies \exists p\ es\ rets. \text{fst-cmd } c = \text{Call } p\ es\ rets$ 
from  $\langle \text{if } (b)\ c_1\ \text{else } c_2 \vdash \text{Label } (l + 1) - \text{CEdge } (p, es, rets) \rightarrow_p n' \rangle$   $\langle \text{labels } c_1\ l\ c \rangle$ 
obtain  $nx$  where  $c_1 \vdash \text{Label } l - \text{CEdge } (p, es, rets) \rightarrow_p nx$ 
  apply – apply(erule Proc-CFG.cases,auto)
  apply(case-tac n) apply auto
  by(case-tac n)(auto dest:label-less-num-inner-nodes)
from  $IH[OF\ this]$  show ?case by simp
next
case (Labels-CondFalse  $c_2\ l\ c\ b\ c_1$ )
note  $IH = \langle \bigwedge n'. c_2 \vdash \text{Label } l - \text{CEdge } (p, es, rets) \rightarrow_p n' \rangle$ 
   $\implies \exists p\ es\ rets. \text{fst-cmd } c = \text{Call } p\ es\ rets$ 
from  $\langle \text{if } (b)\ c_1\ \text{else } c_2 \vdash \text{Label } (l + \# : c_1 + 1) - \text{CEdge } (p, es, rets) \rightarrow_p n' \rangle$ 
   $\langle \text{labels } c_2\ l\ c \rangle$ 
obtain  $nx$  where  $c_2 \vdash \text{Label } l - \text{CEdge } (p, es, rets) \rightarrow_p nx$ 
  apply – apply(erule Proc-CFG.cases,auto)
  apply(case-tac n) apply(auto dest:Proc-CFG-sourcelabel-less-num-nodes)
  by(case-tac n) auto
from  $IH[OF\ this]$  show ?case by simp
next

```



```

case (Labels-WhileBody  $c' l c b$ )
note  $IH = \langle \wedge n'. c' \vdash \text{Label } l - \text{CEdge } (p, es, rets) \rightarrow_p n' \rangle$ 
 $\implies \exists p es rets. \text{fst-cmd } c = \text{Call } p es rets \rangle$ 
from  $\langle \text{while } (b) c' \vdash \text{Label } (l + 2) - \text{CEdge } (p, es, rets) \rightarrow_p n' \rangle \langle \text{labels } c' l c \rangle$ 
obtain  $nx$  where  $c' \vdash \text{Label } l - \text{CEdge } (p, es, rets) \rightarrow_p nx$ 
apply – apply(erule Proc-CFG.cases, auto dest: Proc-CFG-Call-Labels)
by(case-tac  $n$ ) auto
from  $IH[OF \text{ this}]$  show ?case by simp
next
case (Labels-WhileExit  $b c' n'$ )
have  $\text{while } (b) c' \vdash \text{Label } 1 - \text{IEdge } \uparrow id \rightarrow_p \text{Exit}$  by(rule Proc-CFG-WhileFalseSkip)
with  $\langle \text{while } (b) c' \vdash \text{Label } 1 - \text{CEdge } (p, es, rets) \rightarrow_p n' \rangle$ 
have False by(rule Proc-CFG-Call-Intra-edge-not-same-source)
thus ?case by simp
next
case (Labels-Call  $px esx retsx$ )
from  $\langle \text{Call } px esx retsx \vdash \text{Label } 1 - \text{CEdge } (p, es, rets) \rightarrow_p n' \rangle$ 
show ?case by(fastforce elim: Proc-CFG.cases)
qed

```

2.7.2 Definition of Def and Use sets

ParamDefs

lemma *PCFG-CallEdge-THE-rets*:

```

 $prog \vdash n - \text{CEdge } (p, es, rets) \rightarrow_p n'$ 
 $\implies (\text{THE } rets'. \exists p' es' n. prog \vdash n - \text{CEdge}(p', es', rets') \rightarrow_p n') = rets$ 
by(fastforce intro:the-equality dest:Proc-CFG-Call-nodes-eq')

```

definition *ParamDefs-proc* :: $cmd \Rightarrow label \Rightarrow vname \text{ list}$

```

where ParamDefs-proc  $c n \equiv$ 
  if  $(\exists n' p' es' rets'. c \vdash n' - \text{CEdge}(p', es', rets') \rightarrow_p n)$  then
     $(\text{THE } rets'. \exists p' es' n'. c \vdash n' - \text{CEdge}(p', es', rets') \rightarrow_p n)$ 
  else  $\square$ 

```

lemma *in-procs-THE-in-procs-cmd*:

```

 $\llbracket \text{well-formed procs}; (p, ins, outs, c) \in \text{set procs} \rrbracket$ 
 $\implies (\text{THE } c'. \exists ins' outs'. (p, ins', outs', c') \in \text{set procs}) = c$ 
by(fastforce intro:the-equality)

```

definition *ParamDefs* :: $wf\text{-prog} \Rightarrow node \Rightarrow vname \text{ list}$

```

where ParamDefs  $wfp n \equiv \text{let } (prog, procs) = \text{Rep-wf-prog } wfp; (p, l) = n \text{ in}$ 
  if  $(p = \text{Main})$  then ParamDefs-proc  $prog l$ 
  else if  $(\exists ins outs c. (p, ins, outs, c) \in \text{set procs})$ 
    then ParamDefs-proc  $(\text{THE } c'. \exists ins' outs'. (p, ins', outs', c') \in \text{set procs}) l$ 
  else  $\square$ 

```

lemma *ParamDefs-Main-Return-target*:
 $\llbracket \text{Rep-wf-prog } wfp = (prog, procs); prog \vdash n - CEdge(p', es, rets) \rightarrow_p n' \rrbracket$
 $\implies \text{ParamDefs } wfp \text{ (Main, } n') = rets$
by(*fastforce dest:PCFG-CallEdge-THE-rets simp:ParamDefs-def ParamDefs-proc-def*)

lemma *ParamDefs-Proc-Return-target*:
assumes *Rep-wf-prog* $wfp = (prog, procs)$
and $(p, ins, outs, c) \in \text{set } procs$ **and** $c \vdash n - CEdge(p', es, rets) \rightarrow_p n'$
shows $\text{ParamDefs } wfp \text{ (} p, n') = rets$
proof –
from $\langle \text{Rep-wf-prog } wfp = (prog, procs) \rangle$ **have** *well-formed procs*
by(*fastforce intro:wf-wf-prog*)
with $\langle (p, ins, outs, c) \in \text{set } procs \rangle$ **have** $p \neq \text{Main}$ **by** *fastforce*
moreover
from $\langle \text{well-formed procs} \rangle \langle (p, ins, outs, c) \in \text{set } procs \rangle$
have $(\text{THE } c'. \exists ins' outs'. (p, ins', outs', c') \in \text{set } procs) = c$
by(*rule in-procs-THE-in-procs-cmd*)
ultimately show *?thesis* **using** *assms*
by(*fastforce dest:PCFG-CallEdge-THE-rets simp:ParamDefs-def ParamDefs-proc-def*)
qed

lemma *ParamDefs-Main-IEdge-Nil*:
 $\llbracket \text{Rep-wf-prog } wfp = (prog, procs); prog \vdash n - IEdge et \rightarrow_p n' \rrbracket$
 $\implies \text{ParamDefs } wfp \text{ (Main, } n') = []$
by(*fastforce dest:Proc-CFG-Call-Intra-edge-not-same-target simp:ParamDefs-def ParamDefs-proc-def*)

lemma *ParamDefs-Proc-IEdge-Nil*:
assumes *Rep-wf-prog* $wfp = (prog, procs)$
and $(p, ins, outs, c) \in \text{set } procs$ **and** $c \vdash n - IEdge et \rightarrow_p n'$
shows $\text{ParamDefs } wfp \text{ (} p, n') = []$
proof –
from $\langle \text{Rep-wf-prog } wfp = (prog, procs) \rangle$ **have** *well-formed procs*
by(*fastforce intro:wf-wf-prog*)
with $\langle (p, ins, outs, c) \in \text{set } procs \rangle$ **have** $p \neq \text{Main}$ **by** *fastforce*
moreover
from $\langle \text{well-formed procs} \rangle \langle (p, ins, outs, c) \in \text{set } procs \rangle$
have $(\text{THE } c'. \exists ins' outs'. (p, ins', outs', c') \in \text{set } procs) = c$
by(*rule in-procs-THE-in-procs-cmd*)
ultimately show *?thesis* **using** *assms*
by(*fastforce dest:Proc-CFG-Call-Intra-edge-not-same-target simp:ParamDefs-def ParamDefs-proc-def*)
qed

lemma *ParamDefs-Main-CEdge-Nil*:
 $\llbracket \text{Rep-wf-prog } wfp = (prog, procs); prog \vdash n' - CEdge(p', es, rets) \rightarrow_p n' \rrbracket$
 $\implies \text{ParamDefs } wfp \text{ (Main, } n') = []$
by(*fastforce dest:Proc-CFG-Call-targetnode-no-Call-sourcenode*)

simp:ParamDefs-def ParamDefs-proc-def)

lemma *ParamDefs-Proc-CEdge-Nil*:

assumes *Rep-wf-prog wfp = (prog,procs)*

and $(p,ins,outs,c) \in set\ procs$ **and** $c \vdash n' - CEdge(p',es,rets) \rightarrow_p n''$

shows *ParamDefs wfp (p,n') = []*

proof –

from $\langle Rep-wf-prog\ wfp = (prog,procs) \rangle$ **have** *well-formed procs*

by *(fastforce intro:wf-wf-prog)*

with $\langle (p,ins,outs,c) \in set\ procs \rangle$ **have** $p \neq Main$ **by** *fastforce*

moreover

from $\langle well-formed\ procs \rangle$ $\langle (p,ins,outs,c) \in set\ procs \rangle$

have $(THE\ c'.\ \exists\ ins'\ outs'.\ (p,ins',outs',c') \in set\ procs) = c$

by *(rule in-procs-THE-in-procs-cmd)*

ultimately show *?thesis using assms*

by *(fastforce dest:Proc-CFG-Call-targetnode-no-Call-sourcenode*

simp:ParamDefs-def ParamDefs-proc-def)

qed

lemma **assumes** *valid-edge wfp a* **and** $kind\ a = Q' \leftrightarrow_p f'$

and $(p,ins,outs) \in set\ (lift-procs\ wfp)$

shows *ParamDefs-length:length (ParamDefs wfp (targetnode a)) = length outs*

(is *?length*)

and *Return-update:f' cf cf' = cf'(ParamDefs wfp (targetnode a) [:=] map cf outs)*

(is *?update*)

proof –

from *Rep-wf-prog[of wfp]*

obtain *prog procs* **where** *[simp]:Rep-wf-prog wfp = (prog,procs)*

by *(fastforce simp:wf-prog-def)*

hence *wf prog procs* **by** *(rule wf-wf-prog)*

hence *wf:well-formed procs* **by** *fastforce*

from *assms* **have** $prog,procs \vdash sourcenode\ a - kind\ a \rightarrow targetnode\ a$

by *(simp add:valid-edge-def)*

from *this* $\langle kind\ a = Q' \leftrightarrow_p f' \rangle$ **wf** **have** $?length \wedge ?update$

proof *(induct sourcenode a kind a targetnode a rule:PCFG.induct)*

case $(MainReturn\ l\ p'\ es\ rets\ l'\ insx\ outsx\ cx)$

from $\langle \lambda cf.\ snd\ cf = (Main,\ Label\ l') \leftrightarrow_p \lambda cf\ cf'.\ cf'(rets\ [:=]\ map\ cf\ outsx) =$

$kind\ a \rangle$ $\langle kind\ a = Q' \leftrightarrow_p f' \rangle$ **have** $p' = p$

and $f':f' = (\lambda cf\ cf'.\ cf'(rets\ [:=]\ map\ cf\ outsx))$ **by** *simp-all*

with $\langle well-formed\ procs \rangle$ $\langle (p',insx,outsx,cx) \in set\ procs \rangle$

$\langle (p,ins,outs) \in set\ (lift-procs\ wfp) \rangle$

have *[simp]:outsx = outs* **by** *fastforce*

from $\langle prog \vdash Label\ l - CEdge\ (p',es,rets) \rightarrow_p Label\ l' \rangle$

have *containsCall procs prog [] p'* **by** *(rule Proc-CFG-Call-containsCall)*

with $\langle wf\ prog\ procs \rangle$ $\langle (p',insx,outsx,cx) \in set\ procs \rangle$

$\langle prog \vdash Label\ l - CEdge\ (p',es,rets) \rightarrow_p Label\ l' \rangle$

have $length\ rets = length\ outs$ **by** *fastforce*

```

from  $\langle \text{prog} \vdash \text{Label } l - \text{CEdge } (p', es, \text{rets}) \rightarrow_p \text{Label } l' \rangle$ 
have  $\text{ParamDefs wfp } (\text{Main}, \text{Label } l') = \text{rets}$ 
  by  $(\text{fastforce intro:ParamDefs-Main-Return-target})$ 
with  $\langle (\text{Main}, \text{Label } l') = \text{targetnode } a \rangle f' \langle \text{length } \text{rets} = \text{length } \text{outs} \rangle$ 
show  $?thesis$  by  $\text{simp}$ 
next
case  $(\text{ProcReturn } px \text{ insx } \text{outsx } cx \text{ l } p' \text{ es } \text{rets } l' \text{ ins}' \text{ outs}' c' ps)$ 
from  $\langle \lambda cf. \text{snd } cf = (px, \text{Label } l') \leftrightarrow_p \lambda cf. cf'. cf'(\text{rets } [:=] \text{map } cf \text{ outs}') =$ 
   $\text{kind } a \rangle \langle \text{kind } a = Q' \leftrightarrow_p f' \rangle$ 
have  $p' = p$  and  $f':f' = (\lambda cf. cf'. cf'(\text{rets } [:=] \text{map } cf \text{ outs}'))$ 
  by  $\text{simp-all}$ 
with  $\langle \text{well-formed } \text{procs} \rangle \langle (p', \text{ins}', \text{outs}', c') \in \text{set } \text{procs} \rangle$ 
   $\langle (p, \text{ins}, \text{outs}) \in \text{set } (\text{lift-procs } \text{wfp}) \rangle$ 
have  $[\text{simp}]: \text{outs}' = \text{outs}$  by  $\text{fastforce}$ 
from  $\langle cx \vdash \text{Label } l - \text{CEdge } (p', es, \text{rets}) \rightarrow_p \text{Label } l' \rangle$ 
have  $\text{containsCall } \text{procs } cx \ [] \ p'$  by  $(\text{rule Proc-CFG-Call-containsCall})$ 
with  $\langle \text{containsCall } \text{procs } \text{prog } ps \ px \rangle \langle (px, \text{insx}, \text{outsx}, cx) \in \text{set } \text{procs} \rangle$ 
have  $\text{containsCall } \text{procs } \text{prog } (ps@[px]) \ p'$  by  $(\text{rule containsCall-in-proc})$ 
with  $\langle \text{wf } \text{prog } \text{procs} \rangle \langle (p', \text{ins}', \text{outs}', c') \in \text{set } \text{procs} \rangle$ 
   $\langle cx \vdash \text{Label } l - \text{CEdge } (p', es, \text{rets}) \rightarrow_p \text{Label } l' \rangle$ 
have  $\text{length } \text{rets} = \text{length } \text{outs}$  by  $\text{fastforce}$ 
from  $\langle (px, \text{insx}, \text{outsx}, cx) \in \text{set } \text{procs} \rangle$ 
   $\langle cx \vdash \text{Label } l - \text{CEdge } (p', es, \text{rets}) \rightarrow_p \text{Label } l' \rangle$ 
have  $\text{ParamDefs wfp } (px, \text{Label } l') = \text{rets}$ 
  by  $(\text{fastforce intro:ParamDefs-Proc-Return-target simp:set-conv-nth})$ 
with  $\langle (px, \text{Label } l') = \text{targetnode } a \rangle f' \langle \text{length } \text{rets} = \text{length } \text{outs} \rangle$ 
show  $?thesis$  by  $\text{simp}$ 
qed  $\text{auto}$ 
thus  $?length$  and  $?update$  by  $\text{simp-all}$ 
qed

```

ParamUses

```

fun  $\text{fv} :: \text{expr} \Rightarrow \text{vname set}$ 
where
   $\text{fv } (\text{Val } v) = \{\}$ 
   $|\ \text{fv } (\text{Var } V) = \{V\}$ 
   $|\ \text{fv } (e1 \ll \text{bop} \gg e2) = (\text{fv } e1 \cup \text{fv } e2)$ 

```

lemma rhs-interpret-eq:

```

 $[\text{state-check } cf \ e \ v'; \forall V \in \text{fv } e. cf \ V = cf' \ V]$ 
 $\implies \text{state-check } cf' \ e \ v'$ 

```

proof $(\text{induct } e \text{ arbitrary}:v')$

```

case  $(\text{Val } v)$ 
from  $\langle \text{state-check } cf \ (\text{Val } v) \ v' \rangle$  have  $v' = \text{Some } v$ 
  by  $(\text{fastforce elim:interpret.cases})$ 
thus  $?case$  by  $\text{simp}$ 
next

```

```

case (Var V)
hence cf' (V) = v' by(fastforce elim:interpret.cases)
thus ?case by simp
next
case (BinOp b1 bop b2)
note IH1 = ⟨ $\wedge v'. \llbracket \text{state-check } cf \text{ b1 } v' ; \forall V \in fv \text{ b1}. cf \text{ V} = cf' \text{ V} \rrbracket$ 
   $\implies \text{state-check } cf' \text{ b1 } v' \rangle$ 
note IH2 = ⟨ $\wedge v'. \llbracket \text{state-check } cf \text{ b2 } v' ; \forall V \in fv \text{ b2}. cf \text{ V} = cf' \text{ V} \rrbracket$ 
   $\implies \text{state-check } cf' \text{ b2 } v' \rangle$ 
from ⟨ $\forall V \in fv (b1 \ll bop \gg b2). cf \text{ V} = cf' \text{ V} \rangle$  have  $\forall V \in fv \text{ b1}. cf \text{ V} = cf' \text{ V}$ 
  and  $\forall V \in fv \text{ b2}. cf \text{ V} = cf' \text{ V}$  by simp-all
from ⟨state-check cf (b1  $\ll bop \gg$  b2) v'⟩
have ((state-check cf b1 None  $\wedge v' = \text{None}$ )  $\vee$ 
  (state-check cf b2 None  $\wedge v' = \text{None}$ ))  $\vee$ 
  ( $\exists v_1 v_2. \text{state-check } cf \text{ b1 } (\text{Some } v_1) \wedge \text{state-check } cf \text{ b2 } (\text{Some } v_2) \wedge$ 
  binop bop v1 v2 = v')
apply(cases interpret b1 cf,simp)
apply(cases interpret b2 cf,simp)
by(case-tac binop bop a aa,simp+)
thus ?case apply -
proof(erule disjE)+
  assume state-check cf b1 None  $\wedge v' = \text{None}$ 
  hence check:state-check cf b1 None and v' = None by simp-all
  from IH1[OF check  $\langle \forall V \in fv \text{ b1}. cf \text{ V} = cf' \text{ V} \rangle$ ] have state-check cf' b1 None
  .
  with ⟨v' = None⟩ show ?case by simp
next
assume state-check cf b2 None  $\wedge v' = \text{None}$ 
hence check:state-check cf b2 None and v' = None by simp-all
from IH2[OF check  $\langle \forall V \in fv \text{ b2}. cf \text{ V} = cf' \text{ V} \rangle$ ] have state-check cf' b2 None
  .
  with ⟨v' = None⟩ show ?case by(cases interpret b1 cf') simp+
next
assume  $\exists v_1 v_2. \text{state-check } cf \text{ b1 } (\text{Some } v_1) \wedge$ 
  state-check cf b2 (Some v2)  $\wedge$  binop bop v1 v2 = v'
then obtain v1 v2 where state-check cf b1 (Some v1)
  and state-check cf b2 (Some v2) and binop bop v1 v2 = v' by blast
from  $\langle \forall V \in fv (b1 \ll bop \gg b2). cf \text{ V} = cf' \text{ V} \rangle$  have  $\forall V \in fv \text{ b1}. cf \text{ V} = cf'$ 
V
  by simp
from IH1[OF  $\langle \text{state-check } cf \text{ b1 } (\text{Some } v_1) \rangle$  this]
have interpret b1 cf' = Some v1 .
from  $\langle \forall V \in fv (b1 \ll bop \gg b2). cf \text{ V} = cf' \text{ V} \rangle$  have  $\forall V \in fv \text{ b2}. cf \text{ V} = cf'$ 
V
  by simp
from IH2[OF  $\langle \text{state-check } cf \text{ b2 } (\text{Some } v_2) \rangle$  this]
have interpret b2 cf' = Some v2 .
with ⟨interpret b1 cf' = Some v1⟩ ⟨binop bop v1 v2 = v'⟩
show ?thesis by(cases v') simp+

```

qed
qed

lemma *PCFG-CallEdge-THE-es:*

$prog \vdash n - CEdge(p, es, rets) \rightarrow_p n'$
 $\implies (THE\ es'. \exists p' rets' n'. prog \vdash n - CEdge(p', es', rets') \rightarrow_p n') = es$
by(*fastforce intro:the-equality dest:Proc-CFG-Call-nodes-eq*)

definition *ParamUses-proc* :: *cmd* \Rightarrow *label* \Rightarrow *vname set list*

where *ParamUses-proc* *c* *n* \equiv
if ($\exists n' p' es' rets'. c \vdash n - CEdge(p', es', rets') \rightarrow_p n'$) *then*
(*map* *fv* (*THE* *es'*. $\exists p' rets' n'. c \vdash n - CEdge(p', es', rets') \rightarrow_p n'$))
else []

definition *ParamUses* :: *wf-prog* \Rightarrow *node* \Rightarrow *vname set list*

where *ParamUses* *wfp* *n* \equiv *let* (*prog*, *procs*) = *Rep-wf-prog* *wfp*; (*p*, *l*) = *n* *in*
(*if* (*p* = *Main*) *then* *ParamUses-proc* *prog* *l*
else (*if* ($\exists ins\ outs\ c. (p, ins, outs, c) \in set\ procs$)
then *ParamUses-proc* (*THE* *c'*. $\exists ins'\ outs'. (p, ins', outs', c') \in set\ procs$) *l*
else []))

lemma *ParamUses-Main-Return-target:*

$\llbracket Rep-wf-prog\ wfp = (prog, procs); prog \vdash n - CEdge(p', es, rets) \rightarrow_p n' \rrbracket$
 $\implies ParamUses\ wfp\ (Main, n) = map\ fv\ es$
by(*fastforce dest:PCFG-CallEdge-THE-es simp:ParamUses-def ParamUses-proc-def*)

lemma *ParamUses-Proc-Return-target:*

assumes *Rep-wf-prog* *wfp* = (*prog*, *procs*)
and (*p*, *ins*, *outs*, *c*) \in *set procs* **and** $c \vdash n - CEdge(p', es, rets) \rightarrow_p n'$
shows *ParamUses* *wfp* (*p*, *n*) = *map* *fv* *es*

proof –

from $\langle Rep-wf-prog\ wfp = (prog, procs) \rangle$ **have** *well-formed procs*
by(*fastforce intro:wf-wf-prog*)
with $\langle (p, ins, outs, c) \in set\ procs \rangle$ **have** $p \neq Main$ **by** *fastforce*
moreover
from $\langle well-formed\ procs \rangle$ $\langle (p, ins, outs, c) \in set\ procs \rangle$
have (*THE* *c'*. $\exists ins'\ outs'. (p, ins', outs', c') \in set\ procs$) = *c*
by(*rule in-procs-THE-in-procs-cmd*)
ultimately show *?thesis* **using** *assms*
by(*fastforce dest:PCFG-CallEdge-THE-es simp:ParamUses-def ParamUses-proc-def*)

qed

lemma *ParamUses-Main-IEdge-Nil:*

$\llbracket Rep-wf-prog\ wfp = (prog, procs); prog \vdash n - IEdge\ et \rightarrow_p n' \rrbracket$

$\implies \text{ParamUses wfp (Main,n) = []}$
by(*fastforce dest:Proc-CFG-Call-Intra-edge-not-same-source*
simp:ParamUses-def ParamUses-proc-def)

lemma *ParamUses-Proc-IEdge-Nil:*

assumes *Rep-wf-prog wfp = (prog,procs)*
and $(p,ins,outs,c) \in \text{set procs}$ **and** $c \vdash n - \text{IEdge } et \rightarrow_p n'$
shows $\text{ParamUses wfp (p,n) = []}$

proof –

from $\langle \text{Rep-wf-prog wfp} = (prog,procs) \rangle$ **have** *well-formed procs*
by(*fastforce intro:wf-wf-prog*)
with $\langle (p,ins,outs,c) \in \text{set procs} \rangle$ **have** $p \neq \text{Main}$ **by** *fastforce*
moreover
from $\langle \text{well-formed procs} \rangle$ $\langle (p,ins,outs,c) \in \text{set procs} \rangle$
have $(\text{THE } c'. \exists ins' outs'. (p,ins',outs',c') \in \text{set procs}) = c$
by(*rule in-procs-THE-in-procs-cmd*)
ultimately show *?thesis using assms*
by(*fastforce dest:Proc-CFG-Call-Intra-edge-not-same-source*
simp:ParamUses-def ParamUses-proc-def)

qed

lemma *ParamUses-Main-CEdge-Nil:*

$\llbracket \text{Rep-wf-prog wfp} = (prog,procs); prog \vdash n' - \text{CEdge}(p',es,rets) \rightarrow_p n \rrbracket$
 $\implies \text{ParamUses wfp (Main,n) = []}$

by(*fastforce dest:Proc-CFG-Call-targetnode-no-Call-sourcenode*
simp:ParamUses-def ParamUses-proc-def)

lemma *ParamUses-Proc-CEdge-Nil:*

assumes *Rep-wf-prog wfp = (prog,procs)*
and $(p,ins,outs,c) \in \text{set procs}$ **and** $c \vdash n' - \text{CEdge}(p',es,rets) \rightarrow_p n$
shows $\text{ParamUses wfp (p,n) = []}$

proof –

from $\langle \text{Rep-wf-prog wfp} = (prog,procs) \rangle$ **have** *well-formed procs*
by(*fastforce intro:wf-wf-prog*)
with $\langle (p,ins,outs,c) \in \text{set procs} \rangle$ **have** $p \neq \text{Main}$ **by** *fastforce*
moreover
from $\langle \text{well-formed procs} \rangle$
 $\langle (p,ins,outs,c) \in \text{set procs} \rangle$
have $(\text{THE } c'. \exists ins' outs'. (p,ins',outs',c') \in \text{set procs}) = c$
by(*rule in-procs-THE-in-procs-cmd*)
ultimately show *?thesis using assms*
by(*fastforce dest:Proc-CFG-Call-targetnode-no-Call-sourcenode*
simp:ParamUses-def ParamUses-proc-def)

qed

Def

fun *lhs* :: *cmd* \Rightarrow *vname set*
where

$lhs\ Skip$	$=$	$\{\}$
$ lhs\ (V:=e)$	$=$	$\{V\}$
$ lhs\ (c_1;;c_2)$	$=$	$lhs\ c_1$
$ lhs\ (if\ (b)\ c_1\ else\ c_2)$	$=$	$\{\}$
$ lhs\ (while\ (b)\ c)$	$=$	$\{\}$
$ lhs\ (Call\ p\ es\ rets)$	$=$	$\{\}$

lemma $lhs\text{-fst}\text{-cmd}:lhs\ (fst\text{-cmd}\ c) = lhs\ c$ **by**(*induct c*) *auto*

lemma *Proc-CFG-Call-source-empty-lhs:*

assumes $prog \vdash Label\ l - CEdge\ (p,es,rets) \rightarrow_p\ n'$

shows $lhs\ (label\ prog\ l) = \{\}$

proof –

from $\langle prog \vdash Label\ l - CEdge\ (p,es,rets) \rightarrow_p\ n' \rangle$ **have** $l < \# : prog$

by(*rule Proc-CFG-sourcelabel-less-num-nodes*)

then obtain c' **where** $labels\ prog\ l\ c'$

by(*erule less-num-inner-nodes-label*)

hence $label\ prog\ l = c'$ **by**(*rule labels-label*)

from $\langle labels\ prog\ l\ c' \rangle \langle prog \vdash Label\ l - CEdge\ (p,es,rets) \rightarrow_p\ n' \rangle$

have $\exists p\ es\ rets.\ fst\text{-cmd}\ c' = Call\ p\ es\ rets$

by(*rule Proc-CFG-Call-source-fst-cmd-Call*)

with $lhs\text{-fst}\text{-cmd}$ [*of c'*] **have** $lhs\ c' = \{\}$ **by** *auto*

with $\langle label\ prog\ l = c' \rangle$ **show** *?thesis* **by** *simp*

qed

lemma *in-procs-THE-in-procs-ins:*

$\llbracket well\text{-formed}\ proc; (p,ins,outs,c) \in set\ proc \rrbracket$

$\implies (THE\ ins'. \exists c'\ outs'. (p,ins',outs',c') \in set\ proc) = ins$

by(*fastforce intro:the-equality*)

definition *Def* :: $wf\text{-prog} \Rightarrow node \Rightarrow vname\ set$

where $Def\ wfp\ n \equiv (let\ (prog,procs) = Rep\text{-wf}\text{-prog}\ wfp; (p,l) = n\ in$

(*case l of Label lx* \Rightarrow

(*if p = Main then lhs (label prog lx)*

else (if ($\exists ins\ outs\ c.\ (p,ins,outs,c) \in set\ proc$)

then

$lhs\ (label\ (THE\ c'. \exists ins'\ outs'. (p,ins',outs',c') \in set\ proc) lx)$

else $\{\}$)

$| Entry \Rightarrow if\ (\exists ins\ outs\ c.\ (p,ins,outs,c) \in set\ proc)$

then (*set*

$(THE\ ins'. \exists c'\ outs'. (p,ins',outs',c') \in set\ proc))$ *else* $\{\}$

$| Exit \Rightarrow \{\}$)

$\cup set\ (ParamDefs\ wfp\ n)$

lemma *Entry-Def-empty:Def wfp (Main, Entry) =* $\{\}$

proof –

obtain $prog\ procs$ **where** [*simp*]: $Rep\text{-wf}\text{-prog}\ wfp = (prog,procs)$

by(cases *Rep-wf-prog wfp*) *auto*
hence *well-formed procs* **by**(*fastforce intro:wf-wf-prog*)
thus *?thesis* **by**(*auto simp:Def-def ParamDefs-def ParamDefs-proc-def*)
qed

lemma *Exit-Def-empty:Def wfp (Main, Exit) = {}*
proof –
obtain *prog procs* **where** [*simp*]:*Rep-wf-prog wfp = (prog,procs)*
by(cases *Rep-wf-prog wfp*) *auto*
hence *well-formed procs* **by**(*fastforce intro:wf-wf-prog*)
thus *?thesis*
by(*auto dest:Proc-CFG-Call-Labels simp:Def-def ParamDefs-def ParamDefs-proc-def*)
qed

Use

fun *rhs :: cmd ⇒ vname set*
where
rhs Skip = {}
| *rhs (V:=e)* = *fv e*
| *rhs (c₁;;c₂)* = *rhs c₁*
| *rhs (if (b) c₁ else c₂)* = *fv b*
| *rhs (while (b) c)* = *fv b*
| *rhs (Call p es rets)* = {}

lemma *rhs-fst-cmd:rhs (fst-cmd c) = rhs c* **by**(*induct c*) *auto*

lemma *Proc-CFG-Call-target-empty-rhs:*
assumes *prog ⊢ n –CEdge (p,es,rets)→_p Label l'*
shows *rhs (label prog l') = {}*
proof –
from $\langle \text{prog} \vdash n - \text{CEdge } (p, es, rets) \rightarrow_p \text{Label } l' \rangle$ **have** $l' < \# : \text{prog}$
by(*rule Proc-CFG-targetlabel-less-num-nodes*)
then obtain *c'* **where** *labels prog l' c'*
by(*erule less-num-inner-nodes-label*)
hence *label prog l' = c'* **by**(*rule labels-label*)
from $\langle \text{labels prog } l' c' \rangle \langle \text{prog} \vdash n - \text{CEdge } (p, es, rets) \rightarrow_p \text{Label } l' \rangle$
have *fst-cmd c' = Skip* **by**(*rule Proc-CFG-Call-target-fst-cmd-Skip*)
with *rhs-fst-cmd[of c']* **have** *rhs c' = {}* **by** *simp*
with $\langle \text{label prog } l' = c' \rangle$ **show** *?thesis* **by** *simp*
qed

lemma *in-procs-THE-in-procs-outs:*
 $\llbracket \text{well-formed procs}; (p, ins, outs, c) \in \text{set procs} \rrbracket$
 $\implies (\text{THE } outs'. \exists c' ins'. (p, ins', outs', c') \in \text{set procs}) = outs$
by(*fastforce intro:the-equality*)

definition *Use* :: wf-prog \Rightarrow node \Rightarrow vname set
where *Use wfp n* \equiv (let (prog,procs) = Rep-wf-prog wfp; (p,l) = n in
(case l of Label lx \Rightarrow
(if p = Main then rhs (label prog lx)
else (if (\exists ins outs c. (p,ins,outs,c) \in set procs)
then
rhs (label (THE c'. \exists ins' outs'. (p,ins',outs',c') \in set procs) lx)
else {})))
| Exit \Rightarrow if (\exists ins outs c. (p,ins,outs,c) \in set procs)
then (set (THE outs'. \exists c' ins'. (p,ins',outs',c') \in set procs))
else {}
| Entry \Rightarrow if (\exists ins outs c. (p,ins,outs,c) \in set procs)
then (set (THE ins'. \exists c' outs'. (p,ins',outs',c') \in set procs))
else {}))
 \cup Union (set (ParamUses wfp n)) \cup set (ParamDefs wfp n)

lemma *Entry-Use-empty:Use wfp (Main, Entry) = {}*

proof –

obtain prog procs **where** [simp]:Rep-wf-prog wfp = (prog,procs)
by(cases Rep-wf-prog wfp) auto
hence well-formed procs **by**(fastforce intro:wf-wf-prog)
thus ?thesis **by**(auto dest:Proc-CFG-Call-Labels
simp:Use-def ParamUses-def ParamUses-proc-def ParamDefs-def ParamDefs-proc-def)

qed

lemma *Exit-Use-empty:Use wfp (Main, Exit) = {}*

proof –

obtain prog procs **where** [simp]:Rep-wf-prog wfp = (prog,procs)
by(cases Rep-wf-prog wfp) auto
hence well-formed procs **by**(fastforce intro:wf-wf-prog)
thus ?thesis **by**(auto dest:Proc-CFG-Call-Labels
simp:Use-def ParamUses-def ParamUses-proc-def ParamDefs-def ParamDefs-proc-def)

qed

2.7.3 Lemmas about edges and call frames

lemmas transfers-simps = ProcCFG.transfer.simps[simplified]

declare transfers-simps [simp]

abbreviation state-val :: (('var \rightarrow 'val) \times 'ret) list \Rightarrow 'var \rightarrow 'val

where state-val s V \equiv (fst (hd s)) V

lemma Proc-CFG-edge-no-lhs-equal:

assumes prog \vdash Label l $-$ IEdge et \rightarrow_p n' **and** V \notin lhs (label prog l)

shows state-val (CFG.transfer (lift-procs wfp) et (cf#cfs)) V = fst cf V

proof –

from $\langle \text{prog} \vdash \text{Label } l - \text{IEdge } et \rightarrow_p n' \rangle$
obtain x **where** $\text{IEdge } et = x$ **and** $\text{prog} \vdash \text{Label } l - x \rightarrow_p n'$ **by** *simp-all*
from $\langle \text{prog} \vdash \text{Label } l - x \rightarrow_p n' \rangle \langle \text{IEdge } et = x \rangle \langle V \notin \text{lhs } (\text{label } \text{prog } l) \rangle$
show *?thesis*
proof(*induct prog Label l x n' arbitrary:l rule:Proc-CFG.induct*)
 case (*Proc-CFG-LAss V' e*)
 have $\text{labels } (V' := e) 0 (V' := e)$ **by**(*rule Labels-Base*)
 hence $\text{label } (V' := e) 0 = (V' := e)$ **by**(*rule labels-label*)
 have $V' \in \text{lhs } (V' := e)$ **by** *simp*
 with $\langle V \notin \text{lhs } (\text{label } (V' := e) 0) \rangle$
 $\langle \text{IEdge } et = \text{IEdge } \uparrow \lambda cf. \text{update } cf \ V' \ e \rangle \langle \text{label } (V' := e) 0 = (V' := e) \rangle$
 show *?case* **by** *fastforce*
next
 case (*Proc-CFG-SeqFirst c₁ et' n' c₂*)
 note $\text{IH} = \langle \llbracket \text{IEdge } et = et'; V \notin \text{lhs } (\text{label } c_1 \ l) \rrbracket \rangle$
 $\implies \text{state-val } (\text{CFG.transfer } (\text{lift-procs wfp}) \ et \ (cf \ \# \ cfs)) \ V = \text{fst } cf \ V$
from $\langle c_1 \vdash \text{Label } l - et' \rightarrow_p n' \rangle$ **have** $l < \# : c_1$
 by(*fastforce intro:Proc-CFG-sourcelabel-less-num-nodes*)
 then obtain c' **where** $\text{labels } c_1 \ l \ c'$ **by**(*erule less-num-inner-nodes-label*)
 hence $\text{labels } (c_1 ;; c_2) \ l \ (c' ;; c_2)$ **by**(*rule Labels-Seq1*)
 hence $\text{label } (c_1 ;; c_2) \ l = c' ;; c_2$ **by**(*rule labels-label*)
 with $\langle V \notin \text{lhs } (\text{label } (c_1 ;; c_2) \ l) \rangle \langle \text{labels } c_1 \ l \ c' \rangle$
 have $V \notin \text{lhs } (\text{label } c_1 \ l)$ **by**(*fastforce dest:labels-label*)
 with $\langle \text{IEdge } et = et' \rangle$ **show** *?case* **by** (*rule IH*)
next
 case (*Proc-CFG-SeqConnect c₁ et' c₂*)
 note $\text{IH} = \langle \llbracket \text{IEdge } et = et'; V \notin \text{lhs } (\text{label } c_1 \ l) \rrbracket \rangle$
 $\implies \text{state-val } (\text{CFG.transfer } (\text{lift-procs wfp}) \ et \ (cf \ \# \ cfs)) \ V = \text{fst } cf \ V$
from $\langle c_1 \vdash \text{Label } l - et' \rightarrow_p \text{Exit} \rangle$ **have** $l < \# : c_1$
 by(*fastforce intro:Proc-CFG-sourcelabel-less-num-nodes*)
 then obtain c' **where** $\text{labels } c_1 \ l \ c'$ **by**(*erule less-num-inner-nodes-label*)
 hence $\text{labels } (c_1 ;; c_2) \ l \ (c' ;; c_2)$ **by**(*rule Labels-Seq1*)
 hence $\text{label } (c_1 ;; c_2) \ l = c' ;; c_2$ **by**(*rule labels-label*)
 with $\langle V \notin \text{lhs } (\text{label } (c_1 ;; c_2) \ l) \rangle \langle \text{labels } c_1 \ l \ c' \rangle$
 have $V \notin \text{lhs } (\text{label } c_1 \ l)$ **by**(*fastforce dest:labels-label*)
 with $\langle \text{IEdge } et = et' \rangle$ **show** *?case* **by** (*rule IH*)
next
 case (*Proc-CFG-SeqSecond c₂ n et' n' c₁ l*)
 note $\text{IH} = \langle \bigwedge l. \llbracket n = \text{Label } l; \text{IEdge } et = et'; V \notin \text{lhs } (\text{label } c_2 \ l) \rrbracket \rangle$
 $\implies \text{state-val } (\text{CFG.transfer } (\text{lift-procs wfp}) \ et \ (cf \ \# \ cfs)) \ V = \text{fst } cf \ V$
from $\langle n \oplus \# : c_1 = \text{Label } l \rangle$ **obtain** l'
 where $n = \text{Label } l'$ **and** $l = l' + \# : c_1$ **by**(*cases n*) *auto*
from $\langle n = \text{Label } l' \rangle \langle c_2 \vdash n - et' \rightarrow_p n' \rangle$ **have** $l' < \# : c_2$
 by(*fastforce intro:Proc-CFG-sourcelabel-less-num-nodes*)
 then obtain c' **where** $\text{labels } c_2 \ l' \ c'$ **by**(*erule less-num-inner-nodes-label*)
 with $\langle l = l' + \# : c_1 \rangle$ **have** $\text{labels } (c_1 ;; c_2) \ l \ c'$
 by(*fastforce intro:Labels-Seq2*)
 hence $\text{label } (c_1 ;; c_2) \ l = c'$ **by**(*rule labels-label*)
 with $\langle V \notin \text{lhs } (\text{label } (c_1 ;; c_2) \ l) \rangle \langle \text{labels } c_2 \ l' \ c' \rangle \langle l = l' + \# : c_1 \rangle$

have $V \notin \text{lhs}(\text{label } c_2 \ l')$ **by**(*fastforce dest:labels-label*)
with $\langle n = \text{Label } l' \rangle \langle \text{IEdge } et = et' \rangle$ **show** ?*case by* (*rule IH*)
next
case (*Proc-CFG-CondThen* $c_1 \ n \ et' \ n' \ b \ c_2 \ l$)
note $IH = \langle \bigwedge l. \llbracket n = \text{Label } l; \text{IEdge } et = et'; V \notin \text{lhs}(\text{label } c_1 \ l) \rrbracket$
 $\implies \text{state-val}(\text{CFG.transfer}(\text{lift-procs wfp}) \ et \ (cf \ \# \ cfs)) \ V = \text{fst } cf \ V$
from $\langle n \oplus 1 = \text{Label } l \rangle$ **obtain** l'
where $n = \text{Label } l'$ **and** $l = l' + 1$ **by**(*cases n*) *auto*
from $\langle n = \text{Label } l' \rangle \langle c_1 \vdash n - et' \rightarrow_p \ n' \rangle$ **have** $l' < \#:c_1$
by(*fastforce intro:Proc-CFG-sourcelabel-less-num-nodes*)
then obtain c' **where** *labels* $c_1 \ l' \ c'$ **by**(*erule less-num-inner-nodes-label*)
with $\langle l = l' + 1 \rangle$ **have** *labels* (*if* (b) c_1 *else* c_2) $l \ c'$
by(*fastforce intro:Labels-CondTrue*)
hence *label* (*if* (b) c_1 *else* c_2) $l = c'$ **by**(*rule labels-label*)
with $\langle V \notin \text{lhs}(\text{label}(\text{if}(\mathit{b}) \ c_1 \ \textit{else} \ c_2) \ l) \rangle \langle \text{labels } c_1 \ l' \ c' \rangle \langle l = l' + 1 \rangle$
have $V \notin \text{lhs}(\text{label } c_1 \ l')$ **by**(*fastforce dest:labels-label*)
with $\langle n = \text{Label } l' \rangle \langle \text{IEdge } et = et' \rangle$ **show** ?*case by* (*rule IH*)
next
case (*Proc-CFG-CondElse* $c_2 \ n \ et' \ n' \ b \ c_1 \ l$)
note $IH = \langle \bigwedge l. \llbracket n = \text{Label } l; \text{IEdge } et = et'; V \notin \text{lhs}(\text{label } c_2 \ l) \rrbracket$
 $\implies \text{state-val}(\text{CFG.transfer}(\text{lift-procs wfp}) \ et \ (cf \ \# \ cfs)) \ V = \text{fst } cf \ V$
from $\langle n \oplus \#:c_1 + 1 = \text{Label } l \rangle$ **obtain** l'
where $n = \text{Label } l'$ **and** $l = l' + \#:c_1 + 1$ **by**(*cases n*) *auto*
from $\langle n = \text{Label } l' \rangle \langle c_2 \vdash n - et' \rightarrow_p \ n' \rangle$ **have** $l' < \#:c_2$
by(*fastforce intro:Proc-CFG-sourcelabel-less-num-nodes*)
then obtain c' **where** *labels* $c_2 \ l' \ c'$ **by**(*erule less-num-inner-nodes-label*)
with $\langle l = l' + \#:c_1 + 1 \rangle$ **have** *labels* (*if* (b) c_1 *else* c_2) $l \ c'$
by(*fastforce intro:Labels-CondFalse*)
hence *label* (*if* (b) c_1 *else* c_2) $l = c'$ **by**(*rule labels-label*)
with $\langle V \notin \text{lhs}(\text{label}(\text{if}(\mathit{b}) \ c_1 \ \textit{else} \ c_2) \ l) \rangle \langle \text{labels } c_2 \ l' \ c' \rangle \langle l = l' + \#:c_1 + 1 \rangle$
have $V \notin \text{lhs}(\text{label } c_2 \ l')$ **by**(*fastforce dest:labels-label*)
with $\langle n = \text{Label } l' \rangle \langle \text{IEdge } et = et' \rangle$ **show** ?*case by* (*rule IH*)
next
case (*Proc-CFG-WhileBody* $c' \ n \ et' \ n' \ b \ l$)
note $IH = \langle \bigwedge l. \llbracket n = \text{Label } l; \text{IEdge } et = et'; V \notin \text{lhs}(\text{label } c' \ l) \rrbracket$
 $\implies \text{state-val}(\text{CFG.transfer}(\text{lift-procs wfp}) \ et \ (cf \ \# \ cfs)) \ V = \text{fst } cf \ V$
from $\langle n \oplus 2 = \text{Label } l \rangle$ **obtain** l'
where $n = \text{Label } l'$ **and** $l = l' + 2$ **by**(*cases n*) *auto*
from $\langle n = \text{Label } l' \rangle \langle c' \vdash n - et' \rightarrow_p \ n' \rangle$ **have** $l' < \#:c'$
by(*fastforce intro:Proc-CFG-sourcelabel-less-num-nodes*)
then obtain cx **where** *labels* $c' \ l' \ cx$ **by**(*erule less-num-inner-nodes-label*)
with $\langle l = l' + 2 \rangle$ **have** *labels* (*while* (b) c') $l \ (cx;;\text{while}(\mathit{b}) \ c')$
by(*fastforce intro:Labels-WhileBody*)
hence *label* (*while* (b) c') $l = cx;;\text{while}(\mathit{b}) \ c'$ **by**(*rule labels-label*)
with $\langle V \notin \text{lhs}(\text{label}(\text{while}(\mathit{b}) \ c') \ l) \rangle \langle \text{labels } c' \ l' \ cx \rangle \langle l = l' + 2 \rangle$
have $V \notin \text{lhs}(\text{label } c' \ l')$ **by**(*fastforce dest:labels-label*)
with $\langle n = \text{Label } l' \rangle \langle \text{IEdge } et = et' \rangle$ **show** ?*case by* (*rule IH*)
next
case (*Proc-CFG-WhileBodyExit* $c' \ n \ et' \ b \ l$)

```

note  $IH = \langle \bigwedge l. \llbracket n = \text{Label } l; \text{ IEdge } et = et'; V \notin \text{lhs}(\text{label } c' l) \rrbracket \rangle$ 
 $\implies \text{state-val}(\text{CFG.transfer}(\text{lift-procs wfp}) et (cf \# cfs)) V = \text{fst } cf V$ 
from  $\langle n \oplus 2 = \text{Label } l \rangle$  obtain  $l'$ 
where  $n = \text{Label } l'$  and  $l = l' + 2$  by(cases  $n$ ) auto
from  $\langle n = \text{Label } l' \rangle \langle c' \vdash n - et' \rightarrow_p \text{Exit} \rangle$  have  $l' < \# : c'$ 
by(fastforce intro:Proc-CFG-sourcelabel-less-num-nodes)
then obtain  $cx$  where  $\text{labels } c' l' cx$  by(erule less-num-inner-nodes-label)
with  $\langle l = l' + 2 \rangle$  have  $\text{labels}(\text{while}(b) c') l (cx;; \text{while}(b) c')$ 
by(fastforce intro:Labels-WhileBody)
hence  $\text{label}(\text{while}(b) c') l = cx;; \text{while}(b) c'$  by(rule labels-label)
with  $\langle V \notin \text{lhs}(\text{label}(\text{while}(b) c') l) \rangle \langle \text{labels } c' l' cx \rangle \langle l = l' + 2 \rangle$ 
have  $V \notin \text{lhs}(\text{label } c' l')$  by(fastforce dest:labels-label)
with  $\langle n = \text{Label } l' \rangle \langle \text{IEdge } et = et' \rangle$  show  $?case$  by (rule  $IH$ )
qed auto
qed

```

lemma *Proc-CFG-edge-uses-only-rhs*:

```

assumes  $\text{prog} \vdash \text{Label } l - \text{IEdge } et \rightarrow_p n'$  and  $\text{CFG.pred } et s$ 
and  $\text{CFG.pred } et s'$  and  $\forall V \in \text{rhs}(\text{label } \text{prog } l). \text{state-val } s V = \text{state-val } s' V$ 
shows  $\forall V \in \text{lhs}(\text{label } \text{prog } l).$ 
 $\text{state-val}(\text{CFG.transfer}(\text{lift-procs wfp}) et s) V =$ 
 $\text{state-val}(\text{CFG.transfer}(\text{lift-procs wfp}) et s') V$ 

```

proof –

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from  $\langle \text{prog} \vdash \text{Label } l - \text{IEdge } et \rightarrow_p n' \rangle$ 
obtain  $x$  where  $\text{IEdge } et = x$  and  $\text{prog} \vdash \text{Label } l - x \rightarrow_p n'$  by simp-all
from  $\langle \text{CFG.pred } et s \rangle$  obtain  $cf \ cfs$  where  $[\text{simp}]: s = cf \# cfs$  by(cases  $s$ ) auto
from  $\langle \text{CFG.pred } et s' \rangle$  obtain  $cf' \ cfs'$  where  $[\text{simp}]: s' = cf' \# cfs'$ 
by(cases  $s'$ ) auto
from  $\langle \text{prog} \vdash \text{Label } l - x \rightarrow_p n' \rangle \langle \text{IEdge } et = x \rangle$ 
 $\langle \forall V \in \text{rhs}(\text{label } \text{prog } l). \text{state-val } s V = \text{state-val } s' V \rangle$ 
show  $?thesis$ 
proof(induct prog Label l x n' arbitrary:l rule:Proc-CFG.induct)
case Proc-CFG-Skip
have  $\text{labels } \text{Skip } 0 \text{ Skip}$  by(rule Labels-Base)
hence  $\text{label } \text{Skip } 0 = \text{Skip}$  by(rule labels-label)
hence  $\forall V. V \notin \text{lhs}(\text{label } \text{Skip } 0)$  by simp
then show  $?case$  by fastforce
next
case (Proc-CFG-LAss  $V e$ )
have  $\text{labels}(V := e) 0 (V := e)$  by(rule Labels-Base)
hence  $\text{label}(V := e) 0 = V := e$  by(rule labels-label)
then have  $\text{lhs}(\text{label}(V := e) 0) = \{V\}$ 
and  $\text{rhs}(\text{label}(V := e) 0) = \text{fv } e$  by auto
with  $\langle \text{IEdge } et = \text{IEdge } \uparrow \lambda cf. \text{update } cf V e \rangle$ 
 $\langle \forall V \in \text{rhs}(\text{label}(V := e) 0). \text{state-val } s V = \text{state-val } s' V \rangle$ 
show  $?case$  by(fastforce intro:rhs-interpret-eq)
next

```

case (*Proc-CFG-LAssSkip* $V e$)
have *labels* ($V := e$) 1 *Skip* **by**(*rule Labels-LAss*)
hence *label* ($V := e$) 1 = *Skip* **by**(*rule labels-label*)
hence $\forall V'. V' \notin \text{lhs}(\text{label}(V := e) 1)$ **by** *simp*
then show ?*case* **by** *fastforce*
next
case (*Proc-CFG-SeqFirst* $c_1 et' n' c_2$)
note $IH = \langle \llbracket IEdge\ et = et' \rrbracket$
 $\forall V \in \text{rhs}(\text{label } c_1\ l). \text{state-val } s\ V = \text{state-val } s'\ V \rrbracket$
 $\implies \forall V \in \text{lhs}(\text{label } c_1\ l). \text{state-val } (CFG.\text{transfer } (\text{lift-procs } wfp)\ et\ s)\ V =$
 $\text{state-val } (CFG.\text{transfer } (\text{lift-procs } wfp)\ et\ s')\ V \rangle$
from $\langle c_1 \vdash \text{Label } l - et' \rightarrow_p n' \rangle$
have $l < \# : c_1$ **by**(*fastforce intro:Proc-CFG-sourcelabel-less-num-nodes*)
then obtain c' **where** *labels* $c_1\ l\ c'$ **by**(*erule less-num-inner-nodes-label*)
hence *labels* $(c_1;;c_2)\ l\ (c';;c_2)$ **by**(*rule Labels-Seq1*)
with $\langle \text{labels } c_1\ l\ c' \rangle \forall V \in \text{rhs}(\text{label } (c_1;;c_2)\ l). \text{state-val } s\ V = \text{state-val } s'\ V \rangle$
have $\forall V \in \text{rhs}(\text{label } c_1\ l). \text{state-val } s\ V = \text{state-val } s'\ V$
by(*fastforce dest:labels-label*)
with $\langle IEdge\ et = et' \rangle$
have $\forall V \in \text{lhs}(\text{label } c_1\ l). \text{state-val } (CFG.\text{transfer } (\text{lift-procs } wfp)\ et\ s)\ V =$
 $\text{state-val } (CFG.\text{transfer } (\text{lift-procs } wfp)\ et\ s')\ V$ **by** (*rule IH*)
with $\langle \text{labels } c_1\ l\ c' \rangle \langle \text{labels } (c_1;;c_2)\ l\ (c';;c_2) \rangle$
show ?*case* **by**(*fastforce dest:labels-label*)
next
case (*Proc-CFG-SeqConnect* $c_1 et' c_2$)
note $IH = \langle \llbracket IEdge\ et = et' \rrbracket$
 $\forall V \in \text{rhs}(\text{label } c_1\ l). \text{state-val } s\ V = \text{state-val } s'\ V \rrbracket$
 $\implies \forall V \in \text{lhs}(\text{label } c_1\ l). \text{state-val } (CFG.\text{transfer } (\text{lift-procs } wfp)\ et\ s)\ V =$
 $\text{state-val } (CFG.\text{transfer } (\text{lift-procs } wfp)\ et\ s')\ V \rangle$
from $\langle c_1 \vdash \text{Label } l - et' \rightarrow_p \text{Exit} \rangle$
have $l < \# : c_1$ **by**(*fastforce intro:Proc-CFG-sourcelabel-less-num-nodes*)
then obtain c' **where** *labels* $c_1\ l\ c'$ **by**(*erule less-num-inner-nodes-label*)
hence *labels* $(c_1;;c_2)\ l\ (c';;c_2)$ **by**(*rule Labels-Seq1*)
with $\langle \text{labels } c_1\ l\ c' \rangle \forall V \in \text{rhs}(\text{label } (c_1;;c_2)\ l). \text{state-val } s\ V = \text{state-val } s'\ V \rangle$
have $\forall V \in \text{rhs}(\text{label } c_1\ l). \text{state-val } s\ V = \text{state-val } s'\ V$
by(*fastforce dest:labels-label*)
with $\langle IEdge\ et = et' \rangle$
have $\forall V \in \text{lhs}(\text{label } c_1\ l). \text{state-val } (CFG.\text{transfer } (\text{lift-procs } wfp)\ et\ s)\ V =$
 $\text{state-val } (CFG.\text{transfer } (\text{lift-procs } wfp)\ et\ s')\ V$ **by** (*rule IH*)
with $\langle \text{labels } c_1\ l\ c' \rangle \langle \text{labels } (c_1;;c_2)\ l\ (c';;c_2) \rangle$
show ?*case* **by**(*fastforce dest:labels-label*)
next
case (*Proc-CFG-SeqSecond* $c_2 n et' n' c_1$)
note $IH = \langle \bigwedge l. \llbracket n = \text{Label } l; IEdge\ et = et' \rrbracket$
 $\forall V \in \text{rhs}(\text{label } c_2\ l). \text{state-val } s\ V = \text{state-val } s'\ V \rrbracket$
 $\implies \forall V \in \text{lhs}(\text{label } c_2\ l). \text{state-val } (CFG.\text{transfer } (\text{lift-procs } wfp)\ et\ s)\ V =$
 $\text{state-val } (CFG.\text{transfer } (\text{lift-procs } wfp)\ et\ s')\ V \rangle$
from $\langle n \oplus \# : c_1 = \text{Label } l \rangle$ **obtain** l' **where** $n = \text{Label } l'$ **and** $l = l' + \# : c_1$
by(*cases n*) *auto*

from $\langle c_2 \vdash n - et' \rightarrow_p n' \rangle \langle n = \text{Label } l' \rangle$
have $l' < \# : c_2$ **by** (*fastforce intro:Proc-CFG-sourcelabel-less-num-nodes*)
then obtain c' **where** *labels* c_2 l' c' **by** (*erule less-num-inner-nodes-label*)
with $\langle l = l' + \# : c_1 \rangle$ **have** *labels* $(c_1;;c_2)$ l c' **by** (*fastforce intro:Labels-Seq2*)
with $\langle \text{labels } c_2$ l' $c' \rangle \langle \forall V \in \text{rhs}(\text{label}(c_1;;c_2) l). \text{state-val } s \ V = \text{state-val } s' \ V \rangle$
have $\forall V \in \text{rhs}(\text{label } c_2 \ l'). \text{state-val } s \ V = \text{state-val } s' \ V$
by (*fastforce dest:labels-label*)
with $\langle n = \text{Label } l' \rangle \langle \text{IEdge } et = et' \rangle$
have $\forall V \in \text{lhs}(\text{label } c_2 \ l'). \text{state-val}(\text{CFG.transfer}(\text{lift-procs wfp}) \ et \ s) \ V =$
 $\text{state-val}(\text{CFG.transfer}(\text{lift-procs wfp}) \ et \ s') \ V$ **by** (*rule IH*)
with $\langle \text{labels } c_2$ l' $c' \rangle \langle \text{labels } (c_1;;c_2)$ l $c' \rangle$
show $?case$ **by** (*fastforce dest:labels-label*)
next
case (*Proc-CFG-CondTrue* b c_1 c_2)
have *labels* $(\text{if}(b) \ c_1 \ \text{else} \ c_2) \ 0$ $(\text{if}(b) \ c_1 \ \text{else} \ c_2)$ **by** (*rule Labels-Base*)
hence *label* $(\text{if}(b) \ c_1 \ \text{else} \ c_2) \ 0 = \text{if}(b) \ c_1 \ \text{else} \ c_2$ **by** (*rule labels-label*)
hence $\forall V. V \notin \text{lhs}(\text{label}(\text{if}(b) \ c_1 \ \text{else} \ c_2) \ 0)$ **by** *simp*
then show $?case$ **by** *fastforce*
next
case (*Proc-CFG-CondFalse* b c_1 c_2)
have *labels* $(\text{if}(b) \ c_1 \ \text{else} \ c_2) \ 0$ $(\text{if}(b) \ c_1 \ \text{else} \ c_2)$ **by** (*rule Labels-Base*)
hence *label* $(\text{if}(b) \ c_1 \ \text{else} \ c_2) \ 0 = \text{if}(b) \ c_1 \ \text{else} \ c_2$ **by** (*rule labels-label*)
hence $\forall V. V \notin \text{lhs}(\text{label}(\text{if}(b) \ c_1 \ \text{else} \ c_2) \ 0)$ **by** *simp*
then show $?case$ **by** *fastforce*
next
case (*Proc-CFG-CondThen* c_1 n et' n' b c_2)
note $IH = \langle \bigwedge l. \llbracket n = \text{Label } l; \text{IEdge } et = et' \rrbracket$
 $\forall V \in \text{rhs}(\text{label } c_1 \ l). \text{state-val } s \ V = \text{state-val } s' \ V \rrbracket$
 $\implies \forall V \in \text{lhs}(\text{label } c_1 \ l). \text{state-val}(\text{CFG.transfer}(\text{lift-procs wfp}) \ et \ s) \ V =$
 $\text{state-val}(\text{CFG.transfer}(\text{lift-procs wfp}) \ et \ s') \ V$
from $\langle n \oplus 1 = \text{Label } l \rangle$ **obtain** l' **where** $n = \text{Label } l'$ **and** $l = l' + 1$
by (*cases n*) *auto*
from $\langle c_1 \vdash n - et' \rightarrow_p n' \rangle \langle n = \text{Label } l' \rangle$
have $l' < \# : c_1$ **by** (*fastforce intro:Proc-CFG-sourcelabel-less-num-nodes*)
then obtain c' **where** *labels* c_1 l' c' **by** (*erule less-num-inner-nodes-label*)
with $\langle l = l' + 1 \rangle$ **have** *labels* $(\text{if}(b) \ c_1 \ \text{else} \ c_2)$ l c'
by (*fastforce intro:Labels-CondTrue*)
with $\langle \text{labels } c_1$ l' $c' \rangle \langle \forall V \in \text{rhs}(\text{label}(\text{if}(b) \ c_1 \ \text{else} \ c_2) \ l). \text{state-val } s \ V =$
 $\text{state-val } s' \ V \rangle$
have $\forall V \in \text{rhs}(\text{label } c_1 \ l'). \text{state-val } s \ V = \text{state-val } s' \ V$
by (*fastforce dest:labels-label*)
with $\langle n = \text{Label } l' \rangle \langle \text{IEdge } et = et' \rangle$
have $\forall V \in \text{lhs}(\text{label } c_1 \ l'). \text{state-val}(\text{CFG.transfer}(\text{lift-procs wfp}) \ et \ s) \ V =$
 $\text{state-val}(\text{CFG.transfer}(\text{lift-procs wfp}) \ et \ s') \ V$ **by** (*rule IH*)
with $\langle \text{labels } c_1$ l' $c' \rangle \langle \text{labels } (\text{if}(b) \ c_1 \ \text{else} \ c_2)$ l $c' \rangle$
show $?case$ **by** (*fastforce dest:labels-label*)
next
case (*Proc-CFG-CondElse* c_2 n et' n' b c_1)
note $IH = \langle \bigwedge l. \llbracket n = \text{Label } l; \text{IEdge } et = et' \rrbracket$

$\forall V \in rhs \text{ (label } c_2 \text{ } l). \text{ state-val } s \text{ } V = \text{state-val } s' \text{ } V \llbracket$
 $\implies \forall V \in lhs \text{ (label } c_2 \text{ } l). \text{ state-val } (CFG.transfer \text{ (lift-procs wfp) } et \text{ } s) \text{ } V =$
 $\text{state-val } (CFG.transfer \text{ (lift-procs wfp) } et \text{ } s') \text{ } V \ggbracket$
from $\langle n \oplus \# : c_1 + 1 = \text{Label } l \rangle$ **obtain** l' **where** $n = \text{Label } l'$ **and** $l = l' +$
 $\# : c_1 + 1$
by $(cases \text{ } n) \text{ auto}$
from $\langle c_2 \vdash n - et' \rightarrow_p n' \rangle \langle n = \text{Label } l' \rangle$
have $l' < \# : c_2$ **by** $(fastforce \text{ intro:Proc-CFG-sourcelabel-less-num-nodes})$
then obtain c' **where** $labels \text{ } c_2 \text{ } l' \text{ } c'$ **by** $(erule \text{ less-num-inner-nodes-label})$
with $\langle l = l' + \# : c_1 + 1 \rangle$ **have** $labels \text{ (if (b) } c_1 \text{ else } c_2) \text{ } l \text{ } c'$
by $(fastforce \text{ intro:Labels-CondFalse})$
with $\langle labels \text{ } c_2 \text{ } l' \text{ } c' \rangle \langle \forall V \in rhs \text{ (label (if (b) } c_1 \text{ else } c_2) \text{ } l). \text{ state-val } s \text{ } V = \text{state-val } s' \text{ } V \rangle$
have $\forall V \in rhs \text{ (label } c_2 \text{ } l'). \text{ state-val } s \text{ } V = \text{state-val } s' \text{ } V$
by $(fastforce \text{ dest:labels-label})$
with $\langle n = \text{Label } l' \rangle \langle IEdge \text{ } et = et' \rangle$
have $\forall V \in lhs \text{ (label } c_2 \text{ } l'). \text{ state-val } (CFG.transfer \text{ (lift-procs wfp) } et \text{ } s) \text{ } V =$
 $\text{state-val } (CFG.transfer \text{ (lift-procs wfp) } et \text{ } s') \text{ } V$ **by** $(rule \text{ IH})$
with $\langle labels \text{ } c_2 \text{ } l' \text{ } c' \rangle \langle labels \text{ (if (b) } c_1 \text{ else } c_2) \text{ } l \text{ } c' \rangle$
show $?case$ **by** $(fastforce \text{ dest:labels-label})$
next
case $(Proc-CFG-WhileTrue \text{ } b \text{ } c')$
have $labels \text{ (while (b) } c') \text{ } 0 \text{ (while (b) } c')$ **by** $(rule \text{ Labels-Base})$
hence $label \text{ (while (b) } c') \text{ } 0 = \text{while (b) } c'$ **by** $(rule \text{ labels-label})$
hence $\forall V. V \notin lhs \text{ (label (while (b) } c') \text{ } 0)$ **by** $simp$
then show $?case$ **by** $fastforce$
next
case $(Proc-CFG-WhileFalse \text{ } b \text{ } c')$
have $labels \text{ (while (b) } c') \text{ } 0 \text{ (while (b) } c')$ **by** $(rule \text{ Labels-Base})$
hence $label \text{ (while (b) } c') \text{ } 0 = \text{while (b) } c'$ **by** $(rule \text{ labels-label})$
hence $\forall V. V \notin lhs \text{ (label (while (b) } c') \text{ } 0)$ **by** $simp$
then show $?case$ **by** $fastforce$
next
case $(Proc-CFG-WhileFalseSkip \text{ } b \text{ } c')$
have $labels \text{ (while (b) } c') \text{ } 1 \text{ Skip}$ **by** $(rule \text{ Labels-WhileExit})$
hence $label \text{ (while (b) } c') \text{ } 1 = \text{Skip}$ **by** $(rule \text{ labels-label})$
hence $\forall V. V \notin lhs \text{ (label (while (b) } c') \text{ } 1)$ **by** $simp$
then show $?case$ **by** $fastforce$
next
case $(Proc-CFG-WhileBody \text{ } c' \text{ } n \text{ } et' \text{ } n' \text{ } b)$
note $IH = \langle \bigwedge l. \llbracket n = \text{Label } l; IEdge \text{ } et = et';$
 $\forall V \in rhs \text{ (label } c' \text{ } l). \text{ state-val } s \text{ } V = \text{state-val } s' \text{ } V \llbracket$
 $\implies \forall V \in lhs \text{ (label } c' \text{ } l). \text{ state-val } (CFG.transfer \text{ (lift-procs wfp) } et \text{ } s) \text{ } V =$
 $\text{state-val } (CFG.transfer \text{ (lift-procs wfp) } et \text{ } s') \text{ } V \ggbracket$
from $\langle n \oplus 2 = \text{Label } l \rangle$ **obtain** l' **where** $n = \text{Label } l'$ **and** $l = l' + 2$
by $(cases \text{ } n) \text{ auto}$
from $\langle c' \vdash n - et' \rightarrow_p n' \rangle \langle n = \text{Label } l' \rangle$
have $l' < \# : c'$ **by** $(fastforce \text{ intro:Proc-CFG-sourcelabel-less-num-nodes})$
then obtain cx **where** $labels \text{ } c' \text{ } l' \text{ } cx$ **by** $(erule \text{ less-num-inner-nodes-label})$

with $\langle l = l' + 2 \rangle$ **have** $\text{labels } \langle \text{while } (b) \ c' \rangle \ l \ (cx;;\text{while } (b) \ c')$
by(*fastforce intro:Labels-WhileBody*)
with $\langle \text{labels } c' \ l' \ cx \rangle \ \forall V \in \text{rhs } \langle \text{label } \langle \text{while } (b) \ c' \rangle \ l \rangle$.
 $\text{state-val } s \ V = \text{state-val } s' \ V$
have $\forall V \in \text{rhs } \langle \text{label } c' \ l' \rangle$. $\text{state-val } s \ V = \text{state-val } s' \ V$
by(*fastforce dest:labels-label*)
with $\langle n = \text{Label } l' \rangle \ \langle \text{IEdge } et = et' \rangle$
have $\forall V \in \text{lhs } \langle \text{label } c' \ l' \rangle$. $\text{state-val } \langle \text{CFG.transfer } \langle \text{lift-procs wfp} \rangle \ et \ s \rangle \ V =$
 $\text{state-val } \langle \text{CFG.transfer } \langle \text{lift-procs wfp} \rangle \ et \ s' \rangle \ V$ **by** (*rule IH*)
with $\langle \text{labels } c' \ l' \ cx \rangle \ \langle \text{labels } \langle \text{while } (b) \ c' \rangle \ l \ (cx;;\text{while } (b) \ c') \rangle$
show $?case$ **by**(*fastforce dest:labels-label*)
next
case (*Proc-CFG-WhileBodyExit* $c' \ n \ et' \ b$)
note $IH = \langle \bigwedge l. \llbracket n = \text{Label } l; \text{IEdge } et = et' \rrbracket$
 $\forall V \in \text{rhs } \langle \text{label } c' \ l \rangle$. $\text{state-val } s \ V = \text{state-val } s' \ V \rrbracket$
 $\implies \forall V \in \text{lhs } \langle \text{label } c' \ l \rangle$. $\text{state-val } \langle \text{CFG.transfer } \langle \text{lift-procs wfp} \rangle \ et \ s \rangle \ V =$
 $\text{state-val } \langle \text{CFG.transfer } \langle \text{lift-procs wfp} \rangle \ et \ s' \rangle \ V$
from $\langle n \oplus 2 = \text{Label } l \rangle$ **obtain** l' **where** $n = \text{Label } l'$ **and** $l = l' + 2$
by(*cases n*) *auto*
from $\langle c' \vdash n - et' \rightarrow_p \text{Exit} \rangle \ \langle n = \text{Label } l' \rangle$
have $l' < \# : c'$ **by**(*fastforce intro:Proc-CFG-sourcelabel-less-num-nodes*)
then obtain cx **where** $\text{labels } c' \ l' \ cx$ **by**(*erule less-num-inner-nodes-label*)
with $\langle l = l' + 2 \rangle$ **have** $\text{labels } \langle \text{while } (b) \ c' \rangle \ l \ (cx;;\text{while } (b) \ c')$
by(*fastforce intro:Labels-WhileBody*)
with $\langle \text{labels } c' \ l' \ cx \rangle \ \forall V \in \text{rhs } \langle \text{label } \langle \text{while } (b) \ c' \rangle \ l \rangle$.
 $\text{state-val } s \ V = \text{state-val } s' \ V$
have $\forall V \in \text{rhs } \langle \text{label } c' \ l' \rangle$. $\text{state-val } s \ V = \text{state-val } s' \ V$
by(*fastforce dest:labels-label*)
with $\langle n = \text{Label } l' \rangle \ \langle \text{IEdge } et = et' \rangle$
have $\forall V \in \text{lhs } \langle \text{label } c' \ l' \rangle$. $\text{state-val } \langle \text{CFG.transfer } \langle \text{lift-procs wfp} \rangle \ et \ s \rangle \ V =$
 $\text{state-val } \langle \text{CFG.transfer } \langle \text{lift-procs wfp} \rangle \ et \ s' \rangle \ V$ **by** (*rule IH*)
with $\langle \text{labels } c' \ l' \ cx \rangle \ \langle \text{labels } \langle \text{while } (b) \ c' \rangle \ l \ (cx;;\text{while } (b) \ c') \rangle$
show $?case$ **by**(*fastforce dest:labels-label*)
next
case (*Proc-CFG-CallSkip* $p \ es \ \text{rets}$)
have $\text{labels } \langle \text{Call } p \ es \ \text{rets} \rangle \ 1 \ \text{Skip}$ **by**(*rule Labels-Call*)
hence $\text{label } \langle \text{Call } p \ es \ \text{rets} \rangle \ 1 = \text{Skip}$ **by**(*rule labels-label*)
hence $\forall V. V \notin \text{lhs } \langle \text{label } \langle \text{Call } p \ es \ \text{rets} \rangle \ 1 \rangle$ **by** *simp*
then show $?case$ **by** *fastforce*
qed *auto*
qed

lemma *Proc-CFG-edge-rhs-pred-eq*:

assumes $\text{prog} \vdash \text{Label } l - \text{IEdge } et \rightarrow_p \ n'$ **and** $\text{CFG.pred } et \ s$
and $\forall V \in \text{rhs } \langle \text{label } \text{prog } l \rangle$. $\text{state-val } s \ V = \text{state-val } s' \ V$
and $\text{length } s = \text{length } s'$
shows $\text{CFG.pred } et \ s'$
proof –

from $\langle \text{prog} \vdash \text{Label } l - \text{IEdge } et \rightarrow_p n' \rangle$
obtain x **where** $\text{IEdge } et = x$ **and** $\text{prog} \vdash \text{Label } l - x \rightarrow_p n'$ **by** *simp-all*
from $\langle \text{CFG.pred } et \ s \rangle$ **obtain** $cf \ cfs$ **where** $[\text{simp}]: s = cf \# cfs$ **by** *(cases s) auto*
from $\langle \text{length } s = \text{length } s' \rangle$ **obtain** $cf' \ cfs'$ **where** $[\text{simp}]: s' = cf' \# cfs'$
by *(cases s') auto*
from $\langle \text{prog} \vdash \text{Label } l - x \rightarrow_p n' \rangle$ $\langle \text{IEdge } et = x \rangle$
 $\langle \forall V \in \text{rhs}(\text{label } \text{prog } l). \text{state-val } s \ V = \text{state-val } s' \ V \rangle$
show *?thesis*
proof *(induct prog Label l x n' arbitrary:l rule:Proc-CFG.induct)*
case *(Proc-CFG-SeqFirst c1 et' n' c2)*
note $\text{IH} = \langle \llbracket \text{IEdge } et = et'; \forall V \in \text{rhs}(\text{label } c_1 \ l). \text{state-val } s \ V = \text{state-val } s' \ V \rrbracket \implies \text{CFG.pred } et \ s' \rangle$
from $\langle c_1 \vdash \text{Label } l - et' \rightarrow_p n' \rangle$
have $l < \# : c_1$ **by** *(fastforce intro:Proc-CFG-sourcelabel-less-num-nodes)*
then obtain c' **where** $\text{labels } c_1 \ l \ c'$ **by** *(erule less-num-inner-nodes-label)*
hence $\text{labels } (c_1;;c_2) \ l \ (c';c_2)$ **by** *(rule Labels-Seq1)*
with $\langle \text{labels } c_1 \ l \ c' \rangle$ $\langle \forall V \in \text{rhs}(\text{label } (c_1;;c_2) \ l). \text{state-val } s \ V = \text{state-val } s' \ V \rangle$
have $\forall V \in \text{rhs}(\text{label } c_1 \ l). \text{state-val } s \ V = \text{state-val } s' \ V$
by *(fastforce dest:labels-label)*
with $\langle \text{IEdge } et = et' \rangle$ **show** *?case by (rule IH)*
next
case *(Proc-CFG-SeqConnect c1 et' c2)*
note $\text{IH} = \langle \llbracket \text{IEdge } et = et'; \forall V \in \text{rhs}(\text{label } c_1 \ l). \text{state-val } s \ V = \text{state-val } s' \ V \rrbracket \implies \text{CFG.pred } et \ s' \rangle$
from $\langle c_1 \vdash \text{Label } l - et' \rightarrow_p \text{Exit} \rangle$
have $l < \# : c_1$ **by** *(fastforce intro:Proc-CFG-sourcelabel-less-num-nodes)*
then obtain c' **where** $\text{labels } c_1 \ l \ c'$ **by** *(erule less-num-inner-nodes-label)*
hence $\text{labels } (c_1;;c_2) \ l \ (c';c_2)$ **by** *(rule Labels-Seq1)*
with $\langle \text{labels } c_1 \ l \ c' \rangle$ $\langle \forall V \in \text{rhs}(\text{label } (c_1;;c_2) \ l). \text{state-val } s \ V = \text{state-val } s' \ V \rangle$
have $\forall V \in \text{rhs}(\text{label } c_1 \ l). \text{state-val } s \ V = \text{state-val } s' \ V$
by *(fastforce dest:labels-label)*
with $\langle \text{IEdge } et = et' \rangle$ **show** *?case by (rule IH)*
next
case *(Proc-CFG-SeqSecond c2 n et' n' c1)*
note $\text{IH} = \langle \bigwedge l. \llbracket n = \text{Label } l; \text{IEdge } et = et'; \forall V \in \text{rhs}(\text{label } c_2 \ l). \text{state-val } s \ V = \text{state-val } s' \ V \rrbracket \implies \text{CFG.pred } et \ s' \rangle$
from $\langle n \oplus \# : c_1 = \text{Label } l \rangle$ **obtain** l' **where** $n = \text{Label } l'$ **and** $l = l' + \# : c_1$
by *(cases n) auto*
from $\langle c_2 \vdash n - et' \rightarrow_p n' \rangle$ $\langle n = \text{Label } l' \rangle$
have $l' < \# : c_2$ **by** *(fastforce intro:Proc-CFG-sourcelabel-less-num-nodes)*
then obtain c' **where** $\text{labels } c_2 \ l' \ c'$ **by** *(erule less-num-inner-nodes-label)*
with $\langle l = l' + \# : c_1 \rangle$ **have** $\text{labels } (c_1;;c_2) \ l \ c'$ **by** *(fastforce intro:Labels-Seq2)*
with $\langle \text{labels } c_2 \ l' \ c' \rangle$ $\langle \forall V \in \text{rhs}(\text{label } (c_1;;c_2) \ l). \text{state-val } s \ V = \text{state-val } s' \ V \rangle$
have $\forall V \in \text{rhs}(\text{label } c_2 \ l'). \text{state-val } s \ V = \text{state-val } s' \ V$
by *(fastforce dest:labels-label)*
with $\langle n = \text{Label } l' \rangle$ $\langle \text{IEdge } et = et' \rangle$ **show** *?case by (rule IH)*
next

case (*Proc-CFG-CondTrue* $b\ c_1\ c_2$)
from $\langle \text{CFG.pred } et\ s \rangle \langle \text{IEdge } et = \text{IEdge } (\lambda cf. \text{state-check } cf\ b\ (\text{Some true})) \rangle_{\surd}$
have *state-check* ($\text{fst } cf$) $b\ (\text{Some true})$ **by** *simp*
moreover
have *labels* ($\text{if } (b)\ c_1\ \text{else } c_2$) $0\ (\text{if } (b)\ c_1\ \text{else } c_2)$ **by**(*rule Labels-Base*)
hence *label* ($\text{if } (b)\ c_1\ \text{else } c_2$) $0 = \text{if } (b)\ c_1\ \text{else } c_2$ **by**(*rule labels-label*)
with $\langle \forall V \in \text{rhs } (\text{label } (\text{if } (b)\ c_1\ \text{else } c_2)\ 0). \text{state-val } s\ V = \text{state-val } s'\ V \rangle$
have $\forall V \in \text{fv } b. \text{state-val } s\ V = \text{state-val } s'\ V$ **by** *fastforce*
ultimately have *state-check* ($\text{fst } cf'$) $b\ (\text{Some true})$
by *simp*(*rule rhs-interpret-eq*)
with $\langle \text{IEdge } et = \text{IEdge } (\lambda cf. \text{state-check } cf\ b\ (\text{Some true})) \rangle_{\surd}$
show *?case* **by** *simp*
next
case (*Proc-CFG-CondFalse* $b\ c_1\ c_2$)
from $\langle \text{CFG.pred } et\ s \rangle$
 $\langle \text{IEdge } et = \text{IEdge } (\lambda cf. \text{state-check } cf\ b\ (\text{Some false})) \rangle_{\surd}$
have *state-check* ($\text{fst } cf$) $b\ (\text{Some false})$ **by** *simp*
moreover
have *labels* ($\text{if } (b)\ c_1\ \text{else } c_2$) $0\ (\text{if } (b)\ c_1\ \text{else } c_2)$ **by**(*rule Labels-Base*)
hence *label* ($\text{if } (b)\ c_1\ \text{else } c_2$) $0 = \text{if } (b)\ c_1\ \text{else } c_2$ **by**(*rule labels-label*)
with $\langle \forall V \in \text{rhs } (\text{label } (\text{if } (b)\ c_1\ \text{else } c_2)\ 0). \text{state-val } s\ V = \text{state-val } s'\ V \rangle$
have $\forall V \in \text{fv } b. \text{state-val } s\ V = \text{state-val } s'\ V$ **by** *fastforce*
ultimately have *state-check* ($\text{fst } cf'$) $b\ (\text{Some false})$
by *simp*(*rule rhs-interpret-eq*)
with $\langle \text{IEdge } et = \text{IEdge } (\lambda cf. \text{state-check } cf\ b\ (\text{Some false})) \rangle_{\surd}$
show *?case* **by** *simp*
next
case (*Proc-CFG-CondThen* $c_1\ n\ et'\ n'\ b\ c_2$)
note $IH = \langle \bigwedge l. \llbracket n = \text{Label } l; \text{IEdge } et = et' \rrbracket$
 $\forall V \in \text{rhs } (\text{label } c_1\ l). \text{state-val } s\ V = \text{state-val } s'\ V \rrbracket$
 $\implies \text{CFG.pred } et\ s'$
from $\langle n \oplus 1 = \text{Label } l \rangle$ **obtain** l' **where** $n = \text{Label } l'$ **and** $l = l' + 1$
by(*cases n*) *auto*
from $\langle c_1 \vdash n - et' \rightarrow_p n' \rangle \langle n = \text{Label } l' \rangle$
have $l' < \# : c_1$ **by**(*fastforce intro:Proc-CFG-sourcelabel-less-num-nodes*)
then obtain c' **where** *labels* $c_1\ l'\ c'$ **by**(*erule less-num-inner-nodes-label*)
with $\langle l = l' + 1 \rangle$ **have** *labels* ($\text{if } (b)\ c_1\ \text{else } c_2$) $l\ c'$
by(*fastforce intro:Labels-CondTrue*)
with $\langle \text{labels } c_1\ l'\ c' \rangle \langle \forall V \in \text{rhs } (\text{label } (\text{if } (b)\ c_1\ \text{else } c_2)\ l). \text{state-val } s\ V = \text{state-val } s'\ V \rangle$
have $\forall V \in \text{rhs } (\text{label } c_1\ l'). \text{state-val } s\ V = \text{state-val } s'\ V$
by(*fastforce dest:labels-label*)
with $\langle n = \text{Label } l' \rangle \langle \text{IEdge } et = et' \rangle$ **show** *?case* **by** (*rule IH*)
next
case (*Proc-CFG-CondElse* $c_2\ n\ et'\ n'\ b\ c_1$)
note $IH = \langle \bigwedge l. \llbracket n = \text{Label } l; \text{IEdge } et = et' \rrbracket$
 $\forall V \in \text{rhs } (\text{label } c_2\ l). \text{state-val } s\ V = \text{state-val } s'\ V \rrbracket$
 $\implies \text{CFG.pred } et\ s'$
from $\langle n \oplus \# : c_1 + 1 = \text{Label } l \rangle$ **obtain** l' **where** $n = \text{Label } l'$ **and** $l = l' +$

```

# : c1 + 1
  by(cases n) auto
  from ⟨c2 ⊢ n - et' →p n'⟩ ⟨n = Label l'⟩
  have l' < # : c2 by(fastforce intro:Proc-CFG-sourcelabel-less-num-nodes)
  then obtain c' where labels c2 l' c' by(erule less-num-inner-nodes-label)
  with ⟨l = l' + # : c1 + 1⟩ have labels (if (b) c1 else c2) l c'
    by(fastforce intro:Labels-CondFalse)
  with ⟨labels c2 l' c'⟩ ⟨∀ V ∈ rhs (label (if (b) c1 else c2) l).
    state-val s V = state-val s' V⟩
  have ∀ V ∈ rhs (label c2 l'). state-val s V = state-val s' V
    by(fastforce dest:labels-label)
  with ⟨n = Label l'⟩ ⟨IEdge et = et'⟩ show ?case by (rule IH)
next
  case (Proc-CFG-WhileTrue b c')
  from ⟨CFG.pred et s⟩ ⟨IEdge et = IEdge (λcf. state-check cf b (Some true))⟩✓
  have state-check (fst cf) b (Some true) by simp
  moreover
  have labels (while (b) c') 0 (while (b) c') by(rule Labels-Base)
  hence label (while (b) c') 0 = while (b) c' by(rule labels-label)
  with ⟨∀ V ∈ rhs (label (while (b) c') 0). state-val s V = state-val s' V⟩
  have ∀ V ∈ fv b. state-val s V = state-val s' V by fastforce
  ultimately have state-check (fst cf') b (Some true)
    by simp(rule rhs-interpret-eq)
  with ⟨IEdge et = IEdge (λcf. state-check cf b (Some true))⟩✓
  show ?case by simp
next
  case (Proc-CFG-WhileFalse b c')
  from ⟨CFG.pred et s⟩
  ⟨IEdge et = IEdge (λcf. state-check cf b (Some false))⟩✓
  have state-check (fst cf) b (Some false) by simp
  moreover
  have labels (while (b) c') 0 (while (b) c') by(rule Labels-Base)
  hence label (while (b) c') 0 = while (b) c' by(rule labels-label)
  with ⟨∀ V ∈ rhs (label (while (b) c') 0). state-val s V = state-val s' V⟩
  have ∀ V ∈ fv b. state-val s V = state-val s' V by fastforce
  ultimately have state-check (fst cf') b (Some false)
    by simp(rule rhs-interpret-eq)
  with ⟨IEdge et = IEdge (λcf. state-check cf b (Some false))⟩✓
  show ?case by simp
next
  case (Proc-CFG-WhileBody c' n et' n' b)
  note IH = ⟨∧ l. [n = Label l; IEdge et = et';
    ∀ V ∈ rhs (label c' l). state-val s V = state-val s' V]
    ⇒ CFG.pred et s'⟩
  from ⟨n ⊕ 2 = Label l⟩ obtain l' where n = Label l' and l = l' + 2
    by(cases n) auto
  from ⟨c' ⊢ n - et' →p n'⟩ ⟨n = Label l'⟩
  have l' < # : c' by(fastforce intro:Proc-CFG-sourcelabel-less-num-nodes)
  then obtain cx where labels c' l' cx by(erule less-num-inner-nodes-label)

```

```

with ⟨ $l = l' + 2$ ⟩ have labels (while (b) c') l (cx;;while (b) c')
  by(fastforce intro:Labels-WhileBody)
with ⟨labels c' l' cx⟩ ⟨ $\forall V \in \text{rhs} \text{ (label (while (b) c') l)}$ ⟩.
  state-val s V = state-val s' V⟩
have  $\forall V \in \text{rhs} \text{ (label c' l')}$ . state-val s V = state-val s' V
  by(fastforce dest:labels-label)
with ⟨n = Label l'⟩ ⟨IEdge et = et'⟩ show ?case by (rule IH)
next
case (Proc-CFG-WhileBodyExit c' n et' b)
note IH = ⟨ $\bigwedge l. \llbracket n = \text{Label } l; \text{IEdge } et = et' \rrbracket$ ⟩
   $\forall V \in \text{rhs} \text{ (label c' l)}$ . state-val s V = state-val s' V
   $\implies \text{CFG.pred } et \text{ s'}$ 
from ⟨ $n \oplus 2 = \text{Label } l$ ⟩ obtain l' where n = Label l' and  $l = l' + 2$ 
  by(cases n) auto
from ⟨c' ⊢ n -et' →p Exit⟩ ⟨n = Label l'⟩
have  $l' < \# : c'$  by(fastforce intro:Proc-CFG-sourcelabel-less-num-nodes)
then obtain cx where labels c' l' cx by(erule less-num-inner-nodes-label)
with ⟨ $l = l' + 2$ ⟩ have labels (while (b) c') l (cx;;while (b) c')
  by(fastforce intro:Labels-WhileBody)
with ⟨labels c' l' cx⟩ ⟨ $\forall V \in \text{rhs} \text{ (label (while (b) c') l)}$ ⟩.
  state-val s V = state-val s' V⟩
have  $\forall V \in \text{rhs} \text{ (label c' l')}$ . state-val s V = state-val s' V
  by(fastforce dest:labels-label)
with ⟨n = Label l'⟩ ⟨IEdge et = et'⟩ show ?case by (rule IH)
qed auto
qed

```

2.7.4 Instantiating the *CFG-wf* locale

interpretation *ProcCFG-wf*:

```

CFG-wf sourcenode targetnode kind valid-edge wfp (Main,Entry)
get-proc get-return-edges wfp lift-procs wfp Main
Def wfp Use wfp ParamDefs wfp ParamUses wfp
for wfp

```

proof –

```

from Rep-wf-prog[of wfp]
obtain prog procs where [simp]:Rep-wf-prog wfp = (prog,procs)
  by(fastforce simp:wf-prog-def)
hence wf prog procs by(rule wf-wf-prog)
hence wf:well-formed procs by fastforce
show CFG-wf sourcenode targetnode kind (valid-edge wfp)
  (Main, Entry) get-proc (get-return-edges wfp) (lift-procs wfp) Main
  (Def wfp) (Use wfp) (ParamDefs wfp) (ParamUses wfp)
proof
from Entry-Def-empty Entry-Use-empty
show Def wfp (Main, Entry) = {}  $\wedge$  Use wfp (Main, Entry) = {} by simp
next
fix a Q r p fs ins outs
assume valid-edge wfp a and kind a = Q:r→pfs

```

and $(p, ins, outs) \in set (lift-procs wfp)$
hence $prog, procs \vdash sourcenode a -kind a \rightarrow targetnode a$
by $(simp\ add:valid-edge-def)$
from $this \langle kind a = Q:r \rightarrow pfs \rangle \langle (p, ins, outs) \in set (lift-procs wfp) \rangle$
show $length (ParamUses wfp (sourcenode a)) = length ins$
proof $(induct\ n \equiv sourcenode a\ et \equiv kind a\ n' \equiv targetnode a\ rule:PCFG.induct)$
case $(MainCall\ l\ p'\ es\ rets\ n'\ insx\ outsx\ cx)$
with wf **have** $[simp]:insx = ins$ **by** $fastforce$
from $\langle prog \vdash Label\ l -CEdge (p', es, rets) \rightarrow_p n' \rangle$
have $containsCall\ procs\ prog \ []\ p'$ **by** $(rule\ Proc-CFG-Call-containsCall)$
with $\langle wf\ prog\ procs \rangle \langle (p', insx, outsx, cx) \in set\ procs \rangle$
 $\langle prog \vdash Label\ l -CEdge (p', es, rets) \rightarrow_p n' \rangle$
have $length\ es = length\ ins$ **by** $fastforce$
from $\langle prog \vdash Label\ l -CEdge (p', es, rets) \rightarrow_p n' \rangle$
have $ParamUses\ wfp (Main, Label\ l) = map\ fv\ es$
by $(fastforce\ intro:ParamUses-Main-Return-target)$
with $\langle (Main, Label\ l) = sourcenode\ a \rangle \langle length\ es = length\ ins \rangle$
show $?case$ **by** $simp$
next
case $(ProcCall\ px\ insx\ outsx\ cx\ l\ p'\ es\ rets\ l'\ ins'\ outs'\ c'\ ps)$
with wf **have** $[simp]:ins' = ins$ **by** $fastforce$
from $\langle cx \vdash Label\ l -CEdge (p', es, rets) \rightarrow_p Label\ l' \rangle$
have $containsCall\ procs\ cx \ []\ p'$ **by** $(rule\ Proc-CFG-Call-containsCall)$
with $\langle containsCall\ procs\ prog\ ps\ px \rangle \langle (px, insx, outsx, cx) \in set\ procs \rangle$
have $containsCall\ procs\ prog (ps@[px])\ p'$ **by** $(rule\ containsCall-in-proc)$
with $\langle wf\ prog\ procs \rangle \langle (p', ins', outs', c') \in set\ procs \rangle$
 $\langle cx \vdash Label\ l -CEdge (p', es, rets) \rightarrow_p Label\ l' \rangle$
have $length\ es = length\ ins$ **by** $fastforce$
from $\langle (px, insx, outsx, cx) \in set\ procs \rangle$
 $\langle cx \vdash Label\ l -CEdge (p', es, rets) \rightarrow_p Label\ l' \rangle$
have $ParamUses\ wfp (px, Label\ l) = map\ fv\ es$
by $(fastforce\ intro:ParamUses-Proc-Return-target\ simp:set-conv-nth)$
with $\langle (px, Label\ l) = sourcenode\ a \rangle \langle length\ es = length\ ins \rangle$
show $?case$ **by** $simp$
qed $auto$
next
fix a **assume** $valid-edge\ wfp\ a$
hence $prog, procs \vdash sourcenode a -kind a \rightarrow targetnode a$
by $(simp\ add:valid-edge-def)$
thus $distinct (ParamDefs wfp (targetnode a))$
proof $(induct\ sourcenode a\ kind a\ targetnode a\ rule:PCFG.induct)$
case $(Main\ n\ n')$
from $\langle prog \vdash n -IEdge (kind a) \rightarrow_p n' \rangle \langle (Main, n') = targetnode a \rangle$
have $ParamDefs\ wfp (Main, n') = []$ **by** $(fastforce\ intro:ParamDefs-Main-IEdge-Nil)$
with $\langle (Main, n') = targetnode a \rangle$ **show** $?case$ **by** $simp$
next
case $(Proc\ p\ ins\ outs\ c\ n\ n')$
from $\langle (p, ins, outs, c) \in set\ procs \rangle \langle c \vdash n -IEdge (kind a) \rightarrow_p n' \rangle$
have $ParamDefs\ wfp (p, n') = []$ **by** $(fastforce\ intro:ParamDefs-Proc-IEdge-Nil)$

```

with  $\langle(p, n') = \text{targetnode } a\rangle$  show ?case by simp
next
case (MainCall l p es rets n' ins outs c)
with  $\langle(p, \text{ins}, \text{outs}, c) \in \text{set procs}\rangle$  wf have [simp]: $p \neq \text{Main}$ 
  by fastforce
from wf  $\langle(p, \text{ins}, \text{outs}, c) \in \text{set procs}\rangle$ 
have (THE  $c'. \exists \text{ins}' \text{outs}'. (p, \text{ins}', \text{outs}', c') \in \text{set procs}$ ) = c
  by(rule in-procs-THE-in-procs-cmd)
with  $\langle(p, \text{Entry}) = \text{targetnode } a\rangle$ [THEN sym] show ?case
  by(auto simp:ParamDefs-def ParamDefs-proc-def)
next
case (ProcCall p ins outs c l p' es' rets' l' ins' outs' c')
with  $\langle(p', \text{ins}', \text{outs}', c') \in \text{set procs}\rangle$  wf
have [simp]: $p' \neq \text{Main}$  by fastforce
from wf  $\langle(p', \text{ins}', \text{outs}', c') \in \text{set procs}\rangle$ 
have (THE  $cx. \exists \text{insx} \text{outsx}. (p', \text{insx}, \text{outsx}, cx) \in \text{set procs}$ ) = c'
  by(rule in-procs-THE-in-procs-cmd)
with  $\langle(p', \text{Entry}) = \text{targetnode } a\rangle$ [THEN sym] show ?case
  by(fastforce simp:ParamDefs-def ParamDefs-proc-def)
next
case (MainReturn l p es rets l' ins outs c)
from  $\langle \text{prog} \vdash \text{Label } l - \text{CEdge } (p, \text{es}, \text{rets}) \rightarrow_p \text{Label } l' \rangle$ 
have containsCall procs prog  $\square$  p by(rule Proc-CFG-Call-containsCall)
with  $\langle \text{wf prog procs} \rangle$   $\langle(p, \text{ins}, \text{outs}, c) \in \text{set procs}\rangle$ 
   $\langle \text{prog} \vdash \text{Label } l - \text{CEdge } (p, \text{es}, \text{rets}) \rightarrow_p \text{Label } l' \rangle$ 
have distinct rets by fastforce
from  $\langle \text{prog} \vdash \text{Label } l - \text{CEdge } (p, \text{es}, \text{rets}) \rightarrow_p \text{Label } l' \rangle$ 
have ParamDefs wfp (Main, Label l') = rets
  by(fastforce intro:ParamDefs-Main-Return-target)
with  $\langle \text{distinct rets} \rangle$   $\langle(\text{Main}, \text{Label } l') = \text{targetnode } a\rangle$  show ?case
  by(fastforce simp:distinct-map inj-on-def)
next
case (ProcReturn p ins outs c l p' es' rets' l' ins' outs' c' ps)
from  $\langle c \vdash \text{Label } l - \text{CEdge } (p', \text{es}', \text{rets}') \rightarrow_p \text{Label } l' \rangle$ 
have containsCall procs c  $\square$  p' by(rule Proc-CFG-Call-containsCall)
with  $\langle \text{containsCall procs prog ps } p \rangle$   $\langle(p, \text{ins}, \text{outs}, c) \in \text{set procs}\rangle$ 
have containsCall procs prog (ps@[p]) p' by(rule containsCall-in-proc)
with  $\langle \text{wf prog procs} \rangle$   $\langle(p', \text{ins}', \text{outs}', c') \in \text{set procs}\rangle$ 
   $\langle c \vdash \text{Label } l - \text{CEdge } (p', \text{es}', \text{rets}') \rightarrow_p \text{Label } l' \rangle$ 
have distinct rets' by fastforce
from  $\langle(p, \text{ins}, \text{outs}, c) \in \text{set procs}\rangle$ 
   $\langle c \vdash \text{Label } l - \text{CEdge } (p', \text{es}', \text{rets}') \rightarrow_p \text{Label } l' \rangle$ 
have ParamDefs wfp (p, Label l') = rets'
  by(fastforce intro:ParamDefs-Proc-Return-target simp:set-conv-nth)
with  $\langle \text{distinct rets}' \rangle$   $\langle(p, \text{Label } l') = \text{targetnode } a\rangle$  show ?case
  by(fastforce simp:distinct-map inj-on-def)
next
case (MainCallReturn n p es rets n')
from  $\langle \text{prog} \vdash n - \text{CEdge } (p, \text{es}, \text{rets}) \rightarrow_p n' \rangle$ 

```

```

have containsCall procs prog [] p by(rule Proc-CFG-Call-containsCall)
with  $\langle wf\ prog\ procs \rangle$  obtain ins outs c where  $(p, ins, outs, c) \in set\ procs$ 
  by(simp add:wf-def) blast
with  $\langle wf\ prog\ procs \rangle$   $\langle containsCall\ procs\ prog\ []\ p \rangle$ 
   $\langle prog \vdash n - CEdge\ (p, es, rets) \rightarrow_p\ n' \rangle$ 
have distinct rets by fastforce
from  $\langle prog \vdash n - CEdge\ (p, es, rets) \rightarrow_p\ n' \rangle$ 
have ParamDefs wfp (Main, n') = rets
  by(fastforce intro:ParamDefs-Main-Return-target)
with  $\langle distinct\ rets \rangle$   $\langle (Main, n') = targetnode\ a \rangle$  show ?case
  by(fastforce simp:distinct-map inj-on-def)
next
case  $(ProcCallReturn\ p\ ins\ outs\ c\ n\ p'\ es'\ rets'\ n'\ ps)$ 
from  $\langle c \vdash n - CEdge\ (p', es', rets') \rightarrow_p\ n' \rangle$ 
have containsCall procs c [] p' by(rule Proc-CFG-Call-containsCall)
from  $\langle Rep\ wf\ prog\ wfp = (prog, procs) \rangle$   $\langle (p, ins, outs, c) \in set\ procs \rangle$ 
   $\langle containsCall\ procs\ prog\ ps\ p \rangle$ 
obtain wfp' where Rep-wf-prog wfp' = (c, procs) by(erule wfp-Call)
hence wf c procs by(rule wf-wf-prog)
with  $\langle containsCall\ procs\ c\ []\ p' \rangle$  obtain ins' outs' c'
  where  $(p', ins', outs', c') \in set\ procs$ 
  by(simp add:wf-def) blast
from  $\langle containsCall\ procs\ prog\ ps\ p \rangle$   $\langle (p, ins, outs, c) \in set\ procs \rangle$ 
   $\langle containsCall\ procs\ c\ []\ p' \rangle$ 
have containsCall procs prog (ps@[p]) p' by(rule containsCall-in-proc)
with  $\langle wf\ prog\ procs \rangle$   $\langle (p', ins', outs', c') \in set\ procs \rangle$ 
   $\langle c \vdash n - CEdge\ (p', es', rets') \rightarrow_p\ n' \rangle$ 
have distinct rets' by fastforce
from  $\langle (p, ins, outs, c) \in set\ procs \rangle$   $\langle c \vdash n - CEdge\ (p', es', rets') \rightarrow_p\ n' \rangle$ 
have ParamDefs wfp (p, n') = rets'
  by(fastforce intro:ParamDefs-Proc-Return-target)
with  $\langle distinct\ rets' \rangle$   $\langle (p, n') = targetnode\ a \rangle$  show ?case
  by(fastforce simp:distinct-map inj-on-def)
qed
next
fix a Q' p f' ins outs
assume valid-edge wfp a and kind a = Q'  $\leftrightarrow$  p f'
  and  $(p, ins, outs) \in set\ (lift\ procs\ wfp)$ 
thus length (ParamDefs wfp (targetnode a)) = length outs
  by(rule ParamDefs-length)
next
fix n V assume CFG.valid-node sourcenode targetnode (valid-edge wfp) n
  and  $V \in set\ (ParamDefs\ wfp\ n)$ 
thus  $V \in Def\ wfp\ n$  by(simp add:Def-def)
next
fix a Q r p fs ins outs V
assume valid-edge wfp a and kind a = Q: r  $\leftrightarrow$  p fs
  and  $(p, ins, outs) \in set\ (lift\ procs\ wfp)$  and  $V \in set\ ins$ 
hence  $prog, procs \vdash sourcenode\ a - kind\ a \rightarrow targetnode\ a$ 

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    by(simp add:valid-edge-def)
  from this ⟨kind a = Q:r↔pfs⟩ ⟨(p, ins, outs) ∈ set (lift-procs wfp)⟩ ⟨V ∈ set
ins⟩
  show V ∈ Def wfp (targetnode a)
  proof(induct n≡sourcnode a et≡kind a n'≡targetnode a rule:PCFG.induct)
    case (MainCall l p' es rets n' insx outsx cx)
    with wf have [simp]:insx = ins by fastforce
    from wf ⟨(p', insx, outsx, cx) ∈ set procs⟩
    have (THE ins'. ∃ c' outs'. (p',ins',outs',c') ∈ set procs) =
      insx by(rule in-procs-THE-in-procs-ins)
    with ⟨(p', Entry) = targetnode a⟩[THEN sym] ⟨V ∈ set ins⟩
      ⟨(p', insx, outsx, cx) ∈ set procs⟩ show ?case by(auto simp:Def-def)
  next
  case (ProcCall px insx outsx cx l p' es rets l' ins' outs' c')
  with wf have [simp]:ins' = ins by fastforce
  from wf ⟨(p', ins', outs', c') ∈ set procs⟩
  have (THE insx. ∃ cx outsx. (p',insx,outsx,cx) ∈ set procs) =
    ins' by(rule in-procs-THE-in-procs-ins)
  with ⟨(p', Entry) = targetnode a⟩[THEN sym] ⟨V ∈ set ins⟩
    ⟨(p', ins', outs', c') ∈ set procs⟩ show ?case by(auto simp:Def-def)
  qed auto
next
fix a Q r p fs
assume valid-edge wfp a and kind a = Q:r↔pfs
hence prog,procs ⊢ sourcnode a -kind a → targetnode a
  by(simp add:valid-edge-def)
from this ⟨kind a = Q:r↔pfs⟩ show Def wfp (sourcnode a) = {}
proof(induct n≡sourcnode a et≡kind a n'≡targetnode a rule:PCFG.induct)
  case (MainCall l p' es rets n' insx outsx cx)
  from ⟨(Main, Label l) = sourcnode a⟩[THEN sym]
    ⟨prog ⊢ Label l -CEdge (p', es, rets)→p n'⟩
  have ParamDefs wfp (sourcnode a) = []
    by(fastforce intro:ParamDefs-Main-CEdge-Nil)
  with ⟨prog ⊢ Label l -CEdge (p', es, rets)→p n'⟩
    ⟨(Main, Label l) = sourcnode a⟩[THEN sym]
  show ?case by(fastforce dest:Proc-CFG-Call-source-empty-lhs simp:Def-def)
next
  case (ProcCall px insx outsx cx l p' es' rets' l' ins' outs' c')
  from ⟨(px, insx, outsx, cx) ∈ set procs⟩ wf
  have [simp]:px ≠ Main by fastforce
  from ⟨cx ⊢ Label l -CEdge (p', es', rets')→p Label l'⟩
  have lhs (label cx l) = {} by(rule Proc-CFG-Call-source-empty-lhs)
  from wf ⟨(px, insx, outsx, cx) ∈ set procs⟩
  have THE:(THE c'. ∃ ins' outs'. (px,ins',outs',c') ∈ set procs) = cx
    by(rule in-procs-THE-in-procs-cmd)
  with ⟨(px, Label l) = sourcnode a⟩[THEN sym]
    ⟨cx ⊢ Label l -CEdge (p', es', rets')→p Label l'⟩ wf
  have ParamDefs wfp (sourcnode a) = []
    by(fastforce dest:Proc-CFG-Call-targetnode-no-Call-sourcnode)

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    [of - - - - Label l] simp:ParamDefs-def ParamDefs-proc-def)
  with ⟨(px, Label l) = sourcenode a⟩[THEN sym] ⟨lhs (label cx l) = {}⟩ THE
  show ?case by(auto simp:Def-def)
qed auto
next
fix n V assume CFG.valid-node sourcenode targetnode (valid-edge wfp) n
  and V ∈ ⋃ set (ParamUses wfp n)
  thus V ∈ Use wfp n by(fastforce simp:Use-def)
next
fix a Q p f ins outs V
assume valid-edge wfp a and kind a = Q↔pf
  and (p, ins, outs) ∈ set (lift-procs wfp) and V ∈ set outs
  hence prog,procs ⊢ sourcenode a -kind a → targetnode a
  by(simp add:valid-edge-def)
from this ⟨kind a = Q↔pf⟩ ⟨(p, ins, outs) ∈ set (lift-procs wfp)⟩ ⟨V ∈ set outs⟩
show V ∈ Use wfp (sourcenode a)
proof(induct sourcenode a kind a targetnode a rule:PCFG.induct)
  case (MainReturn l p' es rets l' insx outsx cx)
  with wf have [simp]:outsx = outs by fastforce
  from wf ⟨(p', insx, outsx, cx) ∈ set procs⟩
  have (THE outs'. ∃ c' ins'. (p',ins',outs',c') ∈ set procs) =
    outsx by(rule in-procs-THE-in-procs-outs)
  with ⟨(p', Exit) = sourcenode a⟩[THEN sym] ⟨V ∈ set outs⟩
  ⟨(p', insx, outsx, cx) ∈ set procs⟩ show ?case by(auto simp:Use-def)
next
  case (ProcReturn px insx outsx cx l p' es' rets' l' ins' outs' c')
  with wf have [simp]:outs' = outs by fastforce
  from wf ⟨(p', ins', outs', c') ∈ set procs⟩
  have (THE outsx. ∃ cx insx. (p',insx,outsx,cx) ∈ set procs) =
    outs' by(rule in-procs-THE-in-procs-outs)
  with ⟨(p', Exit) = sourcenode a⟩[THEN sym] ⟨V ∈ set outs⟩
  ⟨(p', ins', outs', c') ∈ set procs⟩ show ?case by(auto simp:Use-def)
qed auto
next
fix a V s
assume valid-edge wfp a and V ∉ Def wfp (sourcenode a)
  and intra-kind (kind a) and CFG.pred (kind a) s
  hence prog,procs ⊢ sourcenode a -kind a → targetnode a
  by(simp add:valid-edge-def)
from this ⟨V ∉ Def wfp (sourcenode a)⟩ ⟨intra-kind (kind a)⟩ ⟨CFG.pred (kind
a) s⟩
show state-val (CFG.transfer (lift-procs wfp) (kind a) s) V = state-val s V
proof(induct sourcenode a kind a targetnode a rule:PCFG.induct)
  case (Main n n')
  from ⟨CFG.pred (kind a) s⟩ obtain cf cfs where s = cf#cfs by(cases s)
auto
  show ?case
  proof(cases n)
    case (Label l)

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with ⟨V ∉ Def wfp (sourcenode a)⟩ ⟨(Main, n) = sourcenode a⟩
have V ∉ lhs (label prog l) by(fastforce simp:Def-def)
with ⟨prog ⊢ n -IEdge (kind a)→p n'⟩ ⟨n = Label l⟩
have state-val (CFG.transfer (lift-procs wfp) (kind a) (cf#cfs)) V = fst cf
V
  by(fastforce intro:Proc-CFG-edge-no-lhs-equal)
with ⟨s = cf#cfs⟩ show ?thesis by simp
next
case Entry
with ⟨prog ⊢ n -IEdge (kind a)→p n'⟩ ⟨s = cf#cfs⟩
show ?thesis
by(fastforce dest:Proc-CFG-EntryD simp:transfers-simps[of wfp,simplified])
next
case Exit
with ⟨prog ⊢ n -IEdge (kind a)→p n'⟩ have False by fastforce
thus ?thesis by simp
qed
next
case (Proc p ins outs c n n')
from ⟨CFG.pred (kind a) s⟩ obtain cf cfs where s = cf#cfs by(cases s)
auto
from wf ⟨(p, ins, outs, c) ∈ set procs⟩
have THE1:(THE ins'. ∃ c' outs'. (p,ins',outs',c') ∈ set procs) = ins
  by(rule in-procs-THE-in-procs-ins)
from wf ⟨(p, ins, outs, c) ∈ set procs⟩
have THE2:(THE c'. ∃ ins' outs'. (p,ins',outs',c') ∈ set procs) = c
  by(rule in-procs-THE-in-procs-cmd)
from wf ⟨(p, ins, outs, c) ∈ set procs⟩
have [simp]:p ≠ Main by fastforce
show ?case
proof(cases n)
case (Label l)
with ⟨V ∉ Def wfp (sourcenode a)⟩ ⟨(p, n) = sourcenode a⟩
  ⟨(p, ins, outs, c) ∈ set procs⟩ wf THE1 THE2
have V ∉ lhs (label c l) by(fastforce simp:Def-def split:split-if-asm)
with ⟨c ⊢ n -IEdge (kind a)→p n'⟩ ⟨n = Label l⟩
have state-val (CFG.transfer (lift-procs wfp) (kind a) (cf#cfs)) V = fst cf
V
  by(fastforce intro:Proc-CFG-edge-no-lhs-equal)
with ⟨s = cf#cfs⟩ show ?thesis by simp
next
case Entry
with ⟨c ⊢ n -IEdge (kind a)→p n'⟩ ⟨s = cf#cfs⟩
show ?thesis
by(fastforce dest:Proc-CFG-EntryD simp:transfers-simps[of wfp,simplified])
next
case Exit
with ⟨c ⊢ n -IEdge (kind a)→p n'⟩ have False by fastforce
thus ?thesis by simp

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qed
next
  case MainCallReturn thus ?case by(cases s,auto simp:intra-kind-def)
next
  case ProcCallReturn thus ?case by(cases s,auto simp:intra-kind-def)
qed(auto simp:intra-kind-def)
next
fix a s s'
assume valid-edge wfp a
  and  $\forall V \in \text{Use wfp (sourcenode a)}. \text{state-val } s \ V = \text{state-val } s' \ V$ 
  and intra-kind (kind a) and CFG.pred (kind a) s and CFG.pred (kind a) s'
hence prog,procs  $\vdash$  sourcenode a  $\rightarrow$  kind a  $\rightarrow$  targetnode a
  by(simp add:valid-edge-def)
from  $\langle \text{CFG.pred (kind a) } s \rangle$  obtain cf cfs where [simp]:s = cf#cfs
  by(cases s) auto
from  $\langle \text{CFG.pred (kind a) } s' \rangle$  obtain cf' cfs' where [simp]:s' = cf'#cfs'
  by(cases s') auto
from  $\langle \text{prog,procs } \vdash \text{ sourcenode a } \rightarrow \text{ kind a } \rightarrow \text{ targetnode a} \rangle$   $\langle \text{intra-kind (kind a)} \rangle$ 
 $\langle \forall V \in \text{Use wfp (sourcenode a)}. \text{state-val } s \ V = \text{state-val } s' \ V \rangle$ 
 $\langle \text{CFG.pred (kind a) } s \rangle$   $\langle \text{CFG.pred (kind a) } s' \rangle$ 
show  $\forall V \in \text{Def wfp (sourcenode a)}$ .
  state-val (CFG.transfer (lift-procs wfp) (kind a) s) V =
  state-val (CFG.transfer (lift-procs wfp) (kind a) s') V
proof(induct sourcenode a kind a targetnode a rule:PCFG.induct)
  case (Main n n')
  show ?case
  proof(cases n)
    case (Label l)
    with  $\langle \forall V \in \text{Use wfp (sourcenode a)}. \text{state-val } s \ V = \text{state-val } s' \ V \rangle$ 
 $\langle (\text{Main}, n) = \text{sourcenode a} \rangle$  [THEN sym]
  have rhs: $\forall V \in \text{rhs (label prog l)}. \text{state-val } s \ V = \text{state-val } s' \ V$ 
    and PDef: $\forall V \in \text{set (ParamDefs wfp (sourcenode a))}$ .
      state-val s V = state-val s' V
    by(auto simp:Use-def)
  from rhs  $\langle \text{prog } \vdash \text{ n } \rightarrow_p \text{ n}' \rangle$   $\langle n = \text{Label } l \rangle$   $\langle \text{CFG.pred (kind a) } s \rangle$ 
 $\langle \text{CFG.pred (kind a) } s' \rangle$ 
  have lhs: $\forall V \in \text{lhs (label prog l)}$ .
    state-val (CFG.transfer (lift-procs wfp) (kind a) s) V =
    state-val (CFG.transfer (lift-procs wfp) (kind a) s') V
  by  $\text{--(rule Proc-CFG-edge-uses-only-rhs,auto)}$ 
  from PDef  $\langle \text{prog } \vdash \text{ n } \rightarrow_p \text{ n}' \rangle$   $\langle (\text{Main}, n) = \text{sourcenode a} \rangle$  [THEN sym]
  have  $\forall V \in \text{set (ParamDefs wfp (sourcenode a))}$ .
    state-val (CFG.transfer (lift-procs wfp) (kind a) s) V =
    state-val (CFG.transfer (lift-procs wfp) (kind a) s') V
  by(fastforce dest:Proc-CFG-Call-follows-id-edge
    simp:ParamDefs-def ParamDefs-proc-def transfers-simps[of wfp,simplified]
    split:split-if-asm)

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```

with lhs ⟨(Main, n) = sourcenode a⟩[THEN sym] Label show ?thesis
  by(fastforce simp:Def-def)
next
case Entry
with ⟨(Main, n) = sourcenode a⟩[THEN sym]
show ?thesis by(fastforce simp:Entry-Def-empty)
next
case Exit
with ⟨prog ⊢ n -IEdge (kind a)→p n'⟩ have False by fastforce
thus ?thesis by simp
qed
next
case (Proc p ins outs c n n')
show ?case
proof(cases n)
case (Label l)
with ⟨∀ V∈Use wfp (sourcenode a). state-val s V = state-val s' V⟩ wf
  ⟨(p, n) = sourcenode a⟩[THEN sym] ⟨(p, ins, outs, c) ∈ set procs⟩
have rhs:∀ V∈rhs (label c l). state-val s V = state-val s' V
  and PDef:∀ V∈set (ParamDefs wfp (sourcenode a)).
    state-val s V = state-val s' V
  by(auto dest:in-procs-THE-in-procs-cmd simp:Use-def split:split-if-asm)
from rhs ⟨c ⊢ n -IEdge (kind a)→p n'⟩ ⟨n = Label l⟩ ⟨CFG.pred (kind a)
s)
  ⟨CFG.pred (kind a) s'⟩
have lhs:∀ V∈lhs (label c l).
  state-val (CFG.transfer (lift-procs wfp) (kind a) s) V =
  state-val (CFG.transfer (lift-procs wfp) (kind a) s') V
  by -(rule Proc-CFG-edge-uses-only-rhs,auto)
from ⟨(p, ins, outs, c) ∈ set procs⟩ wf have [simp]:p ≠ Main by fastforce
from wf ⟨(p, ins, outs, c) ∈ set procs⟩
have THE:(THE c'. ∃ ins' outs'. (p,ins',outs',c') ∈ set procs) = c
  by(fastforce intro:in-procs-THE-in-procs-cmd)
with PDef ⟨c ⊢ n -IEdge (kind a)→p n'⟩ ⟨(p, n) = sourcenode a⟩[THEN
sym]
have ∀ V∈set (ParamDefs wfp (sourcenode a)).
  state-val (CFG.transfer (lift-procs wfp) (kind a) s) V =
  state-val (CFG.transfer (lift-procs wfp) (kind a) s') V
  by(fastforce dest:Proc-CFG-Call-follows-id-edge
  simp:ParamDefs-def ParamDefs-proc-def transfers-simps[of wfp,simplified]
  split:split-if-asm)
with lhs ⟨(p, n) = sourcenode a⟩[THEN sym] Label THE
show ?thesis by(auto simp:Def-def)
next
case Entry
with wf ⟨(p, ins, outs, c) ∈ set procs⟩ have ParamDefs wfp (p,n) = []
  by(fastforce simp:ParamDefs-def ParamDefs-proc-def)
moreover
from Entry ⟨c ⊢ n -IEdge (kind a)→p n'⟩ ⟨(p, ins, outs, c) ∈ set procs⟩

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have ParamUses wfp  $(p,n) = []$  by(fastforce intro:ParamUses-Proc-IEdge-Nil)
  ultimately have  $\forall V \in \text{set } \text{ins}. \text{state-val } s \ V = \text{state-val } s' \ V$ 
    using wf  $\langle (p, \text{ins}, \text{outs}, c) \in \text{set } \text{procs} \rangle \langle (p,n) = \text{sourcenode } a \rangle$ 
     $\langle \forall V \in \text{Use } \text{wfp} \ (\text{sourcenode } a). \text{state-val } s \ V = \text{state-val } s' \ V \rangle$  Entry
    by(fastforce dest:in-procs-THE-in-procs-ins simp:Use-def split:split-if-asm)
  with  $\langle c \vdash n - \text{IEdge } (\text{kind } a) \rightarrow_p n' \rangle$  Entry
  have  $\forall V \in \text{set } \text{ins}. \text{state-val } (\text{CFG.transfer } (\text{lift-procs } \text{wfp}) (\text{kind } a) \ s) \ V =$ 
     $\text{state-val } (\text{CFG.transfer } (\text{lift-procs } \text{wfp}) (\text{kind } a) \ s') \ V$ 
  by(fastforce dest:Proc-CFG-EntryD simp:transfers-simps[of wfp,simplified])
  with  $\langle (p,n) = \text{sourcenode } a \rangle$  [THEN sym] Entry wf
     $\langle (p, \text{ins}, \text{outs}, c) \in \text{set } \text{procs} \rangle \langle \text{ParamDefs } \text{wfp} \ (p,n) = [] \rangle$ 
  show ?thesis by(auto dest:in-procs-THE-in-procs-ins simp:Def-def)
next
  case Exit
  with  $\langle c \vdash n - \text{IEdge } (\text{kind } a) \rightarrow_p n' \rangle$  have False by fastforce
  thus ?thesis by simp
qed
qed(auto simp:intra-kind-def)
next
fix a s fix s':: $((\text{char list} \rightarrow \text{val}) \times \text{node}) \text{ list}$ 
assume valid-edge wfp a and CFG.pred (kind a) s
  and  $\forall V \in \text{Use } \text{wfp} \ (\text{sourcenode } a). \text{state-val } s \ V = \text{state-val } s' \ V$ 
  and  $\text{length } s = \text{length } s'$  and  $\text{snd } (\text{hd } s) = \text{snd } (\text{hd } s')$ 
hence  $\text{prog,procs} \vdash \text{sourcenode } a - \text{kind } a \rightarrow \text{targetnode } a$ 
  by(simp add:valid-edge-def)
from  $\langle \text{CFG.pred } (\text{kind } a) \ s \rangle$  obtain cf cfs where  $[\text{simp}]: s = \text{cf} \# \text{cfs}$ 
  by(cases s) auto
from  $\langle \text{length } s = \text{length } s' \rangle$  obtain cf' cfs' where  $[\text{simp}]: s' = \text{cf}' \# \text{cfs}'$ 
  by(cases s') auto
from  $\langle \text{prog,procs} \vdash \text{sourcenode } a - \text{kind } a \rightarrow \text{targetnode } a \rangle \langle \text{CFG.pred } (\text{kind } a)$ 
s)  $\langle \forall V \in \text{Use } \text{wfp} \ (\text{sourcenode } a). \text{state-val } s \ V = \text{state-val } s' \ V \rangle$ 
 $\langle \text{length } s = \text{length } s' \rangle \langle \text{snd } (\text{hd } s) = \text{snd } (\text{hd } s') \rangle$ 
show CFG.pred (kind a) s'
proof(induct sourcenode a kind a targetnode a rule:PCFG.induct)
  case (Main n n')
  show ?case
proof(cases n)
  case (Label l)
  with  $\langle \forall V \in \text{Use } \text{wfp} \ (\text{sourcenode } a). \text{state-val } s \ V = \text{state-val } s' \ V \rangle$ 
     $\langle (\text{Main}, n) = \text{sourcenode } a \rangle$ 
  have  $\forall V \in \text{rhs } (\text{label } \text{prog } l). \text{state-val } s \ V = \text{state-val } s' \ V$ 
    by(fastforce simp:Use-def)
  with  $\langle \text{prog} \vdash n - \text{IEdge } (\text{kind } a) \rightarrow_p n' \rangle$  Label  $\langle \text{CFG.pred } (\text{kind } a) \ s \rangle$ 
     $\langle \text{length } s = \text{length } s' \rangle$ 
  show ?thesis by(fastforce intro:Proc-CFG-edge-rhs-pred-eq)
next
  case Entry
  with  $\langle \text{prog} \vdash n - \text{IEdge } (\text{kind } a) \rightarrow_p n' \rangle \langle \text{CFG.pred } (\text{kind } a) \ s \rangle$ 

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  show ?thesis by(fastforce dest:Proc-CFG-EntryD)
next
  case Exit
  with ⟨prog ⊢ n -IEdge (kind a)→p n'⟩ have False by fastforce
  thus ?thesis by simp
qed
next
  case (Proc p ins outs c n n')
  show ?case
  proof(cases n)
    case (Label l)
    with ⟨∀ V∈Use wfp (sourcenode a). state-val s V = state-val s' V⟩ wf
      ⟨(p, n) = sourcenode a[THEN sym] ⟨(p, ins, outs, c) ∈ set procs⟩⟩
    have ∀ V∈rhs (label c l). state-val s V = state-val s' V
      by(auto dest:in-procs-THE-in-procs-cmd simp:Use-def split:split-if-asm)
    with ⟨c ⊢ n -IEdge (kind a)→p n'⟩ Label ⟨CFG.pred (kind a) s⟩
      ⟨length s = length s'⟩
    show ?thesis by(fastforce intro:Proc-CFG-edge-rhs-pred-eq)
  next
    case Entry
    with ⟨c ⊢ n -IEdge (kind a)→p n'⟩ ⟨CFG.pred (kind a) s⟩
    show ?thesis by(fastforce dest:Proc-CFG-EntryD)
  next
    case Exit
    with ⟨c ⊢ n -IEdge (kind a)→p n'⟩ have False by fastforce
    thus ?thesis by simp
  qed
next
  case (MainReturn l p es rets l' ins outs c)
  with ⟨λcf. snd cf = (Main, Label l')↔pλcf cf'. cf'(rets [:=] map cf outs) =
    kind a[THEN sym]⟩
  show ?case by fastforce
next
  case (ProcReturn p ins outs c l p' es rets l' ins' outs' c')
  with ⟨λcf. snd cf = (p, Label l')↔pλcf cf'. cf'(rets [:=] map cf outs') =
    kind a[THEN sym]⟩
  show ?case by fastforce
qed(auto dest:sym)
next
  fix a Q r p fs ins outs
  assume valid-edge wfp a and kind a = Q:r↔pfs
  and (p, ins, outs) ∈ set (lift-procs wfp)
  hence prog,procs ⊢ sourcenode a -kind a→ targetnode a
  by(simp add:valid-edge-def)
  from this ⟨kind a = Q:r↔pfs⟩ ⟨(p, ins, outs) ∈ set (lift-procs wfp)⟩
  show length fs = length ins
  proof(induct rule:PCFG.induct)
    case (MainCall l p' es rets n' ins' outs' c)
    hence fs = map interpret es and p' = p by simp-all

```

```

with  $\langle p, ins, outs \rangle \in set \ (lift-procs \ wfp)$ 
   $\langle p', ins', outs', c \rangle \in set \ procs$ 
have  $[simp]: ins' = ins$  by fastforce
from  $\langle prog \vdash Label \ l - CEdge \ (p', es, rets) \rightarrow_p \ n' \rangle$ 
have containsCall procs prog [] p' by  $(rule \ Proc-CFG-Call-containsCall)$ 
with  $\langle wf \ prog \ procs \rangle \langle p', ins', outs', c \rangle \in set \ procs$ 
   $\langle prog \vdash Label \ l - CEdge \ (p', es, rets) \rightarrow_p \ n' \rangle$ 
have  $length \ es = length \ ins$  by fastforce
with  $\langle fs = map \ interpret \ es \rangle$  show  $?case$  by simp
next
case  $(ProcCall \ px \ insx \ outsx \ c \ l \ p' \ es' \ rets' \ l' \ ins' \ outs' \ c' \ ps)$ 
hence  $fs = map \ interpret \ es'$  and  $p' = p$  by simp-all
with  $\langle p, ins, outs \rangle \in set \ (lift-procs \ wfp)$ 
   $\langle p', ins', outs', c' \rangle \in set \ procs$ 
have  $[simp]: ins' = ins$  by fastforce
from  $\langle c \vdash Label \ l - CEdge \ (p', es', rets') \rightarrow_p \ Label \ l' \rangle$ 
have containsCall procs c [] p' by  $(rule \ Proc-CFG-Call-containsCall)$ 
with  $\langle containsCall \ procs \ prog \ ps \ px \rangle \langle (px, insx, outsx, c) \in set \ procs \rangle$ 
have containsCall procs prog (ps@[px]) p' by  $(rule \ containsCall-in-proc)$ 
with  $\langle wf \ prog \ procs \rangle \langle p', ins', outs', c' \rangle \in set \ procs$ 
   $\langle c \vdash Label \ l - CEdge \ (p', es', rets') \rightarrow_p \ Label \ l' \rangle$ 
have  $length \ es' = length \ ins$  by fastforce
with  $\langle fs = map \ interpret \ es' \rangle$  show  $?case$  by simp
qed auto
next
fix  $a \ Q \ r \ p \ fs \ a' \ Q' \ r' \ p' \ fs' \ s \ s'$ 
assume valid-edge wfp a and  $kind \ a = Q:r \hookrightarrow_p fs$ 
  and valid-edge wfp a' and  $kind \ a' = Q':r' \hookrightarrow_{p'} fs'$ 
  and  $sourcenode \ a = sourcenode \ a'$ 
hence  $prog, procs \vdash sourcenode \ a - kind \ a \rightarrow targetnode \ a$ 
  and  $prog, procs \vdash sourcenode \ a' - kind \ a' \rightarrow targetnode \ a'$ 
  by  $(simp-all \ add:valid-edge-def)$ 
from  $this \ \langle kind \ a = Q:r \hookrightarrow_p fs \rangle \langle kind \ a' = Q':r' \hookrightarrow_{p'} fs' \rangle$  show  $a = a'$ 
proof  $(induct \ sourcenode \ a \ kind \ a \ targetnode \ a \ rule:PCFG.induct)$ 
case  $(MainCall \ l \ px \ es \ rets \ n' \ insx \ outsx \ cx)$ 
from  $\langle prog, procs \vdash sourcenode \ a' - kind \ a' \rightarrow targetnode \ a' \rangle$ 
   $\langle kind \ a' = Q':r' \hookrightarrow_{p'} fs' \rangle$ 
   $\langle (Main, Label \ l) = sourcenode \ a \rangle \langle sourcenode \ a = sourcenode \ a' \rangle$ 
   $\langle prog \vdash Label \ l - CEdge \ (px, es, rets) \rightarrow_p \ n' \rangle \ wf$ 
have  $targetnode \ a' = (px, Entry)$ 
  by  $(fastforce \ elim!:PCFG.cases \ dest:Proc-CFG-Call-nodes-eq)$ 
with  $\langle valid-edge \ wfp \ a \rangle \langle valid-edge \ wfp \ a' \rangle$ 
   $\langle sourcenode \ a = sourcenode \ a' \rangle \langle (px, Entry) = targetnode \ a \rangle \ wf$ 
have  $kind \ a = kind \ a'$  by  $(fastforce \ intro:Proc-CFG-edge-det \ simp:valid-edge-def)$ 
with  $\langle sourcenode \ a = sourcenode \ a' \rangle \langle (px, Entry) = targetnode \ a \rangle$ 
   $\langle targetnode \ a' = (px, Entry) \rangle$ 
show  $?case$  by  $(cases \ a, cases \ a', auto)$ 
next
case  $(ProcCall \ px \ ins \ outs \ c \ l \ px' \ es \ rets \ l' \ insx \ outsx \ cx)$ 

```


with *wf* **have** $px \neq \text{Main}$ **by** *fastforce*
with $\langle \text{prog}, \text{procs} \vdash \text{sourcenode } a' - \text{kind } a' \rightarrow \text{targetnode } a' \rangle$
 $\langle \text{kind } a' = Q':r' \hookrightarrow_p r'fs' \rangle$
 $\langle (px, \text{Label } l) = \text{sourcenode } a \rangle \langle \text{sourcenode } a = \text{sourcenode } a' \rangle$
 $\langle c \vdash \text{Label } l - \text{CEdge } (px', es, \text{rets}) \rightarrow_p \text{Label } l' \rangle$
 $\langle (px', \text{insx}, \text{outsx}, cx) \in \text{set } \text{procs} \rangle \langle (px, \text{ins}, \text{outs}, c) \in \text{set } \text{procs} \rangle$
have $\text{targetnode } a' = (px', \text{Entry})$
proof(*induct* $n \equiv \text{sourcenode } a' \text{ et } \equiv \text{kind } a' \text{ n}' \equiv \text{targetnode } a'$ *rule:PCFG.induct*)
case (*ProcCall* $p \text{ insa } \text{outsa } ca \text{ la } p'a \text{ es' } \text{rets' } l'a \text{ ins' } \text{outs' } c'$)
hence [*simp*]: $px = p \text{ l} = \text{la}$ **by**(*auto dest:sym*)
from $\langle (p, \text{insa}, \text{outsa}, ca) \in \text{set } \text{procs} \rangle$
 $\langle (px, \text{ins}, \text{outs}, c) \in \text{set } \text{procs} \rangle$ **wf** **have** [*simp*]: $ca = c$ **by** *auto*
from $\langle ca \vdash \text{Label } la - \text{CEdge } (p'a, es', \text{rets}') \rightarrow_p \text{Label } l'a \rangle$
 $\langle c \vdash \text{Label } l - \text{CEdge } (px', es, \text{rets}) \rightarrow_p \text{Label } l' \rangle$
have $p'a = px'$ **by**(*fastforce dest:Proc-CFG-Call-nodes-eq*)
with $\langle (p'a, \text{Entry}) = \text{targetnode } a' \rangle$ **show** *?case* **by** *simp*
qed(*auto dest:sym*)
with $\langle \text{valid-edge wfp } a \rangle \langle \text{valid-edge wfp } a' \rangle$
 $\langle \text{sourcenode } a = \text{sourcenode } a' \rangle \langle (px', \text{Entry}) = \text{targetnode } a \rangle$ **wf**
have $\text{kind } a = \text{kind } a'$ **by**(*fastforce intro:Proc-CFG-edge-det simp:valid-edge-def*)
with $\langle \text{sourcenode } a = \text{sourcenode } a' \rangle \langle (px', \text{Entry}) = \text{targetnode } a \rangle$
 $\langle \text{targetnode } a' = (px', \text{Entry}) \rangle$ **show** *?case* **by**(*cases a,cases a',auto*)
qed *auto*
next
fix $a \ Q \ r \ p \ fs \ i \ \text{ins} \ \text{outs} \ \text{fix } s \ s':((\text{char list} \rightarrow \text{val}) \times \text{node}) \ \text{list}$
assume *valid-edge wfp* a **and** $\text{kind } a = Q:r \hookrightarrow_p fs$ **and** $i < \text{length } \text{ins}$
and $(p, \text{ins}, \text{outs}) \in \text{set } (\text{lift-procs } \text{wfp})$
and $\forall V \in \text{ParamUses } \text{wfp } (\text{sourcenode } a) ! i. \text{state-val } s \ V = \text{state-val } s' \ V$
hence $\text{prog}, \text{procs} \vdash \text{sourcenode } a - \text{kind } a \rightarrow \text{targetnode } a$
by(*simp add:valid-edge-def*)
from *this* $\langle \text{kind } a = Q:r \hookrightarrow_p fs \rangle \langle i < \text{length } \text{ins} \rangle$
 $\langle (p, \text{ins}, \text{outs}) \in \text{set } (\text{lift-procs } \text{wfp}) \rangle$
 $\langle \forall V \in \text{ParamUses } \text{wfp } (\text{sourcenode } a) ! i. \text{state-val } s \ V = \text{state-val } s' \ V \rangle$
show $\text{CFG.params } fs \ (\text{state-val } s) ! i = \text{CFG.params } fs \ (\text{state-val } s') ! i$
proof(*induct* $\text{sourcenode } a \ \text{kind } a \ \text{targetnode } a$ *rule:PCFG.induct*)
case (*MainCall* $l \ p' \ \text{es} \ \text{rets} \ n' \ \text{insx} \ \text{outsx} \ cx$)
with *wf* **have** [*simp*]: $\text{insx} = \text{ins } fs = \text{map } \text{interpret } \text{es}$ **by** *auto*
from $\langle \text{prog} \vdash \text{Label } l - \text{CEdge } (p', es, \text{rets}) \rightarrow_p n' \rangle$
have *containsCall* $\text{procs } \text{prog} \ [] \ p'$ **by**(*rule Proc-CFG-Call-containsCall*)
with $\langle \text{wf } \text{prog } \text{procs} \rangle \langle (p', \text{insx}, \text{outsx}, cx) \in \text{set } \text{procs} \rangle$
 $\langle \text{prog} \vdash \text{Label } l - \text{CEdge } (p', es, \text{rets}) \rightarrow_p n' \rangle$
have $\text{length } \text{es} = \text{length } \text{ins}$ **by** *fastforce*
with $\langle i < \text{length } \text{ins} \rangle$ **have** $i < \text{length } (\text{map } \text{interpret } \text{es})$ **by** *simp*
from $\langle \text{prog} \vdash \text{Label } l - \text{CEdge } (p', es, \text{rets}) \rightarrow_p n' \rangle$
have $\text{ParamUses } \text{wfp } (\text{Main}, \text{Label } l) = \text{map } \text{fv } \text{es}$
by(*fastforce intro:ParamUses-Main-Return-target*)
with $\langle \forall V \in \text{ParamUses } \text{wfp } (\text{sourcenode } a) ! i. \text{state-val } s \ V = \text{state-val } s' \ V \rangle$
 $\langle i < \text{length } (\text{map } \text{interpret } \text{es}) \rangle \langle (\text{Main}, \text{Label } l) = \text{sourcenode } a \rangle$
have $((\text{map } (\lambda e \text{ cf. } \text{interpret } e \text{ cf}) \ \text{es})!i) \ (\text{fst } (\text{hd } s)) =$

```

      ((map (λe cf. interpret e cf) es)!i) (fst (hd s'))
    by(cases interpret (es ! i) (fst (hd s)))(auto dest:rhs-interpret-eq)
  with ⟨i < length (map interpret es)⟩ show ?case by(simp add:ProcCFG.params-nth)
next
  case (ProcCall px insx outsx cx l p' es' rets' l' ins' outs' c' ps)
  with wf have [simp]:ins' = ins by fastforce
  from ⟨cx ⊢ Label l - CEdge (p', es', rets') →p Label l'⟩
  have containsCall procs cx [] p' by(rule Proc-CFG-Call-containsCall)
  with ⟨containsCall procs prog ps px⟩ ⟨(px, insx, outsx, cx) ∈ set procs⟩
  have containsCall procs prog (ps@[px]) p' by(rule containsCall-in-proc)
  with ⟨wf prog procs⟩ ⟨(p', ins', outs', c') ∈ set procs⟩
  ⟨cx ⊢ Label l - CEdge (p', es', rets') →p Label l'⟩
  have length es' = length ins by fastforce
  from ⟨λs. True:(px, Label l') ↦p map interpret es' = kind a⟩ ⟨kind a =
Q:r↦pfs)
  have fs = map interpret es' by simp-all
  from ⟨i < length ins⟩ ⟨fs = map interpret es'⟩
  ⟨length es' = length ins⟩ have i < length fs by simp
  from ⟨(px, insx, outsx, cx) ∈ set procs⟩
  ⟨cx ⊢ Label l - CEdge (p', es', rets') →p Label l'⟩
  have ParamUses wfp (px,Label l) = map fv es'
  by(auto intro!:ParamUses-Proc-Return-target simp:set-conv-nth)
  with ⟨∀ V ∈ ParamUses wfp (sourcenode a) ! i. state-val s V = state-val s' V⟩
  ⟨(px, Label l) = sourcenode a⟩ ⟨i < length fs⟩
  ⟨fs = map interpret es'⟩
  have ((map (λe cf. interpret e cf) es')!i) (fst (hd s)) =
  ((map (λe cf. interpret e cf) es')!i) (fst (hd s'))
  by(cases interpret (es' ! i) (fst (hd s)))(auto dest:rhs-interpret-eq)
  with ⟨i < length fs⟩ ⟨fs = map interpret es'⟩
  show ?case by(simp add:ProcCFG.params-nth)
qed auto
next
  fix a Q' p f' ins outs cf cf'
  assume valid-edge wfp a and kind a = Q'↔pf'
  and (p, ins, outs) ∈ set (lift-procs wfp)
  thus f' cf cf' = cf'(ParamDefs wfp (targetnode a) [:=] map cf outs)
  by(rule Return-update)
next
  fix a a'
  assume valid-edge wfp a and valid-edge wfp a'
  and sourcenode a = sourcenode a' and targetnode a ≠ targetnode a'
  and intra-kind (kind a) and intra-kind (kind a')
  with wf show ∃ Q Q'. kind a = (Q)✓ ∧ kind a' = (Q')✓ ∧
  (∀ cf. (Q cf → ¬ Q' cf) ∧ (Q' cf → ¬ Q cf))
  by(auto dest:Proc-CFG-deterministic simp:valid-edge-def)
qed
qed

```

2.7.5 Instantiating the *CFGExit-wf* locale

interpretation *ProcCFGExit-wf*:

CFGExit-wf sourcenode targetnode kind valid-edge wfp (*Main,Entry*)

get-proc get-return-edges wfp lift-procs wfp *Main* (*Main,Exit*)

Def wfp Use wfp ParamDefs wfp ParamUses wfp

for wfp

proof

from *Exit-Def-empty* *Exit-Use-empty*

show Def wfp (*Main, Exit*) = {} \wedge Use wfp (*Main, Exit*) = {} **by** *simp*

qed

end

2.8 Lemmas concerning paths to instantiate locale Postdomination

theory *ValidPaths* **imports** *WellFormed* *../StaticInter/Postdomination* **begin**

2.8.1 Intraprocedural paths from method entry and to method exit

abbreviation *path* :: *wf-prog* \Rightarrow *node* \Rightarrow *edge list* \Rightarrow *node* \Rightarrow *bool* (*-* \vdash *-* \dashrightarrow^* *-*)

where *wfp* \vdash *n* \dashrightarrow^* *n'* \equiv *CFG.path* sourcenode targetnode (*valid-edge wfp*)
n as n'

definition *label-incrs* :: *edge list* \Rightarrow *nat* \Rightarrow *edge list* (*-* \oplus *s* - 60)

where *as* \oplus *s* \equiv *map* ($\lambda((p,n),et,(p',n')). ((p,n \oplus i),et,(p',n' \oplus i))$) *as*

declare *One-nat-def* [*simp del*]

From *prog* **to** *prog;;c2*

lemma *Proc-CFG-edge-SeqFirst-nodes-Label*:

prog \vdash *Label l* \dashrightarrow_p *Label l'* \implies *prog;;c2* \vdash *Label l* \dashrightarrow_p *Label l'*

proof(*induct prog Label l et Label l' rule:Proc-CFG.induct*)

case (*Proc-CFG-SeqSecond* *c2'* *n* *et* *n'* *c1*)

hence (*c1;; c2'*);; *c2* \vdash *n* \oplus $\#$:*c1* \dashrightarrow_p *n'* \oplus $\#$:*c1*

by(*fastforce intro:Proc-CFG-SeqFirst Proc-CFG.Proc-CFG-SeqSecond*)

with $\langle n \oplus \# : c_1 = \text{Label } l \rangle$ $\langle n' \oplus \# : c_1 = \text{Label } l' \rangle$ **show** *?case* **by** *fastforce*

next

case (*Proc-CFG-CondThen* *c1* *n* *et* *n'* *b* *c2'*)

hence *if* (*b*) *c1* *else* *c2'*;; *c2* \vdash *n* \oplus 1 \dashrightarrow_p *n'* \oplus 1

by(*fastforce intro:Proc-CFG-SeqFirst Proc-CFG.Proc-CFG-CondThen*)

with $\langle n \oplus 1 = \text{Label } l \rangle$ $\langle n' \oplus 1 = \text{Label } l' \rangle$ **show** *?case* **by** *fastforce*

next

case (*Proc-CFG-CondElse* *c1* *n* *et* *n'* *b* *c2'*)

hence if $(b) c_2'$ else c_1 ;; $c_2 \vdash n \oplus \#:c_2' + 1 -et \rightarrow_p n' \oplus (\#:c_2' + 1)$
by $(fastforce \text{ intro:Proc-CFG-SeqFirst Proc-CFG.Proc-CFG-CondElse})$
with $\langle n \oplus \#:c_2' + 1 = \text{Label } l \rangle \langle n' \oplus \#:c_2' + 1 = \text{Label } l' \rangle$ **show** $?case$ **by** $fastforce$
next
case $(Proc-CFG-WhileBody \ c' \ n \ et \ n' \ b)$
hence while $(b) \ c'$;; $c_2 \vdash n \oplus 2 -et \rightarrow_p n' \oplus 2$
by $(fastforce \text{ intro:Proc-CFG-SeqFirst Proc-CFG.Proc-CFG-WhileBody})$
with $\langle n \oplus 2 = \text{Label } l \rangle \langle n' \oplus 2 = \text{Label } l' \rangle$ **show** $?case$ **by** $fastforce$
next
case $(Proc-CFG-WhileBodyExit \ c' \ n \ et \ b)$
hence while $(b) \ c'$;; $c_2 \vdash n \oplus 2 -et \rightarrow_p \text{Label } 0$
by $(fastforce \text{ intro:Proc-CFG-SeqFirst Proc-CFG.Proc-CFG-WhileBodyExit})$
with $\langle n \oplus 2 = \text{Label } l \rangle \langle 0 = l' \rangle$ **show** $?case$ **by** $fastforce$
qed $(auto \text{ intro:Proc-CFG.intros})$

lemma $Proc-CFG-edge-SeqFirst-source-Label$:

assumes $prog \vdash \text{Label } l -et \rightarrow_p n'$
obtains nx **where** $prog;;c_2 \vdash \text{Label } l -et \rightarrow_p nx$
proof $(atomize-elim)$
from $\langle prog \vdash \text{Label } l -et \rightarrow_p n' \rangle$ **obtain** n **where** $prog \vdash n -et \rightarrow_p n'$ **and** $\text{Label } l = n$
by $simp$
thus $\exists nx. prog;;c_2 \vdash \text{Label } l -et \rightarrow_p nx$
proof $(induct \text{ prog } n \ et \ n' \ \text{rule:Proc-CFG.induct})$
case $(Proc-CFG-SeqSecond \ c_2' \ n \ et \ n' \ c_1)$
show $?case$
proof $(cases \ n' = \text{Exit})$
case True
with $\langle c_2' \vdash n -et \rightarrow_p n' \rangle \langle n \neq \text{Entry} \rangle$ **have** $c_1;; c_2' \vdash n \oplus \#:c_1 -et \rightarrow_p \text{Exit}$
 $\oplus \#:c_1$
by $(fastforce \text{ intro:Proc-CFG.Proc-CFG-SeqSecond})$
moreover **from** $\langle n \neq \text{Entry} \rangle$ **have** $n \oplus \#:c_1 \neq \text{Entry}$ **by** $(cases \ n)$ **auto**
ultimately
have $c_1;; c_2'$;; $c_2 \vdash n \oplus \#:c_1 -et \rightarrow_p \text{Label } (\#:c_1;; c_2')$
by $(fastforce \text{ intro:Proc-CFG-SeqConnect})$
with $\langle \text{Label } l = n \oplus \#:c_1 \rangle$ **show** $?thesis$ **by** $fastforce$
next
case False
with $Proc-CFG-SeqSecond$
have $(c_1;; c_2')$;; $c_2 \vdash n \oplus \#:c_1 -et \rightarrow_p n' \oplus \#:c_1$
by $(fastforce \text{ intro:Proc-CFG-SeqFirst Proc-CFG.Proc-CFG-SeqSecond})$
with $\langle \text{Label } l = n \oplus \#:c_1 \rangle$ **show** $?thesis$ **by** $fastforce$
qed
next
case $(Proc-CFG-CondThen \ c_1 \ n \ et \ n' \ b \ c_2')$
show $?case$
proof $(cases \ n' = \text{Exit})$

```

case True
with  $\langle c_1 \vdash n -et\rightarrow_p n' \rangle \langle n \neq \text{Entry} \rangle$ 
have if (b)  $c_1$  else  $c_2' \vdash n \oplus 1 -et\rightarrow_p \text{Exit} \oplus 1$ 
  by(fastforce intro:Proc-CFG.Proc-CFG-CondThen)
moreover from  $\langle n \neq \text{Entry} \rangle$  have  $n \oplus 1 \neq \text{Entry}$  by(cases n) auto
ultimately
have if (b)  $c_1$  else  $c_2';; c_2 \vdash n \oplus 1 -et\rightarrow_p \text{Label } (\#: \text{if } (b) c_1 \text{ else } c_2')$ 
  by(fastforce intro:Proc-CFG-SeqConnect)
with  $\langle \text{Label } l = n \oplus 1 \rangle$  show ?thesis by fastforce
next
case False
hence  $n' \oplus 1 \neq \text{Exit}$  by(cases n') auto
with Proc-CFG-CondThen
have if (b)  $c_1$  else  $c_2';; c_2 \vdash \text{Label } l -et\rightarrow_p n' \oplus 1$ 
  by(fastforce intro:Proc-CFG-SeqFirst Proc-CFG.Proc-CFG-CondThen)
with  $\langle \text{Label } l = n \oplus 1 \rangle$  show ?thesis by fastforce
qed
next
case (Proc-CFG-CondElse  $c_1$  n et  $n'$  b  $c_2'$ )
show ?case
proof(cases  $n' = \text{Exit}$ )
  case True
  with  $\langle c_1 \vdash n -et\rightarrow_p n' \rangle \langle n \neq \text{Entry} \rangle$ 
  have if (b)  $c_2'$  else  $c_1 \vdash n \oplus (\#:c_2' + 1) -et\rightarrow_p \text{Exit} \oplus (\#:c_2' + 1)$ 
    by(fastforce intro:Proc-CFG.Proc-CFG-CondElse)
  moreover from  $\langle n \neq \text{Entry} \rangle$  have  $n \oplus (\#:c_2' + 1) \neq \text{Entry}$  by(cases n)
auto
  ultimately
  have if (b)  $c_2'$  else  $c_1;; c_2 \vdash n \oplus (\#:c_2' + 1) -et\rightarrow_p$ 
     $\text{Label } (\#: \text{if } (b) c_2' \text{ else } c_1)$ 
    by(fastforce intro:Proc-CFG-SeqConnect)
  with  $\langle \text{Label } l = n \oplus (\#:c_2' + 1) \rangle$  show ?thesis by fastforce
  next
  case False
  hence  $n' \oplus (\#:c_2' + 1) \neq \text{Exit}$  by(cases n') auto
  with Proc-CFG-CondElse
  have if (b)  $c_2'$  else  $c_1;; c_2 \vdash \text{Label } l -et\rightarrow_p n' \oplus (\#:c_2' + 1)$ 
    by(fastforce intro:Proc-CFG-SeqFirst Proc-CFG.Proc-CFG-CondElse)
  with  $\langle \text{Label } l = n \oplus (\#:c_2' + 1) \rangle$  show ?thesis by fastforce
  qed
qed (auto intro:Proc-CFG.intros)
qed

```

lemma *Proc-CFG-edge-SeqFirst-target-Label*:

```

 $\llbracket \text{prog} \vdash n -et\rightarrow_p n'; \text{Label } l' = n \rrbracket \implies \text{prog};; c_2 \vdash n -et\rightarrow_p \text{Label } l'$ 
proof(induct prog n et  $n'$  rule:Proc-CFG.induct)
case (Proc-CFG-SeqSecond  $c_2'$  n et  $n'$   $c_1$ )
from  $\langle \text{Label } l' = n' \oplus \#:c_1 \rangle$  have  $n' \neq \text{Exit}$  by(cases  $n'$ ) auto

```

with *Proc-CFG-SeqSecond*
show ?case **by**(*fastforce intro:Proc-CFG-SeqFirst intro:Proc-CFG.Proc-CFG-SeqSecond*)
next
case (*Proc-CFG-CondThen c₁ n et n' b c₂*)
from $\langle \text{Label } l' = n' \oplus 1 \rangle$ **have** $n' \neq \text{Exit}$ **by**(*cases n'*) *auto*
with *Proc-CFG-CondThen*
show ?case **by**(*fastforce intro:Proc-CFG-SeqFirst Proc-CFG.Proc-CFG-CondThen*)
qed (*auto intro:Proc-CFG.intros*)

lemma *PCFG-edge-SeqFirst-source-Label*:

assumes *prog,procs* $\vdash (p, \text{Label } l) -et \rightarrow (p', n')$
obtains *nx* **where** *prog;;c₂,procs* $\vdash (p, \text{Label } l) -et \rightarrow (p', nx)$

proof(*atomize-elim*)

from $\langle \text{prog,procs} \vdash (p, \text{Label } l) -et \rightarrow (p', n') \rangle$
show $\exists nx. \text{prog;;c}_2, \text{procs} \vdash (p, \text{Label } l) -et \rightarrow (p', nx)$
proof(*induct (p, Label l) et (p', n') rule:PCFG.induct*)
case (*Main et*)
from $\langle \text{prog} \vdash \text{Label } l -I\text{Edge } et \rightarrow_p n' \rangle$
obtain *nx'* **where** *prog;;c₂* $\vdash \text{Label } l -I\text{Edge } et \rightarrow_p nx'$
by(*auto elim:Proc-CFG-edge-SeqFirst-source-Label*)
with $\langle \text{Main} = p \rangle \langle \text{Main} = p' \rangle$ **show** ?case
by(*fastforce dest:PCFG.Main*)

next

case (*Proc ins outs c et ps*)
from $\langle \text{containsCall procs prog ps p} \rangle$
have *containsCall procs (prog;;c₂) ps p* **by** *simp*
with *Proc* **show** ?case **by**(*fastforce dest:PCFG.Proc*)

next

case (*MainCall es rets nx ins outs c*)
from $\langle \text{prog} \vdash \text{Label } l -C\text{Edge } (p', es, rets) \rightarrow_p nx \rangle$
obtain *lx* **where** [*simp*]: $nx = \text{Label } lx$ **by**(*fastforce dest:Proc-CFG-Call-Labels*)
with $\langle \text{prog} \vdash \text{Label } l -C\text{Edge } (p', es, rets) \rightarrow_p nx \rangle$
have *prog;;c₂* $\vdash \text{Label } l -C\text{Edge } (p', es, rets) \rightarrow_p \text{Label } lx$
by(*auto intro:Proc-CFG-edge-SeqFirst-nodes-Label*)
with *MainCall* **show** ?case **by**(*fastforce dest:PCFG.MainCall*)

next

case (*ProcCall ins outs c es' rets' l' ins' outs' c' ps*)
from $\langle \text{containsCall procs prog ps p} \rangle$
have *containsCall procs (prog;;c₂) ps p* **by** *simp*
with *ProcCall* **show** ?case **by**(*fastforce intro:PCFG.ProcCall*)

next

case (*MainCallReturn px es rets*)
from $\langle \text{prog} \vdash \text{Label } l -C\text{Edge } (px, es, rets) \rightarrow_p n' \rangle \langle \text{Main} = p \rangle$
obtain *nx''* **where** *prog;;c₂* $\vdash \text{Label } l -C\text{Edge } (px, es, rets) \rightarrow_p nx''$
by(*auto elim:Proc-CFG-edge-SeqFirst-source-Label*)
with *MainCallReturn* **show** ?case **by**(*fastforce dest:PCFG.MainCallReturn*)

next

case (*ProcCallReturn ins outs c px' es' rets' ps*)

```

    from ⟨containsCall procs prog ps p⟩
    have containsCall procs (prog;;c2) ps p by simp
    with ProcCallReturn show ?case by(fastforce dest!:PCFG.ProcCallReturn)
  qed
qed

lemma PCFG-edge-SeqFirst-target-Label:
  prog,procs ⊢ (p,n) -et→ (p',Label l')
  ⇒ prog;;c2,procs ⊢ (p,n) -et→ (p',Label l')
proof(induct (p,n) et (p',Label l') rule:PCFG.induct)
  case Main
  thus ?case by(fastforce dest:Proc-CFG-edge-SeqFirst-target-Label intro:PCFG.Main)
next
  case (Proc ins outs c et ps)
  from ⟨containsCall procs prog ps p⟩
  have containsCall procs (prog;;c2) ps p by simp
  with Proc show ?case by(fastforce dest:PCFG.Proc)
next
  case MainReturn thus ?case
  by(fastforce dest:Proc-CFG-edge-SeqFirst-target-Label
    intro!:PCFG.MainReturn[simplified])
next
  case (ProcReturn ins outs c lx es' rets' ins' outs' c' ps)
  from ⟨containsCall procs prog ps p'⟩
  have containsCall procs (prog;;c2) ps p' by simp
  with ProcReturn show ?case by(fastforce intro:PCFG.ProcReturn)
next
  case MainCallReturn thus ?case
  by(fastforce dest:Proc-CFG-edge-SeqFirst-target-Label intro:PCFG.MainCallReturn)
next
  case (ProcCallReturn ins outs c px' es' rets' ps)
  from ⟨containsCall procs prog ps p⟩
  have containsCall procs (prog;;c2) ps p by simp
  with ProcCallReturn show ?case by(fastforce dest!:PCFG.ProcCallReturn)
qed

```

```

lemma path-SeqFirst:
  assumes Rep-wf-prog wfp = (prog,procs) and Rep-wf-prog wfp' = (prog;;c2,procs)
  shows ⟦wfp ⊢ (p,n) -as→* (p,Label l); ∀ a ∈ set as. intra-kind (kind a)⟧
  ⇒ wfp' ⊢ (p,n) -as→* (p,Label l)
proof(induct (p,n) as (p,Label l) arbitrary:n rule:ProcCFG.path.induct)
  case empty-path
  from ⟨CFG.valid-node sourcenode targetnode (valid-edge wfp) (p, Label l)⟩
  ⟨Rep-wf-prog wfp = (prog, procs)⟩ ⟨Rep-wf-prog wfp' = (prog;; c2, procs)⟩
  have CFG.valid-node sourcenode targetnode (valid-edge wfp') (p, Label l)
  apply(auto simp:ProcCFG.valid-node-def valid-edge-def)
  apply(erule PCFG-edge-SeqFirst-source-Label,fastforce)

```

```

    by(drule PCFG-edge-SeqFirst-target-Label,fastforce)
  thus ?case by(fastforce intro:ProcCFG.empty-path)
next
case (Cons-path n'' as a nx)
note IH = (∧n. [∃n'' = (p, n); ∀a∈set as. intra-kind (kind a)])
  ⇒ wfp' ⊢ (p, n) -as→* (p, Label l)
note [simp] = (Rep-wf-prog wfp = (prog,procs)) (Rep-wf-prog wfp' = (prog;;c2,procs))
from (Rep-wf-prog wfp = (prog,procs)) have wf:well-formed procs
  by(fastforce intro:wf-wf-prog)
from (∀a∈set (a # as). intra-kind (kind a)) have intra-kind (kind a)
  and ∀a∈set as. intra-kind (kind a) by simp-all
from (valid-edge wfp a) (sourcnode a = (p, nx)) (targetnode a = n'')
  (intra-kind (kind a)) wf
obtain nx' where n'' = (p, nx')
  by(auto elim:PCFG.cases simp:valid-edge-def intra-kind-def)
from IH[OF this (∀a∈set as. intra-kind (kind a))]
have path:wfp' ⊢ (p, nx') -as→* (p, Label l) .
have valid-edge wfp' a
proof(cases nx')
  case (Label lx)
  with (valid-edge wfp a) (sourcnode a = (p, nx)) (targetnode a = n'')
    (∃n'' = (p, nx')) show ?thesis
    by(fastforce intro:PCFG-edge-SeqFirst-target-Label
      simp:intra-kind-def valid-edge-def)
next
case Entry
with (valid-edge wfp a) (targetnode a = n'') (∃n'' = (p, nx'))
  (intra-kind (kind a)) have False
  by(auto elim:PCFG.cases simp:valid-edge-def intra-kind-def)
thus ?thesis by simp
next
case Exit
with path (∀a∈set as. intra-kind (kind a)) have False
  by(induct (p, nx') as (p, Label l) rule:ProcCFG.path.induct)
  (auto elim!:PCFG.cases simp:valid-edge-def intra-kind-def)
thus ?thesis by simp
qed
with (sourcnode a = (p, nx)) (targetnode a = n'') (∃n'' = (p, nx')) path
show ?case by(fastforce intro:ProcCFG.Cons-path)
qed

```

From prog to $c_1;;prog$

lemma Proc-CFG-edge-SeqSecond-source-not-Entry:

$\llbracket prog \vdash n -et\rightarrow_p n'; n \neq Entry \rrbracket \implies c_1;;prog \vdash n \oplus \# : c_1 -et\rightarrow_p n' \oplus \# : c_1$
 by(*induct* rule:Proc-CFG.induct)(*fastforce* *intro*:Proc-CFG-SeqSecond Proc-CFG.intros)+

lemma PCFG-Main-edge-SeqSecond-source-not-Entry:


```

[[prog,procs ⊢ (Main,n) -et→ (p',n'); n ≠ Entry; intra-kind et; well-formed
procs]]
⇒ c₁;;prog,procs ⊢ (Main,n ⊕ #:c₁) -et→ (p',n' ⊕ #:c₁)
proof(induct (Main,n) et (p',n') rule:PCFG.induct)
  case Main
  thus ?case
  by(fastforce dest:Proc-CFG-edge-SeqSecond-source-not-Entry intro:PCFG.Main)
next
  case (MainCallReturn p es rets)
  from ⟨prog ⊢ n -CEdge (p, es, rets)→p n'⟩ ⟨n ≠ Entry⟩
  have c₁;;prog ⊢ n ⊕ #:c₁ -CEdge (p, es, rets)→p n' ⊕ #:c₁
    by(rule Proc-CFG-edge-SeqSecond-source-not-Entry)
  with MainCallReturn show ?case by(fastforce intro:PCFG.MainCallReturn)
qed (auto simp:intra-kind-def)

```

lemma *valid-node-Main-SeqSecond*:

```

assumes CFG.valid-node sourcenode targetnode (valid-edge wfp) (Main,n)
and n ≠ Entry and Rep-wf-prog wfp = (prog,procs)
and Rep-wf-prog wfp' = (c₁;;prog,procs)
shows CFG.valid-node sourcenode targetnode (valid-edge wfp') (Main, n ⊕ #:c₁)

```

proof –

```

note [simp] = ⟨Rep-wf-prog wfp = (prog,procs)⟩ ⟨Rep-wf-prog wfp' = (c₁;;prog,procs)⟩
from ⟨Rep-wf-prog wfp = (prog,procs)⟩ have wf:well-formed procs
  by(fastforce intro:wf-wf-prog)
from ⟨CFG.valid-node sourcenode targetnode (valid-edge wfp) (Main,n)⟩
obtain a where prog,procs ⊢ sourcenode a -kind a→ targetnode a
  and (Main,n) = sourcenode a ∨ (Main,n) = targetnode a
  by(fastforce simp:ProcCFG.valid-node-def valid-edge-def)
from this ⟨n ≠ Entry⟩ wf show ?thesis
proof(induct sourcenode a kind a targetnode a rule:PCFG.induct)
  case (Main nx nx')
  from ⟨(Main,n) = sourcenode a ∨ (Main,n) = targetnode a⟩ show ?case
  proof
    assume (Main,n) = sourcenode a
    with ⟨(Main, nx) = sourcenode a⟩[THEN sym] have [simp]:nx = n by simp
    from ⟨n ≠ Entry⟩ ⟨prog ⊢ nx -IEdge (kind a)→p nx'⟩
    have c₁;;prog ⊢ n ⊕ #:c₁ -IEdge (kind a)→p nx' ⊕ #:c₁
      by(fastforce intro:Proc-CFG-edge-SeqSecond-source-not-Entry)
    hence c₁;;prog,procs ⊢ (Main,n ⊕ #:c₁) -kind a→ (Main,nx' ⊕ #:c₁)
      by(rule PCFG.Main)
    thus ?thesis by(simp add:ProcCFG.valid-node-def)(fastforce simp:valid-edge-def)
  next
    assume (Main, n) = targetnode a
    show ?thesis
    proof(cases nx = Entry)
      case True
      with ⟨prog ⊢ nx -IEdge (kind a)→p nx'⟩
      have nx' = Exit ∨ nx' = Label 0 by(fastforce dest:Proc-CFG-EntryD)
    
```

```

thus ?thesis
proof
  assume  $nx' = Exit$ 
  with  $\langle (Main, n) = targetnode\ a \rangle \langle (Main, nx') = targetnode\ a \rangle [THEN\ sym]$ 
  show ?thesis by simp
next
  assume  $nx' = Label\ 0$ 
  obtain  $l\ etx$  where  $c_1 \vdash Label\ l - IEdge\ etx \rightarrow_p\ Exit$  and  $l \leq \#:c_1$ 
    by (erule Proc-CFG-Exit-edge)
  hence  $c_1;;prog \vdash Label\ l - IEdge\ etx \rightarrow_p\ Label\ \#:c_1$ 
    by (fastforce intro:Proc-CFG-SeqConnect)
  with  $\langle nx' = Label\ 0 \rangle$ 
  have  $c_1;;prog,procs \vdash (Main, Label\ l) - etx \rightarrow (Main, nx' \oplus \#:c_1)$ 
    by (fastforce intro:PCFG.Main)
  with  $\langle (Main, n) = targetnode\ a \rangle \langle (Main, nx') = targetnode\ a \rangle [THEN\ sym]$ 
  show ?thesis
    by (simp add:ProcCFG.valid-node-def)(fastforce simp:valid-edge-def)
  qed
next
  case False
  with  $\langle prog \vdash nx - IEdge\ (kind\ a) \rightarrow_p\ nx' \rangle$ 
  have  $c_1;;prog \vdash nx \oplus \#:c_1 - IEdge\ (kind\ a) \rightarrow_p\ nx' \oplus \#:c_1$ 
    by (fastforce intro:Proc-CFG-edge-SeqSecond-source-not-Entry)
  hence  $c_1;;prog,procs \vdash (Main, nx \oplus \#:c_1) - kind\ a \rightarrow (Main, nx' \oplus \#:c_1)$ 
    by (rule PCFG.Main)
  with  $\langle (Main, n) = targetnode\ a \rangle \langle (Main, nx') = targetnode\ a \rangle [THEN\ sym]$ 
  show ?thesis by (simp add:ProcCFG.valid-node-def)(fastforce simp:valid-edge-def)
  qed
qed
next
  case (Proc  $p\ ins\ outs\ c\ nx\ n'\ ps$ )
  from  $\langle (p, nx) = sourcenode\ a \rangle [THEN\ sym] \langle (p, n') = targetnode\ a \rangle [THEN\ sym]$ 
   $\langle (Main, n) = sourcenode\ a \vee (Main, n) = targetnode\ a \rangle$ 
   $\langle (p, ins, outs, c) \in set\ procs \rangle \langle well\text{-}formed\ procs \rangle$  have False by fastforce
  thus ?case by simp
next
  case (MainCall  $l\ p\ es\ rets\ n'\ ins\ outs\ c$ )
  from  $\langle (p, ins, outs, c) \in set\ procs \rangle wf \langle (p, Entry) = targetnode\ a \rangle [THEN\ sym]$ 
   $\langle (Main, Label\ l) = sourcenode\ a \rangle [THEN\ sym]$ 
   $\langle (Main, n) = sourcenode\ a \vee (Main, n) = targetnode\ a \rangle$ 
  have [simp]:  $n = Label\ l$  by fastforce
  from  $\langle prog \vdash Label\ l - CEdge\ (p, es, rets) \rightarrow_p\ n' \rangle$ 
  have  $c_1;;prog \vdash Label\ l \oplus \#:c_1 - CEdge\ (p, es, rets) \rightarrow_p\ n' \oplus \#:c_1$ 
    by -(rule Proc-CFG-edge-SeqSecond-source-not-Entry, auto)
  with  $\langle (p, ins, outs, c) \in set\ procs \rangle$ 
  have  $c_1;;prog,procs \vdash (Main, Label\ (l + \#:c_1))$ 
     $-(\lambda s. True): (Main, n' \oplus \#:c_1) \hookrightarrow_p map\ (\lambda e\ cf. interpret\ e\ cf)\ es \rightarrow (p, Entry)$ 
    by (fastforce intro:PCFG.MainCall)
  thus ?case by (simp add:ProcCFG.valid-node-def)(fastforce simp:valid-edge-def)

```

```

next
  case (ProcCall p ins outs c l p' es' rets' l' ins' outs' c')
  from ⟨(p, Label l) = sourcenode a⟩[THEN sym]
    ⟨(p', Entry) = targetnode a⟩[THEN sym] ⟨well-formed procs⟩
    ⟨(p, ins, outs, c) ∈ set procs⟩ ⟨(p', ins', outs', c') ∈ set procs⟩
    ⟨(Main, n) = sourcenode a ∨ (Main, n) = targetnode a⟩
  have False by fastforce
  thus ?case by simp
next
  case (MainReturn l p es rets l' ins outs c)
  from ⟨(p, ins, outs, c) ∈ set procs⟩ wf ⟨(p, Exit) = sourcenode a⟩[THEN sym]
    ⟨(Main, Label l') = targetnode a⟩[THEN sym]
    ⟨(Main, n) = sourcenode a ∨ (Main, n) = targetnode a⟩
  have [simp]: n = Label l' by fastforce
  from ⟨prog ⊢ Label l -CEdge (p, es, rets)→p Label l'⟩
  have c1::prog ⊢ Label l ⊕ #:c1 -CEdge (p, es, rets)→p Label l' ⊕ #:c1
    by -(rule Proc-CFG-edge-SeqSecond-source-not-Entry, auto)
  with ⟨(p, ins, outs, c) ∈ set procs⟩
  have c1::prog,procs ⊢ (p,Exit) -(λcf. snd cf = (Main,Label l' ⊕ #:c1))↔p
    (λcf cf'. cf'(rets[:=] map cf outs))→(Main,Label (l' + #:c1))
    by(fastforce intro:PCFG.MainReturn)
  thus ?case by(simp add:ProcCFG.valid-node-def)(fastforce simp:valid-edge-def)
next
  case (ProcReturn p ins outs c l p' es' rets' l' ins' outs' c' ps)
  from ⟨(p', Exit) = sourcenode a⟩[THEN sym]
    ⟨(p, Label l') = targetnode a⟩[THEN sym] ⟨well-formed procs⟩
    ⟨(p, ins, outs, c) ∈ set procs⟩ ⟨(p', ins', outs', c') ∈ set procs⟩
    ⟨(Main, n) = sourcenode a ∨ (Main, n) = targetnode a⟩
  have False by fastforce
  thus ?case by simp
next
  case (MainCallReturn nx p es rets nx')
  from ⟨(Main, n) = sourcenode a ∨ (Main, n) = targetnode a⟩ show ?case
  proof
    assume (Main, n) = sourcenode a
    with ⟨(Main, nx) = sourcenode a⟩[THEN sym] have [simp]: nx = n by simp
    from ⟨n ≠ Entry⟩ ⟨prog ⊢ nx -CEdge (p, es, rets)→p nx'⟩
    have c1::prog ⊢ n ⊕ #:c1 -CEdge (p, es, rets)→p nx' ⊕ #:c1
      by(fastforce intro:Proc-CFG-edge-SeqSecond-source-not-Entry)
    hence c1::prog,procs ⊢ (Main, n ⊕ #:c1) -(λs. False)√→(Main, nx' ⊕ #:c1)
      by -(rule PCFG.MainCallReturn)
    thus ?thesis by(simp add:ProcCFG.valid-node-def)(fastforce simp:valid-edge-def)
  next
    assume (Main, n) = targetnode a
    from ⟨prog ⊢ nx -CEdge (p, es, rets)→p nx'⟩
    have nx ≠ Entry by(fastforce dest:Proc-CFG-Call-Labels)
    with ⟨prog ⊢ nx -CEdge (p, es, rets)→p nx'⟩
    have c1::prog ⊢ nx ⊕ #:c1 -CEdge (p, es, rets)→p nx' ⊕ #:c1
      by(fastforce intro:Proc-CFG-edge-SeqSecond-source-not-Entry)

```

hence $c_1;;prog,procs \vdash (Main, nx \oplus \#:c_1) -(\lambda s. False)_{\surd} \rightarrow (Main, nx' \oplus \#:c_1)$
by $-(rule\ PCFG.MainCallReturn)$
with $\langle (Main, n) = targetnode\ a \rangle \langle (Main, nx') = targetnode\ a \rangle [THEN\ sym]$
show $?thesis$ **by** $(simp\ add:ProcCFG.valid-node-def)(fastforce\ simp:valid-edge-def)$
qed
next
case $(ProcCallReturn\ p\ ins\ outs\ c\ nx\ p'\ es'\ rets'\ n'\ ps)$
from $\langle (p, nx) = sourcenode\ a \rangle [THEN\ sym] \langle (p, n') = targetnode\ a \rangle [THEN\ sym]$
 $\langle (p, ins, outs, c) \in set\ procs \rangle \langle well\ formed\ procs \rangle$
 $\langle (Main, n) = sourcenode\ a \vee (Main, n) = targetnode\ a \rangle$
have $False$ **by** $fastforce$
thus $?case$ **by** $simp$
qed
qed

lemma *path-Main-SeqSecond*:

assumes $Rep\ wf\ prog\ wfp = (prog,procs)$ **and** $Rep\ wf\ prog\ wfp' = (c_1;;prog,procs)$
shows $\llbracket wfp \vdash (Main, n) -as \rightarrow^* (p', n'); \forall a \in set\ as. intra\ kind\ (kind\ a); n \neq Entry \rrbracket$

$\implies wfp' \vdash (Main, n \oplus \#:c_1) -as \oplus s \#:c_1 \rightarrow^* (p', n' \oplus \#:c_1)$

proof $(induct\ (Main, n)\ as\ (p', n')\ arbitrary:n\ rule:ProcCFG.path.induct)$

case *empty-path*

from $\langle CFG.valid\ node\ sourcenode\ targetnode\ (valid\ edge\ wfp) \rangle (Main, n')$

$\langle n' \neq Entry \rangle \langle Rep\ wf\ prog\ wfp = (prog,procs) \rangle$

$\langle Rep\ wf\ prog\ wfp' = (c_1;;prog,procs) \rangle$

have $CFG.valid\ node\ sourcenode\ targetnode\ (valid\ edge\ wfp') \rangle (Main, n' \oplus \#:c_1)$

by $(fastforce\ intro:valid\ node\ Main\ SeqSecond)$

with $\langle Main = p' \rangle$ **show** $?case$

by $(fastforce\ intro:ProcCFG.empty\ path\ simp:label\ incrs\ def)$

next

case $(Cons\ path\ n''\ as\ a\ n)$

note $IH = \langle \bigwedge n. \llbracket n'' = (Main, n); \forall a \in set\ as. intra\ kind\ (kind\ a); n \neq Entry \rrbracket$

$\implies wfp' \vdash (Main, n \oplus \#:c_1) -as \oplus s \#:c_1 \rightarrow^* (p', n' \oplus \#:c_1) \rangle$

note $[simp] = \langle Rep\ wf\ prog\ wfp = (prog,procs) \rangle \langle Rep\ wf\ prog\ wfp' = (c_1;;prog,procs) \rangle$

from $\langle Rep\ wf\ prog\ wfp = (prog,procs) \rangle$ **have** $wf:well\ formed\ procs$

by $(fastforce\ intro:wf\ wf\ prog)$

from $\langle \forall a \in set\ (a \# as). intra\ kind\ (kind\ a) \rangle$ **have** $intra\ kind\ (kind\ a)$

and $\forall a \in set\ as. intra\ kind\ (kind\ a)$ **by** $simp\ all$

from $\langle valid\ edge\ wfp\ a \rangle \langle sourcenode\ a = (Main, n) \rangle \langle targetnode\ a = n'' \rangle$

$\langle intra\ kind\ (kind\ a) \rangle wf$

obtain nx'' **where** $n'' = (Main, nx'')$ **and** $nx'' \neq Entry$

by $(auto\ elim!:ProcCFG.cases\ simp:valid\ edge\ def\ intra\ kind\ def)$

from $IH[OF\ \langle n'' = (Main, nx'') \rangle \langle \forall a \in set\ as. intra\ kind\ (kind\ a) \rangle \langle nx'' \neq Entry \rangle]$

have $path:wfp' \vdash (Main, nx'' \oplus \#:c_1) -as \oplus s \#:c_1 \rightarrow^* (p', n' \oplus \#:c_1)$.

from $\langle valid\ edge\ wfp\ a \rangle \langle sourcenode\ a = (Main, n) \rangle \langle targetnode\ a = n'' \rangle$

$\langle n'' = (Main, nx'') \rangle \langle n \neq Entry \rangle \langle intra\ kind\ (kind\ a) \rangle wf$

have $c_1;; prog,procs \vdash (Main, n \oplus \#:c_1) -kind\ a \rightarrow (Main, nx'' \oplus \#:c_1)$

by $(fastforce\ intro:PCFG\ Main\ edge\ SeqSecond\ source\ not\ Entry\ simp:valid\ edge\ def)$

with $\langle \text{sourcenode } a = (\text{Main}, n) \rangle \langle \text{targetnode } a = n' \rangle \langle n'' = (\text{Main}, nx'') \rangle$
show $?case \text{ apply}(\text{cases } a) \text{ apply}(\text{clarsimp simp:label-incrs-def})$
by $(\text{auto intro:ProcCFG.Cons-path simp:valid-edge-def})$
qed

From prog to if (b) prog else c₂

lemma Proc-CFG-edge-CondTrue-source-not-Entry:

$\llbracket \text{prog} \vdash n \text{ --et--} \rightarrow_p n'; n \neq \text{Entry} \rrbracket \implies \text{if } (b) \text{ prog else } c_2 \vdash n \oplus 1 \text{ --et--} \rightarrow_p n' \oplus 1$
by $(\text{induct rule:Proc-CFG.induct})(\text{fastforce intro:Proc-CFG-CondThen Proc-CFG.intros})+$

lemma PCFG-Main-edge-CondTrue-source-not-Entry:

$\llbracket \text{prog,procs} \vdash (\text{Main}, n) \text{ --et--} \rightarrow (p', n'); n \neq \text{Entry}; \text{intra-kind } et; \text{well-formed procs} \rrbracket$

$\implies \text{if } (b) \text{ prog else } c_2, \text{procs} \vdash (\text{Main}, n \oplus 1) \text{ --et--} \rightarrow (p', n' \oplus 1)$

proof $(\text{induct } (\text{Main}, n) \text{ et } (p', n') \text{ rule:PCFG.induct})$

case *Main*

thus $?case \text{ by}(\text{fastforce dest:Proc-CFG-edge-CondTrue-source-not-Entry intro:PCFG.Main})$

next

case $(\text{MainCallReturn } p \text{ es } \text{rets})$

from $\langle \text{prog} \vdash n \text{ --CEdge } (p, \text{es}, \text{rets}) \rightarrow_p n' \rangle \langle n \neq \text{Entry} \rangle$

have $\text{if } (b) \text{ prog else } c_2 \vdash n \oplus 1 \text{ --CEdge } (p, \text{es}, \text{rets}) \rightarrow_p n' \oplus 1$

by $(\text{rule Proc-CFG-edge-CondTrue-source-not-Entry})$

with MainCallReturn **show** $?case \text{ by}(\text{fastforce intro:PCFG.MainCallReturn})$

qed $(\text{auto simp:intra-kind-def})$

lemma valid-node-Main-CondTrue:

assumes $\text{CFG.valid-node sourcenode targetnode } (\text{valid-edge wfp}) (\text{Main}, n)$

and $n \neq \text{Entry}$ **and** $\text{Rep-wf-prog wfp} = (\text{prog,procs})$

and $\text{Rep-wf-prog wfp}' = (\text{if } (b) \text{ prog else } c_2, \text{procs})$

shows $\text{CFG.valid-node sourcenode targetnode } (\text{valid-edge wfp}') (\text{Main}, n \oplus 1)$

proof –

note $[\text{simp}] = \langle \text{Rep-wf-prog wfp} = (\text{prog,procs}) \rangle$

$\langle \text{Rep-wf-prog wfp}' = (\text{if } (b) \text{ prog else } c_2, \text{procs}) \rangle$

from $\langle \text{Rep-wf-prog wfp} = (\text{prog,procs}) \rangle$ **have** $\text{wf:well-formed procs}$

by $(\text{fastforce intro:wf-wf-prog})$

from $\langle \text{CFG.valid-node sourcenode targetnode } (\text{valid-edge wfp}) (\text{Main}, n) \rangle$

obtain a **where** $\text{prog,procs} \vdash \text{sourcenode } a \text{ --kind } a \rightarrow \text{targetnode } a$

and $(\text{Main}, n) = \text{sourcenode } a \vee (\text{Main}, n) = \text{targetnode } a$

by $(\text{fastforce simp:ProcCFG.valid-node-def valid-edge-def})$

from this $\langle n \neq \text{Entry} \rangle$ **wf** **show** $?thesis$

proof $(\text{induct sourcenode } a \text{ kind } a \text{ targetnode } a \text{ rule:PCFG.induct})$

case $(\text{Main } nx \text{ } nx')$

from $\langle (\text{Main}, n) = \text{sourcenode } a \vee (\text{Main}, n) = \text{targetnode } a \rangle$ **show** $?case$

proof

assume $(\text{Main}, n) = \text{sourcenode } a$

with $\langle (\text{Main}, nx) = \text{sourcenode } a \rangle$ $[\text{THEN sym}]$ **have** $[\text{simp}]: nx = n$ **by** simp

```

from  $\langle n \neq \text{Entry} \rangle \langle \text{prog} \vdash nx \text{ --IEdge } (\text{kind } a) \rightarrow_p nx' \rangle$ 
have  $\text{if } (b) \text{ prog else } c_2 \vdash n \oplus 1 \text{ --IEdge } (\text{kind } a) \rightarrow_p nx' \oplus 1$ 
  by  $(\text{fastforce intro:Proc-CFG-edge-CondTrue-source-not-Entry})$ 
hence  $\text{if } (b) \text{ prog else } c_2, \text{procs} \vdash (\text{Main}, n \oplus 1) \text{ --kind } a \rightarrow (\text{Main}, nx' \oplus 1)$ 
  by  $(\text{rule PCFG.Main})$ 
thus  $?thesis$  by  $(\text{simp add:ProcCFG.valid-node-def})(\text{fastforce simp:valid-edge-def})$ 
next
assume  $(\text{Main}, n) = \text{targetnode } a$ 
show  $?thesis$ 
proof  $(\text{cases } nx = \text{Entry})$ 
  case  $\text{True}$ 
    with  $\langle \text{prog} \vdash nx \text{ --IEdge } (\text{kind } a) \rightarrow_p nx' \rangle$ 
    have  $nx' = \text{Exit} \vee nx' = \text{Label } 0$  by  $(\text{fastforce dest:Proc-CFG-EntryD})$ 
    thus  $?thesis$ 
    proof
      assume  $nx' = \text{Exit}$ 
      with  $\langle (\text{Main}, n) = \text{targetnode } a \rangle \langle (\text{Main}, nx') = \text{targetnode } a \rangle [\text{THEN sym}]$ 
      show  $?thesis$  by  $\text{simp}$ 
    next
      assume  $nx' = \text{Label } 0$ 
      have  $\text{if } (b) \text{ prog else } c_2 \vdash \text{Label } 0$ 
         $\text{--IEdge } (\lambda cf. \text{state-check } cf \ b \ (\text{Some } \text{true})) \checkmark \rightarrow_p \text{Label } 1$ 
        by  $(\text{rule Proc-CFG-CondTrue})$ 
      with  $\langle nx' = \text{Label } 0 \rangle$ 
      have  $\text{if } (b) \text{ prog else } c_2, \text{procs} \vdash (\text{Main}, \text{Label } 0)$ 
         $\text{--}(\lambda cf. \text{state-check } cf \ b \ (\text{Some } \text{true})) \checkmark \rightarrow (\text{Main}, nx' \oplus 1)$ 
        by  $(\text{fastforce intro:PCFG.Main})$ 
      with  $\langle (\text{Main}, n) = \text{targetnode } a \rangle \langle (\text{Main}, nx') = \text{targetnode } a \rangle [\text{THEN sym}]$ 
      show  $?thesis$ 
      by  $(\text{simp add:ProcCFG.valid-node-def})(\text{fastforce simp:valid-edge-def})$ 
    qed
  next
  case  $\text{False}$ 
    with  $\langle \text{prog} \vdash nx \text{ --IEdge } (\text{kind } a) \rightarrow_p nx' \rangle$ 
    have  $\text{if } (b) \text{ prog else } c_2 \vdash nx \oplus 1 \text{ --IEdge } (\text{kind } a) \rightarrow_p nx' \oplus 1$ 
      by  $(\text{fastforce intro:Proc-CFG-edge-CondTrue-source-not-Entry})$ 
    hence  $\text{if } (b) \text{ prog else } c_2, \text{procs} \vdash (\text{Main}, nx \oplus 1) \text{ --kind } a \rightarrow$ 
       $(\text{Main}, nx' \oplus 1)$  by  $(\text{rule PCFG.Main})$ 
    with  $\langle (\text{Main}, n) = \text{targetnode } a \rangle \langle (\text{Main}, nx') = \text{targetnode } a \rangle [\text{THEN sym}]$ 
    show  $?thesis$  by  $(\text{simp add:ProcCFG.valid-node-def})(\text{fastforce simp:valid-edge-def})$ 
    qed
  qed
next
case  $(\text{Proc } p \ \text{ins} \ \text{outs} \ c \ nx \ n' \ ps)$ 
from  $\langle (p, nx) = \text{sourcenode } a \rangle [\text{THEN sym}] \langle (p, n') = \text{targetnode } a \rangle [\text{THEN sym}]$ 
   $\langle (p, \text{ins}, \text{outs}, c) \in \text{set } \text{procs} \rangle \langle \text{well-formed } \text{procs} \rangle$ 
   $\langle (\text{Main}, n) = \text{sourcenode } a \vee (\text{Main}, n) = \text{targetnode } a \rangle$ 
have  $\text{False}$  by  $\text{fastforce}$ 
thus  $?case$  by  $\text{simp}$ 

```

```

next
  case (MainCall l p es rets n' ins outs c)
  from ⟨(p, ins, outs, c) ∈ set procs⟩ ⟨(p, Entry) = targetnode a⟩ [THEN sym]
    ⟨(Main, Label l) = sourcenode a⟩ [THEN sym] wf
    ⟨(Main, n) = sourcenode a ∨ (Main, n) = targetnode a⟩
  have [simp]: n = Label l by fastforce
  from ⟨prog ⊢ Label l - CEdge (p, es, rets) →p n'⟩
  have if (b) prog else c2 ⊢ Label l ⊕ 1 - CEdge (p, es, rets) →p n' ⊕ 1
    by -(rule Proc-CFG-edge-CondTrue-source-not-Entry, auto)
  with ⟨(p, ins, outs, c) ∈ set procs⟩
  have if (b) prog else c2, procs ⊢ (Main, Label (l + 1))
    -(λs. True): (Main, n' ⊕ 1) ↔p map (λe cf. interpret e cf) es → (p, Entry)
    by (fastforce intro: PCFG.MainCall)
  thus ?case by (simp add: ProcCFG.valid-node-def) (fastforce simp: valid-edge-def)
next
  case (ProcCall p ins outs c l p' es' rets' l' ins' outs' c' ps)
  from ⟨(p, Label l) = sourcenode a⟩ [THEN sym]
    ⟨(p', Entry) = targetnode a⟩ [THEN sym] ⟨well-formed procs⟩
    ⟨(p, ins, outs, c) ∈ set procs⟩ ⟨(p', ins', outs', c') ∈ set procs⟩
    ⟨(Main, n) = sourcenode a ∨ (Main, n) = targetnode a⟩
  have False by fastforce
  thus ?case by simp
next
  case (MainReturn l p es rets l' ins outs c)
  from ⟨(p, ins, outs, c) ∈ set procs⟩ ⟨(p, Exit) = sourcenode a⟩ [THEN sym]
    ⟨(Main, Label l') = targetnode a⟩ [THEN sym] wf
    ⟨(Main, n) = sourcenode a ∨ (Main, n) = targetnode a⟩
  have [simp]: n = Label l' by fastforce
  from ⟨prog ⊢ Label l - CEdge (p, es, rets) →p Label l'⟩
  have if (b) prog else c2 ⊢ Label l ⊕ 1 - CEdge (p, es, rets) →p Label l' ⊕ 1
    by -(rule Proc-CFG-edge-CondTrue-source-not-Entry, auto)
  with ⟨(p, ins, outs, c) ∈ set procs⟩
  have if (b) prog else c2, procs ⊢ (p, Exit) -(λcf. snd cf = (Main, Label l' ⊕
1)) ↔p
    (λcf cf'. cf'(rets [:=] map cf outs)) → (Main, Label (l' + 1))
    by (fastforce intro: PCFG.MainReturn)
  thus ?case by (simp add: ProcCFG.valid-node-def) (fastforce simp: valid-edge-def)
next
  case (ProcReturn p ins outs c l p' es' rets' l' ins' outs' c' ps)
  from ⟨(p', Exit) = sourcenode a⟩ [THEN sym]
    ⟨(p, Label l') = targetnode a⟩ [THEN sym] ⟨well-formed procs⟩
    ⟨(p, ins, outs, c) ∈ set procs⟩ ⟨(p', ins', outs', c') ∈ set procs⟩
    ⟨(Main, n) = sourcenode a ∨ (Main, n) = targetnode a⟩
  have False by fastforce
  thus ?case by simp
next
  case (MainCallReturn n x p es rets n x')
  from ⟨(Main, n) = sourcenode a ∨ (Main, n) = targetnode a⟩ show ?case
proof

```

```

assume  $\langle \text{Main}, n \rangle = \text{sourcenode } a$ 
with  $\langle \text{Main}, nx \rangle = \text{sourcenode } a \rangle [\text{THEN } \text{sym}]$  have  $[\text{simp}]: nx = n$  by  $\text{simp}$ 
from  $\langle n \neq \text{Entry} \rangle \langle \text{prog} \vdash nx - \text{CEdge } (p, es, \text{rets}) \rightarrow_p nx' \rangle$ 
have  $\text{if } (b) \text{ prog else } c_2 \vdash n \oplus 1 - \text{CEdge } (p, es, \text{rets}) \rightarrow_p nx' \oplus 1$ 
by  $(\text{fastforce intro: Proc-CFG-edge-CondTrue-source-not-Entry})$ 
hence  $\text{if } (b) \text{ prog else } c_2, \text{procs} \vdash (\text{Main}, n \oplus 1) - (\lambda s. \text{False}) \checkmark \rightarrow$ 
 $(\text{Main}, nx' \oplus 1)$  by  $-(\text{rule PCFG.MainCallReturn})$ 
thus  $?thesis$  by  $(\text{simp add: ProcCFG.valid-node-def})(\text{fastforce simp: valid-edge-def})$ 
next
assume  $\langle \text{Main}, n \rangle = \text{targetnode } a$ 
from  $\langle \text{prog} \vdash nx - \text{CEdge } (p, es, \text{rets}) \rightarrow_p nx' \rangle$ 
have  $nx \neq \text{Entry}$  by  $(\text{fastforce dest: Proc-CFG-Call-Labels})$ 
with  $\langle \text{prog} \vdash nx - \text{CEdge } (p, es, \text{rets}) \rightarrow_p nx' \rangle$ 
have  $\text{if } (b) \text{ prog else } c_2 \vdash nx \oplus 1 - \text{CEdge } (p, es, \text{rets}) \rightarrow_p nx' \oplus 1$ 
by  $(\text{fastforce intro: Proc-CFG-edge-CondTrue-source-not-Entry})$ 
hence  $\text{if } (b) \text{ prog else } c_2, \text{procs} \vdash (\text{Main}, nx \oplus 1) - (\lambda s. \text{False}) \checkmark \rightarrow (\text{Main}, nx'$ 
 $\oplus 1)$ 
by  $-(\text{rule PCFG.MainCallReturn})$ 
with  $\langle \text{Main}, n \rangle = \text{targetnode } a \rangle \langle \text{Main}, nx' \rangle = \text{targetnode } a \rangle [\text{THEN } \text{sym}]$ 
show  $?thesis$  by  $(\text{simp add: ProcCFG.valid-node-def})(\text{fastforce simp: valid-edge-def})$ 
qed
next
case  $(\text{ProcCallReturn } p \text{ ins } \text{outs } c \text{ nx } p' \text{ es}' \text{ rets}' n' \text{ ps})$ 
from  $\langle \langle p, nx \rangle = \text{sourcenode } a \rangle [\text{THEN } \text{sym}] \langle \langle p, n' \rangle = \text{targetnode } a \rangle [\text{THEN } \text{sym}]$ 
 $\langle \langle p, \text{ins}, \text{outs}, c \rangle \in \text{set } \text{procs} \rangle \langle \text{well-formed } \text{procs} \rangle$ 
 $\langle \langle \text{Main}, n \rangle = \text{sourcenode } a \vee \langle \text{Main}, n \rangle = \text{targetnode } a \rangle$ 
have  $\text{False}$  by  $\text{fastforce}$ 
thus  $?case$  by  $\text{simp}$ 
qed
qed

```

lemma *path-Main-CondTrue:*

```

assumes  $\text{Rep-wf-prog } \text{wfp} = (\text{prog}, \text{procs})$ 
and  $\text{Rep-wf-prog } \text{wfp}' = (\text{if } (b) \text{ prog else } c_2, \text{procs})$ 
shows  $\llbracket \text{wfp} \vdash (\text{Main}, n) - \text{as} \rightarrow^* (p', n'); \forall a \in \text{set } \text{as}. \text{intra-kind } (\text{kind } a); n \neq$ 
 $\text{Entry} \rrbracket$ 
 $\implies \text{wfp}' \vdash (\text{Main}, n \oplus 1) - \text{as} \oplus s \rightarrow^* (p', n' \oplus 1)$ 
proof  $(\text{induct } (\text{Main}, n) \text{ as } (p', n') \text{ arbitrary: } n \text{ rule: ProcCFG.path.induct})$ 
case empty-path
from  $\langle \text{CFG.valid-node } \text{sourcenode } \text{targetnode } (\text{valid-edge } \text{wfp}) (\text{Main}, n') \rangle$ 
 $\langle n' \neq \text{Entry} \rangle \langle \text{Rep-wf-prog } \text{wfp} = (\text{prog}, \text{procs}) \rangle$ 
 $\langle \text{Rep-wf-prog } \text{wfp}' = (\text{if } (b) \text{ prog else } c_2, \text{procs}) \rangle$ 
have  $\text{CFG.valid-node } \text{sourcenode } \text{targetnode } (\text{valid-edge } \text{wfp}') (\text{Main}, n' \oplus 1)$ 
by  $(\text{fastforce intro: valid-node-Main-CondTrue})$ 
with  $\langle \text{Main} = p' \rangle$  show  $?case$ 
by  $(\text{fastforce intro: ProcCFG.empty-path simp: label-incrs-def})$ 
next
case  $(\text{Cons-path } n'' \text{ as } a \text{ } n)$ 

```


note $IH = \langle \wedge n. \llbracket n'' = (Main, n); \forall a \in set\ as.\ intra\text{-}kind\ (kind\ a); n \neq Entry \rrbracket \Rightarrow wfp' \vdash (Main, n \oplus 1) - as \oplus s\ 1 \rightarrow^* (p', n' \oplus 1) \rangle$
note $[simp] = \langle Rep\text{-}wf\text{-}prog\ wfp = (prog, procs) \rangle$
 $\langle Rep\text{-}wf\text{-}prog\ wfp' = (if\ (b)\ prog\ else\ c_2, procs) \rangle$
from $\langle Rep\text{-}wf\text{-}prog\ wfp = (prog, procs) \rangle$ **have** $wf:well\text{-}formed\ procs$
by $(fastforce\ intro:wf\text{-}wf\text{-}prog)$
from $\langle \forall a \in set\ (a \# as).\ intra\text{-}kind\ (kind\ a) \rangle$ **have** $intra\text{-}kind\ (kind\ a)$
and $\forall a \in set\ as.\ intra\text{-}kind\ (kind\ a)$ **by** $simp\text{-}all$
from $\langle valid\text{-}edge\ wfp\ a \rangle \langle sourcenode\ a = (Main, n) \rangle \langle targetnode\ a = n'' \rangle$
 $\langle intra\text{-}kind\ (kind\ a) \rangle\ wf$
obtain nx'' **where** $n'' = (Main, nx'')$ **and** $nx'' \neq Entry$
by $(auto\ elim!:PCFG.cases\ simp:valid\text{-}edge\text{-}def\ intra\text{-}kind\text{-}def)$
from $IH[OF\ \langle n'' = (Main, nx'') \rangle \langle \forall a \in set\ as.\ intra\text{-}kind\ (kind\ a) \rangle \langle nx'' \neq Entry \rangle]$
have $path:wfp' \vdash (Main, nx'' \oplus 1) - as \oplus s\ 1 \rightarrow^* (p', n' \oplus 1)$.
from $\langle valid\text{-}edge\ wfp\ a \rangle \langle sourcenode\ a = (Main, n) \rangle \langle targetnode\ a = n'' \rangle$
 $\langle n'' = (Main, nx'') \rangle \langle n \neq Entry \rangle \langle intra\text{-}kind\ (kind\ a) \rangle\ wf$
have $if\ (b)\ prog\ else\ c_2, procs \vdash (Main, n \oplus 1) - kind\ a \rightarrow (Main, nx'' \oplus 1)$
by $(fastforce\ intro:PCFG\text{-}Main\text{-}edge\text{-}CondTrue\text{-}source\text{-}not\text{-}Entry\ simp:valid\text{-}edge\text{-}def)$
with $path\ \langle sourcenode\ a = (Main, n) \rangle \langle targetnode\ a = n'' \rangle \langle n'' = (Main, nx'') \rangle$
show $?case$
apply $(cases\ a)$ **apply** $(clarsimp\ simp:label\text{-}incrs\text{-}def)$
by $(auto\ intro:ProcCFG.Cons\text{-}path\ simp:valid\text{-}edge\text{-}def)$
qed

From prog to if (b) c₁ else prog

lemma *Proc-CFG-edge-CondFalse-source-not-Entry*:

$\llbracket prog \vdash n - et \rightarrow_p n'; n \neq Entry \rrbracket$

$\Rightarrow if\ (b)\ c_1\ else\ prog \vdash n \oplus (\#:c_1 + 1) - et \rightarrow_p n' \oplus (\#:c_1 + 1)$

by $(induct\ rule:Proc\text{-}CFG.induct)(fastforce\ intro:Proc\text{-}CFG\text{-}CondElse\ Proc\text{-}CFG.intros)+$

lemma *PCFG-Main-edge-CondFalse-source-not-Entry*:

$\llbracket prog, procs \vdash (Main, n) - et \rightarrow (p', n'); n \neq Entry; intra\text{-}kind\ et; well\text{-}formed\ procs \rrbracket$

$\Rightarrow if\ (b)\ c_1\ else\ prog, procs \vdash (Main, n \oplus (\#:c_1 + 1)) - et \rightarrow (p', n' \oplus (\#:c_1 + 1))$

proof $(induct\ (Main, n)\ et\ (p', n')\ rule:PCFG.induct)$

case *Main*

thus $?case$

by $(fastforce\ dest:Proc\text{-}CFG\text{-}edge\text{-}CondFalse\text{-}source\text{-}not\text{-}Entry\ intro:PCFG.Main)$

next

case $(MainCallReturn\ p\ es\ rets)$

from $\langle prog \vdash n - CEdge\ (p, es, rets) \rightarrow_p n' \rangle \langle n \neq Entry \rangle$

have $if\ (b)\ c_1\ else\ prog \vdash n \oplus (\#:c_1 + 1) - CEdge\ (p, es, rets) \rightarrow_p n' \oplus (\#:c_1 + 1)$

by $(rule\ Proc\text{-}CFG\text{-}edge\text{-}CondFalse\text{-}source\text{-}not\text{-}Entry)$

with *MainCallReturn* **show** $?case$ **by** $(fastforce\ intro:PCFG.MainCallReturn)$

qed $(auto\ simp:intra\text{-}kind\text{-}def)$

lemma *valid-node-Main-CondFalse*:

assumes $CFG.valid\text{-}node\ sourcenode\ targetnode\ (valid\text{-}edge\ wfp)\ (Main, n)$
and $n \neq Entry$ **and** $Rep\text{-}wf\text{-}prog\ wfp = (prog, procs)$
and $Rep\text{-}wf\text{-}prog\ wfp' = (if\ (b)\ c_1\ else\ prog, procs)$
shows $CFG.valid\text{-}node\ sourcenode\ targetnode\ (valid\text{-}edge\ wfp')$
 $(Main, n \oplus (\#:c_1 + 1))$

proof –

note $[simp] = \langle Rep\text{-}wf\text{-}prog\ wfp = (prog, procs) \rangle$
 $\langle Rep\text{-}wf\text{-}prog\ wfp' = (if\ (b)\ c_1\ else\ prog, procs) \rangle$

from $\langle Rep\text{-}wf\text{-}prog\ wfp = (prog, procs) \rangle$ **have** $wf:well\text{-}formed\ procs$
by $(fastforce\ intro:wf\text{-}wf\text{-}prog)$

from $\langle CFG.valid\text{-}node\ sourcenode\ targetnode\ (valid\text{-}edge\ wfp)\ (Main, n) \rangle$

obtain a **where** $prog, procs \vdash sourcenode\ a\ \text{-}kind\ a \rightarrow targetnode\ a$
and $(Main, n) = sourcenode\ a \vee (Main, n) = targetnode\ a$
by $(fastforce\ simp:ProcCFG.valid\text{-}node\text{-}def\ valid\text{-}edge\text{-}def)$

from $\langle n \neq Entry \rangle$ wf **show** $?thesis$

proof $(induct\ sourcenode\ a\ kind\ a\ targetnode\ a\ rule:PCFG.induct)$
case $(Main\ nx\ nx')$
from $\langle (Main, n) = sourcenode\ a \vee (Main, n) = targetnode\ a \rangle$ **show** $?case$

proof
assume $(Main, n) = sourcenode\ a$
with $\langle (Main, nx) = sourcenode\ a \rangle [THEN\ sym]$ **have** $[simp]:nx = n$ **by** $simp$
from $\langle n \neq Entry \rangle$ $\langle prog \vdash nx \text{-}IEdge\ (kind\ a) \rightarrow_p\ nx' \rangle$
have $if\ (b)\ c_1\ else\ prog \vdash n \oplus (\#:c_1 + 1) \text{-}IEdge\ (kind\ a) \rightarrow_p\ nx' \oplus (\#:c_1$
 $+ 1)$
by $(fastforce\ intro:Proc\text{-}CFG\text{-}edge\text{-}CondFalse\text{-}source\text{-}not\text{-}Entry)$
hence $if\ (b)\ c_1\ else\ prog, procs \vdash (Main, n \oplus (\#:c_1 + 1)) \text{-}kind\ a \rightarrow$
 $(Main, nx' \oplus (\#:c_1 + 1))$ **by** $(rule\ PCFG.Main)$

thus $?thesis$ **by** $(simp\ add:ProcCFG.valid\text{-}node\text{-}def)(fastforce\ simp:valid\text{-}edge\text{-}def)$

next
assume $(Main, n) = targetnode\ a$
show $?thesis$

proof $(cases\ nx = Entry)$
case $True$
with $\langle prog \vdash nx \text{-}IEdge\ (kind\ a) \rightarrow_p\ nx' \rangle$
have $nx' = Exit \vee nx' = Label\ 0$ **by** $(fastforce\ dest:Proc\text{-}CFG\text{-}EntryD)$
thus $?thesis$

proof
assume $nx' = Exit$
with $\langle (Main, n) = targetnode\ a \rangle$ $\langle (Main, nx') = targetnode\ a \rangle [THEN\ sym]$
show $?thesis$ **by** $simp$

next
assume $nx' = Label\ 0$
have $if\ (b)\ c_1\ else\ prog \vdash Label\ 0$
 $\text{-}IEdge\ (\lambda cf.\ state\text{-}check\ cf\ b\ (Some\ false)) \checkmark \rightarrow_p\ Label\ (\#:c_1 + 1)$
by $(rule\ Proc\text{-}CFG\text{-}CondFalse)$
with $\langle nx' = Label\ 0 \rangle$

```

have if (b) c1 else prog,procs ⊢ (Main,Label 0)
  -(λcf. state-check cf b (Some false))✓→ (Main,nx' ⊕ (#:c1 + 1))
  by(fastforce intro:PCFG.Main)
with ⟨(Main, n) = targetnode a⟩ ⟨(Main, nx') = targetnode a⟩[THEN sym]
show ?thesis
  by(simp add:ProcCFG.valid-node-def)(fastforce simp:valid-edge-def)
qed
next
case False
with ⟨prog ⊢ nx -IEdge (kind a)→p nx'⟩
  have if (b) c1 else prog ⊢ nx ⊕ (#:c1 + 1) -IEdge (kind a)→p nx' ⊕
  (#:c1 + 1)
  by(fastforce intro:Proc-CFG-edge-CondFalse-source-not-Entry)
  hence if (b) c1 else prog,procs ⊢ (Main,nx ⊕ (#:c1 + 1)) -kind a→
  (Main,nx' ⊕ (#:c1 + 1)) by(rule PCFG.Main)
  with ⟨(Main, n) = targetnode a⟩ ⟨(Main, nx') = targetnode a⟩[THEN sym]
  show ?thesis by(simp add:ProcCFG.valid-node-def)(fastforce simp:valid-edge-def)
qed
qed
next
case (Proc p ins outs c nx n' ps)
from ⟨(p, nx) = sourcenode a⟩[THEN sym] ⟨(p, n') = targetnode a⟩[THEN sym]
  ⟨(p, ins, outs, c) ∈ set procs⟩ ⟨well-formed procs⟩
  ⟨(Main, n) = sourcenode a ∨ (Main, n) = targetnode a⟩
have False by fastforce
thus ?case by simp
next
case (MainCall l p es rets n' ins outs c)
from ⟨(p, ins, outs, c) ∈ set procs⟩ ⟨(p, Entry) = targetnode a⟩[THEN sym]
  ⟨(Main, Label l) = sourcenode a⟩[THEN sym] wf
  ⟨(Main, n) = sourcenode a ∨ (Main, n) = targetnode a⟩
have [simp]:n = Label l by fastforce
from ⟨prog ⊢ Label l -CEdge (p, es, rets)→p n'⟩
have if (b) c1 else prog ⊢ Label l ⊕ (#:c1 + 1) -CEdge (p, es, rets)→p
  n' ⊕ (#:c1 + 1) by -(rule Proc-CFG-edge-CondFalse-source-not-Entry,auto)
with ⟨(p, ins, outs, c) ∈ set procs⟩
have if (b) c1 else prog,procs ⊢ (Main,Label (l + (#:c1 + 1)))
  -(λs. True):(Main,n' ⊕ (#:c1 + 1))↔pmap (λe cf. interpret e cf) es→
  (p,Entry)
  by(fastforce intro:PCFG.MainCall)
thus ?case by(simp add:ProcCFG.valid-node-def)(fastforce simp:valid-edge-def)
next
case (ProcCall p ins outs c l p' es' rets' l' ins' outs' c' ps)
from ⟨(p, Label l) = sourcenode a⟩[THEN sym]
  ⟨(p', Entry) = targetnode a⟩[THEN sym] ⟨well-formed procs⟩
  ⟨(p, ins, outs, c) ∈ set procs⟩ ⟨(p', ins', outs', c') ∈ set procs⟩
  ⟨(Main, n) = sourcenode a ∨ (Main, n) = targetnode a⟩
have False by fastforce
thus ?case by simp

```

next
case (*MainReturn* $l\ p\ es\ rets\ l'\ ins\ outs\ c$)
from $\langle(p, ins, outs, c) \in set\ procs\rangle \langle(p, Exit) = sourcenode\ a\rangle[THEN\ sym]$
 $\langle(Main, Label\ l') = targetnode\ a\rangle[THEN\ sym]\ wf$
 $\langle(Main, n) = sourcenode\ a \vee (Main, n) = targetnode\ a\rangle$
have [*simp*]: $n = Label\ l'$ **by** *fastforce*
from $\langle prog \vdash Label\ l - CEdge\ (p, es, rets) \rightarrow_p\ Label\ l' \rangle$
have *if* (b) c_1 *else* $prog \vdash Label\ l \oplus (\#:c_1 + 1) - CEdge\ (p, es, rets) \rightarrow_p$
 $Label\ l' \oplus (\#:c_1 + 1)$ **by** $-(rule\ Proc-CFG-edge-CondFalse-source-not-Entry, auto)$
with $\langle(p, ins, outs, c) \in set\ procs\rangle$
have *if* (b) c_1 *else* $prog, procs \vdash (p, Exit)$
 $-(\lambda cf. snd\ cf = (Main, Label\ l' \oplus (\#:c_1 + 1))) \leftarrow_p$
 $(\lambda cf\ cf'. cf'(rets\ [:=]\ map\ cf\ outs)) \rightarrow (Main, Label\ (l' + (\#:c_1 + 1)))$
by (*fastforce* *intro:PCFG.MainReturn*)
thus *?case* **by** (*simp* *add:ProcCFG.valid-node-def*)(*fastforce* *simp:valid-edge-def*)
next
case (*ProcReturn* $p\ ins\ outs\ c\ l\ p'\ es'\ rets'\ l'\ ins'\ outs'\ c'\ ps$)
from $\langle(p', Exit) = sourcenode\ a\rangle[THEN\ sym]$
 $\langle(p, Label\ l') = targetnode\ a\rangle[THEN\ sym]\ \langle well\text{-formed}\ procs \rangle$
 $\langle(p, ins, outs, c) \in set\ procs\rangle \langle(p', ins', outs', c') \in set\ procs\rangle$
 $\langle(Main, n) = sourcenode\ a \vee (Main, n) = targetnode\ a\rangle$
have *False* **by** *fastforce*
thus *?case* **by** *simp*
next
case (*MainCallReturn* $nx\ p\ es\ rets\ nx'$)
from $\langle(Main, n) = sourcenode\ a \vee (Main, n) = targetnode\ a\rangle$ **show** *?case*
proof
assume $(Main, n) = sourcenode\ a$
with $\langle(Main, nx) = sourcenode\ a\rangle[THEN\ sym]$ **have** [*simp*]: $nx = n$ **by** *simp*
from $\langle nx \neq Entry \rangle \langle prog \vdash nx - CEdge\ (p, es, rets) \rightarrow_p\ nx' \rangle$
have *if* (b) c_1 *else* $prog \vdash n \oplus (\#:c_1 + 1) - CEdge\ (p, es, rets) \rightarrow_p$
 $nx' \oplus (\#:c_1 + 1)$ **by** (*fastforce* *intro:Proc-CFG-edge-CondFalse-source-not-Entry*)
hence *if* (b) c_1 *else* $prog, procs \vdash (Main, n \oplus (\#:c_1 + 1))$
 $-(\lambda s. False) \checkmark \rightarrow (Main, nx' \oplus (\#:c_1 + 1))$ **by** $-(rule\ PCFG.MainCallReturn)$
thus *?thesis* **by** (*simp* *add:ProcCFG.valid-node-def*)(*fastforce* *simp:valid-edge-def*)
next
assume $(Main, n) = targetnode\ a$
from $\langle prog \vdash nx - CEdge\ (p, es, rets) \rightarrow_p\ nx' \rangle$
have $nx \neq Entry$ **by** (*fastforce* *dest:Proc-CFG-Call-Labels*)
with $\langle prog \vdash nx - CEdge\ (p, es, rets) \rightarrow_p\ nx' \rangle$
have *if* (b) c_1 *else* $prog \vdash nx \oplus (\#:c_1 + 1) - CEdge\ (p, es, rets) \rightarrow_p$
 $nx' \oplus (\#:c_1 + 1)$ **by** (*fastforce* *intro:Proc-CFG-edge-CondFalse-source-not-Entry*)
hence *if* (b) c_1 *else* $prog, procs \vdash (Main, nx \oplus (\#:c_1 + 1))$
 $-(\lambda s. False) \checkmark \rightarrow (Main, nx' \oplus (\#:c_1 + 1))$ **by** $-(rule\ PCFG.MainCallReturn)$
with $\langle(Main, n) = targetnode\ a\rangle \langle(Main, nx') = targetnode\ a\rangle[THEN\ sym]$
show *?thesis* **by** (*simp* *add:ProcCFG.valid-node-def*)(*fastforce* *simp:valid-edge-def*)
qed
next
case (*ProcCallReturn* $p\ ins\ outs\ c\ nx\ p'\ es'\ rets'\ n'\ ps$)

```

from  $\langle (p, nx) = \text{sourcenode } a \rangle [\text{THEN } \text{sym}] \langle (p, n') = \text{targetnode } a \rangle [\text{THEN } \text{sym}]$ 
   $\langle (p, \text{ins}, \text{outs}, c) \in \text{set } \text{procs} \rangle \langle \text{well-formed } \text{procs} \rangle$ 
   $\langle (\text{Main}, n) = \text{sourcenode } a \vee (\text{Main}, n) = \text{targetnode } a \rangle$ 
have False by fastforce
thus ?case by simp
qed
qed

```

lemma *path-Main-CondFalse*:

```

assumes Rep-wf-prog wfp = (prog,procs)
and Rep-wf-prog wfp' = (if (b) c1 else prog,procs)
shows  $\llbracket wfp \vdash (\text{Main}, n) -\text{as} \rightarrow^* (p', n'); \forall a \in \text{set } \text{as}. \text{intra-kind } (\text{kind } a); n \neq \text{Entry} \rrbracket$ 
 $\implies wfp' \vdash (\text{Main}, n \oplus (\# : c_1 + 1)) -\text{as} \oplus s (\# : c_1 + 1) \rightarrow^* (p', n' \oplus (\# : c_1 + 1))$ 
proof (induct (Main, n) as (p', n') arbitrary: n rule: ProcCFG.path.induct)
case empty-path
from  $\langle \text{CFG.valid-node sourcenode targetnode (valid-edge wfp) (Main}, n') \rangle$ 
   $\langle n' \neq \text{Entry} \rangle \langle \text{Rep-wf-prog wfp} = (\text{prog}, \text{procs}) \rangle$ 
   $\langle \text{Rep-wf-prog wfp}' = (\text{if } (b) c_1 \text{ else } \text{prog}, \text{procs}) \rangle$ 
have CFG.valid-node sourcenode targetnode (valid-edge wfp') (Main, n'  $\oplus$  ( $\# : c_1 + 1$ ))
by (fastforce intro: valid-node-Main-CondFalse)
with  $\langle \text{Main} = p' \rangle$  show ?case
by (fastforce intro: ProcCFG.empty-path simp: label-incrs-def)
next
case (Cons-path n'' as a n)
note IH =  $\langle \bigwedge n. \bigwedge n. \llbracket n'' = (\text{Main}, n); \forall a \in \text{set } \text{as}. \text{intra-kind } (\text{kind } a); n \neq \text{Entry} \rrbracket$ 
 $\implies wfp' \vdash (\text{Main}, n \oplus (\# : c_1 + 1)) -\text{as} \oplus s (\# : c_1 + 1) \rightarrow^* (p', n' \oplus (\# : c_1 + 1))$ )
note [simp] =  $\langle \text{Rep-wf-prog wfp} = (\text{prog}, \text{procs}) \rangle$ 
   $\langle \text{Rep-wf-prog wfp}' = (\text{if } (b) c_1 \text{ else } \text{prog}, \text{procs}) \rangle$ 
from  $\langle \text{Rep-wf-prog wfp} = (\text{prog}, \text{procs}) \rangle$  have wf: well-formed procs
by (fastforce intro: wf-wf-prog)
from  $\langle \forall a \in \text{set } (a \# \text{as}). \text{intra-kind } (\text{kind } a) \rangle$  have intra-kind (kind a)
and  $\langle \forall a \in \text{set } \text{as}. \text{intra-kind } (\text{kind } a) \rangle$  by simp-all
from  $\langle \text{valid-edge wfp } a \rangle \langle \text{sourcenode } a = (\text{Main}, n) \rangle \langle \text{targetnode } a = n'' \rangle$ 
   $\langle \text{intra-kind } (\text{kind } a) \rangle$  wf
obtain nx'' where n'' = (Main, nx'') and nx''  $\neq$  Entry
by (auto elim!: PCFG.cases simp: valid-edge-def intra-kind-def)
from IH [OF  $\langle n'' = (\text{Main}, nx'') \rangle \langle \forall a \in \text{set } \text{as}. \text{intra-kind } (\text{kind } a) \rangle \langle nx'' \neq \text{Entry} \rangle$ ]
have path: wfp'  $\vdash$  (Main, nx''  $\oplus$  ( $\# : c_1 + 1$ )) -as  $\oplus$  s ( $\# : c_1 + 1$ )  $\rightarrow^*$  (p', n'  $\oplus$  ( $\# : c_1 + 1$ )).
from  $\langle \text{valid-edge wfp } a \rangle \langle \text{sourcenode } a = (\text{Main}, n) \rangle \langle \text{targetnode } a = n'' \rangle$ 
   $\langle n'' = (\text{Main}, nx'') \rangle \langle n \neq \text{Entry} \rangle \langle \text{intra-kind } (\text{kind } a) \rangle$  wf
have if (b) c1 else prog,procs  $\vdash$  (Main, n  $\oplus$  ( $\# : c_1 + 1$ )) -kind a  $\rightarrow$  (Main, nx''  $\oplus$  ( $\# : c_1 + 1$ ))
by (fastforce intro: PCFG-Main-edge-CondFalse-source-not-Entry simp: valid-edge-def)

```

with $\langle \text{path } \langle \text{sourcenode } a = (\text{Main}, n) \rangle \langle \text{targetnode } a = n'' \rangle \langle n'' = (\text{Main}, nx'') \rangle$
show $?case$
apply $(\text{cases } a)$ **apply** $(\text{clarsimp simp:label-incrs-def})$
by $(\text{auto intro:ProcCFG.Cons-path simp:valid-edge-def})$
qed

From prog to while (b) prog

lemma *Proc-CFG-edge-WhileBody-source-not-Entry*:

$\llbracket \text{prog} \vdash n -et \rightarrow_p n'; n \neq \text{Entry}; n' \neq \text{Exit} \rrbracket$
 $\implies \text{while } (b) \text{ prog} \vdash n \oplus 2 -et \rightarrow_p n' \oplus 2$

by $(\text{induct rule:Proc-CFG.induct})(\text{fastforce intro:Proc-CFG-WhileBody Proc-CFG.intros})+$

lemma *PCFG-Main-edge-WhileBody-source-not-Entry*:

$\llbracket \text{prog,procs} \vdash (\text{Main}, n) -et \rightarrow (p', n'); n \neq \text{Entry}; n' \neq \text{Exit}; \text{intra-kind } et;$
 $\text{well-formed procs} \rrbracket \implies \text{while } (b) \text{ prog,procs} \vdash (\text{Main}, n \oplus 2) -et \rightarrow (p', n' \oplus 2)$

proof $(\text{induct } (\text{Main}, n) \text{ et } (p', n') \text{ rule:PCFG.induct})$

case *Main*

thus $?case$

by $(\text{fastforce dest:Proc-CFG-edge-WhileBody-source-not-Entry intro:PCFG.Main})$

next

case $(\text{MainCallReturn } p \text{ es } \text{rets})$

from $\langle \text{prog} \vdash n -CEdge (p, \text{es}, \text{rets}) \rightarrow_p n' \rangle \langle n \neq \text{Entry} \rangle \langle n' \neq \text{Exit} \rangle$

have $\text{while } (b) \text{ prog} \vdash n \oplus 2 -CEdge (p, \text{es}, \text{rets}) \rightarrow_p n' \oplus 2$

by $(\text{rule Proc-CFG-edge-WhileBody-source-not-Entry})$

with *MainCallReturn* **show** $?case$ **by** $(\text{fastforce intro:PCFG.MainCallReturn})$

qed $(\text{auto simp:intra-kind-def})$

lemma *valid-node-Main-WhileBody*:

assumes $\text{CFG.valid-node sourcenode targetnode } (\text{valid-edge wfp}) (\text{Main}, n)$

and $n \neq \text{Entry}$ **and** $\text{Rep-wf-prog wfp} = (\text{prog,procs})$

and $\text{Rep-wf-prog wfp}' = (\text{while } (b) \text{ prog,procs})$

shows $\text{CFG.valid-node sourcenode targetnode } (\text{valid-edge wfp}') (\text{Main}, n \oplus 2)$

proof –

note $[\text{simp}] = \langle \text{Rep-wf-prog wfp} = (\text{prog,procs}) \rangle$

$\langle \text{Rep-wf-prog wfp}' = (\text{while } (b) \text{ prog,procs}) \rangle$

from $\langle \text{Rep-wf-prog wfp} = (\text{prog,procs}) \rangle$ **have** $\text{wf:well-formed procs}$

by $(\text{fastforce intro:wf-wf-prog})$

from $\langle \text{CFG.valid-node sourcenode targetnode } (\text{valid-edge wfp}) (\text{Main}, n) \rangle$

obtain a **where** $\text{procs} \vdash \text{sourcenode } a -\text{kind } a \rightarrow \text{targetnode } a$

and $(\text{Main}, n) = \text{sourcenode } a \vee (\text{Main}, n) = \text{targetnode } a$

by $(\text{fastforce simp:ProcCFG.valid-node-def valid-edge-def})$

from $\text{this } \langle n \neq \text{Entry} \rangle \text{ wf}$ **show** $?thesis$

proof $(\text{induct sourcenode } a \text{ kind } a \text{ targetnode } a \text{ rule:PCFG.induct})$

case $(\text{Main } nx \text{ } nx')$

from $\langle (\text{Main}, n) = \text{sourcenode } a \vee (\text{Main}, n) = \text{targetnode } a \rangle$ **show** $?case$

proof

```

assume  $\langle Main, n \rangle = \text{sourcenode } a$ 
with  $\langle (Main, nx) = \text{sourcenode } a \rangle [THEN \text{ sym}]$  have  $[simp]: nx = n$  by simp
show ?thesis
proof (cases  $nx' = \text{Exit}$ )
  case True
    with  $\langle n \neq \text{Entry} \rangle \langle \text{prog} \vdash nx - \text{IEdge } (kind \ a) \rightarrow_p \ nx' \rangle$ 
    have  $\text{while } (b) \text{ prog} \vdash n \oplus 2 - \text{IEdge } (kind \ a) \rightarrow_p \ \text{Label } 0$ 
      by (fastforce intro: Proc-CFG-WhileBodyExit)
    hence  $\text{while } (b) \text{ prog, procs} \vdash (Main, n \oplus 2) - kind \ a \rightarrow (Main, \text{Label } 0)$ 
      by (rule PCFG.Main)
  thus ?thesis by (simp add: ProcCFG.valid-node-def) (fastforce simp: valid-edge-def)
next
  case False
    with  $\langle n \neq \text{Entry} \rangle \langle \text{prog} \vdash nx - \text{IEdge } (kind \ a) \rightarrow_p \ nx' \rangle$ 
    have  $\text{while } (b) \text{ prog} \vdash n \oplus 2 - \text{IEdge } (kind \ a) \rightarrow_p \ nx' \oplus 2$ 
      by (fastforce intro: Proc-CFG-edge-WhileBody-source-not-Entry)
    hence  $\text{while } (b) \text{ prog, procs} \vdash (Main, n \oplus 2) - kind \ a \rightarrow (Main, nx' \oplus 2)$ 
      by (rule PCFG.Main)
  thus ?thesis by (simp add: ProcCFG.valid-node-def) (fastforce simp: valid-edge-def)
qed
next
assume  $\langle Main, n \rangle = \text{targetnode } a$ 
show ?thesis
proof (cases  $nx = \text{Entry}$ )
  case True
    with  $\langle \text{prog} \vdash nx - \text{IEdge } (kind \ a) \rightarrow_p \ nx' \rangle$ 
    have  $nx' = \text{Exit} \vee nx' = \text{Label } 0$  by (fastforce dest: Proc-CFG-EntryD)
    thus ?thesis
  proof
    assume  $nx' = \text{Exit}$ 
    with  $\langle (Main, n) = \text{targetnode } a \rangle \langle (Main, nx') = \text{targetnode } a \rangle [THEN \text{ sym}]$ 
    show ?thesis by simp
  next
    assume  $nx' = \text{Label } 0$ 
    have  $\text{while } (b) \text{ prog} \vdash \text{Label } 0$ 
       $- \text{IEdge } (\lambda cf. \text{state-check } cf \ b \ (\text{Some } \text{true})) \surd \rightarrow_p \ \text{Label } 2$ 
      by (rule Proc-CFG-WhileTrue)
    hence  $\text{while } (b) \text{ prog, procs} \vdash (Main, \text{Label } 0)$ 
       $- (\lambda cf. \text{state-check } cf \ b \ (\text{Some } \text{true})) \surd \rightarrow (Main, \text{Label } 2)$ 
      by (fastforce intro: PCFG.Main)
    with  $\langle (Main, n) = \text{targetnode } a \rangle \langle (Main, nx') = \text{targetnode } a \rangle [THEN \text{ sym}]$ 
       $\langle nx' = \text{Label } 0 \rangle$  show ?thesis
      by (simp add: ProcCFG.valid-node-def) (fastforce simp: valid-edge-def)
  qed
next
  case False
show ?thesis
proof (cases  $nx' = \text{Exit}$ )
  case True

```

```

with  $\langle (Main, n) = \text{targetnode } a \rangle \langle (Main, nx') = \text{targetnode } a \rangle [THEN \text{ sym}]$ 
show  $?thesis$  by simp
next
  case False
  with  $\langle \text{prog} \vdash nx -IEdge (\text{kind } a) \rightarrow_p nx' \rangle \langle nx \neq \text{Entry} \rangle$ 
  have  $\text{while } (b) \text{ prog} \vdash nx \oplus 2 -IEdge (\text{kind } a) \rightarrow_p nx' \oplus 2$ 
    by (fastforce intro:Proc-CFG-edge-WhileBody-source-not-Entry)
  hence  $\text{while } (b) \text{ prog, procs} \vdash (Main, nx \oplus 2) -\text{kind } a \rightarrow$ 
     $(Main, nx' \oplus 2)$  by (rule PCFG.Main)
  with  $\langle (Main, n) = \text{targetnode } a \rangle \langle (Main, nx') = \text{targetnode } a \rangle [THEN \text{ sym}]$ 
  show  $?thesis$ 
    by (simp add:ProcCFG.valid-node-def)(fastforce simp:valid-edge-def)
  qed
qed
qed
next
  case  $(Proc \text{ p ins outs c } nx \text{ n' ps})$ 
  from  $\langle (p, nx) = \text{sourcenode } a \rangle [THEN \text{ sym}] \langle (p, n') = \text{targetnode } a \rangle [THEN \text{ sym}]$ 
     $\langle (Main, n) = \text{sourcenode } a \vee (Main, n) = \text{targetnode } a \rangle$ 
     $\langle (p, \text{ins}, \text{outs}, c) \in \text{set procs} \rangle \langle \text{well-formed procs} \rangle$ 
  have False by fastforce
  thus  $?case$  by simp
next
  case  $(MainCall \text{ l p es rets n' ins outs c})$ 
  from  $\langle (p, \text{ins}, \text{outs}, c) \in \text{set procs} \rangle \langle (p, \text{Entry}) = \text{targetnode } a \rangle [THEN \text{ sym}]$ 
     $\langle (Main, \text{Label } l) = \text{sourcenode } a \rangle [THEN \text{ sym}] \text{ wf}$ 
     $\langle (Main, n) = \text{sourcenode } a \vee (Main, n) = \text{targetnode } a \rangle$ 
  have  $[simp]: n = \text{Label } l$  by fastforce
  from  $\langle \text{prog} \vdash \text{Label } l -CEdge (p, \text{es}, \text{rets}) \rightarrow_p n' \rangle$  have  $n' \neq \text{Exit}$ 
    by (fastforce dest:Proc-CFG-Call-Labels)
  with  $\langle \text{prog} \vdash \text{Label } l -CEdge (p, \text{es}, \text{rets}) \rightarrow_p n' \rangle$ 
  have  $\text{while } (b) \text{ prog} \vdash \text{Label } l \oplus 2 -CEdge (p, \text{es}, \text{rets}) \rightarrow_p$ 
     $n' \oplus 2$  by  $-(\text{rule Proc-CFG-edge-WhileBody-source-not-Entry, auto})$ 
  with  $\langle (p, \text{ins}, \text{outs}, c) \in \text{set procs} \rangle$ 
  have  $\text{while } (b) \text{ prog, procs} \vdash (Main, \text{Label } l \oplus 2)$ 
     $-(\lambda s. \text{True}): (Main, n' \oplus 2) \leftrightarrow_p \text{map } (\lambda e \text{ cf. interpret } e \text{ cf}) \text{ es} \rightarrow (p, \text{Entry})$ 
    by (fastforce intro:PCFG.MainCall)
  thus  $?case$  by (simp add:ProcCFG.valid-node-def)(fastforce simp:valid-edge-def)
next
  case  $(ProcCall \text{ p ins outs c l p' es' rets' l' ins' outs' c'})$ 
  from  $\langle (p, \text{Label } l) = \text{sourcenode } a \rangle [THEN \text{ sym}]$ 
     $\langle (p', \text{Entry}) = \text{targetnode } a \rangle [THEN \text{ sym}] \langle \text{well-formed procs} \rangle$ 
     $\langle (p, \text{ins}, \text{outs}, c) \in \text{set procs} \rangle \langle (p', \text{ins}', \text{outs}', c') \in \text{set procs} \rangle$ 
     $\langle (Main, n) = \text{sourcenode } a \vee (Main, n) = \text{targetnode } a \rangle$ 
  have False by fastforce
  thus  $?case$  by simp
next
  case  $(MainReturn \text{ l p es rets l' ins outs c})$ 
  from  $\langle (p, \text{ins}, \text{outs}, c) \in \text{set procs} \rangle \langle (p, \text{Exit}) = \text{sourcenode } a \rangle [THEN \text{ sym}]$ 

```


$\langle (Main, Label\ l') = targetnode\ a \rangle [THEN\ sym]\ wf$
 $\langle (Main, n) = sourcenode\ a \vee (Main, n) = targetnode\ a \rangle$
have $[simp]: n = Label\ l'$ **by** *fastforce*
from $\langle prog \vdash Label\ l - CEdge\ (p, es, rets) \rightarrow_p\ Label\ l' \rangle$
have $while\ (b)\ prog \vdash Label\ l \oplus 2 - CEdge\ (p, es, rets) \rightarrow_p$
 $Label\ l' \oplus 2$ **by** $-(rule\ Proc-CFG-edge-WhileBody-source-not-Entry, auto)$
with $\langle (p, ins, outs, c) \in set\ procs \rangle$
have $while\ (b)\ prog, procs \vdash (p, Exit) - (\lambda cf. snd\ cf = (Main, Label\ l' \oplus 2)) \leftrightarrow_p$
 $(\lambda cf\ cf'. cf'(rets\ [:=]\ map\ cf\ outs)) \rightarrow (Main, Label\ l' \oplus 2)$
by $(fastforce\ intro: PCFG.MainReturn)$
thus $?case\ by\ (simp\ add: ProcCFG.valid-node-def)(fastforce\ simp: valid-edge-def)$
next
case $(ProcReturn\ p\ ins\ outs\ c\ l\ p'\ es'\ rets'\ l'\ ins'\ outs'\ c'\ ps)$
from $\langle (p', Exit) = sourcenode\ a \rangle [THEN\ sym]$
 $\langle (p, Label\ l') = targetnode\ a \rangle [THEN\ sym]\ \langle well-formed\ procs \rangle$
 $\langle (p, ins, outs, c) \in set\ procs \rangle\ \langle (p', ins', outs', c') \in set\ procs \rangle$
 $\langle (Main, n) = sourcenode\ a \vee (Main, n) = targetnode\ a \rangle$
have *False* **by** *fastforce*
thus $?case\ by\ simp$
next
case $(MainCallReturn\ nx\ p\ es\ rets\ nx')$
from $\langle (Main, n) = sourcenode\ a \vee (Main, n) = targetnode\ a \rangle$ **show** $?case$
proof
assume $(Main, n) = sourcenode\ a$
with $\langle (Main, nx) = sourcenode\ a \rangle [THEN\ sym]$ **have** $[simp]: nx = n$ **by** *simp*
from $\langle prog \vdash nx - CEdge\ (p, es, rets) \rightarrow_p\ nx' \rangle$ **have** $nx' \neq Exit$
by $(fastforce\ dest: Proc-CFG-Call-Labels)$
with $\langle n \neq Entry \rangle\ \langle prog \vdash nx - CEdge\ (p, es, rets) \rightarrow_p\ nx' \rangle$
have $while\ (b)\ prog \vdash n \oplus 2 - CEdge\ (p, es, rets) \rightarrow_p$
 $nx' \oplus 2$ **by** $(fastforce\ intro: Proc-CFG-edge-WhileBody-source-not-Entry)$
hence $while\ (b)\ prog, procs \vdash (Main, n \oplus 2) - (\lambda s. False) \checkmark \rightarrow (Main, nx' \oplus 2)$
by $-(rule\ PCFG.MainCallReturn)$
thus $?thesis\ by\ (simp\ add: ProcCFG.valid-node-def)(fastforce\ simp: valid-edge-def)$
next
assume $(Main, n) = targetnode\ a$
from $\langle prog \vdash nx - CEdge\ (p, es, rets) \rightarrow_p\ nx' \rangle$
have $nx \neq Entry$ **and** $nx' \neq Exit$ **by** $(auto\ dest: Proc-CFG-Call-Labels)$
with $\langle prog \vdash nx - CEdge\ (p, es, rets) \rightarrow_p\ nx' \rangle$
have $while\ (b)\ prog \vdash nx \oplus 2 - CEdge\ (p, es, rets) \rightarrow_p$
 $nx' \oplus 2$ **by** $(fastforce\ intro: Proc-CFG-edge-WhileBody-source-not-Entry)$
hence $while\ (b)\ prog, procs \vdash (Main, nx \oplus 2) - (\lambda s. False) \checkmark \rightarrow (Main, nx' \oplus$
2)
by $-(rule\ PCFG.MainCallReturn)$
with $\langle (Main, n) = targetnode\ a \rangle\ \langle (Main, nx') = targetnode\ a \rangle [THEN\ sym]$
show $?thesis\ by\ (simp\ add: ProcCFG.valid-node-def)(fastforce\ simp: valid-edge-def)$
qed
next
case $(ProcCallReturn\ p\ ins\ outs\ c\ nx\ p'\ es'\ rets'\ n'\ ps)$
from $\langle (p, nx) = sourcenode\ a \rangle [THEN\ sym]\ \langle (p, n') = targetnode\ a \rangle [THEN\ sym]$

```

    ⟨(p, ins, outs, c) ∈ set procs⟩ ⟨well-formed procs⟩
    ⟨(Main, n) = sourcenode a ∨ (Main, n) = targetnode a⟩
  have False by fastforce
  thus ?case by simp
qed
qed

```

lemma *path-Main-WhileBody*:

```

  assumes Rep-wf-prog wfp = (prog,procs)
  and Rep-wf-prog wfp' = (while (b) prog,procs)
  shows ⟦wfp ⊢ (Main,n) -as→* (p',n'); ∀ a ∈ set as. intra-kind (kind a);
    n ≠ Entry; n' ≠ Exit⟧ ⟹ wfp' ⊢ (Main,n ⊕ 2) -as ⊕s 2→* (p',n' ⊕ 2)
proof(induct (Main,n) as (p',n') arbitrary:n rule:ProcCFG.path.induct)
  case empty-path
  from ⟨CFG.valid-node sourcenode targetnode (valid-edge wfp) (Main, n')⟩
    ⟨n' ≠ Entry⟩ ⟨Rep-wf-prog wfp = (prog,procs)⟩
    ⟨Rep-wf-prog wfp' = (while (b) prog,procs)⟩
  have CFG.valid-node sourcenode targetnode (valid-edge wfp') (Main, n' ⊕ 2)
    by(fastforce intro:valid-node-Main-WhileBody)
  with ⟨Main = p'⟩ show ?case
  by(fastforce intro:ProcCFG.empty-path simp:label-incrs-def)
next
  case (Cons-path n'' as a n)
  note IH = ⟨∧ n. ⟦n'' = (Main, n); ∀ a ∈ set as. intra-kind (kind a); n ≠ Entry;
    n' ≠ Exit⟧ ⟹ wfp' ⊢ (Main, n ⊕ 2) -as ⊕s 2→* (p', n' ⊕ 2)⟩
  note [simp] = ⟨Rep-wf-prog wfp = (prog,procs)⟩
    ⟨Rep-wf-prog wfp' = (while (b) prog,procs)⟩
  from ⟨Rep-wf-prog wfp = (prog,procs)⟩ have wf:well-formed procs
  by(fastforce intro:wf-wf-prog)
  from ⟨∀ a ∈ set (a # as). intra-kind (kind a)⟩ have intra-kind (kind a)
  and ∀ a ∈ set as. intra-kind (kind a) by simp-all
  from ⟨valid-edge wfp a⟩ ⟨sourcenode a = (Main, n)⟩ ⟨targetnode a = n''⟩
    ⟨intra-kind (kind a)⟩ wf
  obtain nx'' where n'' = (Main,nx'') and nx'' ≠ Entry
  by(auto elim!:PCFG.cases simp:valid-edge-def intra-kind-def)
  from IH[OF ⟨n'' = (Main,nx'')⟩ ⟨∀ a ∈ set as. intra-kind (kind a)⟩
    ⟨nx'' ≠ Entry⟩ ⟨n' ≠ Exit⟩]
  have path:wfp' ⊢ (Main, nx'' ⊕ 2) -as ⊕s 2→* (p', n' ⊕ 2) .
  with ⟨n' ≠ Exit⟩ have nx'' ≠ Exit by(fastforce dest:ProcCFGExit.path-Exit-source)
  with ⟨valid-edge wfp a⟩ ⟨sourcenode a = (Main, n)⟩ ⟨targetnode a = n''⟩
    ⟨n'' = (Main,nx'')⟩ ⟨n ≠ Entry⟩ ⟨intra-kind (kind a)⟩ wf
  have while (b) prog,procs ⊢ (Main, n ⊕ 2) -kind a→ (Main, nx'' ⊕ 2)
  by(fastforce intro:PCFG-Main-edge-WhileBody-source-not-Entry simp:valid-edge-def)
  with path ⟨sourcenode a = (Main, n)⟩ ⟨targetnode a = n''⟩ ⟨n'' = (Main,nx'')⟩
  show ?case
  apply(cases a) apply(clarsimp simp:label-incrs-def)
  by(auto intro:ProcCFG.Cons-path simp:valid-edge-def)
qed

```

Existence of intraprocedural paths

lemma *Label-Proc-CFG-Entry-Exit-path-Main*:

assumes $\text{Rep-wf-prog wfp} = (\text{prog}, \text{procs})$ **and** $l < \#:\text{prog}$

obtains $as\ as'$ **where** $wfp \vdash (\text{Main}, \text{Label } l) -as \rightarrow^* (\text{Main}, \text{Exit})$

and $\forall a \in \text{set } as. \text{intra-kind } (\text{kind } a)$

and $wfp \vdash (\text{Main}, \text{Entry}) -as' \rightarrow^* (\text{Main}, \text{Label } l)$

and $\forall a \in \text{set } as'. \text{intra-kind } (\text{kind } a)$

proof(*atomize-elim*)

from $\langle \text{Rep-wf-prog wfp} = (\text{prog}, \text{procs}) \rangle$ **have** $wf:\text{well-formed procs}$

by(*fastforce intro:wf-wf-prog*)

from $\langle l < \#:\text{prog} \rangle \langle \text{Rep-wf-prog wfp} = (\text{prog}, \text{procs}) \rangle$

show $\exists as\ as'. wfp \vdash (\text{Main}, \text{Label } l) -as \rightarrow^* (\text{Main}, \text{Exit}) \wedge$

$(\forall a \in \text{set } as. \text{intra-kind } (\text{kind } a)) \wedge$

$wfp \vdash (\text{Main}, \text{Entry}) -as' \rightarrow^* (\text{Main}, \text{Label } l) \wedge (\forall a \in \text{set } as'. \text{intra-kind } (\text{kind } a))$

a)

proof(*induct prog arbitrary:l wfp*)

case *Skip*

note $[simp] = \langle \text{Rep-wf-prog wfp} = (\text{Skip}, \text{procs}) \rangle$

from $\langle l < \#:\text{Skip} \rangle$ **have** $[simp]:l = 0$ **by** *simp*

have $wfp \vdash (\text{Main}, \text{Entry}) -[((\text{Main}, \text{Entry}), (\lambda s. \text{True})_{\surd}, (\text{Main}, \text{Label } 0))] \rightarrow^*$
 $(\text{Main}, \text{Label } 0)$

by(*fastforce intro:ProcCFG.path.intros Main Proc-CFG-Entry*
simp:valid-edge-def ProcCFG.valid-node-def)

moreover

have $wfp \vdash (\text{Main}, \text{Label } l) -[((\text{Main}, \text{Label } l), \uparrow id, (\text{Main}, \text{Exit}))] \rightarrow^* (\text{Main}, \text{Exit})$

by(*fastforce intro:ProcCFG.path.intros Main Proc-CFG-Skip simp:valid-edge-def*)

ultimately show *?case* **by**(*fastforce simp:intra-kind-def*)

next

case (*LAss V e*)

note $[simp] = \langle \text{Rep-wf-prog wfp} = (V := e, \text{procs}) \rangle$

from $\langle l < \#:V := e \rangle$ **have** $l = 0 \vee l = 1$ **by** *auto*

thus *?case*

proof

assume $[simp]:l = 0$

have $wfp \vdash (\text{Main}, \text{Entry}) -[((\text{Main}, \text{Entry}), (\lambda s. \text{True})_{\surd}, (\text{Main}, \text{Label } 0))] \rightarrow^*$
 $(\text{Main}, \text{Label } 0)$

by(*fastforce intro:ProcCFG.path.intros Main Proc-CFG-Entry*
simp:valid-edge-def ProcCFG.valid-node-def)

moreover

have $wfp \vdash (\text{Main}, \text{Label } 0)$

$-((\text{Main}, \text{Label } 0), \uparrow(\lambda cf. \text{update cf } V e), (\text{Main}, \text{Label } 1)) \#$

$[((\text{Main}, \text{Label } 1), \uparrow id, (\text{Main}, \text{Exit}))] \rightarrow^* (\text{Main}, \text{Exit})$

by(*fastforce intro:ProcCFG.Cons-path ProcCFG.path.intros Main Proc-CFG-LAss*

Proc-CFG-LAssSkip simp:valid-edge-def ProcCFG.valid-node-def)

ultimately show *?thesis* **by**(*fastforce simp:intra-kind-def*)

next

assume $[simp]:l = 1$

have $wfp \vdash (Main, Entry) - ((Main, Entry), (\lambda s. True) \vee, (Main, Label 0)) \#$
 $[[(Main, Label 0), \uparrow(\lambda cf. update\ cf\ V\ e), (Main, Label 1)] \rightarrow^* (Main, Label 1)]$
by $(fastforce\ intro: ProcCFG.path.intros\ Main\ Proc-CFG-LAss\ ProcCFG.Cons-path)$

$Main\ Proc-CFG-Entry\ simp: ProcCFG.valid-node-def\ valid-edge-def)$

moreover

have $wfp \vdash (Main, Label 1) - [((Main, Label 1), \uparrow id, (Main, Exit))] \rightarrow^*$
 $(Main, Exit)$ **by** $(fastforce\ intro: ProcCFG.path.intros\ Main\ Proc-CFG-LAssSkip$
 $simp: valid-edge-def\ ProcCFG.valid-node-def)$

ultimately show $?thesis$ **by** $(fastforce\ simp: intra-kind-def)$

qed

next

case $(Seq\ c_1\ c_2)$

note $IH1 = \langle \bigwedge l\ wfp. [l < \#:c_1; Rep-wf-prog\ wfp = (c_1, procs)] \implies$

$\exists as\ as'. wfp \vdash (Main, Label\ l) - as \rightarrow^* (Main, Exit) \wedge$

$(\forall a \in set\ as. intra-kind\ (kind\ a)) \wedge$

$wfp \vdash (Main, Entry) - as' \rightarrow^* (Main, Label\ l) \wedge (\forall a \in set\ as'. intra-kind\ (kind$
 $a)) \rangle$

note $IH2 = \langle \bigwedge l\ wfp. [l < \#:c_2; Rep-wf-prog\ wfp = (c_2, procs)] \implies$

$\exists as\ as'. wfp \vdash (Main, Label\ l) - as \rightarrow^* (Main, Exit) \wedge$

$(\forall a \in set\ as. intra-kind\ (kind\ a)) \wedge$

$wfp \vdash (Main, Entry) - as' \rightarrow^* (Main, Label\ l) \wedge (\forall a \in set\ as'. intra-kind\ (kind$
 $a)) \rangle$

note $[simp] = \langle Rep-wf-prog\ wfp = (c_1;; c_2, procs) \rangle$

show $?case$

proof $(cases\ l < \#:c_1)$

case $True$

from $\langle Rep-wf-prog\ wfp = (c_1;; c_2, procs) \rangle$

obtain wfp' **where** $[simp]: Rep-wf-prog\ wfp' = (c_1, procs)$ **by** $(erule\ wfp-Seq1)$

from $IH1[OF\ True\ this]$ **obtain** $as\ as'$

where $path1: wfp' \vdash (Main, Label\ l) - as \rightarrow^* (Main, Exit)$

and $intra1: \forall a \in set\ as. intra-kind\ (kind\ a)$

and $path2: wfp' \vdash (Main, Entry) - as' \rightarrow^* (Main, Label\ l)$

and $intra2: \forall a \in set\ as'. intra-kind\ (kind\ a)$ **by** $blast$

from $path1$ **have** $as \neq []$ **by** $(fastforce\ elim: ProcCFG.path.cases)$

then obtain $ax\ asx$ **where** $[simp]: as = asx@[ax]$

by $(cases\ as\ rule: rev-cases)\ fastforce+$

with $path1$ **have** $wfp' \vdash (Main, Label\ l) - asx \rightarrow^* sourcenode\ ax$

and $valid-edge\ wfp'\ ax$ **and** $targetnode\ ax = (Main, Exit)$

by $(auto\ elim: ProcCFG.path-split-snoc)$

from $\langle valid-edge\ wfp'\ ax \rangle$ $\langle targetnode\ ax = (Main, Exit) \rangle$

obtain nx **where** $sourcenode\ ax = (Main, nx)$

by $(fastforce\ elim: PCFG.cases\ simp: valid-edge-def)$

with $\langle wfp' \vdash (Main, Label\ l) - asx \rightarrow^* sourcenode\ ax \rangle$ **have** $nx \neq Entry$

by $fastforce$

moreover

from $\langle valid-edge\ wfp'\ ax \rangle$ $\langle sourcenode\ ax = (Main, nx) \rangle$ **have** $nx \neq Exit$

by $(fastforce\ intro: ProcCFG.Exit.Exit-source)$

ultimately obtain lx **where** $[simp]: nx = Label\ lx$ **by** $(cases\ nx)\ auto$

```

with  $\langle wfp' \vdash (Main, Label\ l) - asx \rightarrow^* sourcenode\ ax \rangle$ 
   $\langle sourcenode\ ax = (Main, nx) \rangle\ intra1$ 
have  $path3:wfp \vdash (Main, Label\ l) - asx \rightarrow^* (Main, Label\ lx)$ 
  by  $-(rule\ path\ SeqFirst, auto)$ 
from  $\langle valid\ edge\ wfp'\ ax \rangle\ \langle targetnode\ ax = (Main, Exit) \rangle$ 
   $\langle sourcenode\ ax = (Main, nx) \rangle\ wf$ 
obtain  $etx$  where  $c_1 \vdash Label\ lx - etx \rightarrow_p Exit$ 
  by  $(fastforce\ elim!: PCFG.cases\ simp: valid\ edge\ def)$ 
then obtain  $et$  where  $[simp]: etx = IEdge\ et$ 
  by  $(cases\ etx)(auto\ dest: Proc\ CFG\ Call\ Labels)$ 
with  $\langle c_1 \vdash Label\ lx - etx \rightarrow_p Exit \rangle$  have  $intra\ kind\ et$ 
  by  $(fastforce\ intro: Proc\ CFG\ IEdge\ intra\ kind)$ 
from  $\langle c_1 \vdash Label\ lx - etx \rightarrow_p Exit \rangle\ path3$ 
have  $path4:wfp \vdash (Main, Label\ l) - asx @$ 
   $[[ (Main, Label\ lx), et, (Main, Label\ 0 \oplus \#:c_1) ]]$   $\rightarrow^* (Main, Label\ 0 \oplus \#:c_1)$ 
by  $(fastforce\ intro: Proc\ CFG.path\ Append\ Proc\ CFG.path.intros\ Proc\ CFG\ SeqConnect$ 
   $Main\ simp: Proc\ CFG.valid\ node\ def\ valid\ edge\ def)$ 
from  $\langle Rep\ wf\ prog\ wfp = (c_1;; c_2, procs) \rangle$ 
obtain  $wfp''$  where  $[simp]: Rep\ wf\ prog\ wfp'' = (c_2, procs)$  by  $(erule\ wfp\ Seq2)$ 
from  $IH2[OF\ -\ this, of\ 0]$  obtain  $asx'$ 
  where  $wfp'' \vdash (Main, Label\ 0) - asx' \rightarrow^* (Main, Exit)$ 
  and  $\forall a \in set\ asx'.\ intra\ kind\ (kind\ a)$  by  $blast$ 
with  $path4\ intra1\ \langle intra\ kind\ et \rangle$  have  $wfp \vdash (Main, Label\ l)$ 
   $- (asx @ [ (Main, Label\ lx), et, (Main, Label\ 0 \oplus \#:c_1) ]) @ (asx' \oplus s \ #:c_1) \rightarrow^*$ 
   $(Main, Exit \oplus \#:c_1)$ 
  by  $-(erule\ Proc\ CFG.path\ Append, rule\ path\ Main\ SeqSecond, auto)$ 
moreover
from  $intra1\ \langle intra\ kind\ et \rangle\ \langle \forall a \in set\ asx'.\ intra\ kind\ (kind\ a) \rangle$ 
have  $\forall a \in set\ ((asx @ [ (Main, Label\ lx), et, (Main, Label\ \#:c_1) ]) @ (asx' \oplus s$ 
 $\ #:c_1))$ .
   $intra\ kind\ (kind\ a)$  by  $(auto\ simp: label\ incrs\ def)$ 
moreover
from  $path2\ intra2$  have  $wfp \vdash (Main, Entry) - as' \rightarrow^* (Main, Label\ l)$ 
  by  $-(rule\ path\ SeqFirst, auto)$ 
ultimately show  $?thesis$  using  $\langle \forall a \in set\ as'.\ intra\ kind\ (kind\ a) \rangle$  by  $fastforce$ 
next
case  $False$ 
hence  $\#:c_1 \leq l$  by  $simp$ 
then obtain  $l'$  where  $[simp]: l = l' + \#:c_1$  and  $l' = l - \#:c_1$  by  $simp$ 
from  $\langle l < \#:c_1;; c_2 \rangle$  have  $l' < \#:c_2$  by  $simp$ 
from  $\langle Rep\ wf\ prog\ wfp = (c_1;; c_2, procs) \rangle$ 
obtain  $wfp'$  where  $[simp]: Rep\ wf\ prog\ wfp' = (c_2, procs)$  by  $(erule\ wfp\ Seq2)$ 
from  $IH2[OF\ \langle l' < \#:c_2 \rangle\ this]$  obtain  $as\ as'$ 
  where  $path1:wfp' \vdash (Main, Label\ l') - as \rightarrow^* (Main, Exit)$ 
  and  $intra1: \forall a \in set\ as.\ intra\ kind\ (kind\ a)$ 
  and  $path2:wfp' \vdash (Main, Entry) - as' \rightarrow^* (Main, Label\ l')$ 
  and  $intra2: \forall a \in set\ as'.\ intra\ kind\ (kind\ a)$  by  $blast$ 
from  $path1\ intra1$ 
have  $wfp \vdash (Main, Label\ l' \oplus \#:c_1) - as \oplus s \ #:c_1 \rightarrow^* (Main, Exit \oplus \#:c_1)$ 

```

```

    by  $-(rule\ path\ Main\ Seq\ Second, auto)$ 
  moreover
  from  $path2$  have  $as' \neq []$  by  $(fastforce\ elim: ProcCFG.path.cases)$ 
  with  $path2$  obtain  $ax' asx'$  where  $[simp]: as' = ax' \# asx'$ 
    and  $sourcenode\ ax' = (Main, Entry)$  and  $valid\ edge\ wfp' ax'$ 
    and  $wfp' \vdash targetnode\ ax' - asx' \rightarrow^* (Main, Label\ l')$ 
    by  $-(erule\ ProcCFG.path.split.Cons, fastforce+)$ 
  from  $\langle wfp' \vdash targetnode\ ax' - asx' \rightarrow^* (Main, Label\ l') \rangle$ 
  have  $targetnode\ ax' \neq (Main, Exit)$  by  $fastforce$ 
  with  $\langle valid\ edge\ wfp' ax' \rangle \langle sourcenode\ ax' = (Main, Entry) \rangle wf$ 
  have  $targetnode\ ax' = (Main, Label\ 0)$ 
    by  $(fastforce\ elim: PCFG.cases\ dest: Proc-CFG-EntryD\ simp: valid\ edge\ def)$ 
  with  $\langle wfp' \vdash targetnode\ ax' - asx' \rightarrow^* (Main, Label\ l') \rangle intra2$ 
  have  $path3: wfp \vdash (Main, Label\ 0 \oplus \#:c_1) - asx' \oplus s \#:c_1 \rightarrow^*$ 
     $(Main, Label\ l' \oplus \#:c_1)$  by  $-(rule\ path\ Main\ Seq\ Second, auto)$ 
  from  $\langle Rep\ wf\ prog\ wfp = (c_1; c_2, procs) \rangle$ 
  obtain  $wfp''$  where  $[simp]: Rep\ wf\ prog\ wfp'' = (c_1, procs)$  by  $(erule\ wfp\ Seq1)$ 
  from  $IH1[OF - this, of\ 0]$  obtain  $xs$ 
    where  $wfp'' \vdash (Main, Label\ 0) - xs \rightarrow^* (Main, Exit)$ 
    and  $\forall a \in set\ xs. intra\ kind\ (kind\ a)$  by  $blast$ 
  from  $\langle wfp'' \vdash (Main, Label\ 0) - xs \rightarrow^* (Main, Exit) \rangle$  have  $xs \neq []$ 
    by  $(fastforce\ elim: ProcCFG.path.cases)$ 
  then obtain  $x\ xs'$  where  $[simp]: xs = xs' @ [x]$ 
    by  $(cases\ xs\ rule: rev.cases)\ fastforce+$ 
  with  $\langle wfp'' \vdash (Main, Label\ 0) - xs \rightarrow^* (Main, Exit) \rangle$ 
  have  $wfp'' \vdash (Main, Label\ 0) - xs' \rightarrow^* sourcenode\ x$ 
    and  $valid\ edge\ wfp''\ x$  and  $targetnode\ x = (Main, Exit)$ 
    by  $(auto\ elim: ProcCFG.path.split.snoc)$ 
  from  $\langle valid\ edge\ wfp''\ x \rangle \langle targetnode\ x = (Main, Exit) \rangle$ 
  obtain  $nx$  where  $sourcenode\ x = (Main, nx)$ 
    by  $(fastforce\ elim: PCFG.cases\ simp: valid\ edge\ def)$ 
  with  $\langle wfp'' \vdash (Main, Label\ 0) - xs' \rightarrow^* sourcenode\ x \rangle$  have  $nx \neq Entry$ 
    by  $fastforce$ 
  from  $\langle valid\ edge\ wfp''\ x \rangle \langle sourcenode\ x = (Main, nx) \rangle$  have  $nx \neq Exit$ 
    by  $(fastforce\ intro: ProcCFG.Exit.Exit\ source)$ 
  with  $\langle nx \neq Entry \rangle$  obtain  $lx$  where  $[simp]: nx = Label\ lx$  by  $(cases\ nx)\ auto$ 
  from  $\langle wfp'' \vdash (Main, Label\ 0) - xs' \rightarrow^* sourcenode\ x \rangle$ 
     $\langle sourcenode\ x = (Main, nx) \rangle \langle \forall a \in set\ xs. intra\ kind\ (kind\ a) \rangle$ 
  have  $wfp \vdash (Main, Entry)$ 
     $-((Main, Entry), (\lambda s. True) \surd, (Main, Label\ 0)) \# xs' \rightarrow^* sourcenode\ x$ 
  apply  $simp$  apply  $(rule\ path\ Seq\ First[OF\ \langle Rep\ wf\ prog\ wfp'' = (c_1, procs) \rangle])$ 
  apply  $(auto\ intro!: ProcCFG.Cons\ path)$ 
  by  $(auto\ intro: Main\ Proc-CFG-Entry\ simp: valid\ edge\ def\ intra\ kind\ def)$ 
  with  $\langle valid\ edge\ wfp''\ x \rangle \langle targetnode\ x = (Main, Exit) \rangle path3$ 
     $\langle sourcenode\ x = (Main, nx) \rangle \langle nx \neq Entry \rangle \langle sourcenode\ x = (Main, nx) \rangle wf$ 
  have  $wfp \vdash (Main, Entry) -(((Main, Entry), (\lambda s. True) \surd, (Main, Label\ 0)) \# xs' @$ 
     $[(sourcenode\ x, kind\ x, (Main, Label\ \#:c_1)])] @ (asx' \oplus s \#:c_1) \rightarrow^*$ 
     $(Main, Label\ l' \oplus \#:c_1))$ 

```

```

    by(fastforce intro:ProcCFG.path-Append ProcCFG.path.intros Main
       Proc-CFG-SeqConnect elim!:PCFG.cases dest:Proc-CFG-Call-Labels
       simp:ProcCFG.valid-node-def valid-edge-def)
  ultimately show ?thesis using intra1 intra2  $\langle \forall a \in \text{set } xs. \text{intra-kind } (\text{kind } a) \rangle$ 
a))
  by(fastforce simp:label-incrs-def intra-kind-def)
qed
next
case (Cond b c1 c2)
note IH1 =  $\langle \bigwedge l \text{ wfp. } \llbracket l < \# : c_1; \text{Rep-wf-prog wfp} = (c_1, \text{procs}) \rrbracket \implies$ 
 $\exists as \ as'. \text{wfp} \vdash (\text{Main}, \text{Label } l) -as \rightarrow^* (\text{Main}, \text{Exit}) \wedge$ 
 $(\forall a \in \text{set } as. \text{intra-kind } (\text{kind } a)) \wedge$ 
 $\text{wfp} \vdash (\text{Main}, \text{Entry}) -as' \rightarrow^* (\text{Main}, \text{Label } l) \wedge (\forall a \in \text{set } as'. \text{intra-kind } (\text{kind } a)) \rangle$ 
a))
note IH2 =  $\langle \bigwedge l \text{ wfp. } \llbracket l < \# : c_2; \text{Rep-wf-prog wfp} = (c_2, \text{procs}) \rrbracket \implies$ 
 $\exists as \ as'. \text{wfp} \vdash (\text{Main}, \text{Label } l) -as \rightarrow^* (\text{Main}, \text{Exit}) \wedge$ 
 $(\forall a \in \text{set } as. \text{intra-kind } (\text{kind } a)) \wedge$ 
 $\text{wfp} \vdash (\text{Main}, \text{Entry}) -as' \rightarrow^* (\text{Main}, \text{Label } l) \wedge (\forall a \in \text{set } as'. \text{intra-kind } (\text{kind } a)) \rangle$ 
a))
note [simp] =  $\langle \text{Rep-wf-prog wfp} = (\text{if } (b) \ c_1 \ \text{else } \ c_2, \text{procs}) \rangle$ 
show ?case
proof(cases l = 0)
  case True
  from  $\langle \text{Rep-wf-prog wfp} = (\text{if } (b) \ c_1 \ \text{else } \ c_2, \text{procs}) \rangle$ 
obtain wfp' where [simp]:  $\text{Rep-wf-prog wfp}' = (c_1, \text{procs})$  by(erule wfp-CondTrue)
from IH1[OF - this, of 0] obtain as
  where path:  $\text{wfp}' \vdash (\text{Main}, \text{Label } 0) -as \rightarrow^* (\text{Main}, \text{Exit})$ 
  and intra:  $\forall a \in \text{set } as. \text{intra-kind } (\text{kind } a)$  by blast
have  $\text{if } (b) \ c_1 \ \text{else } \ c_2, \text{procs} \vdash (\text{Main}, \text{Label } 0)$ 
 $-(\lambda cf. \text{state-check } cf \ b \ (\text{Some } \text{true}))_{\surd} \rightarrow (\text{Main}, \text{Label } 0 \oplus 1)$ 
by(fastforce intro:Main Proc-CFG-CondTrue)
with path intra have  $\text{wfp}' \vdash (\text{Main}, \text{Label } 0)$ 
 $-\lceil ((\text{Main}, \text{Label } 0), (\lambda cf. \text{state-check } cf \ b \ (\text{Some } \text{true}))_{\surd}, (\text{Main}, \text{Label } 0 \oplus$ 
1)) \rceil @
 $(as \oplus s \ 1) \rightarrow^* (\text{Main}, \text{Exit} \oplus 1)$ 
apply - apply(rule ProcCFG.path-Append) apply(rule ProcCFG.path.intros)+
prefer 5 apply(rule path-Main-CondTrue)
apply(auto intro:ProcCFG.path.intros simp:valid-edge-def)
by(fastforce simp:ProcCFG.valid-node-def valid-edge-def)
moreover
have  $\text{if } (b) \ c_1 \ \text{else } \ c_2, \text{procs} \vdash (\text{Main}, \text{Entry}) -(\lambda s. \text{True})_{\surd} \rightarrow$ 
 $(\text{Main}, \text{Label } 0)$  by(fastforce intro:Main Proc-CFG-Entry)
hence  $\text{wfp}' \vdash (\text{Main}, \text{Entry}) -\lceil ((\text{Main}, \text{Entry}), (\lambda s. \text{True})_{\surd}, (\text{Main}, \text{Label } 0)) \rceil \rightarrow^*$ 
 $(\text{Main}, \text{Label } 0)$ 
by(fastforce intro:ProcCFG.path.intros
   simp:ProcCFG.valid-node-def valid-edge-def)
ultimately show ?thesis using  $\langle l = 0 \rangle \langle \forall a \in \text{set } as. \text{intra-kind } (\text{kind } a) \rangle$ 
by(fastforce simp:label-incrs-def intra-kind-def)

```

```

next
case False
hence  $0 < l$  by simp
then obtain  $l'$  where  $[simp]: l = l' + 1$  and  $l' = l - 1$  by simp
show ?thesis
proof(cases  $l' < \# : c_1$ )
case True
from  $\langle Rep\text{-}wf\text{-}prog\ wfp = (if\ (b)\ c_1\ else\ c_2,\ procs) \rangle$ 
obtain  $wfp'$  where  $[simp]: Rep\text{-}wf\text{-}prog\ wfp' = (c_1,\ procs)$ 
by(erule wfp-CondTrue)
from IH1[OF True this] obtain  $as'$ 
where  $path1:wfp' \vdash (Main,\ Label\ l') -as \rightarrow^* (Main,\ Exit)$ 
and  $intra1:\forall a \in set\ as.\ intra\text{-}kind\ (kind\ a)$ 
and  $path2:wfp' \vdash (Main,\ Entry) -as' \rightarrow^* (Main,\ Label\ l')$ 
and  $intra2:\forall a \in set\ as'.\ intra\text{-}kind\ (kind\ a)$  by blast
from path1 intra1
have  $wfp \vdash (Main,\ Label\ l' \oplus 1) -as \oplus s\ 1 \rightarrow^* (Main,\ Exit \oplus 1)$ 
by  $-(rule\ path\text{-}Main\text{-}CondTrue,\ auto)$ 
moreover
from path2 obtain  $ax'\ asx'$  where  $[simp]: as' = ax' \# asx'$ 
and  $sourcenode\ ax' = (Main,\ Entry)$  and  $valid\text{-}edge\ wfp'\ ax'$ 
and  $wfp' \vdash targetnode\ ax' -asx' \rightarrow^* (Main,\ Label\ l')$ 
by  $-(erule\ ProcCFG.path.cases,\ fastforce+)$ 
with wf have  $targetnode\ ax' = (Main,\ Label\ 0)$ 
by(fastforce elim:PCFG.cases dest:Proc-CFG-EntryD Proc-CFG-Call-Labels

      simp:valid-edge-def)
with  $\langle wfp' \vdash targetnode\ ax' -asx' \rightarrow^* (Main,\ Label\ l') \rangle$  intra2
have  $wfp \vdash (Main,\ Entry) -((Main,\ Entry),(\lambda s.\ True)_{\surd},(Main,\ Label\ 0))\#$ 
   $((Main,\ Label\ 0),(\lambda cf.\ state\text{-}check\ cf\ b\ (Some\ true))_{\surd},(Main,\ Label\ 0 \oplus$ 
1))\#
   $(asx' \oplus s\ 1) \rightarrow^* (Main,\ Label\ l' \oplus 1)$ 
apply  $-\ apply(rule\ ProcCFG.path.intros) + \ apply(rule\ path\text{-}Main\text{-}CondTrue)$ 

by(auto intro:Main Proc-CFG-Entry Proc-CFG-CondTrue simp:valid-edge-def)
ultimately show ?thesis using intra1 intra2
by(fastforce simp:label-incrs-def intra-kind-def)
next
case False
hence  $\# : c_1 \leq l'$  by simp
then obtain  $l''$  where  $[simp]: l' = l'' + \# : c_1$  and  $l'' = l' - \# : c_1$  by simp
from  $\langle l < \# : (if\ (b)\ c_1\ else\ c_2) \rangle$  have  $l'' < \# : c_2$  by simp
from  $\langle Rep\text{-}wf\text{-}prog\ wfp = (if\ (b)\ c_1\ else\ c_2,\ procs) \rangle$ 
obtain  $wfp''$  where  $[simp]: Rep\text{-}wf\text{-}prog\ wfp'' = (c_2,\ procs)$ 
by(erule wfp-CondFalse)
from IH2[OF  $l'' < \# : c_2$  this] obtain  $as'$ 
where  $path1:wfp'' \vdash (Main,\ Label\ l'') -as \rightarrow^* (Main,\ Exit)$ 
and  $intra1:\forall a \in set\ as.\ intra\text{-}kind\ (kind\ a)$ 
and  $path2:wfp'' \vdash (Main,\ Entry) -as' \rightarrow^* (Main,\ Label\ l'')$ 

```


and $\text{intra2}:\forall a \in \text{set } as'. \text{intra-kind } (\text{kind } a) \text{ by blast}$
from path1 intra1
have $wfp \vdash (\text{Main}, \text{Label } l'' \oplus (\# : c_1 + 1)) - as \oplus s (\# : c_1 + 1) \rightarrow^*$
 $(\text{Main}, \text{Exit} \oplus (\# : c_1 + 1))$
by $-(\text{rule path-Main-CondFalse}, \text{auto simp:add-assoc})$
moreover
from path2 **obtain** $ax' asx'$ **where** $[\text{simp}]: as' = ax' \# asx'$
and $\text{sourcenode } ax' = (\text{Main}, \text{Entry})$ **and** $\text{valid-edge } wfp'' ax'$
and $wfp'' \vdash \text{targetnode } ax' - asx' \rightarrow^* (\text{Main}, \text{Label } l'')$
by $-(\text{erule ProcCFG.path.cases}, \text{fastforce+})$
with wf **have** $\text{targetnode } ax' = (\text{Main}, \text{Label } 0)$
by $(\text{fastforce elim:PCFG.cases dest:Proc-CFG-EntryD Proc-CFG-Call-Labels}$
 $\text{simp:valid-edge-def})$
with $\langle wfp'' \vdash \text{targetnode } ax' - asx' \rightarrow^* (\text{Main}, \text{Label } l'') \rangle \text{intra2}$
have $wfp \vdash (\text{Main}, \text{Entry}) - ((\text{Main}, \text{Entry}), (\lambda s. \text{True}))_{\surd}, (\text{Main}, \text{Label } 0) \#$
 $((\text{Main}, \text{Label } 0), (\lambda cf. \text{state-check } cf \ b \ (\text{Some } \text{false})))_{\surd},$
 $(\text{Main}, \text{Label } (\# : c_1 + 1)) \# (asx' \oplus s (\# : c_1 + 1)) \rightarrow^*$
 $(\text{Main}, \text{Label } l'' \oplus (\# : c_1 + 1))$
apply $- \text{apply}(\text{rule ProcCFG.path.intros}) + \text{apply}(\text{rule path-Main-CondFalse})$
by $(\text{auto intro:Main Proc-CFG-Entry Proc-CFG-CondFalse simp:valid-edge-def})$
ultimately show $?thesis$ **using** intra1 intra2
by $(\text{fastforce simp:label-incrs-def intra-kind-def add-assoc})$
qed
qed
next
case $(\text{While } b \ c')$
note $IH = \langle \bigwedge l \ wfp. \llbracket l < \# : c'; \text{Rep-wf-prog } wfp = (c', \text{procs}) \rrbracket \implies$
 $\exists as \ as'. wfp \vdash (\text{Main}, \text{Label } l) - as \rightarrow^* (\text{Main}, \text{Exit}) \wedge$
 $(\forall a \in \text{set } as. \text{intra-kind } (\text{kind } a)) \wedge$
 $wfp \vdash (\text{Main}, \text{Entry}) - as' \rightarrow^* (\text{Main}, \text{Label } l) \wedge (\forall a \in \text{set } as'. \text{intra-kind } (\text{kind}$
 $a)) \rangle$
note $[\text{simp}] = \langle \text{Rep-wf-prog } wfp = (\text{while } (b) \ c', \text{procs}) \rangle$
show $?case$
proof $(\text{cases } l = 0)$
case True
hence $wfp \vdash (\text{Main}, \text{Label } l) -$
 $((\text{Main}, \text{Label } 0), (\lambda cf. \text{state-check } cf \ b \ (\text{Some } \text{false})))_{\surd}, (\text{Main}, \text{Label } 1) \#$
 $[((\text{Main}, \text{Label } 1), \uparrow \text{id}, (\text{Main}, \text{Exit}))] \rightarrow^* (\text{Main}, \text{Exit})$
by $(\text{fastforce intro:ProcCFG.path.intros Main Proc-CFG-WhileFalseSkip}$
 $\text{Proc-CFG-WhileFalse simp:valid-edge-def})$
moreover
have $\text{while } (b) \ c' \vdash \text{Entry} - \text{IEdge } (\lambda s. \text{True})_{\surd} \rightarrow_p \text{Label } 0$ **by** $(\text{rule Proc-CFG-Entry})$
with $\langle l = 0 \rangle$ **have** $wfp \vdash (\text{Main}, \text{Entry})$
 $- [((\text{Main}, \text{Entry}), (\lambda s. \text{True}))_{\surd}, (\text{Main}, \text{Label } 0)] \rightarrow^* (\text{Main}, \text{Label } l)$
by $(\text{fastforce intro:ProcCFG.path.intros Main}$
 $\text{simp:ProcCFG.valid-node-def valid-edge-def})$
ultimately show $?thesis$ **by** $(\text{fastforce simp:intra-kind-def})$
next

```

case False
hence  $1 \leq l$  by simp
thus ?thesis
proof(cases  $l < 2$ )
  case True
    with  $\langle 1 \leq l \rangle$  have [simp]: $l = 1$  by simp
  have  $wfp \vdash (Main, Label\ l) - [((Main, Label\ 1), \uparrow id, (Main, Exit))] \rightarrow^* (Main, Exit)$ 
    by(fastforce intro:ProcCFG.path.intros Main Proc-CFG-WhileFalseSkip
      simp:valid-edge-def)
  moreover
  have  $while\ (b)\ c' \vdash Label\ 0 - IEdge\ (\lambda cf.\ state-check\ cf\ b\ (Some\ false)) \checkmark \rightarrow_p$ 
    Label\ 1 by(rule Proc-CFG-WhileFalse)
  hence  $wfp \vdash (Main, Entry) - ((Main, Entry), (\lambda s.\ True) \checkmark, (Main, Label\ 0)) \#$ 
     $[((Main, Label\ 0), (\lambda cf.\ state-check\ cf\ b\ (Some\ false)) \checkmark, (Main, Label\ 1))] \rightarrow^*$ 
     $(Main, Label\ l)$ 
    by(fastforce intro:ProcCFG.path.intros Main Proc-CFG-Entry
      simp:ProcCFG.valid-node-def valid-edge-def)
  ultimately show ?thesis by(fastforce simp:intra-kind-def)
next
  case False
  with  $\langle 1 \leq l \rangle$  have  $2 \leq l$  by simp
  then obtain  $l'$  where [simp]: $l = l' + 2$  and  $l' = l - 2$ 
    by(simp del:add-2-eq-Suc')
  from  $\langle l < \# : while\ (b)\ c' \rangle$  have  $l' < \# : c'$  by simp
  from  $\langle Rep-wf-prog\ wfp = (while\ (b)\ c',\ procs) \rangle$ 
  obtain  $wfp'$  where [simp]: $Rep-wf-prog\ wfp' = (c',\ procs)$ 
    by(erule wfp-WhileBody)
  from IH[OF  $\langle l' < \# : c' \rangle$  this] obtain as as'
    where  $path1 : wfp' \vdash (Main, Label\ l') - as \rightarrow^* (Main, Exit)$ 
    and  $intra1 : \forall a \in set\ as.\ intra-kind\ (kind\ a)$ 
    and  $path2 : wfp' \vdash (Main, Entry) - as' \rightarrow^* (Main, Label\ l')$ 
    and  $intra2 : \forall a \in set\ as'.\ intra-kind\ (kind\ a)$  by blast
  from  $path1$  have  $as \neq []$  by(fastforce elim:ProcCFG.path.cases)
  with  $path1$  obtain  $ax\ asx$  where [simp]: $as = asx @ [ax]$ 
    and  $wfp' \vdash (Main, Label\ l') - asx \rightarrow^* sourcenode\ ax$ 
    and  $valid-edge\ wfp'\ ax$  and  $targetnode\ ax = (Main, Exit)$ 
    by  $-(erule\ ProcCFG.path-split-snoc, fastforce+)$ 
  with  $wf$  obtain  $lx\ etx$  where  $sourcenode\ ax = (Main, Label\ lx)$ 
    and  $intra-kind\ (kind\ ax)$ 
  apply(auto elim!:PCFG.cases dest:Proc-CFG-Call-Labels simp:valid-edge-def)
    by(case-tac n)(auto dest:Proc-CFG-IEdge-intra-kind)
  with  $\langle wfp' \vdash (Main, Label\ l') - asx \rightarrow^* sourcenode\ ax \rangle$  intra1
  have  $wfp \vdash (Main, Label\ l' \oplus 2) - asx \oplus s\ 2 \rightarrow^* (Main, Label\ lx \oplus 2)$ 
    by  $-(rule\ path-Main-WhileBody, auto)$ 
  from  $\langle valid-edge\ wfp'\ ax \rangle$   $\langle sourcenode\ ax = (Main, Label\ lx) \rangle$ 
   $\langle targetnode\ ax = (Main, Exit) \rangle$   $\langle intra-kind\ (kind\ ax) \rangle$  wf
  have  $while\ (b)\ c', procs \vdash (Main, Label\ lx \oplus 2) - kind\ ax \rightarrow$ 
     $(Main, Label\ 0)$ 

```

by(*fastforce* *intro*!:*Main Proc-CFG-WhileBodyExit* *elim*!:*PCFG.cases*
dest:*Proc-CFG-Call-Labels simp:valid-edge-def*)

hence $wfp \vdash (Main, Label\ lx \oplus 2)$
 $-((Main, Label\ lx \oplus 2), kind\ ax, (Main, Label\ 0))\#$
 $((Main, Label\ 0), (\lambda cf. state-check\ cf\ b\ (Some\ false))\surd, (Main, Label\ 1))\#$
 $[((Main, Label\ 1), \uparrow id, (Main, Exit))]\rightarrow^* (Main, Exit)$
by(*fastforce* *intro*:*ProcCFG.path.intros Main Proc-CFG-WhileFalse*
Proc-CFG-WhileFalseSkip simp:valid-edge-def)

with $\langle wfp \vdash (Main, Label\ l' \oplus 2) - asx \oplus s\ 2 \rightarrow^* (Main, Label\ lx \oplus 2) \rangle$
have $wfp \vdash (Main, Label\ l) - (asx \oplus s\ 2)\@$
 $((Main, Label\ lx \oplus 2), kind\ ax, (Main, Label\ 0))\#$
 $((Main, Label\ 0), (\lambda cf. state-check\ cf\ b\ (Some\ false))\surd, (Main, Label\ 1))\#$
 $[((Main, Label\ 1), \uparrow id, (Main, Exit))]\rightarrow^* (Main, Exit)$
by(*fastforce* *intro*:*ProcCFG.path.Append*)

moreover

from *path2* **have** $as' \neq []$ **by**(*fastforce* *elim*:*ProcCFG.path.cases*)

with *path2* **obtain** $ax' asx'$ **where** [*simp*]: $as' = ax' \# asx'$
and $wfp' \vdash targetnode\ ax' - asx' \rightarrow^* (Main, Label\ l')$
and *valid-edge* $wfp' ax'$ **and** *sourcenode* $ax' = (Main, Entry)$
by $-(erule\ ProcCFG.path-split-Cons, fastforce+)$

with *wf* **have** *targetnode* $ax' = (Main, Label\ 0)$ **and** *intra-kind* (*kind* ax')
by(*fastforce* *elim*!:*PCFG.cases* *dest*:*Proc-CFG-Call-Labels*
Proc-CFG-EntryD simp:intra-kind-def valid-edge-def) $+$

with $\langle wfp' \vdash targetnode\ ax' - asx' \rightarrow^* (Main, Label\ l') \rangle$ *intra2*
have $wfp \vdash (Main, Label\ 0 \oplus 2) - asx' \oplus s\ 2 \rightarrow^* (Main, Label\ l' \oplus 2)$
by $-(rule\ path-Main-WhileBody, auto\ simp\ del:add-2-eq-Suc')$

hence $wfp \vdash (Main, Entry) - ((Main, Entry), (\lambda s. True)\surd, (Main, Label\ 0))\#$
 $((Main, Label\ 0), (\lambda cf. state-check\ cf\ b\ (Some\ true))\surd, (Main, Label\ 2))\#$
 $(asx' \oplus s\ 2) \rightarrow^* (Main, Label\ l)$
by(*fastforce* *intro*:*ProcCFG.path.intros Main Proc-CFG-WhileTrue*
Proc-CFG-Entry simp:valid-edge-def)

ultimately *show* *?thesis* **using** $\langle intra-kind\ (kind\ ax') \rangle$ *intra1* *intra2*
by(*fastforce* *simp*:*label-incrs-def intra-kind-def*)

qed

qed

next

case (*Call p es rets*)

note *Rep* [*simp*] = $\langle Rep-wf-prog\ wfp = (Call\ p\ es\ rets, procs) \rangle$

have *cC*:*containsCall* *procs* (*Call p es rets*) $[]\ p$ **by** *simp*

show *?case*

proof(*cases* $l = 0$)

case *True*

have $wfp \vdash (Main, Label\ 0) - ((Main, Label\ 0), (\lambda s. False)\surd, (Main, Label\ 1))\#$
 $[((Main, Label\ 1), \uparrow id, (Main, Exit))]\rightarrow^* (Main, Exit)$

by(*fastforce* *intro*:*ProcCFG.path.intros Main Proc-CFG-CallSkip MainCallReturn*
Proc-CFG-Call simp:valid-edge-def)

moreover

have *Call p es rets, procs* $\vdash (Main, Entry) - (\lambda s. True)\surd \rightarrow (Main, Label\ 0)$
by(*fastforce* *intro*:*Main Proc-CFG-Entry*)

```

hence  $wfp \vdash (Main, Entry) - [((Main, Entry), (\lambda s. True)_{\checkmark}, (Main, Label 0))] \rightarrow^*$ 
   $(Main, Label 0)$ 
by (fastforce intro: ProcCFG.path.intros
  simp: ProcCFG.valid-node-def valid-edge-def)
ultimately show ?thesis using  $\langle l = 0 \rangle$  by (fastforce simp: intra-kind-def)
next
case False
with  $\langle l < \# : Call\ p\ es\ rets \rangle$  have  $l = 1$  by simp
have  $wfp \vdash (Main, Label 1) - [((Main, Label 1), \uparrow id, (Main, Exit))] \rightarrow^* (Main, Exit)$ 
by (fastforce intro: ProcCFG.path.intros Main Proc-CFG-CallSkip
  simp: valid-edge-def)
moreover
have  $Call\ p\ es\ rets, procs \vdash (Main, Label 0) - (\lambda s. False)_{\checkmark} \rightarrow (Main, Label 1)$ 
by (fastforce intro: MainCallReturn Proc-CFG-Call)
hence  $wfp \vdash (Main, Entry) - ((Main, Entry), (\lambda s. True)_{\checkmark}, (Main, Label 0)) \#$ 
   $[((Main, Label 0), (\lambda s. False)_{\checkmark}, (Main, Label 1))] \rightarrow^* (Main, Label 1)$ 
by (fastforce intro: ProcCFG.path.intros Main Proc-CFG-Entry
  simp: ProcCFG.valid-node-def valid-edge-def)
ultimately show ?thesis using  $\langle l = 1 \rangle$  by (fastforce simp: intra-kind-def)
qed
qed
qed

```

2.8.2 Lifting from edges in procedure Main to arbitrary procedures

lemma *lift-edge-Main-Main*:

```

 $\llbracket c, procs \vdash (Main, n) - et \rightarrow (Main, n'); (p, ins, outs, c) \in set\ procs;$ 
 $containsCall\ procs\ prog\ ps\ p; well-formed\ procs \rrbracket$ 
 $\implies prog, procs \vdash (p, n) - et \rightarrow (p, n')$ 

```

proof (*induct* $(Main, n)$ *et* $(Main, n')$ *rule: PCFG.induct*)

case *Main* **thus** *?case* **by** (*fastforce* *intro: Proc*)

next

case *MainCallReturn* **thus** *?case* **by** (*fastforce* *intro: ProcCallReturn*)

qed *auto*

lemma *lift-edge-Main-Proc*:

```

 $\llbracket c, procs \vdash (Main, n) - et \rightarrow (q, n'); q \neq Main; (p, ins, outs, c) \in set\ procs;$ 
 $containsCall\ procs\ prog\ ps\ p; well-formed\ procs \rrbracket$ 
 $\implies \exists et'. prog, procs \vdash (p, n) - et' \rightarrow (q, n')$ 

```

proof (*induct* $(Main, n)$ *et* (q, n') *rule: PCFG.induct*)

case $(MainCall\ l\ esx\ retsx\ n'x\ insx\ outsx\ cx)$

from $\langle c \vdash Label\ l - CEdge\ (q, esx, retsx) \rightarrow_p\ n'x \rangle$

obtain l' **where** $[simp]: n'x = Label\ l'$ **by** (*fastforce* *dest: Proc-CFG-Call-Labels*)

with *MainCall* **have** $prog, procs \vdash (p, n)$

$-(\lambda s. True): (p, n'x) \hookrightarrow_q map\ (\lambda e\ cf. interpret\ e\ cf)\ esx \rightarrow (q, n')$

by (*fastforce* *intro: ProcCall*)

thus *?case* **by** *fastforce*

qed *auto*

lemma *lift-edge-Proc-Main*:

```

[[c,procs ⊢ (q, n) -et→ (Main, n'); q ≠ Main; (p,ins,outs,c) ∈ set procs;
containsCall procs prog ps p; well-formed procs]]
⇒ ∃ et'. prog,procs ⊢ (q, n) -et'→ (p, n')
proof(induct (q,n) et (Main,n') rule:PCFG.induct)
case (MainReturn l esx retsx l' insx outsx cx)
note [simp] = ⟨Exit = n⟩[THEN sym] ⟨Label l' = n'⟩[THEN sym]
from MainReturn have prog,procs ⊢ (q,Exit) -⟨λcf. snd cf = (p,Label l')⟩↔q
  (λcf cf'. cf'(retsx [:=] map cf outsx))→ (p,Label l')
by(fastforce intro!:ProcReturn)
thus ?case by fastforce
qed auto

```

```

fun lift-edge :: edge ⇒ pname ⇒ edge
where lift-edge a p = ((p,snd(sourcenode a)),kind a,(p,snd(targetnode a)))

```

```

fun lift-path :: edge list ⇒ pname ⇒ edge list
where lift-path as p = map (λa. lift-edge a p) as

```

lemma *lift-path-Proc*:

```

assumes Rep-wf-prog wfp' = (c,procs) and Rep-wf-prog wfp = (prog,procs)
and (p,ins,outs,c) ∈ set procs and containsCall procs prog ps p
shows [[wfp' ⊢ (Main,n) -as→* (Main,n'); ∀ a ∈ set as. intra-kind (kind a)]]
⇒ wfp ⊢ (p,n) -lift-path as p→* (p,n')
proof(induct (Main,n) as (Main,n') arbitrary:n rule:ProcCFG.path.induct)
case empty-path
from ⟨Rep-wf-prog wfp = (prog,procs)⟩ have wf:well-formed procs
by(fastforce intro:wf-wf-prog)
from ⟨CFG.valid-node sourcenode targetnode (valid-edge wfp') (Main, n')⟩
  assms wf
have CFG.valid-node sourcenode targetnode (valid-edge wfp) (p,n')
apply(auto simp:ProcCFG.valid-node-def valid-edge-def)
apply(case-tac ab = Main)
apply(fastforce dest:lift-edge-Main-Main)
apply(fastforce dest!:lift-edge-Main-Proc)
apply(case-tac a = Main)
apply(fastforce dest:lift-edge-Main-Main)
by(fastforce dest!:lift-edge-Proc-Main)
thus ?case by(fastforce dest:ProcCFG.empty-path)
next
case (Cons-path m'' as a n)
note IH = ⟨∧ n. [[m'' = (Main, n); ∀ a ∈ set as. intra-kind (kind a)]]
⇒ wfp ⊢ (p, n) -lift-path as p→* (p, n')⟩
from ⟨Rep-wf-prog wfp = (prog,procs)⟩ have wf:well-formed procs
by(fastforce intro:wf-wf-prog)
from ⟨∀ a ∈ set (a # as). intra-kind (kind a)⟩ have intra-kind (kind a)

```

and $\forall a \in \text{set } as. \text{intra-kind } (\text{kind } a)$ **by** *simp-all*
from $\langle \text{valid-edge wfp}' a \rangle \langle \text{intra-kind } (\text{kind } a) \rangle \langle \text{sourcenode } a = (\text{Main}, n) \rangle$
 $\langle \text{targetnode } a = m'' \rangle \langle \text{Rep-wf-prog wfp}' = (c, \text{procs}) \rangle$
obtain n'' **where** $m'' = (\text{Main}, n'')$
by (*fastforce elim:PCFG.cases simp:valid-edge-def intra-kind-def*)
with $\langle \text{valid-edge wfp}' a \rangle \langle \text{Rep-wf-prog wfp}' = (c, \text{procs}) \rangle$
 $\langle \text{sourcenode } a = (\text{Main}, n) \rangle \langle \text{targetnode } a = m'' \rangle$
 $\langle (p, \text{ins}, \text{outs}, c) \in \text{set } \text{procs} \rangle \langle \text{containsCall } \text{procs } \text{prog } ps \ p \rangle$
 $\langle \text{Rep-wf-prog wfp} = (\text{prog}, \text{procs}) \rangle \text{wf}$
have $\text{prog}, \text{procs} \vdash (p, n) \text{--kind } a \rightarrow (p, n'')$
by (*auto intro:lift-edge-Main-Main simp:valid-edge-def*)
from $IH[OF \langle m'' = (\text{Main}, n'') \rangle \langle \forall a \in \text{set } as. \text{intra-kind } (\text{kind } a) \rangle]$
have $\text{wfp} \vdash (p, n'') \text{--lift-path } as \ p \rightarrow^* (p, n'')$.
with $\langle \text{prog}, \text{procs} \vdash (p, n) \text{--kind } a \rightarrow (p, n'') \rangle \langle \text{Rep-wf-prog wfp} = (\text{prog}, \text{procs}) \rangle$
 $\langle \text{sourcenode } a = (\text{Main}, n) \rangle \langle \text{targetnode } a = m'' \rangle \langle m'' = (\text{Main}, n'') \rangle$
show ?*case* **by** *simp (rule ProcCFG.Cons-path, auto simp:valid-edge-def)*
qed

2.8.3 Existence of paths from Entry and to Exit

lemma *Label-Proc-CFG-Entry-Exit-path-Proc*:

assumes $\text{Rep-wf-prog wfp} = (\text{prog}, \text{procs})$ **and** $l < \# : c$
and $(p, \text{ins}, \text{outs}, c) \in \text{set } \text{procs}$ **and** $\text{containsCall } \text{procs } \text{prog } ps \ p$
obtains $as \ as'$ **where** $\text{wfp} \vdash (p, \text{Label } l) \text{--} as \rightarrow^* (p, \text{Exit})$
and $\forall a \in \text{set } as. \text{intra-kind } (\text{kind } a)$
and $\text{wfp} \vdash (p, \text{Entry}) \text{--} as' \rightarrow^* (p, \text{Label } l)$
and $\forall a \in \text{set } as'. \text{intra-kind } (\text{kind } a)$
proof (*atomize-elim*)
from $\langle \text{Rep-wf-prog wfp} = (\text{prog}, \text{procs}) \rangle \langle (p, \text{ins}, \text{outs}, c) \in \text{set } \text{procs} \rangle$
 $\langle \text{containsCall } \text{procs } \text{prog } ps \ p \rangle$
obtain wfp' **where** $\text{Rep-wf-prog wfp}' = (c, \text{procs})$ **by** (*erule wfp-Call*)
from *this* $\langle l < \# : c \rangle$ **obtain** $as \ as'$ **where** $\text{wfp}' \vdash (\text{Main}, \text{Label } l) \text{--} as \rightarrow^* (\text{Main}, \text{Exit})$
and $\forall a \in \text{set } as. \text{intra-kind } (\text{kind } a)$
and $\text{wfp}' \vdash (\text{Main}, \text{Entry}) \text{--} as' \rightarrow^* (\text{Main}, \text{Label } l)$
and $\forall a \in \text{set } as'. \text{intra-kind } (\text{kind } a)$
by (*erule Label-Proc-CFG-Entry-Exit-path-Main*)
from $\langle \text{Rep-wf-prog wfp}' = (c, \text{procs}) \rangle \langle \text{Rep-wf-prog wfp} = (\text{prog}, \text{procs}) \rangle$
 $\langle (p, \text{ins}, \text{outs}, c) \in \text{set } \text{procs} \rangle \langle \text{containsCall } \text{procs } \text{prog } ps \ p \rangle$
 $\langle \text{wfp}' \vdash (\text{Main}, \text{Label } l) \text{--} as \rightarrow^* (\text{Main}, \text{Exit}) \rangle \langle \forall a \in \text{set } as. \text{intra-kind } (\text{kind } a) \rangle$
have $\text{wfp} \vdash (p, \text{Label } l) \text{--lift-path } as \ p \rightarrow^* (p, \text{Exit})$
by (*fastforce intro:lift-path-Proc*)
moreover
from $\langle \text{Rep-wf-prog wfp}' = (c, \text{procs}) \rangle \langle \text{Rep-wf-prog wfp} = (\text{prog}, \text{procs}) \rangle$
 $\langle (p, \text{ins}, \text{outs}, c) \in \text{set } \text{procs} \rangle \langle \text{containsCall } \text{procs } \text{prog } ps \ p \rangle$
 $\langle \text{wfp}' \vdash (\text{Main}, \text{Entry}) \text{--} as' \rightarrow^* (\text{Main}, \text{Label } l) \rangle \langle \forall a \in \text{set } as'. \text{intra-kind } (\text{kind } a) \rangle$
have $\text{wfp} \vdash (p, \text{Entry}) \text{--lift-path } as' \ p \rightarrow^* (p, \text{Label } l)$
by (*fastforce intro:lift-path-Proc*)
moreover

from $\langle \forall a \in \text{set } as. \text{intra-kind } (kind\ a) \rangle \langle \forall a \in \text{set } as'. \text{intra-kind } (kind\ a) \rangle$
have $\forall a \in \text{set } (lift\text{-path } as\ p). \text{intra-kind } (kind\ a)$
and $\forall a \in \text{set } (lift\text{-path } as'\ p). \text{intra-kind } (kind\ a)$ **by** *auto*
ultimately
show $\exists as\ as'. wfp \vdash (p, Label\ l) -as \rightarrow^* (p, Exit) \wedge$
 $(\forall a \in \text{set } as. \text{intra-kind } (kind\ a)) \wedge wfp \vdash (p, Entry) -as' \rightarrow^* (p, Label\ l) \wedge$
 $(\forall a \in \text{set } as'. \text{intra-kind } (kind\ a))$ **by** *fastforce*
qed

lemma *Entry-to-Entry-and-Exit-to-Exit:*

assumes *Rep-wf-prog wfp = (prog,procs)*
and *containsCall procs prog ps p* **and** $(p, ins, outs, c) \in \text{set } procs$
obtains *as as' where CFG.valid-path' sourcenode targetnode kind*
 $(valid\text{-edge } wfp) (get\text{-return-edges } wfp) (Main, Entry) as (p, Entry)$
and *CFG.valid-path' sourcenode targetnode kind*
 $(valid\text{-edge } wfp) (get\text{-return-edges } wfp) (p, Exit) as' (Main, Exit)$
proof(*atomize-elim*)
from $\langle containsCall\ procs\ prog\ ps\ p \rangle \langle (p, ins, outs, c) \in \text{set } procs \rangle$
show $\exists as\ as'. CFG.valid-path' sourcenode targetnode kind (valid\text{-edge } wfp)$
 $(get\text{-return-edges } wfp) (Main, Entry) as (p, Entry) \wedge$
 $CFG.valid-path' sourcenode targetnode kind (valid\text{-edge } wfp)$
 $(get\text{-return-edges } wfp) (p, Exit) as' (Main, Exit)$
proof(*induct ps arbitrary:p ins outs c rule:rev-induct*)
case *Nil*
from $\langle containsCall\ procs\ prog\ []\ p \rangle$
obtain $lx\ es\ rets\ lx'$ **where** $prog \vdash Label\ lx -CEdge (p, es, rets) \rightarrow_p Label\ lx'$
by(*erule containsCall-empty-Proc-CFG-Call-edge*)
with $\langle (p, ins, outs, c) \in \text{set } procs \rangle$
have $prog, procs \vdash (Main, Label\ lx) -(\lambda s. True):(Main, Label\ lx') \leftrightarrow_p$
 $map (\lambda e\ cf. interpret\ e\ cf) es \rightarrow (p, Entry)$
and $prog, procs \vdash (p, Exit) -(\lambda cf. snd\ cf = (Main, Label\ lx')) \leftrightarrow_p$
 $(\lambda cf\ cf'. cf'(rets :=] map\ cf\ outs)) \rightarrow (Main, Label\ lx')$
by $-(rule\ MainCall, assumption+, rule\ MainReturn)$
with $\langle Rep-wf-prog\ wfp = (prog,procs) \rangle$
have $wfp \vdash (Main, Label\ lx) -[((Main, Label\ lx),$
 $(\lambda s. True):(Main, Label\ lx') \leftrightarrow_p map (\lambda e\ cf. interpret\ e\ cf) es, (p, Entry))] \rightarrow^*$
 $(p, Entry)$
and $wfp \vdash (p, Exit) -[((p, Exit), (\lambda cf. snd\ cf = (Main, Label\ lx')) \leftrightarrow_p$
 $(\lambda cf\ cf'. cf'(rets :=] map\ cf\ outs)), (Main, Label\ lx'))] \rightarrow^* (Main, Label\ lx')$
by(*fastforce intro:ProcCFG.path.intros*
 $simp:ProcCFG.valid-node-def valid-edge-def$)
moreover
from $\langle prog \vdash Label\ lx -CEdge (p, es, rets) \rightarrow_p Label\ lx' \rangle$
have $lx < \# : prog$ **and** $lx' < \# : prog$
by(*auto intro:Proc-CFG-sourcelabel-less-num-nodes*
 $Proc-CFG-targetlabel-less-num-nodes$)
from $\langle Rep-wf-prog\ wfp = (prog,procs) \rangle \langle lx < \# : prog \rangle$ **obtain** *as*
where $wfp \vdash (Main, Entry) -as \rightarrow^* (Main, Label\ lx)$

```

and  $\forall a \in \text{set } as. \text{intra-kind } (kind\ a)$ 
by  $-(erule\ Label\ Proc\ CFG\ Entry\ Exit\ path\ Main)$ 
moreover
from  $\langle Rep\text{-}wf\text{-}prog\ wfp = (prog, procs) \rangle \langle lx' < \# : prog \rangle$  obtain  $as'$ 
where  $wfp \vdash (Main, Label\ lx') - as' \rightarrow^* (Main, Exit)$ 
and  $\forall a \in \text{set } as'. \text{intra-kind } (kind\ a)$ 
by  $-(erule\ Label\ Proc\ CFG\ Entry\ Exit\ path\ Main)$ 
moreover
from  $\langle \forall a \in \text{set } as. \text{intra-kind } (kind\ a) \rangle$ 
have  $CFG.\text{valid-path kind } (get\text{-}return\text{-}edges\ wfp)$ 
 $(as@[((Main, Label\ lx'), (\lambda s. True):(Main, Label\ lx') \hookrightarrow_p$ 
 $map\ (\lambda e\ cf. \text{interpret } e\ cf)\ es, (p, Entry))])$ 
by  $(fastforce\ intro: ProcCFG.\text{same-level-path-valid-path-Append}$ 
 $ProcCFG.\text{intra-same-level-path simp: ProcCFG.\text{valid-path-def})$ 
moreover
from  $\langle \forall a \in \text{set } as'. \text{intra-kind } (kind\ a) \rangle$ 
have  $CFG.\text{valid-path kind } (get\text{-}return\text{-}edges\ wfp)$ 
 $([(p, Exit), (\lambda cf. \text{snd } cf = (Main, Label\ lx')) \hookrightarrow_p$ 
 $(\lambda cf\ cf'. cf'(rets\ [:=]\ map\ cf\ outs)), (Main, Label\ lx')]) @ as')$ 
by  $(fastforce\ intro: ProcCFG.\text{valid-path-same-level-path-Append}$ 
 $ProcCFG.\text{intra-same-level-path simp: ProcCFG.\text{valid-path-def})$ 
ultimately show  $?case$  by  $(fastforce\ intro: ProcCFG.\text{path-Append simp: ProcCFG.\text{vp-def})$ 
next
case  $(snoc\ p'\ ps')$ 
note  $IH = \langle \bigwedge p\ ins\ outs\ c.$ 
 $\llbracket containsCall\ procs\ prog\ ps'\ p; (p, ins, outs, c) \in \text{set } procs \rrbracket$ 
 $\implies \exists as\ as'. CFG.\text{valid-path}'\ \text{sourcenode}\ \text{targetnode}\ \text{kind}\ (valid\text{-}edge\ wfp)$ 
 $(get\text{-}return\text{-}edges\ wfp)\ (Main, Entry)\ as\ (p, Entry) \wedge$ 
 $CFG.\text{valid-path}'\ \text{sourcenode}\ \text{targetnode}\ \text{kind}\ (valid\text{-}edge\ wfp)$ 
 $(get\text{-}return\text{-}edges\ wfp)\ (p, Exit)\ as'\ (Main, Exit) \rangle$ 
from  $\langle containsCall\ procs\ prog\ (ps' @ [p'])\ p \rangle$ 
obtain  $ins'\ outs'\ c'$  where  $(p', ins', outs', c') \in \text{set } procs$ 
and  $containsCall\ procs\ c' \llbracket p$ 
and  $containsCall\ procs\ prog\ ps'\ p'$  by  $(auto\ elim: containsCallE)$ 
from  $IH[OF\ \langle containsCall\ procs\ prog\ ps'\ p' \rangle \langle (p', ins', outs', c') \in \text{set } procs \rangle]$ 
obtain  $as\ as'$  where  $pathE: CFG.\text{valid-path}'\ \text{sourcenode}\ \text{targetnode}\ \text{kind}$ 
 $(valid\text{-}edge\ wfp)\ (get\text{-}return\text{-}edges\ wfp)\ (Main, Entry)\ as\ (p', Entry)$ 
and  $pathX: CFG.\text{valid-path}'\ \text{sourcenode}\ \text{targetnode}\ \text{kind}\ (valid\text{-}edge\ wfp)$ 
 $(get\text{-}return\text{-}edges\ wfp)\ (p', Exit)\ as'\ (Main, Exit)$  by  $blast$ 
from  $\langle containsCall\ procs\ c' \llbracket p \rangle$ 
obtain  $lx\ es\ rets\ lx'$  where  $edge: c' \vdash Label\ lx - CEdge\ (p, es, rets) \rightarrow_p\ Label\ lx'$ 
by  $(erule\ containsCall\ empty\ Proc\ CFG\ Call\ edge)$ 
hence  $lx < \# : c'$  and  $lx' < \# : c'$ 
by  $(auto\ intro: Proc\ CFG\ sourcelabel\ less\ num\ nodes$ 
 $Proc\ CFG\ targetlabel\ less\ num\ nodes)$ 
from  $\langle lx < \# : c' \rangle \langle Rep\text{-}wf\text{-}prog\ wfp = (prog, procs) \rangle \langle (p', ins', outs', c') \in \text{set } procs \rangle$ 
 $\langle containsCall\ procs\ prog\ ps'\ p' \rangle$  obtain  $asx$ 
where  $wfp \vdash (p', Entry) - asx \rightarrow^* (p', Label\ lx)$ 
and  $\forall a \in \text{set } asx. \text{intra-kind } (kind\ a)$ 

```



```

    by(fastforce elim:Label-Proc-CFG-Entry-Exit-path-Proc)
  with pathE have pathE2:CFG.valid-path' sourcenode targetnode kind
    (valid-edge wfp) (get-return-edges wfp) (Main, Entry) (as@asx) (p', Label lx)
  by(fastforce intro:ProcCFG.path-Append ProcCFG.valid-path-same-level-path-Append
    ProcCFG.intras-same-level-path simp:ProcCFG.vp-def)
  from ⟨lx' < #:c'⟩ ⟨Rep-wf-prog wfp = (prog,procs)⟩
    ⟨(p',ins',outs',c') ∈ set procs⟩ ⟨containsCall procs prog ps' p'⟩
  obtain asx' where wfp ⊢ (p',Label lx') - asx' →* (p',Exit)
    and ∀ a ∈ set asx'. intra-kind (kind a)
    by(fastforce elim:Label-Proc-CFG-Entry-Exit-path-Proc)
  with pathX have pathX2:CFG.valid-path' sourcenode targetnode kind
    (valid-edge wfp) (get-return-edges wfp) (p', Label lx') (asx'@as') (Main, Exit)
  by(fastforce intro:ProcCFG.path-Append ProcCFG.same-level-path-valid-path-Append
    ProcCFG.intras-same-level-path simp:ProcCFG.vp-def)
  from edge ⟨(p,ins,outs,c) ∈ set procs⟩ ⟨(p',ins',outs',c') ∈ set procs⟩
    ⟨containsCall procs prog ps' p'⟩
  have prog,procs ⊢ (p',Label lx) - (λs. True):(p',Label lx') ↦p
    map (λe cf. interpret e cf) es → (p,Entry)
    and prog,procs ⊢ (p,Exit) - (λcf. snd cf = (p',Label lx')) ↦p
    (λcf cf'. cf'(rets [:=] map cf outs)) → (p',Label lx')
    by(fastforce intro:ProcCall ProcReturn)+
  with ⟨Rep-wf-prog wfp = (prog,procs)⟩
  have path:wfp ⊢ (p',Label lx) - [(p',Label lx),(λs. True):(p',Label lx') ↦p
    map (λe cf. interpret e cf) es,(p,Entry))] →* (p,Entry)
    and path':wfp ⊢ (p,Exit) - [(p,Exit),(λcf. snd cf = (p',Label lx')) ↦p
    (λcf cf'. cf'(rets [:=] map cf outs)),(p',Label lx')]] →*
    (p',Label lx')
    by(fastforce intro:ProcCFG.path.intros
      simp:ProcCFG.valid-node-def valid-edge-def)+
  from path pathE2 have CFG.valid-path' sourcenode targetnode kind (valid-edge
wfp)
    (get-return-edges wfp) (Main, Entry) ((as@asx)@[((p',Label lx),
(λs. True):(p',Label lx') ↦p map (λe cf. interpret e cf) es,(p,Entry))])
    (p,Entry)
    apply(unfold ProcCFG.vp-def) apply(rule conjI)
    apply(fastforce intro:ProcCFG.path-Append)
    by(unfold ProcCFG.valid-path-def,fastforce intro:ProcCFG.vpa-snoc-Call)
  moreover
  from path' pathX2 have CFG.valid-path' sourcenode targetnode kind
    (valid-edge wfp) (get-return-edges wfp) (p,Exit)
    ([((p,Exit),(λcf. snd cf = (p',Label lx')) ↦p
(λcf cf'. cf'(rets [:=] map cf outs))),(p',Label lx')])@ (asx'@as') (Main, Exit)
    apply(unfold ProcCFG.vp-def) apply(rule conjI)
    apply(fastforce intro:ProcCFG.path-Append)
    by(simp add:ProcCFG.valid-path-def ProcCFG.valid-path-def)
  ultimately show ?case by blast
qed
qed

```

lemma *edge-valid-paths*:

assumes $\text{prog,procs} \vdash \text{sourcenode } a \text{ --kind } a \rightarrow \text{targetnode } a$
and $\text{disj}:(p,n) = \text{sourcenode } a \vee (p,n) = \text{targetnode } a$
and $[\text{simp}]:\text{Rep-wf-prog wfp} = (\text{prog,procs})$
shows $\exists as \text{ as}'. \text{CFG.valid-path}' \text{ sourcenode targetnode kind (valid-edge wfp)}$
 $(\text{get-return-edges wfp}) (\text{Main,Entry}) as (p,n) \wedge$
 $\text{CFG.valid-path}' \text{ sourcenode targetnode kind (valid-edge wfp)}$
 $(\text{get-return-edges wfp}) (p,n) as' (\text{Main,Exit})$

proof –

from $\langle \text{Rep-wf-prog wfp} = (\text{prog,procs}) \rangle$ **have** $wf:\text{well-formed procs}$
by $(\text{fastforce intro:wf-wf-prog})$

from $\langle \text{prog,procs} \vdash \text{sourcenode } a \text{ --kind } a \rightarrow \text{targetnode } a \rangle$
show $?thesis$

proof $(\text{induct sourcenode } a \text{ kind } a \text{ targetnode } a \text{ rule:PCFG.induct})$
case $(\text{Main } nx \text{ } nx')$
from $\langle (\text{Main}, nx) = \text{sourcenode } a \rangle [\text{THEN sym}] \langle (\text{Main}, nx') = \text{targetnode } a \rangle$
 $[\text{THEN sym}]$
disj **have** $[\text{simp}]:p = \text{Main}$ **by** auto
have $\text{prog,procs} \vdash (\text{Main}, \text{Entry}) \text{ --}(\lambda s. \text{False})_{\surd} \rightarrow (\text{Main}, \text{Exit})$
by $(\text{fastforce intro:PCFG.Main Proc-CFG-Entry-Exit})$
hence $\text{EXpath:wfp} \vdash (\text{Main,Entry}) \text{ --}[(\text{Main,Entry}),(\lambda s. \text{False})_{\surd},(\text{Main,Exit})] \rightarrow^*$
 (Main,Exit)
by $(\text{fastforce intro:ProcCFG.path.intros}$
 $\text{simp:valid-edge-def ProcCFG.valid-node-def})$
show $?case$

proof $(\text{cases } n)$
case $(\text{Label } l)$
with $\langle \text{prog} \vdash nx \text{ --IEdge } (\text{kind } a) \rightarrow_p nx' \rangle \langle (\text{Main}, nx) = \text{sourcenode } a \rangle$
 $[\text{THEN sym}]$
 $\langle (\text{Main}, nx') = \text{targetnode } a \rangle [\text{THEN sym}] \text{ disj}$
have $l < \#:\text{prog}$ **by** $(\text{auto intro:Proc-CFG-sourcelabel-less-num-nodes}$
 $\text{Proc-CFG-targetlabel-less-num-nodes})$
with $\langle \text{Rep-wf-prog wfp} = (\text{prog,procs}) \rangle$
obtain $as \text{ as}'$ **where** $wfp \vdash (\text{Main,Entry}) \text{ --}as \rightarrow^* (\text{Main,Label } l)$
and $\forall a \in \text{set } as. \text{intra-kind } (\text{kind } a)$
and $wfp \vdash (\text{Main,Label } l) \text{ --}as' \rightarrow^* (\text{Main,Exit})$
and $\forall a \in \text{set } as'. \text{intra-kind } (\text{kind } a)$
by $(\text{erule Label-Proc-CFG-Entry-Exit-path-Main})+$
with Label **show** $?thesis$
apply $(\text{rule-tac } x=as \text{ in } exI)$ **apply** $(\text{rule-tac } x=as' \text{ in } exI)$ **apply** simp
by $(\text{fastforce intro:ProcCFG.intra-path-vp simp:ProcCFG.intra-path-def})$

next
case Entry
hence $wfp \vdash (\text{Main,Entry}) \text{ --}[] \rightarrow^* (\text{Main},n)$ **by** $(\text{fastforce intro:ProcCFG.empty-path})$
with EXpath **show** $?thesis$ **by** $(\text{fastforce simp:ProcCFG.vp-def ProcCFG.valid-path-def})$

next
case Exit
hence $wfp \vdash (\text{Main},n) \text{ --}[] \rightarrow^* (\text{Main,Exit})$ **by** $(\text{fastforce intro:ProcCFG.empty-path})$

```

with Exit EXpath show ?thesis using Exit
  apply(rule-tac x=[((Main,Entry),(λs. False)✓,(Main,Exit))] in exI)
  apply simp
  by(fastforce intro:ProcCFG.intra-path-vp
      simp:ProcCFG.intra-path-def intra-kind-def)
qed
next
case (Proc px ins outs c nx nx' ps)
from ⟨(px, ins, outs, c) ∈ set procs⟩ wf have [simp]:px ≠ Main by auto
  from disj ⟨(px, nx) = sourcenode a⟩[THEN sym] ⟨(px, nx') = targetnode
a) [THEN sym]
  have [simp]:p = px by auto
  from ⟨Rep-wf-prog wfp = (prog,procs)⟩
    ⟨containsCall procs prog ps px⟩ ⟨(px, ins, outs, c) ∈ set procs⟩
  obtain asx asx' where path:CFG.valid-path' sourcenode targetnode kind
    (valid-edge wfp) (get-return-edges wfp) (Main,Entry) asx (px,Entry)
  and path':CFG.valid-path' sourcenode targetnode kind
    (valid-edge wfp) (get-return-edges wfp) (px,Exit) asx' (Main,Exit)
  by -(erule Entry-to-Entry-and-Exit-to-Exit)+
  from ⟨containsCall procs prog ps px⟩ ⟨(px, ins, outs, c) ∈ set procs⟩
  have prog,procs ⊢ (px, Entry) -(λs. False)✓→ (px, Exit)
  by(fastforce intro:PCFG.Proc Proc-CFG-Entry-Exit)
  hence EXpath:wfp ⊢ (px,Entry) -[⟨(px,Entry),(λs. False)✓,(px,Exit)⟩]→*
    (px,Exit) by(fastforce intro:ProcCFG.path.intros
      simp:valid-edge-def ProcCFG.valid-node-def)
  show ?case
  proof(cases n)
  case (Label l)
  with ⟨c ⊢ nx -IEdge (kind a)→p nx'⟩ disj ⟨(px, nx) = sourcenode a⟩[THEN
sym]
    ⟨(px, nx') = targetnode a⟩[THEN sym]
  have l < #:c by(auto intro:Proc-CFG-sourcelabel-less-num-nodes
    Proc-CFG-targetlabel-less-num-nodes)
  with ⟨Rep-wf-prog wfp = (prog,procs)⟩ ⟨(px, ins, outs, c) ∈ set procs⟩
    ⟨containsCall procs prog ps px⟩
  obtain as as' where wfp ⊢ (px,Entry) -as→* (px,Label l)
  and ∀ a ∈ set as. intra-kind (kind a)
  and wfp ⊢ (px,Label l) -as'→* (px,Exit)
  and ∀ a ∈ set as'. intra-kind (kind a)
  by -(erule Label-Proc-CFG-Entry-Exit-path-Proc)+
  with path path' show ?thesis using Label
  apply(rule-tac x=asx@as in exI) apply(rule-tac x=as'@asx' in exI)
  by(auto intro:ProcCFG.path-Append ProcCFG.valid-path-same-level-path-Append
    ProcCFG.same-level-path-valid-path-Append ProcCFG.intras-same-level-path
      simp:ProcCFG.vp-def)
  next
  case Entry
  from EXpath path' have CFG.valid-path' sourcenode targetnode kind
    (valid-edge wfp) (get-return-edges wfp) (px,Entry)

```

```

      (((px,Entry),(λs. False)✓,(px,Exit))@asx') (Main, Exit)
    apply(unfold ProcCFG.vp-def) apply(erule conjE) apply(rule conjI)
    by(fastforce intro:ProcCFG.path-Append
      ProcCFG.same-level-path-valid-path-Append ProcCFG.intra-same-level-path
      simp:intra-kind-def)+
  with path Entry show ?thesis by simp blast
next
case Exit
with path EXpath path' show ?thesis
  apply(rule-tac x=asx@(((px,Entry),(λs. False)✓,(px,Exit))) in exI)
  apply simp
  by(fastforce intro:ProcCFG.path-Append
    ProcCFG.valid-path-same-level-path-Append ProcCFG.intra-same-level-path
    simp:ProcCFG.vp-def ProcCFG.intra-path-def intra-kind-def)
qed
next
case (MainCall l px es rets nx' ins outs c)
from disj show ?case
proof
  assume (p,n) = sourcenode a
  with ⟨(Main, Label l) = sourcenode a⟩[THEN sym]
  have [simp]:n = Label l p = Main by simp-all
  with ⟨prog ⊢ Label l -CEdge (px, es, rets)→p nx'⟩ have l < #:prog
    by(fastforce intro:Proc-CFG-sourcelabel-less-num-nodes)
  with ⟨Rep-wf-prog wfp = (prog,procs)⟩
  obtain as as' where wfp ⊢ (Main,Entry) -as→* (Main,Label l)
    and ∀ a ∈ set as. intra-kind (kind a)
    and wfp ⊢ (Main,Label l) -as'→* (Main,Exit)
    and ∀ a ∈ set as'. intra-kind (kind a)
    by -(erule Label-Proc-CFG-Entry-Exit-path-Main)+
  thus ?thesis
    by(fastforce intro:ProcCFG.intra-path-vp simp:ProcCFG.intra-path-def)
next
  assume (p,n) = targetnode a
  with ⟨(px, Entry) = targetnode a⟩[THEN sym]
  have [simp]:n = Entry p = px by simp-all
  from ⟨prog ⊢ Label l -CEdge (px, es, rets)→p nx'⟩
  have containsCall procs prog [] px
    by(rule Proc-CFG-Call-containsCall)
  with ⟨Rep-wf-prog wfp = (prog,procs)⟩ ⟨(px, ins, outs, c) ∈ set procs⟩
  obtain as' where Xpath:CFG.valid-path' sourcenode targetnode kind
    (valid-edge wfp) (get-return-edges wfp) (px,Exit) as' (Main,Exit)
    by -(erule Entry-to-Entry-and-Exit-to-Exit)
  from ⟨containsCall procs prog [] px⟩ ⟨(px, ins, outs, c) ∈ set procs⟩
  have prog,procs ⊢ (px, Entry) -(λs. False)✓→ (px, Exit)
    by(fastforce intro:PCFG.Proc Proc-CFG-Entry-Exit)
  hence wfp ⊢ (px,Entry) -[(((px,Entry),(λs. False)✓,(px,Exit)))]→* (px,Exit)
    by(fastforce intro:ProcCFG.path.intros
      simp:valid-edge-def ProcCFG.valid-node-def)

```

```

with  $Xpath$  have  $CFG.valid-path'$   $sourcenode$   $targetnode$   $kind$ 
  ( $valid-edge$   $wfp$ ) ( $get-return-edges$   $wfp$ ) ( $px,Entry$ )
  ( $[(px,Entry),(\lambda s. False)\surd,(px,Exit)]@as'$ ) ( $Main,Exit$ )
apply( $unfold$   $ProcCFG.vp-def$ ) apply( $erule$   $conjE$ ) apply( $rule$   $conjI$ )
by( $fastforce$   $intro:ProcCFG.path-Append$ 
   $ProcCFG.same-level-path-valid-path-Append$   $ProcCFG.intras-same-level-path$ 
   $simp:intra-kind-def$ )+
with  $\langle containsCall$   $procs$   $prog$   $\square$   $px \rangle$   $\langle Rep-wf-prog$   $wfp = (prog,procs) \rangle$ 
   $\langle (px, ins, outs, c) \in set$   $procs \rangle$ 
show  $?thesis$  by( $fastforce$   $elim:Entry-to-Entry-and-Exit-to-Exit$ )
qed
next
case ( $ProcCall$   $px$   $ins$   $outs$   $c$   $l$   $p'$   $es'$   $rets'$   $l'$   $ins'$   $outs'$   $c'$   $ps$ )
from  $disj$  show  $?case$ 
proof
  assume  $(p,n) = sourcenode$   $a$ 
  with  $\langle (px, Label$   $l) = sourcenode$   $a \rangle[THEN$   $sym]$ 
  have [ $simp$ ]: $n = Label$   $l$   $p = px$  by  $simp-all$ 
  with  $\langle c \vdash Label$   $l - CEdge$   $(p', es', rets') \rightarrow_p$   $Label$   $l' \rangle$  have  $l < \# : c$ 
    by( $fastforce$   $intro:Proc-CFG-sourcelabel-less-num-nodes$ )
  from  $\langle Rep-wf-prog$   $wfp = (prog,procs) \rangle$   $\langle l < \# : c \rangle$ 
     $\langle containsCall$   $procs$   $prog$   $ps$   $px \rangle$   $\langle (px, ins, outs, c) \in set$   $procs \rangle$ 
  obtain  $as$   $as'$  where  $wfp \vdash (px,Label$   $l) - as \rightarrow^*$   $(px,Exit)$ 
    and  $\forall a \in set$   $as. intra-kind$   $(kind$   $a)$ 
    and  $wfp \vdash (px,Entry) - as' \rightarrow^*$   $(px,Label$   $l)$ 
    and  $\forall a \in set$   $as'. intra-kind$   $(kind$   $a)$ 
    by  $-(erule$   $Label-Proc-CFG-Entry-Exit-path-Proc)$ +
  moreover
from  $\langle Rep-wf-prog$   $wfp = (prog,procs) \rangle$   $\langle containsCall$   $procs$   $prog$   $ps$   $px \rangle$ 
   $\langle (px, ins, outs, c) \in set$   $procs \rangle$  obtain  $asx$   $asx'$ 
  where  $CFG.valid-path'$   $sourcenode$   $targetnode$   $kind$ 
    ( $valid-edge$   $wfp$ ) ( $get-return-edges$   $wfp$ ) ( $Main,Entry$ )  $asx$   $(px,Entry)$ 
  and  $CFG.valid-path'$   $sourcenode$   $targetnode$   $kind$ 
    ( $valid-edge$   $wfp$ ) ( $get-return-edges$   $wfp$ ) ( $px,Exit$ )  $asx'$  ( $Main,Exit$ )
  by  $-(erule$   $Entry-to-Entry-and-Exit-to-Exit)$ +
  ultimately show  $?thesis$ 
  apply( $rule-tac$   $x=asx@as'$  in  $exI$ ) apply( $rule-tac$   $x=as@asx'$  in  $exI$ )
by( $auto$   $intro:ProcCFG.path-Append$   $ProcCFG.valid-path-same-level-path-Append$ 
   $ProcCFG.same-level-path-valid-path-Append$   $ProcCFG.intras-same-level-path$ 
   $simp:ProcCFG.vp-def$ )
next
  assume  $(p,n) = targetnode$   $a$ 
  with  $\langle (p', Entry) = targetnode$   $a \rangle[THEN$   $sym]$ 
  have [ $simp$ ]: $n = Entry$   $p = p'$  by  $simp-all$ 
  from  $\langle c \vdash Label$   $l - CEdge$   $(p', es', rets') \rightarrow_p$   $Label$   $l' \rangle$ 
  have  $containsCall$   $procs$   $c$   $\square$   $p'$  by( $rule$   $Proc-CFG-Call-containsCall$ )
  with  $\langle containsCall$   $procs$   $prog$   $ps$   $px \rangle$   $\langle (px, ins, outs, c) \in set$   $procs \rangle$ 
  have  $containsCall$   $procs$   $prog$   $(ps@[px])$   $p'$ 
    by( $rule$   $containsCall-in-proc$ )

```

```

with  $\langle p', ins', outs', c' \rangle \in set\ procs$ 
have  $prog, procs \vdash (p', Entry) -(\lambda s. False)_{\surd} \rightarrow (p', Exit)$ 
  by  $(fastforce\ intro: PCFG.Proc\ Proc-CFG-Entry-Exit)$ 
hence  $wfp \vdash (p', Entry) -[((p', Entry), (\lambda s. False)_{\surd}, (p', Exit))]$   $\rightarrow^* (p', Exit)$ 
  by  $(fastforce\ intro: ProcCFG.path.intros)$ 
   $simp: valid-edge-def\ ProcCFG.valid-node-def$ 
moreover
from  $\langle Rep-wf-prog\ wfp = (prog, procs) \rangle \langle p', ins', outs', c' \rangle \in set\ procs$ 
   $\langle containsCall\ procs\ prog\ (ps@[px])\ p' \rangle$ 
obtain  $as\ as'$  where  $CFG.valid-path'$   $sourcenode\ targetnode\ kind$ 
   $(valid-edge\ wfp)\ (get-return-edges\ wfp)\ (Main, Entry)\ as\ (p', Entry)$ 
  and  $CFG.valid-path'$   $sourcenode\ targetnode\ kind$ 
   $(valid-edge\ wfp)\ (get-return-edges\ wfp)\ (p', Exit)\ as'\ (Main, Exit)$ 
  by  $-(erule\ Entry-to-Entry-and-Exit-to-Exit)+$ 
ultimately show  $?thesis$ 
  apply  $(rule-tac\ x=as\ in\ exI)$ 
  apply  $(rule-tac\ x=[((p', Entry), (\lambda s. False)_{\surd}, (p', Exit))]$   $@as'\ in\ exI)$ 
  apply  $(unfold\ ProcCFG.vp-def)$ 
  by  $(fastforce\ intro: ProcCFG.path-Append)$ 
   $ProcCFG.same-level-path-valid-path-Append\ ProcCFG.intra-same-level-path$ 
   $simp: intra-kind-def)+$ 
qed
next
case  $(MainReturn\ l\ px\ es\ rets\ l'\ ins\ outs\ c)$ 
from  $disj\ show\ ?case$ 
proof
  assume  $(p, n) = sourcenode\ a$ 
  with  $\langle (px, Exit) = sourcenode\ a \rangle [THEN\ sym]$ 
  have  $[simp]: n = Exit\ p = px$  by  $simp-all$ 
  from  $\langle prog \vdash Label\ l - CEdge\ (px, es, rets) \rightarrow_p\ Label\ l' \rangle$ 
  have  $containsCall\ procs\ prog\ []\ px$  by  $(rule\ Proc-CFG-Call-containsCall)$ 
  with  $\langle (px, ins, outs, c) \in set\ procs \rangle$ 
  have  $prog, procs \vdash (px, Entry) -(\lambda s. False)_{\surd} \rightarrow (px, Exit)$ 
  by  $(fastforce\ intro: PCFG.Proc\ Proc-CFG-Entry-Exit)$ 
hence  $wfp \vdash (px, Entry) -[((px, Entry), (\lambda s. False)_{\surd}, (px, Exit))]$   $\rightarrow^* (px, Exit)$ 
  by  $(fastforce\ intro: ProcCFG.path.intros)$ 
   $simp: valid-edge-def\ ProcCFG.valid-node-def$ 
moreover
from  $\langle Rep-wf-prog\ wfp = (prog, procs) \rangle \langle (px, ins, outs, c) \in set\ procs \rangle$ 
   $\langle containsCall\ procs\ prog\ []\ px \rangle$ 
obtain  $as\ as'$  where  $CFG.valid-path'$   $sourcenode\ targetnode\ kind$ 
   $(valid-edge\ wfp)\ (get-return-edges\ wfp)\ (Main, Entry)\ as\ (px, Entry)$ 
  and  $CFG.valid-path'$   $sourcenode\ targetnode\ kind$ 
   $(valid-edge\ wfp)\ (get-return-edges\ wfp)\ (px, Exit)\ as'\ (Main, Exit)$ 
  by  $-(erule\ Entry-to-Entry-and-Exit-to-Exit)+$ 
ultimately show  $?thesis$ 
  apply  $(rule-tac\ x=as@[((px, Entry), (\lambda s. False)_{\surd}, (px, Exit))]$   $in\ exI)$ 
  apply  $(rule-tac\ x=as'\ in\ exI)$ 
  apply  $(unfold\ ProcCFG.vp-def)$ 

```

```

    by(fastforce intro:ProcCFG.path-Append
      ProcCFG.valid-path-same-level-path-Append ProcCFG.intra-same-level-path
        simp:intra-kind-def)+
next
  assume (p, n) = targetnode a
  with ⟨(Main, Label l') = targetnode a⟩[THEN sym]
  have [simp]:n = Label l' p = Main by simp-all
  with ⟨prog ⊢ Label l - CEdge (px, es, rets)→p Label l'⟩ have l' < #:prog
    by(fastforce intro:Proc-CFG-targetlabel-less-num-nodes)
  with ⟨Rep-wf-prog wfp = (prog,procs)⟩
  obtain as as' where wfp ⊢ (Main,Entry) -as→* (Main,Label l')
    and ∀ a ∈ set as. intra-kind (kind a)
    and wfp ⊢ (Main,Label l') -as'→* (Main,Exit)
    and ∀ a ∈ set as'. intra-kind (kind a)
  by -(erule Label-Proc-CFG-Entry-Exit-path-Main)+
  thus ?thesis
  by(fastforce intro:ProcCFG.intra-path-vp simp:ProcCFG.intra-path-def)
qed
next
  case (ProcReturn px ins outs c l p' es' rets' l' ins' outs' c' ps)
  from disj show ?case
  proof
    assume (p,n) = sourcenode a
    with ⟨(p', Exit) = sourcenode a⟩[THEN sym]
    have [simp]:n = Exit p = p' by simp-all
    from ⟨c ⊢ Label l - CEdge (p', es', rets')→p Label l'⟩
    have containsCall procs c [] p' by(rule Proc-CFG-Call-containsCall)
    with ⟨containsCall procs prog ps px⟩ ⟨(px, ins, outs, c) ∈ set procs⟩
    have containsCall procs prog (ps@[px]) p'
      by(rule containsCall-in-proc)
    with ⟨(p', ins', outs', c') ∈ set procs⟩
    have prog.procs ⊢ (p', Entry) - (λs. False)✓→ (p', Exit)
      by(fastforce intro:PCFG.Proc Proc-CFG-Entry-Exit)
    hence wfp ⊢ (p',Entry) -[((p',Entry),(λs. False)✓,(p',Exit))]✓→* (p',Exit)
      by(fastforce intro:ProcCFG.path.intros
        simp:valid-edge-def ProcCFG.valid-node-def)
  moreover
  from ⟨Rep-wf-prog wfp = (prog,procs)⟩ ⟨(p', ins', outs', c') ∈ set procs⟩
  ⟨containsCall procs prog (ps@[px]) p'⟩
  obtain as as' where CFG.valid-path' sourcenode targetnode kind
    (valid-edge wfp) (get-return-edges wfp) (Main,Entry) as (p',Entry)
    and CFG.valid-path' sourcenode targetnode kind
    (valid-edge wfp) (get-return-edges wfp) (p',Exit) as' (Main,Exit)
  by -(erule Entry-to-Entry-and-Exit-to-Exit)+
  ultimately show ?thesis
    apply(rule-tac x=as@[((p',Entry),(λs. False)✓,(p',Exit))]) in exI
    apply(rule-tac x=as' in exI)
    apply(unfold ProcCFG.vp-def)
    by(fastforce intro:ProcCFG.path-Append

```

```

    ProcCFG.valid-path-same-level-path-Append ProcCFG.intras-same-level-path
    simp:intra-kind-def)+
next
  assume (p, n) = targetnode a
  with ⟨(px, Label l') = targetnode a⟩[THEN sym]
  have [simp]:n = Label l' p = px by simp-all
  with ⟨c ⊢ Label l - CEdge (p', es', rets') →p Label l'⟩ have l' < #:c
    by(fastforce intro:Proc-CFG-targetlabel-less-num-nodes)
  from ⟨Rep-wf-prog wfp = (prog,procs)⟩ ⟨l' < #:c⟩
    ⟨containsCall procs prog ps px⟩ ⟨(px, ins, outs, c) ∈ set procs⟩
  obtain as as' where wfp ⊢ (px,Label l') -as→* (px,Exit)
    and ∀ a ∈ set as. intra-kind (kind a)
    and wfp ⊢ (px,Entry) -as'→* (px,Label l')
    and ∀ a ∈ set as'. intra-kind (kind a)
    by -(erule Label-Proc-CFG-Entry-Exit-path-Proc)+
  moreover
  from ⟨Rep-wf-prog wfp = (prog,procs)⟩ ⟨containsCall procs prog ps px⟩
    ⟨(px, ins, outs, c) ∈ set procs⟩ obtain asx asx'
    where CFG.valid-path' sourcenode targetnode kind
      (valid-edge wfp) (get-return-edges wfp) (Main,Entry) asx (px,Entry)
    and CFG.valid-path' sourcenode targetnode kind
      (valid-edge wfp) (get-return-edges wfp) (px,Exit) asx' (Main,Exit)
    by -(erule Entry-to-Entry-and-Exit-to-Exit)+
  ultimately show ?thesis
    apply(rule-tac x=asx@as' in exI) apply(rule-tac x=as@asx' in exI)
  by(auto intro:ProcCFG.path-Append ProcCFG.valid-path-same-level-path-Append
    ProcCFG.same-level-path-valid-path-Append ProcCFG.intras-same-level-path
    simp:ProcCFG.vp-def)
qed
next
  case (MainCallReturn nx px es rets nx')
  from ⟨prog ⊢ nx - CEdge (px, es, rets) →p nx'⟩ disj
    ⟨(Main, nx) = sourcenode a⟩[THEN sym] ⟨(Main, nx') = targetnode a⟩[THEN
sym]
  obtain l where [simp]:n = Label l p = Main
    by(fastforce dest:Proc-CFG-Call-Labels)
  from ⟨prog ⊢ nx - CEdge (px, es, rets) →p nx'⟩ disj
    ⟨(Main, nx) = sourcenode a⟩[THEN sym] ⟨(Main, nx') = targetnode a⟩[THEN
sym]
  have l < #:prog by(auto intro:Proc-CFG-sourcelabel-less-num-nodes
    Proc-CFG-targetlabel-less-num-nodes)
  with ⟨Rep-wf-prog wfp = (prog,procs)⟩
  obtain as as' where wfp ⊢ (Main,Entry) -as→* (Main,Label l)
    and ∀ a ∈ set as. intra-kind (kind a)
    and wfp ⊢ (Main,Label l) -as'→* (Main,Exit)
    and ∀ a ∈ set as'. intra-kind (kind a)
    by -(erule Label-Proc-CFG-Entry-Exit-path-Main)+
  thus ?thesis
    apply(rule-tac x=as in exI) apply(rule-tac x=as' in exI) apply simp

```



```

    by(fastforce intro:ProcCFG.intra-path-vp simp:ProcCFG.intra-path-def)
  next
  case (ProcCallReturn px ins outs c nx p' es' rets' nx' ps)
  from ⟨(px, ins, outs, c) ∈ set procs⟩ wf have [simp]:px ≠ Main by auto
  from ⟨c ⊢ nx - CEdge (p', es', rets') →p nx'⟩ disj
  ⟨(px, nx) = sourcenode a⟩ [THEN sym] ⟨(px, nx') = targetnode a⟩ [THEN sym]
  obtain l where [simp]:n = Label l p = px
  by(fastforce dest:Proc-CFG-Call-Labels)
  from ⟨c ⊢ nx - CEdge (p', es', rets') →p nx'⟩ disj
  ⟨(px, nx) = sourcenode a⟩ [THEN sym] ⟨(px, nx') = targetnode a⟩ [THEN sym]
  have l < #:c
  by(auto intro:Proc-CFG-sourcelabel-less-num-nodes
    Proc-CFG-targetlabel-less-num-nodes)
  with ⟨Rep-wf-prog wfp = (prog,procs)⟩ ⟨(px, ins, outs, c) ∈ set procs⟩
  ⟨containsCall procs prog ps px⟩
  obtain as as' where wfp ⊢ (px,Entry) -as→* (px,Label l)
  and ∀ a ∈ set as. intra-kind (kind a)
  and wfp ⊢ (px,Label l) -as'→* (px,Exit)
  and ∀ a ∈ set as'. intra-kind (kind a)
  by -(erule Label-Proc-CFG-Entry-Exit-path-Proc)+
  moreover
  from ⟨Rep-wf-prog wfp = (prog,procs)⟩
  ⟨containsCall procs prog ps px⟩ ⟨(px, ins, outs, c) ∈ set procs⟩
  obtain asx asx' where CFG.valid-path' sourcenode targetnode kind
  (valid-edge wfp) (get-return-edges wfp) (Main,Entry) asx (px,Entry)
  and CFG.valid-path' sourcenode targetnode kind
  (valid-edge wfp) (get-return-edges wfp) (px,Exit) asx' (Main,Exit)
  by -(erule Entry-to-Entry-and-Exit-to-Exit)+
  ultimately show ?thesis
  apply(rule-tac x=asx@as in exI) apply(rule-tac x=as'@asx' in exI)
  by(auto intro:ProcCFG.path-Append ProcCFG.valid-path-same-level-path-Append
    ProcCFG.same-level-path-valid-path-Append ProcCFG.intra-same-level-path
    simp:ProcCFG.vp-def)
  qed
  qed

```

2.8.4 Instantiating the *Postdomination* locale

interpretation *ProcPostdomination*:

```

  Postdomination sourcenode targetnode kind valid-edge wfp (Main,Entry)
  get-proc get-return-edges wfp lift-procs wfp Main (Main,Exit)
  for wfp

```

proof –

```

  from Rep-wf-prog[of wfp]
  obtain prog procs where [simp]:Rep-wf-prog wfp = (prog,procs)
  by(fastforce simp:wfp-prog-def)
  hence wf:well-formed procs by(fastforce intro:wfp-wf-prog)
  show Postdomination sourcenode targetnode kind (valid-edge wfp)
  (Main, Entry) get-proc (get-return-edges wfp) (lift-procs wfp) Main (Main, Exit)

```

```

proof
  fix  $m$ 
  assume  $CFG.valid\text{-}node\ sourcenode\ targetnode\ (valid\text{-}edge\ wfp)\ m$ 
  then obtain  $a$  where  $valid\text{-}edge\ wfp\ a$ 
    and  $m = sourcenode\ a \vee m = targetnode\ a$ 
    by( $fastforce\ simp:ProcCFG.valid\text{-}node\text{-}def$ )
  obtain  $p\ n$  where  $[simp]:m = (p,n)$  by( $cases\ m$ ) auto
  from  $\langle valid\text{-}edge\ wfp\ a \rangle \langle m = sourcenode\ a \vee m = targetnode\ a \rangle$ 
     $\langle Rep\text{-}wf\text{-}prog\ wfp = (prog,procs) \rangle$ 
  show  $\exists as. CFG.valid\text{-}path'\ sourcenode\ targetnode\ kind\ (valid\text{-}edge\ wfp)$ 
     $(get\text{-}return\text{-}edges\ wfp)\ (Main, Entry)\ as\ m$ 
    by( $auto\ dest!:edge\text{-}valid\text{-}paths\ simp:valid\text{-}edge\text{-}def$ )
next
  fix  $m$ 
  assume  $CFG.valid\text{-}node\ sourcenode\ targetnode\ (valid\text{-}edge\ wfp)\ m$ 
  then obtain  $a$  where  $valid\text{-}edge\ wfp\ a$ 
    and  $m = sourcenode\ a \vee m = targetnode\ a$ 
    by( $fastforce\ simp:ProcCFG.valid\text{-}node\text{-}def$ )
  obtain  $p\ n$  where  $[simp]:m = (p,n)$  by( $cases\ m$ ) auto
  from  $\langle valid\text{-}edge\ wfp\ a \rangle \langle m = sourcenode\ a \vee m = targetnode\ a \rangle$ 
     $\langle Rep\text{-}wf\text{-}prog\ wfp = (prog,procs) \rangle$ 
  show  $\exists as. CFG.valid\text{-}path'\ sourcenode\ targetnode\ kind\ (valid\text{-}edge\ wfp)$ 
     $(get\text{-}return\text{-}edges\ wfp)\ m\ as\ (Main,Exit)$ 
    by( $auto\ dest!:edge\text{-}valid\text{-}paths\ simp:valid\text{-}edge\text{-}def$ )
next
  fix  $n\ n'$ 
  assume  $mex1:CFGExit.method\text{-}exit\ sourcenode\ kind\ (valid\text{-}edge\ wfp)\ (Main,Exit)$ 
 $n$ 
  and  $mex2:CFGExit.method\text{-}exit\ sourcenode\ kind\ (valid\text{-}edge\ wfp)\ (Main,Exit)$ 
 $n'$ 
  and  $get\text{-}proc\ n = get\text{-}proc\ n'$ 
  from  $mex1$ 
  have  $n = (Main,Exit) \vee (\exists a\ Q\ p\ f. n = sourcenode\ a \wedge valid\text{-}edge\ wfp\ a \wedge$ 
     $kind\ a = Q \leftrightarrow pf)$  by( $simp\ add:ProcCFGExit.method\text{-}exit\text{-}def$ )
  thus  $n = n'$ 
  proof
    assume  $n = (Main,Exit)$ 
    from  $mex2$  have  $n' = (Main,Exit) \vee (\exists a\ Q\ p\ f. n' = sourcenode\ a \wedge$ 
       $valid\text{-}edge\ wfp\ a \wedge kind\ a = Q \leftrightarrow pf)$ 
    by( $simp\ add:ProcCFGExit.method\text{-}exit\text{-}def$ )
    thus ?thesis
  proof
    assume  $n' = (Main,Exit)$ 
    with  $\langle n = (Main,Exit) \rangle$  show ?thesis by  $simp$ 
  next
  assume  $\exists a\ Q\ p\ f. n' = sourcenode\ a \wedge$ 
     $valid\text{-}edge\ wfp\ a \wedge kind\ a = Q \leftrightarrow pf$ 
  then obtain  $a\ Q\ p\ f$  where  $n' = sourcenode\ a$ 
    and  $valid\text{-}edge\ wfp\ a$  and  $kind\ a = Q \leftrightarrow pf$  by  $blast$ 

```

```

    from ⟨valid-edge wfp a⟩ ⟨kind a =  $Q \leftrightarrow pf$ ⟩
    have get-proc (sourcenode a) = p by(rule ProcCFG.get-proc-return)
    with ⟨get-proc n = get-proc n'⟩ ⟨n = (Main,Exit)⟩ ⟨n' = sourcenode a⟩
    have get-proc (Main,Exit) = p by simp
    hence p = Main by simp
    with ⟨kind a =  $Q \leftrightarrow pf$ ⟩ have kind a =  $Q \leftrightarrow Mainf$  by simp
    with ⟨valid-edge wfp a⟩ have False by(rule ProcCFG.Main-no-return-source)
    thus ?thesis by simp
  qed
next
assume  $\exists a Q p f. n = sourcenode a \wedge$ 
  valid-edge wfp a  $\wedge$  kind a =  $Q \leftrightarrow pf$ 
then obtain a Q p f where n = sourcenode a
  and valid-edge wfp a and kind a =  $Q \leftrightarrow pf$  by blast
from ⟨valid-edge wfp a⟩ ⟨kind a =  $Q \leftrightarrow pf$ ⟩
have get-proc (sourcenode a) = p by(rule ProcCFG.get-proc-return)
from mex2 have  $n' = (Main,Exit) \vee (\exists a Q p f. n' = sourcenode a \wedge$ 
  valid-edge wfp a  $\wedge$  kind a =  $Q \leftrightarrow pf$ )
  by(simp add:ProcCFGExit.method-exit-def)
thus ?thesis
proof
  assume  $n' = (Main,Exit)$ 
  from ⟨get-proc (sourcenode a) = p⟩ ⟨get-proc n = get-proc n'⟩
    ⟨ $n' = (Main,Exit)$ ⟩ ⟨n = sourcenode a⟩
  have get-proc (Main,Exit) = p by simp
  hence p = Main by simp
  with ⟨kind a =  $Q \leftrightarrow pf$ ⟩ have kind a =  $Q \leftrightarrow Mainf$  by simp
  with ⟨valid-edge wfp a⟩ have False by(rule ProcCFG.Main-no-return-source)
  thus ?thesis by simp
next
assume  $\exists a Q p f. n' = sourcenode a \wedge$ 
  valid-edge wfp a  $\wedge$  kind a =  $Q \leftrightarrow pf$ 
then obtain a' Q' p' f' where  $n' = sourcenode a'$ 
  and valid-edge wfp a' and kind a' =  $Q' \leftrightarrow_p f'$  by blast
from ⟨valid-edge wfp a'⟩ ⟨kind a' =  $Q' \leftrightarrow_p f'$ ⟩
have get-proc (sourcenode a') = p' by(rule ProcCFG.get-proc-return)
with ⟨get-proc n = get-proc n'⟩ ⟨get-proc (sourcenode a) = p⟩
  ⟨n = sourcenode a⟩ ⟨ $n' = sourcenode a'$ ⟩
have  $p' = p$  by simp
from ⟨valid-edge wfp a⟩ ⟨kind a =  $Q \leftrightarrow pf$ ⟩
have sourcenode a = (p,Exit) by(auto elim:PCFG.cases simp:valid-edge-def)
  from ⟨valid-edge wfp a'⟩ ⟨kind a' =  $Q' \leftrightarrow_p f'$ ⟩
have sourcenode a' = (p',Exit) by(auto elim:PCFG.cases simp:valid-edge-def)
  with ⟨n = sourcenode a⟩ ⟨ $n' = sourcenode a'$ ⟩ ⟨ $p' = p$ ⟩
    ⟨sourcenode a = (p,Exit)⟩ show ?thesis by simp
  qed
qed
qed
qed
qed

```

end

Instantiation of the SDG locale **theory ProcSDG imports ValidPaths ../StaticInter/SDG**
begin

interpretation Proc-SDG:

SDG sourcenode targetnode kind valid-edge wfp (Main,Entry)

get-proc get-return-edges wfp lift-procs wfp Main (Main,Exit)

Def wfp Use wfp ParamDefs wfp ParamUses wfp

for wfp ..

end

Chapter 3

A Control Flow Graph for Jinja Byte Code

3.1 Formalizing the CFG

```
theory JVMCFG imports ../StaticInter/BasicDefs ../../Jinja/BV/BVExample
begin
```

```
declare lesub-list-impl-same-size [simp del]
declare listE-length [simp del]
```

3.1.1 Type definitions

Wellformed Programs

```
definition wf-jvmprog = {(P, Phi). wf-jvm-prog Phi P}
```

```
typedef wf-jvmprog = wf-jvmprog
```

```
proof
```

```
  show (E, Phi) ∈ wf-jvmprog
    unfolding wf-jvmprog-def by (auto intro: wf-prog)
qed
```

```
hide-const Phi E
```

```
abbreviation PROG :: wf-jvmprog ⇒ jvm-prog
  where PROG P ≡ fst(Rep-wf-jvmprog(P))
```

```
abbreviation TYPING :: wf-jvmprog ⇒ tyP
  where TYPING P ≡ snd(Rep-wf-jvmprog(P))
```

```
lemma wf-jvmprog-is-wf-typ: wf-jvm-prog TYPING P (PROG P)
using Rep-wf-jvmprog [of P]
  by (auto simp: wf-jvmprog-def split-beta)
```

lemma *wf-jvmprog-is-wf*: *wf-jvm-prog* (*PROG P*)
using *wf-jvmprog-is-wf-typ* **unfolding** *wf-jvm-prog-def*
by *blast*

Interprocedural CFG

type-synonym *jvm-method* = *wf-jvmprog* × *cname* × *mname*
datatype *var* = *Heap* | *Local nat* | *Stack nat* | *Exception*
datatype *val* = *Hp heap* | *Value Value.val*

type-synonym *state* = *var* → *val*

definition *valid-state* :: *state* ⇒ *bool*
where *valid-state s* ≡ (∀ *val*. *s Heap* ≠ *Some (Value val)*)
 ∧ (*s Exception* = *None* ∨ (∃ *addr*. *s Exception* = *Some (Value (Addr addr))*))
 ∧ (∀ *var*. *var* ≠ *Heap* ∧ *var* ≠ *Exception* → (∀ *h*. *s var* ≠ *Some (Hp h)*))

fun *the-Heap* :: *val* ⇒ *heap*
where *the-Heap (Hp h)* = *h*

fun *the-Value* :: *val* ⇒ *Value.val*
where *the-Value (Value v)* = *v*

abbreviation *heap-of* :: *state* ⇒ *heap*
where *heap-of s* ≡ *the-Heap (the (s Heap))*

abbreviation *exc-flag* :: *state* ⇒ *addr option*
where *exc-flag s* ≡ *case (s Exception) of None* ⇒ *None*
 | *Some v* ⇒ *Some (THE a. v = Value (Addr a))*

abbreviation *stkAt* :: *state* ⇒ *nat* ⇒ *Value.val*
where *stkAt s n* ≡ *the-Value (the (s (Stack n)))*

abbreviation *locAt* :: *state* ⇒ *nat* ⇒ *Value.val*
where *locAt s n* ≡ *the-Value (the (s (Local n)))*

datatype *nodeType* = *Enter* | *Normal* | *Return* | *Exceptional pc option nodeType*
type-synonym *cfg-node* = *cname* × *mname* × *pc option* × *nodeType*

type-synonym
cfg-edge = *cfg-node* × (*var*, *val*, *cname* × *mname* × *pc*, *cname* × *mname*)
edge-kind × *cfg-node*

definition *ClassMain* :: *wf-jvmprog* ⇒ *cname*
where *ClassMain P* ≡ *SOME Name. ¬ is-class (PROG P) Name*

definition *MethodMain* :: *wf-jvmprog* ⇒ *mname*
where *MethodMain P* ≡ *SOME Name.*
 ∀ *C D fs ms*. *class (PROG P) C* = [(*D*, *fs*, *ms*)] → (∀ *m* ∈ *set ms*. *Name* ≠

fst m)

definition *stkLength* :: *jvm-method* \Rightarrow *pc* \Rightarrow *nat*
where
stkLength m pc \equiv *let* (*P*, *C*, *M*) = *m* *in* (
if (*C* = *ClassMain P*) *then* 1 *else* (
 length (*fst*(*the*((*TYPING P*) *C* *M*) ! *pc*)))
))

definition *locLength* :: *jvm-method* \Rightarrow *pc* \Rightarrow *nat*
where
locLength m pc \equiv *let* (*P*, *C*, *M*) = *m* *in* (
if (*C* = *ClassMain P*) *then* 1 *else* (
 length (*snd*(*the*((*TYPING P*) *C* *M*) ! *pc*)))
))

lemma *ex-new-class-name*: $\exists C. \neg$ *is-class P C*

proof –

have \neg *finite* (*UNIV* :: *cname set*)

by (*rule infinite-UNIV-listI*)

hence $\exists C. C \notin$ *set* (*map fst P*)

by –(*rule ex-new-if-finite, auto*)

then obtain *C* **where** *C* \notin *set* (*map fst P*)

by *blast*

have \neg *is-class P C*

proof

assume *is-class P C*

then obtain *D fs ms* **where** *class P C* = [*D*, *fs*, *ms*]

by *auto*

with $\langle C \notin$ *set* (*map fst P*) \rangle **show** *False*

by (*auto dest: map-of-SomeD intro!: image-eqI simp: class-def*)

qed

thus *?thesis*

by *blast*

qed

lemma *ClassMain-unique-in-P*:

assumes *is-class* (*PROG P*) *C*

shows *ClassMain P* \neq *C*

proof –

from *ex-new-class-name* [*of PROG P*] **obtain** *D* **where** \neg *is-class* (*PROG P*)

D

by *blast*

with \langle *is-class* (*PROG P*) *C* \rangle **show** *?thesis*

unfolding *ClassMain-def*

by –(*rule someI2, fastforce+*)

qed

lemma *map-of-fstD*: \llbracket *map-of xs a* = [*b*]; $\forall x \in$ *set xs. fst x* \neq *a* $\rrbracket \Longrightarrow$ *False*

by (induct xs, auto)

lemma map-of-fstE: $\llbracket \text{map-of } xs \ a = \lfloor b \rfloor; \exists x \in \text{set } xs. \text{fst } x = a \implies \text{thesis} \rrbracket \implies \text{thesis}$

by (induct xs) (auto split: split-if-asm)

lemma ex-unique-method-name:

$\exists \text{Name}. \forall C \ D \ fs \ ms. \text{class } (\text{PROG } P) \ C = \lfloor (D, fs, ms) \rfloor \longrightarrow (\forall m \in \text{set } ms. \text{Name} \neq \text{fst } m)$

proof –

from wf-jvmprog-is-wf [of P]

have distinct-fst (PROG P)

by (simp add: wf-jvm-prog-def wf-jvm-prog-phi-def wf-prog-def)

hence $\{C. \exists D \ fs \ ms. \text{class } (\text{PROG } P) \ C = \lfloor (D, fs, ms) \rfloor\} = \text{fst } ' \text{set } (\text{PROG } P)$

by (fastforce elim: map-of-fstE simp: class-def intro: map-of-SomeI)

hence finite $\{C. \exists D \ fs \ ms. \text{class } (\text{PROG } P) \ C = \lfloor (D, fs, ms) \rfloor\}$

by auto

moreover have $\{ms. \exists C \ D \ fs. \text{class } (\text{PROG } P) \ C = \lfloor (D, fs, ms) \rfloor\} = \text{snd } ' \text{snd } ' \text{the } ' (\lambda C. \text{class } (\text{PROG } P) \ C) ' \{C. \exists D \ fs \ ms. \text{class } (\text{PROG } P) \ C = \lfloor (D, fs, ms) \rfloor\}$

by (fastforce intro: rev-image-eqI map-of-SomeI simp: class-def)

ultimately have finite $\{ms. \exists C \ D \ fs. \text{class } (\text{PROG } P) \ C = \lfloor (D, fs, ms) \rfloor\}$

by auto

moreover have $\neg \text{finite } (\text{UNIV} :: \text{mname set})$

by (rule infinite-UNIV-listI)

ultimately

have $\exists \text{Name}. \text{Name} \notin \text{fst } ' (\bigcup ms \in \{ms. \exists C \ D \ fs. \text{class } (\text{PROG } P) \ C = \lfloor (D, fs, ms) \rfloor\}. \text{set } ms)$

by \neg (rule ex-new-if-finite, auto)

thus ?thesis

by fastforce

qed

lemma MethodMain-unique-in-P:

assumes $\text{PROG } P \vdash D \text{ sees } M:Ts \rightarrow T = mb \text{ in } C$

shows $\text{MethodMain } P \neq M$

proof –

from ex-unique-method-name [of P] obtain M'

where $\bigwedge C \ D \ fs \ ms. \text{class } (\text{PROG } P) \ C = \lfloor (D, fs, ms) \rfloor \implies (\forall m \in \text{set } ms. M' \neq \text{fst } m)$

by blast

with $\langle \text{PROG } P \vdash D \text{ sees } M:Ts \rightarrow T = mb \text{ in } C \rangle$

show ?thesis

unfolding MethodMain-def

by \neg (rule someI2-ex, fastforce, fastforce dest!: visible-method-exists elim: map-of-fstE)

qed

lemma ClassMain-is-no-class [dest!]: $\text{is-class } (\text{PROG } P) (\text{ClassMain } P) \implies \text{False}$

proof (*erule rev-notE*)
from *ex-new-class-name* [of *PROG P*] **obtain** *C* **where** \neg *is-class* (*PROG P*)
C
by *blast*
thus \neg *is-class* (*PROG P*) (*ClassMain P*) **unfolding** *ClassMain-def*
by (*rule someI*)
qed

lemma *MethodMain-not-seen* [*dest!*]: *PROG P* \vdash *C* *sees* (*MethodMain P*):*Ts* \rightarrow *T*
 $=$ *mb in D* \implies *False*
by (*fastforce dest: MethodMain-unique-in-P*)

lemma *no-Call-from-ClassMain* [*dest!*]: *PROG P* \vdash *ClassMain P* *sees* *M*:*Ts* \rightarrow *T*
 $=$ *mb in C* \implies *False*
by (*fastforce dest: sees-method-is-class*)

lemma *no-Call-in-ClassMain* [*dest!*]: *PROG P* \vdash *C* *sees* *M*:*Ts* \rightarrow *T* $=$ *mb in ClassMain P* \implies *False*
by (*fastforce dest: sees-method-idemp*)

inductive *JVMCFG* :: *jvm-method* \Rightarrow *cfg-node* \Rightarrow (*var*, *val*, *cname* \times *mname* \times *pc*, *cname* \times *mname*) *edge-kind* \Rightarrow *cfg-node* \Rightarrow *bool* ($- \vdash - \dashrightarrow -$)

and *reachable* :: *jvm-method* \Rightarrow *cfg-node* \Rightarrow *bool* ($- \vdash \Rightarrow -$)

where

Entry-reachable: (*P*, *C0*, *Main*) $\vdash \Rightarrow$ (*ClassMain P*, *MethodMain P*, *None*, *Enter*)

| *reachable-step*: $\llbracket P \vdash \Rightarrow n; P \vdash n \text{ --}(e)\text{--} n' \rrbracket \implies P \vdash \Rightarrow n'$

| *Main-to-Call*: (*P*, *C0*, *Main*) $\vdash \Rightarrow$ (*ClassMain P*, *MethodMain P*, $\lfloor 0 \rfloor$, *Enter*)

\implies (*P*, *C0*, *Main*) \vdash (*ClassMain P*, *MethodMain P*, $\lfloor 0 \rfloor$, *Enter*) \dashrightarrow (*ClassMain P*, *MethodMain P*, $\lfloor 0 \rfloor$, *Normal*)

| *Main-Call-LFalse*: (*P*, *C0*, *Main*) $\vdash \Rightarrow$ (*ClassMain P*, *MethodMain P*, $\lfloor 0 \rfloor$, *Normal*)

\implies (*P*, *C0*, *Main*) \vdash (*ClassMain P*, *MethodMain P*, $\lfloor 0 \rfloor$, *Normal*) \dashrightarrow ($\lambda s. \text{False}$) $\checkmark \rightarrow$ (*ClassMain P*, *MethodMain P*, $\lfloor 0 \rfloor$, *Return*)

| *Main-Call*: $\llbracket (P, C0, Main) \vdash \Rightarrow (ClassMain P, MethodMain P, \lfloor 0 \rfloor, Normal);$

PROG P \vdash *C0* *sees* *Main*: $\lfloor \rfloor \rightarrow T = (mxs, mxl_0, is, xt)$ *in D*;

initParams = $\llbracket (\lambda s. s \text{ Heap}), (\lambda s. \lfloor Value Null \rfloor) \rrbracket$;

ek = $(\lambda (s, ret). \text{True}): (ClassMain P, MethodMain P, 0) \hookrightarrow (D, Main) \text{initParams}$

\rrbracket

\implies (*P*, *C0*, *Main*) \vdash (*ClassMain P*, *MethodMain P*, $\lfloor 0 \rfloor$, *Normal*) \dashrightarrow (*D*, *Main*, *None*, *Enter*)

| *Main-Return-to-Exit*: (*P*, *C0*, *Main*) $\vdash \Rightarrow$ (*ClassMain P*, *MethodMain P*, $\lfloor 0 \rfloor$, *Return*)

\implies (*P*, *C0*, *Main*) \vdash (*ClassMain P*, *MethodMain P*, $\lfloor 0 \rfloor$, *Return*) \dashrightarrow ($\uparrow id$) \rightarrow (*ClassMain P*, *MethodMain P*, *None*, *Return*)

| *Method-LFalse*: (*P*, *C0*, *Main*) $\vdash \Rightarrow$ (*C*, *M*, *None*, *Enter*)

\implies (*P*, *C0*, *Main*) \vdash (*C*, *M*, *None*, *Enter*) \dashrightarrow ($\lambda s. \text{False}$) $\checkmark \rightarrow$ (*C*, *M*, *None*, *Return*)

| *Method-LTrue*: (*P*, *C0*, *Main*) $\vdash \Rightarrow$ (*C*, *M*, *None*, *Enter*)

$\implies (P, C0, Main) \vdash (C, M, None, Enter) -(\lambda s. True)_{\surd} \rightarrow (C, M, [0], Enter)$
| CFG-Load: $\llbracket C \neq \text{ClassMain } P; (P, C0, Main) \vdash \Rightarrow (C, M, [pc], Enter);$
instrs-of (PROG P) C M ! pc = Load n;
 $ek = \uparrow(\lambda s. s(\text{Stack } (\text{stkLength } (P, C, M) \text{ pc}) := s(\text{Local } n))) \rrbracket$
 $\implies (P, C0, Main) \vdash (C, M, [pc], Enter) -(\lambda s. True)_{\surd} \rightarrow (C, M, [Suc \text{ pc}], Enter)$
| CFG-Store: $\llbracket C \neq \text{ClassMain } P; (P, C0, Main) \vdash \Rightarrow (C, M, [pc], Enter);$
instrs-of (PROG P) C M ! pc = Store n;
 $ek = \uparrow(\lambda s. s(\text{Local } n := s(\text{Stack } (\text{stkLength } (P, C, M) \text{ pc} - 1)))) \rrbracket$
 $\implies (P, C0, Main) \vdash (C, M, [pc], Enter) -(\lambda s. True)_{\surd} \rightarrow (C, M, [Suc \text{ pc}], Enter)$
| CFG-Push: $\llbracket C \neq \text{ClassMain } P; (P, C0, Main) \vdash \Rightarrow (C, M, [pc], Enter);$
instrs-of (PROG P) C M ! pc = Push v;
 $ek = \uparrow(\lambda s. s(\text{Stack } (\text{stkLength } (P, C, M) \text{ pc}) \mapsto \text{Value } v)) \rrbracket$
 $\implies (P, C0, Main) \vdash (C, M, [pc], Enter) -(\lambda s. True)_{\surd} \rightarrow (C, M, [Suc \text{ pc}], Enter)$
| CFG-Pop: $\llbracket C \neq \text{ClassMain } P; (P, C0, Main) \vdash \Rightarrow (C, M, [pc], Enter);$
instrs-of (PROG P) C M ! pc = Pop;
 $ek = \uparrow id \rrbracket$
 $\implies (P, C0, Main) \vdash (C, M, [pc], Enter) -(\lambda s. True)_{\surd} \rightarrow (C, M, [Suc \text{ pc}], Enter)$
| CFG-IAdd: $\llbracket C \neq \text{ClassMain } P; (P, C0, Main) \vdash \Rightarrow (C, M, [pc], Enter);$
instrs-of (PROG P) C M ! pc = IAdd;
 $ek = \uparrow(\lambda s. \text{let } i1 = \text{the-Intg } (\text{stkAt } s(\text{stkLength } (P, C, M) \text{ pc} - 1));$
 $i2 = \text{the-Intg } (\text{stkAt } s(\text{stkLength } (P, C, M) \text{ pc} - 2))$
 $\text{in } s(\text{Stack } (\text{stkLength } (P, C, M) \text{ pc} - 2) \mapsto \text{Value } (\text{Intg } (i1 + i2)))) \rrbracket$
 $\implies (P, C0, Main) \vdash (C, M, [pc], Enter) -(\lambda s. True)_{\surd} \rightarrow (C, M, [Suc \text{ pc}], Enter)$
| CFG-Goto: $\llbracket C \neq \text{ClassMain } P; (P, C0, Main) \vdash \Rightarrow (C, M, [pc], Enter);$
instrs-of (PROG P) C M ! pc = Goto i
 $\implies (P, C0, Main) \vdash (C, M, [pc], Enter) -((\lambda s. True)_{\surd}) \rightarrow (C, M, [nat (\text{int } pc + i)], Enter)$
| CFG-CmpEq: $\llbracket C \neq \text{ClassMain } P; (P, C0, Main) \vdash \Rightarrow (C, M, [pc], Enter);$
instrs-of (PROG P) C M ! pc = CmpEq;
 $ek = \uparrow(\lambda s. \text{let } e1 = \text{stkAt } s(\text{stkLength } (P, C, M) \text{ pc} - 1);$
 $e2 = \text{stkAt } s(\text{stkLength } (P, C, M) \text{ pc} - 2)$
 $\text{in } s(\text{Stack } (\text{stkLength } (P, C, M) \text{ pc} - 2) \mapsto \text{Value } (\text{Bool } (e1 = e2)))) \rrbracket$
 $\implies (P, C0, Main) \vdash (C, M, [pc], Enter) -(\lambda s. True)_{\surd} \rightarrow (C, M, [Suc \text{ pc}], Enter)$
| CFG-IfFalse-False: $\llbracket C \neq \text{ClassMain } P; (P, C0, Main) \vdash \Rightarrow (C, M, [pc],$
Enter);
instrs-of (PROG P) C M ! pc = IfFalse i;
 $i \neq 1;$
 $ek = (\lambda s. \text{stkAt } s(\text{stkLength } (P, C, M) \text{ pc} - 1) = \text{Bool } False)_{\surd} \rrbracket$
 $\implies (P, C0, Main) \vdash (C, M, [pc], Enter) -(\lambda s. True)_{\surd} \rightarrow (C, M, [nat (\text{int } pc + i)],$
Enter);
| CFG-IfFalse-True: $\llbracket C \neq \text{ClassMain } P; (P, C0, Main) \vdash \Rightarrow (C, M, [pc], Enter);$
instrs-of (PROG P) C M ! pc = IfFalse i;
 $ek = (\lambda s. \text{stkAt } s(\text{stkLength } (P, C, M) \text{ pc} - 1) \neq \text{Bool } False \vee i = 1)_{\surd} \rrbracket$
 $\implies (P, C0, Main) \vdash (C, M, [pc], Enter) -(\lambda s. True)_{\surd} \rightarrow (C, M, [Suc \text{ pc}], Enter)$
| CFG-New-Check-Normal: $\llbracket C \neq \text{ClassMain } P; (P, C0, Main) \vdash \Rightarrow (C, M, [pc],$
Enter);
instrs-of (PROG P) C M ! pc = New Cl;

$$\begin{aligned}
& ek = (\lambda s. \text{new-Addr } (\text{heap-of } s) \neq \text{None})_{\checkmark} \llbracket \\
& \implies (P, C0, \text{Main}) \vdash (C, M, \lfloor pc \rfloor, \text{Enter}) \text{--}(ek) \rightarrow (C, M, \lfloor pc \rfloor, \text{Normal}) \\
& \mid \text{CFG-New-Check-Exceptional: } \llbracket C \neq \text{ClassMain } P; (P, C0, \text{Main}) \vdash \Rightarrow (C, M, \\
& \lfloor pc \rfloor, \text{Enter}); \\
& \text{instrs-of } (PROG P) C M ! pc = \text{New Cl}; \\
& pc' = (\text{case } (\text{match-ex-table } (PROG P) \text{OutOfMemory } pc \text{ (ex-table-of } (PROG \\
& P) C M)) \text{ of} \\
& \quad \text{None} \Rightarrow \text{None} \\
& \quad \mid \text{Some } (pc'', d) \Rightarrow \lfloor pc'' \rfloor); \\
& ek = (\lambda s. \text{new-Addr } (\text{heap-of } s) = \text{None})_{\checkmark} \llbracket \\
& \implies (P, C0, \text{Main}) \vdash (C, M, \lfloor pc \rfloor, \text{Enter}) \text{--}(ek) \rightarrow (C, M, \lfloor pc \rfloor, \text{Exceptional } pc' \\
& \text{Enter}) \\
& \mid \text{CFG-New-Update: } \llbracket C \neq \text{ClassMain } P; (P, C0, \text{Main}) \vdash \Rightarrow (C, M, \lfloor pc \rfloor, \\
& \text{Normal}); \\
& \text{instrs-of } (PROG P) C M ! pc = \text{New Cl}; \\
& ek = \uparrow (\lambda s. \text{let } a = \text{the } (\text{new-Addr } (\text{heap-of } s)) \\
& \quad \text{in } s(\text{Heap} \mapsto \text{Hp } ((\text{heap-of } s)(a \mapsto \text{blank } (PROG P) \text{Cl}))) \\
& \quad (\text{Stack } (\text{stkLength}(P, C, M) pc) \mapsto \text{Value } (\text{Addr } a))) \llbracket \\
& \implies (P, C0, \text{Main}) \vdash (C, M, \lfloor pc \rfloor, \text{Normal}) \text{--}(ek) \rightarrow (C, M, \lfloor \text{Suc } pc \rfloor, \text{Enter}) \\
& \mid \text{CFG-New-Exceptional-prop: } \llbracket C \neq \text{ClassMain } P; (P, C0, \text{Main}) \vdash \Rightarrow (C, M, \\
& \lfloor pc \rfloor, \text{Exceptional } \text{None } \text{Enter}); \\
& \text{instrs-of } (PROG P) C M ! pc = \text{New Cl}; \\
& ek = \uparrow (\lambda s. s(\text{Exception} \mapsto \text{Value } (\text{Addr } (\text{addr-of-sys-xcpt } \text{OutOfMemory})))) \llbracket \\
& \implies (P, C0, \text{Main}) \vdash (C, M, \lfloor pc \rfloor, \text{Exceptional } \text{None } \text{Enter}) \text{--}(ek) \rightarrow (C, M, \\
& \text{None}, \text{Return}) \\
& \mid \text{CFG-New-Exceptional-handle: } \llbracket C \neq \text{ClassMain } P; (P, C0, \text{Main}) \vdash \Rightarrow (C, M, \\
& \lfloor pc \rfloor, \text{Exceptional } \lfloor pc' \rfloor \text{Enter}); \\
& \text{instrs-of } (PROG P) C M ! pc = \text{New Cl}; \\
& ek = \uparrow (\lambda s. s(\text{Exception} := \text{None}) \\
& \quad (\text{Stack } (\text{stkLength } (P, C, M) pc' - 1) \mapsto \text{Value } (\text{Addr } (\text{addr-of-sys-xcpt } \\
& \text{OutOfMemory})))) \llbracket \\
& \implies (P, C0, \text{Main}) \vdash (C, M, \lfloor pc \rfloor, \text{Exceptional } \lfloor pc' \rfloor \text{Enter}) \text{--}(ek) \rightarrow (C, M, \\
& \lfloor pc' \rfloor, \text{Enter}) \\
& \mid \text{CFG-Getfield-Check-Normal: } \llbracket C \neq \text{ClassMain } P; (P, C0, \text{Main}) \vdash \Rightarrow (C, M, \\
& \lfloor pc \rfloor, \text{Enter}); \\
& \text{instrs-of } (PROG P) C M ! pc = \text{Getfield } F \text{ Cl}; \\
& ek = (\lambda s. \text{stkAt } s (\text{stkLength } (P, C, M) pc - 1) \neq \text{Null})_{\checkmark} \llbracket \\
& \implies (P, C0, \text{Main}) \vdash (C, M, \lfloor pc \rfloor, \text{Enter}) \text{--}(ek) \rightarrow (C, M, \lfloor pc \rfloor, \text{Normal}) \\
& \mid \text{CFG-Getfield-Check-Exceptional: } \llbracket C \neq \text{ClassMain } P; (P, C0, \text{Main}) \vdash \Rightarrow (C, \\
& M, \lfloor pc \rfloor, \text{Enter}); \\
& \text{instrs-of } (PROG P) C M ! pc = \text{Getfield } F \text{ Cl}; \\
& pc' = (\text{case } (\text{match-ex-table } (PROG P) \text{NullPointer } pc \text{ (ex-table-of } (PROG P) \\
& C M)) \text{ of} \\
& \quad \text{None} \Rightarrow \text{None} \\
& \quad \mid \text{Some } (pc'', d) \Rightarrow \lfloor pc'' \rfloor); \\
& ek = (\lambda s. \text{stkAt } s (\text{stkLength } (P, C, M) pc - 1) = \text{Null})_{\checkmark} \llbracket \\
& \implies (P, C0, \text{Main}) \vdash (C, M, \lfloor pc \rfloor, \text{Enter}) \text{--}(ek) \rightarrow (C, M, \lfloor pc \rfloor, \text{Exceptional } pc' \\
& \text{Enter}) \\
& \mid \text{CFG-Getfield-Update: } \llbracket C \neq \text{ClassMain } P; (P, C0, \text{Main}) \vdash \Rightarrow (C, M, \lfloor pc \rfloor,
\end{aligned}$$

Normal);
instrs-of (*PROG P*) *C M ! pc = Getfield F Cl*;
 $ek = \uparrow(\lambda s. \text{let } (D, fs) = \text{the } (\text{heap-of } s \text{ (the-Addr } (\text{stkAt } s \text{ (stkLength } (P, C, M) \text{ pc} - 1))))))$
 $\text{in } s(\text{Stack } (\text{stkLength}(P, C, M) \text{ pc} - 1) \mapsto \text{Value } (\text{the } (fs \text{ (F, Cl))}))$
 \Downarrow
 $\Rightarrow (P, C0, \text{Main}) \vdash (C, M, \lfloor pc \rfloor, \text{Normal}) \text{-(ek)} \rightarrow (C, M, \lfloor \text{Suc } pc \rfloor, \text{Enter})$
| *CFG-Getfield-Exceptional-prop*: $\llbracket C \neq \text{ClassMain } P; (P, C0, \text{Main}) \vdash \Rightarrow (C, M, \lfloor pc \rfloor, \text{Exceptional None Enter})$;
instrs-of (*PROG P*) *C M ! pc = Getfield F Cl*;
 $ek = \uparrow(\lambda s. s(\text{Exception} \mapsto \text{Value } (\text{Addr } (\text{addr-of-sys-xcpt } \text{NullPointer})))) \Downarrow$
 $\Rightarrow (P, C0, \text{Main}) \vdash (C, M, \lfloor pc \rfloor, \text{Exceptional None Enter}) \text{-(ek)} \rightarrow (C, M, \text{None}, \text{Return})$
| *CFG-Getfield-Exceptional-handle*: $\llbracket C \neq \text{ClassMain } P; (P, C0, \text{Main}) \vdash \Rightarrow (C, M, \lfloor pc \rfloor, \text{Exceptional } \lfloor pc' \rfloor \text{ Enter})$;
instrs-of (*PROG P*) *C M ! pc = Getfield F Cl*;
 $ek = \uparrow(\lambda s. s(\text{Exception} := \text{None})$
 $(\text{Stack } (\text{stkLength } (P, C, M) \text{ pc}' - 1) \mapsto \text{Value } (\text{Addr } (\text{addr-of-sys-xcpt } \text{NullPointer})))) \Downarrow$
 $\Rightarrow (P, C0, \text{Main}) \vdash (C, M, \lfloor pc \rfloor, \text{Exceptional } \lfloor pc' \rfloor \text{ Enter}) \text{-(ek)} \rightarrow (C, M, \lfloor pc' \rfloor, \text{Enter})$
| *CFG-Putfield-Check-Normal*: $\llbracket C \neq \text{ClassMain } P; (P, C0, \text{Main}) \vdash \Rightarrow (C, M, \lfloor pc \rfloor, \text{Enter})$;
instrs-of (*PROG P*) *C M ! pc = Putfield F Cl*;
 $ek = (\lambda s. \text{stkAt } s \text{ (stkLength } (P, C, M) \text{ pc} - 2) \neq \text{Null})_{\checkmark} \Downarrow$
 $\Rightarrow (P, C0, \text{Main}) \vdash (C, M, \lfloor pc \rfloor, \text{Enter}) \text{-(ek)} \rightarrow (C, M, \lfloor pc \rfloor, \text{Normal})$
| *CFG-Putfield-Check-Exceptional*: $\llbracket C \neq \text{ClassMain } P; (P, C0, \text{Main}) \vdash \Rightarrow (C, M, \lfloor pc \rfloor, \text{Enter})$;
instrs-of (*PROG P*) *C M ! pc = Putfield F Cl*;
 $pc' = (\text{case } (\text{match-ex-table } (\text{PROG } P) \text{ NullPointer } pc \text{ (ex-table-of } (\text{PROG } P) \text{ C M})) \text{ of}$
 $\text{None} \Rightarrow \text{None}$
 $\mid \text{Some } (pc'', d) \Rightarrow \lfloor pc'' \rfloor)$;
 $ek = (\lambda s. \text{stkAt } s \text{ (stkLength } (P, C, M) \text{ pc} - 2) = \text{Null})_{\checkmark} \Downarrow$
 $\Rightarrow (P, C0, \text{Main}) \vdash (C, M, \lfloor pc \rfloor, \text{Enter}) \text{-(ek)} \rightarrow (C, M, \lfloor pc \rfloor, \text{Exceptional } pc' \text{ Enter})$
| *CFG-Putfield-Update*: $\llbracket C \neq \text{ClassMain } P; (P, C0, \text{Main}) \vdash \Rightarrow (C, M, \lfloor pc \rfloor, \text{Normal})$);
instrs-of (*PROG P*) *C M ! pc = Putfield F Cl*;
 $ek = \uparrow(\lambda s. \text{let } v = \text{stkAt } s \text{ (stkLength } (P, C, M) \text{ pc} - 1);$
 $r = \text{stkAt } s \text{ (stkLength } (P, C, M) \text{ pc} - 2);$
 $a = \text{the-Addr } r;$
 $(D, fs) = \text{the } (\text{heap-of } s \text{ } a);$
 $h' = (\text{heap-of } s)(a \mapsto (D, fs((F, Cl) \mapsto v)))$
 $\text{in } s(\text{Heap} \mapsto \text{Hp } h')$) \Downarrow
 $\Rightarrow (P, C0, \text{Main}) \vdash (C, M, \lfloor pc \rfloor, \text{Normal}) \text{-(ek)} \rightarrow (C, M, \lfloor \text{Suc } pc \rfloor, \text{Enter})$
| *CFG-Putfield-Exceptional-prop*: $\llbracket C \neq \text{ClassMain } P; (P, C0, \text{Main}) \vdash \Rightarrow (C, M, \lfloor pc \rfloor, \text{Exceptional None Enter})$;
instrs-of (*PROG P*) *C M ! pc = Putfield F Cl*;

$$\begin{aligned}
& ek = \uparrow(\lambda s. s(\text{Exception} \mapsto \text{Value} (\text{Addr} (\text{addr-of-sys-xcpt} \text{NullPointer})))) \rfloor \\
& \implies (P, C0, \text{Main}) \vdash (C, M, \lfloor pc \rfloor, \text{Exceptional} \text{None} \text{Enter}) \text{--}(ek)\text{--} (C, M, \\
& \text{None}, \text{Return}) \\
& | \text{CFG-Putfield-Exceptional-handle: } \llbracket C \neq \text{ClassMain } P; (P, C0, \text{Main}) \vdash \implies (C, \\
& M, \lfloor pc \rfloor, \text{Exceptional} \lfloor pc' \rfloor \text{Enter}); \\
& \text{instrs-of } (\text{PROG } P) C M ! pc = \text{Putfield } F Cl; \\
& ek = \uparrow(\lambda s. s(\text{Exception} := \text{None}) \\
& (\text{Stack} (\text{stkLength} (P, C, M) pc' - 1) \mapsto \text{Value} (\text{Addr} (\text{addr-of-sys-xcpt} \\
& \text{NullPointer})))) \rfloor \\
& \implies (P, C0, \text{Main}) \vdash (C, M, \lfloor pc \rfloor, \text{Exceptional} \lfloor pc' \rfloor \text{Enter}) \text{--}(ek)\text{--} (C, M, \\
& \lfloor pc' \rfloor, \text{Enter}) \\
& | \text{CFG-Checkcast-Check-Normal: } \llbracket C \neq \text{ClassMain } P; (P, C0, \text{Main}) \vdash \implies (C, \\
& M, \lfloor pc \rfloor, \text{Enter}); \\
& \text{instrs-of } (\text{PROG } P) C M ! pc = \text{Checkcast } Cl; \\
& ek = (\lambda s. \text{cast-ok} (\text{PROG } P) Cl (\text{heap-of } s) (\text{stkAt } s (\text{stkLength} (P, C, M) pc \\
& - 1)))_{\checkmark} \rfloor \\
& \implies (P, C0, \text{Main}) \vdash (C, M, \lfloor pc \rfloor, \text{Enter}) \text{--}(ek)\text{--} (C, M, \lfloor \text{Suc } pc \rfloor, \text{Enter}) \\
& | \text{CFG-Checkcast-Check-Exceptional: } \llbracket C \neq \text{ClassMain } P; (P, C0, \text{Main}) \vdash \implies (C, \\
& M, \lfloor pc \rfloor, \text{Enter}); \\
& \text{instrs-of } (\text{PROG } P) C M ! pc = \text{Checkcast } Cl; \\
& pc' = (\text{case} (\text{match-ex-table} (\text{PROG } P) \text{ClassCast } pc (\text{ex-table-of} (\text{PROG } P) C \\
& M)) \text{ of} \\
& \quad \text{None} \implies \text{None} \\
& \quad | \text{Some } (pc'', d) \implies \lfloor pc'' \rfloor); \\
& ek = (\lambda s. \neg \text{cast-ok} (\text{PROG } P) Cl (\text{heap-of } s) (\text{stkAt } s (\text{stkLength} (P, C, M) \\
& pc - 1)))_{\checkmark} \rfloor \\
& \implies (P, C0, \text{Main}) \vdash (C, M, \lfloor pc \rfloor, \text{Enter}) \text{--}(ek)\text{--} (C, M, \lfloor pc \rfloor, \text{Exceptional } pc' \\
& \text{Enter}) \\
& | \text{CFG-Checkcast-Exceptional-prop: } \llbracket C \neq \text{ClassMain } P; (P, C0, \text{Main}) \vdash \implies (C, \\
& M, \lfloor pc \rfloor, \text{Exceptional} \text{None} \text{Enter}); \\
& \text{instrs-of } (\text{PROG } P) C M ! pc = \text{Checkcast } Cl; \\
& ek = \uparrow(\lambda s. s(\text{Exception} \mapsto \text{Value} (\text{Addr} (\text{addr-of-sys-xcpt} \text{ClassCast})))) \rfloor \\
& \implies (P, C0, \text{Main}) \vdash (C, M, \lfloor pc \rfloor, \text{Exceptional} \text{None} \text{Enter}) \text{--}(ek)\text{--} (C, M, \\
& \text{None}, \text{Return}) \\
& | \text{CFG-Checkcast-Exceptional-handle: } \llbracket C \neq \text{ClassMain } P; (P, C0, \text{Main}) \vdash \implies (C, \\
& M, \lfloor pc \rfloor, \text{Exceptional} \lfloor pc' \rfloor \text{Enter}); \\
& \text{instrs-of } (\text{PROG } P) C M ! pc = \text{Checkcast } Cl; \\
& ek = \uparrow(\lambda s. s(\text{Exception} := \text{None}) \\
& (\text{Stack} (\text{stkLength} (P, C, M) pc' - 1) \mapsto \text{Value} (\text{Addr} (\text{addr-of-sys-xcpt} \\
& \text{ClassCast})))) \rfloor \\
& \implies (P, C0, \text{Main}) \vdash (C, M, \lfloor pc \rfloor, \text{Exceptional} \lfloor pc' \rfloor \text{Enter}) \text{--}(ek)\text{--} (C, M, \\
& \lfloor pc' \rfloor, \text{Enter}) \\
& | \text{CFG-Throw-Check: } \llbracket C \neq \text{ClassMain } P; (P, C0, \text{Main}) \vdash \implies (C, M, \lfloor pc \rfloor, \\
& \text{Enter}); \\
& \text{instrs-of } (\text{PROG } P) C M ! pc = \text{Throw}; \\
& pc' = \text{None} \vee \text{match-ex-table} (\text{PROG } P) \text{Exc } pc (\text{ex-table-of} (\text{PROG } P) C M) \\
& = \lfloor (\text{the } pc', d) \rfloor; \\
& ek = (\lambda s. \text{let } v = \text{stkAt } s (\text{stkLength} (P, C, M) pc - 1); \\
& \quad Cl = \text{if } (v = \text{Null}) \text{ then } \text{NullPointer} \text{ else } (\text{cname-of } (\text{heap-of } s)
\end{aligned}$$

(the-Addr v)
 in case pc' of
 None \Rightarrow match-ex-table (PROG P) Cl pc (ex-table-of (PROG P) C
 M) = None
 | Some $pc'' \Rightarrow \exists d$. match-ex-table (PROG P) Cl pc (ex-table-of
 (PROG P) C M)
 = $\lfloor (pc'', d) \rfloor$
) \checkmark \llbracket
 $\Rightarrow (P, C0, Main) \vdash (C, M, \lfloor pc \rfloor, Enter) \text{--}(ek) \rightarrow (C, M, \lfloor pc \rfloor, Exceptional\ pc'$
 $Enter)$

 | CFG-Throw-prop: $\llbracket C \neq ClassMain\ P; (P, C0, Main) \vdash \Rightarrow (C, M, \lfloor pc \rfloor, Ex-$
 ceptional None Enter);
 instrs-of (PROG P) C M ! $pc = Throw$;
 $ek = \uparrow(\lambda s. s(Exception \mapsto Value (stkAt\ s\ (stkLength\ (P, C, M)\ pc - 1)))) \llbracket$
 $\Rightarrow (P, C0, Main) \vdash (C, M, \lfloor pc \rfloor, Exceptional\ None\ Enter) \text{--}(ek) \rightarrow (C, M,$
 None, Return)
 | CFG-Throw-handle: $\llbracket C \neq ClassMain\ P; (P, C0, Main) \vdash \Rightarrow (C, M, \lfloor pc \rfloor,$
 Exceptional $\lfloor pc' \rfloor$ Enter);
 $pc' \neq length\ (instrs\ of\ (PROG\ P)\ C\ M)$;
 instrs-of (PROG P) C M ! $pc = Throw$;
 $ek = \uparrow(\lambda s. s(Exception := None)$
 (Stack (stkLength (PROG P) C M) $pc' - 1$) $\mapsto Value (stkAt\ s\ (stkLength$
 (PROG P) C M) $pc - 1)))) \llbracket$
 $\Rightarrow (P, C0, Main) \vdash (C, M, \lfloor pc \rfloor, Exceptional\ \lfloor pc' \rfloor\ Enter) \text{--}(ek) \rightarrow (C, M,$
 $\lfloor pc' \rfloor, Enter)$
 | CFG-Invoke-Check-NP-Normal: $\llbracket C \neq ClassMain\ P; (P, C0, Main) \vdash \Rightarrow (C,$
 $M, \lfloor pc \rfloor, Enter)$;
 instrs-of (PROG P) C M ! $pc = Invoke\ M'\ n$;
 $ek = (\lambda s. stkAt\ s\ (stkLength\ (P, C, M)\ pc - Suc\ n) \neq Null) \checkmark \llbracket$
 $\Rightarrow (P, C0, Main) \vdash (C, M, \lfloor pc \rfloor, Enter) \text{--}(ek) \rightarrow (C, M, \lfloor pc \rfloor, Normal)$
 | CFG-Invoke-Check-NP-Exceptional: $\llbracket C \neq ClassMain\ P; (P, C0, Main) \vdash$
 $\Rightarrow (C, M, \lfloor pc \rfloor, Enter)$;
 instrs-of (PROG P) C M ! $pc = Invoke\ M'\ n$;
 $pc' = (case\ (match\ ex\ table\ (PROG\ P)\ NullPointer\ pc\ (ex\ table\ of\ (PROG\ P)$
 $C\ M))\ of$
 None \Rightarrow None
 | Some $(pc'', d) \Rightarrow \lfloor pc'' \rfloor$);
 $ek = (\lambda s. stkAt\ s\ (stkLength\ (P, C, M)\ pc - Suc\ n) = Null) \checkmark \llbracket$
 $\Rightarrow (P, C0, Main) \vdash (C, M, \lfloor pc \rfloor, Enter) \text{--}(ek) \rightarrow (C, M, \lfloor pc \rfloor, Exceptional\ pc'$
 Enter)
 | CFG-Invoke-NP-prop: $\llbracket C \neq ClassMain\ P;$
 $(P, C0, Main) \vdash \Rightarrow (C, M, \lfloor pc \rfloor, Exceptional\ None\ Enter)$;
 instrs-of (PROG P) C M ! $pc = Invoke\ M'\ n$;
 $ek = \uparrow(\lambda s. s(Exception \mapsto Value (Addr (addr-of-sys-xcpt\ NullPointer)))) \llbracket$
 $\Rightarrow (P, C0, Main) \vdash (C, M, \lfloor pc \rfloor, Exceptional\ None\ Enter) \text{--}(ek) \rightarrow (C, M,$
 None, Return)
 | CFG-Invoke-NP-handle: $\llbracket C \neq ClassMain\ P; (P, C0, Main) \vdash \Rightarrow (C, M, \lfloor pc \rfloor,$
 Exceptional $\lfloor pc' \rfloor$ Enter);

$instrs\text{-}of (PROG P) C M ! pc = Invoke M' n;$
 $ek = \uparrow(\lambda s. s(Exception := None)$
 $(Stack (stkLength (P, C, M) pc' - 1) \mapsto Value (Addr (addr\text{-}of\text{-}sys\text{-}xcpt$
 $NullPointer))))])$
 $\implies (P, C0, Main) \vdash (C, M, [pc], Exceptional [pc'] Enter) \text{-}(ek) \rightarrow (C, M,$
 $[pc'], Enter)$
 $| CFG\text{-}Invoke\text{-}Call: \llbracket C \neq ClassMain P; (P, C0, Main) \vdash \Rightarrow (C, M, [pc], Nor\text{-}$
 $mal);$
 $instrs\text{-}of (PROG P) C M ! pc = Invoke M' n;$
 $TYPING P C M ! pc = [(ST, LT)];$
 $ST ! n = Class D';$
 $PROG P \vdash D' \text{ sees } M': Ts \rightarrow T = (mxs, mxl_0, is, xt) \text{ in } D;$
 $Q = (\lambda(s, ret). \text{ let } r = stkAt s (stkLength (P, C, M) pc - Suc n);$
 $C' = fst (the (heap\text{-}of s (the\text{-}Addr r)))$
 $\text{ in } D = fst (method (PROG P) C' M'));$
 $paramDefs = (\lambda s. s Heap)$
 $\# (\lambda s. s (Stack (stkLength (P, C, M) pc - Suc n)))$
 $\# (rev (map (\lambda i. (\lambda s. s (Stack (stkLength (P, C, M) pc - Suc i))))$
 $[0..<n]));$
 $ek = Q:(C, M, pc) \mapsto_{(D, M')} paramDefs$
 $\implies (P, C0, Main) \vdash (C, M, [pc], Normal) \text{-}(ek) \rightarrow (D, M', None, Enter)$
 $| CFG\text{-}Invoke\text{-}False: \llbracket C \neq ClassMain P; (P, C0, Main) \vdash \Rightarrow (C, M, [pc],$
 $Normal);$
 $instrs\text{-}of (PROG P) C M ! pc = Invoke M' n;$
 $ek = (\lambda s. False)_{\surd}$
 $\implies (P, C0, Main) \vdash (C, M, [pc], Normal) \text{-}(ek) \rightarrow (C, M, [pc], Return)$
 $| CFG\text{-}Invoke\text{-}Return\text{-}Check\text{-}Normal: \llbracket C \neq ClassMain P; (P, C0, Main) \vdash \Rightarrow (C,$
 $M, [pc], Return);$
 $instrs\text{-}of (PROG P) C M ! pc = Invoke M' n;$
 $(TYPING P) C M ! pc = [(ST, LT)];$
 $ST ! n \neq NT;$
 $ek = (\lambda s. s Exception = None)_{\surd}$
 $\implies (P, C0, Main) \vdash (C, M, [pc], Return) \text{-}(ek) \rightarrow (C, M, [Suc pc], Enter)$
 $| CFG\text{-}Invoke\text{-}Return\text{-}Check\text{-}Exceptional: \llbracket C \neq ClassMain P; (P, C0, Main) \vdash$
 $\Rightarrow (C, M, [pc], Return);$
 $instrs\text{-}of (PROG P) C M ! pc = Invoke M' n;$
 $match\text{-}ex\text{-}table (PROG P) Exc pc (ex\text{-}table\text{-}of (PROG P) C M) = [(pc', diff)];$
 $pc' \neq length (instrs\text{-}of (PROG P) C M);$
 $ek = (\lambda s. \exists v d. s Exception = [v] \wedge$
 $match\text{-}ex\text{-}table (PROG P) (cname\text{-}of (heap\text{-}of s) (the\text{-}Addr (the\text{-}Value$
 $v))) pc (ex\text{-}table\text{-}of (PROG P) C M) = [(pc', d)])_{\surd}$
 $\implies (P, C0, Main) \vdash (C, M, [pc], Return) \text{-}(ek) \rightarrow (C, M, [pc], Exceptional$
 $[pc'] Return)$
 $| CFG\text{-}Invoke\text{-}Return\text{-}Exceptional\text{-}handle: \llbracket C \neq ClassMain P; (P, C0, Main) \vdash$
 $\Rightarrow (C, M, [pc], Exceptional [pc'] Return);$

$instrs\text{-of } (PROG P) C M ! pc = Invoke M' n;$
 $ek = \uparrow(\lambda s. s(Exception := None,$
 $\quad Stack (stkLength (P, C, M) pc' - 1) := s Exception)) \Downarrow$
 $\implies (P, C0, Main) \vdash (C, M, [pc], Exceptional [pc'] Return) \text{--}(ek)\text{--}\rightarrow (C, M,$
 $[pc'], Enter)$
 $| CFG\text{-Invoke-Return-Exceptional-prop: } \Downarrow C \neq ClassMain P;$
 $(P, C0, Main) \vdash \Rightarrow (C, M, [pc], Return);$
 $instrs\text{-of } (PROG P) C M ! pc = Invoke M' n;$
 $ek = (\lambda s. \exists v. s Exception = [v] \wedge$
 $\quad match\text{-ex-table } (PROG P) (cname\text{-of } (heap\text{-of } s) (the\text{-Addr } (the\text{-Value}$
 $v))) pc (ex\text{-table-of } (PROG P) C M) = None)_{\checkmark} \Downarrow$
 $\implies (P, C0, Main) \vdash (C, M, [pc], Return) \text{--}(ek)\text{--}\rightarrow (C, M, None, Return)$
 $| CFG\text{-Return: } \Downarrow C \neq ClassMain P; (P, C0, Main) \vdash \Rightarrow (C, M, [pc], Enter);$
 $instrs\text{-of } (PROG P) C M ! pc = instr.Return;$
 $ek = \uparrow(\lambda s. s(Stack 0 := s (Stack (stkLength (P, C, M) pc - 1))))$
 \Downarrow
 $\implies (P, C0, Main) \vdash (C, M, [pc], Enter) \text{--}(ek)\text{--}\rightarrow (C, M, None, Return)$
 $| CFG\text{-Return-from-Method: } \Downarrow (P, C0, Main) \vdash \Rightarrow (C, M, None, Return);$
 $(P, C0, Main) \vdash (C', M', [pc'], Normal) \text{--}(Q' : (C', M', pc') \hookrightarrow (C, M) ps)\text{--}\rightarrow$
 $(C, M, None, Enter);$
 $Q = (\lambda (s, ret). ret = (C', M', pc'));$
 $stateUpdate = (\lambda s s'. s'(Heap := s Heap,$
 $\quad Exception := s Exception,$
 $\quad Stack (stkLength (P, C', M') (Suc pc') - 1) := s (Stack 0))$
 $);$
 $ek = Q \leftrightarrow_{(C, M)} stateUpdate$
 \Downarrow
 $\implies (P, C0, Main) \vdash (C, M, None, Return) \text{--}(ek)\text{--}\rightarrow (C', M', [pc'], Return)$

lemma JVMCFG-edge-det: $\Downarrow P \vdash n \text{--}(et)\text{--}\rightarrow n'; P \vdash n \text{--}(et')\text{--}\rightarrow n' \Downarrow \implies et = et'$
by $(erule JVMCFG.cases) (erule JVMCFG.cases, (fastforce dest: sees\text{-method-fun})+)$

lemma sourcenode-reachable: $P \vdash n \text{--}(ek)\text{--}\rightarrow n' \implies P \vdash \Rightarrow n$
by $(erule JVMCFG.cases, auto)$

lemma targetnode-reachable:
assumes $edge: P \vdash n \text{--}(ek)\text{--}\rightarrow n'$
shows $P \vdash \Rightarrow n'$

proof –
from $edge$ **have** $P \vdash \Rightarrow n$
by $\text{--}(drule sourcenode-reachable)$
with $edge$ **show** $?thesis$
by $\text{--}(rule JVMCFG-reachable.intros)$
qed

lemmas JVMCFG-reachable-inducts = JVMCFG-reachable.inducts $[split\text{-format } (complete)]$

lemma *ClassMain-imp-MethodMain*:

$(P, C0, Main) \vdash (C', M', pc', nt') -ek \rightarrow (ClassMain\ P, M, pc, nt) \implies M = MethodMain\ P$

$(P, C0, Main) \vdash \implies (ClassMain\ P, M, pc, nt) \implies M = MethodMain\ P$

proof (*induct* $P == P\ C0 \equiv C0\ Main \equiv Main\ C'\ M'\ pc'\ nt'\ ek\ C'' == ClassMain\ P\ M\ pc\ nt$ **and**

$P == P\ C0 \equiv C0\ Main \equiv Main\ C'' == ClassMain\ P\ M\ pc\ nt$

rule: JVMCFG-reachable-inducts)

case *CFG-Return-from-Method*

thus *?case*

by (*fastforce elim: JVMCFG.cases*)

qed *auto*

lemma *ClassMain-no-Call-target* [*dest!*]:

$(P, C0, Main) \vdash (C, M, pc, nt) -Q:(C', M', pc') \hookrightarrow_{(D, M'')} paramDefs \rightarrow (ClassMain\ P, M''', pc'', nt')$

$\implies False$

and

$(P, C0, Main) \vdash \implies (C, M, pc, nt) \implies True$

by (*induct* $P\ C0\ Main\ C\ M\ pc\ nt\ ek == Q:(C', M', pc') \hookrightarrow_{(D, M'')} paramDefs$

$C'' == ClassMain\ P\ M''' \ pc'' \ nt'$ **and**

$P\ C0\ Main\ C\ M\ pc\ nt$

rule: JVMCFG-reachable-inducts) *auto*

lemma *method-of-src-and-trg-exists*:

$\llbracket (P, C0, Main) \vdash (C', M', pc', nt') -ek \rightarrow (C, M, pc, nt); C \neq ClassMain\ P; C' \neq ClassMain\ P \rrbracket$

$\implies (\exists Ts\ T\ mb. (PROG\ P) \vdash C\ sees\ M:Ts \rightarrow T = mb\ in\ C) \wedge$

$(\exists Ts\ T\ mb. (PROG\ P) \vdash C'\ sees\ M':Ts \rightarrow T = mb\ in\ C')$

and *method-of-reachable-node-exists*:

$\llbracket (P, C0, Main) \vdash \implies (C, M, pc, nt); C \neq ClassMain\ P \rrbracket$

$\implies \exists Ts\ T\ mb. (PROG\ P) \vdash C\ sees\ M:Ts \rightarrow T = mb\ in\ C$

proof (*induct* *rule: JVMCFG-reachable-inducts*)

case *CFG-Invoke-Call*

thus *?case*

by (*blast dest: sees-method-idemp*)

next

case (*reachable-step* $P\ C0\ Main\ C\ M\ pc\ nt\ ek\ C'\ M'\ pc'\ nt'$)

show *?case*

proof (*cases* $C = ClassMain\ P$)

case *True*

with $\langle (P, C0, Main) \vdash (C, M, pc, nt) -ek \rightarrow (C', M', pc', nt') \rangle \langle C' \neq ClassMain\ P \rangle$

show *?thesis*

proof *cases*

case *Main-Call*

thus *?thesis*

by (*blast dest: sees-method-idemp*)

qed *auto*

next
case *False*
with *reachable-step show ?thesis*
by *simp*
qed
qed *simp-all*

lemma $\llbracket (P, C0, Main) \vdash (C', M', pc', nt') -ek \rightarrow (C, M, pc, nt); C \neq \text{ClassMain } P; C' \neq \text{ClassMain } P \rrbracket$
 $\implies (\text{case } pc \text{ of } \text{None} \implies \text{True} \mid$
 $\lfloor pc'' \rfloor \implies (\text{TYPING } P) C M ! pc'' \neq \text{None} \wedge pc'' < \text{length } (\text{instrs-of } (\text{PROG } P) C M)) \wedge$
 $(\text{case } pc' \text{ of } \text{None} \implies \text{True} \mid$
 $\lfloor pc'' \rfloor \implies (\text{TYPING } P) C' M' ! pc'' \neq \text{None} \wedge pc'' < \text{length } (\text{instrs-of } (\text{PROG } P) C' M'))$
and *instr-of-reachable-node-typable*: $\llbracket (P, C0, Main) \vdash \Rightarrow (C, M, pc, nt); C \neq \text{ClassMain } P \rrbracket$
 $\implies \text{case } pc \text{ of } \text{None} \implies \text{True} \mid$
 $\lfloor pc'' \rfloor \implies (\text{TYPING } P) C M ! pc'' \neq \text{None} \wedge pc'' < \text{length } (\text{instrs-of } (\text{PROG } P) C M)$

proof (*induct rule: JVMCFG-reachable-inducts*)
case (*CFG-Load* $C P C0 Main M pc n ek$)
from $\langle (P, C0, Main) \vdash \Rightarrow (C, M, \lfloor pc \rfloor, \text{Enter}) \rangle \langle C \neq \text{ClassMain } P \rangle$
obtain $Ts T mxs mxl_0 is xt$ **where** $\text{PROG } P \vdash C \text{ sees } M:Ts \rightarrow T = (mxs, mxl_0, is, xt)$ *in* C
and *instrs-of* $(\text{PROG } P) C M = is$
by $-(\text{drule } \text{method-of-reachable-node-exists, auto})$
with *CFG-Load show ?case*
by (*fastforce dest!:* *wt-jvm-prog-impl-wt-instr [OF wf-jvmprog-is-wf-ty]*)

next
case (*CFG-Store* $C P C0 Main M pc n ek$)
from $\langle (P, C0, Main) \vdash \Rightarrow (C, M, \lfloor pc \rfloor, \text{Enter}) \rangle \langle C \neq \text{ClassMain } P \rangle$
obtain $Ts T mxs mxl_0 is xt$ **where** $\text{PROG } P \vdash C \text{ sees } M:Ts \rightarrow T = (mxs, mxl_0, is, xt)$ *in* C
and *instrs-of* $(\text{PROG } P) C M = is$
by $-(\text{drule } \text{method-of-reachable-node-exists, auto})$
with *CFG-Store show ?case*
by (*fastforce dest!:* *wt-jvm-prog-impl-wt-instr [OF wf-jvmprog-is-wf-ty]*)

next
case (*CFG-Push* $C P C0 Main M pc v ek$)
from $\langle (P, C0, Main) \vdash \Rightarrow (C, M, \lfloor pc \rfloor, \text{Enter}) \rangle \langle C \neq \text{ClassMain } P \rangle$
obtain $Ts T mxs mxl_0 is xt$ **where** $\text{PROG } P \vdash C \text{ sees } M:Ts \rightarrow T = (mxs, mxl_0, is, xt)$ *in* C
and *instrs-of* $(\text{PROG } P) C M = is$
by $-(\text{drule } \text{method-of-reachable-node-exists, auto})$
with *CFG-Push show ?case*
by (*fastforce dest!:* *wt-jvm-prog-impl-wt-instr [OF wf-jvmprog-is-wf-ty]*)

next
case (*CFG-Pop* $C P C0 Main M pc ek$)

```

from  $\langle(P, C0, Main) \vdash \Rightarrow(C, M, \lfloor pc \rfloor, Enter)\rangle \langle C \neq ClassMain P \rangle$ 
obtain  $Ts T mxs mxl_0 is xt$  where  $PROG P \vdash C$  sees  $M:Ts \rightarrow T = (mxs, mxl_0,$ 
 $is, xt)$  in  $C$ 
  and  $instrs-of (PROG P) C M = is$ 
  by  $-(drule\ method-of-reachable-node-exists, auto)$ 
with  $CFG-Pop$  show  $?case$ 
  by  $(fastforce\ dest!: wt-jvm-prog-impl-wt-instr [OF\ wf-jvmprog-is-wf-typr])$ 
next
case  $(CFG-IAdd C P C0 Main M pc ek)$ 
from  $\langle(P, C0, Main) \vdash \Rightarrow(C, M, \lfloor pc \rfloor, Enter)\rangle \langle C \neq ClassMain P \rangle$ 
obtain  $Ts T mxs mxl_0 is xt$  where  $PROG P \vdash C$  sees  $M:Ts \rightarrow T = (mxs, mxl_0,$ 
 $is, xt)$  in  $C$ 
  and  $instrs-of (PROG P) C M = is$ 
  by  $-(drule\ method-of-reachable-node-exists, auto)$ 
with  $CFG-IAdd$  show  $?case$ 
  by  $(fastforce\ dest!: wt-jvm-prog-impl-wt-instr [OF\ wf-jvmprog-is-wf-typr])$ 
next
case  $(CFG-Goto C P C0 Main M pc i)$ 
from  $\langle(P, C0, Main) \vdash \Rightarrow(C, M, \lfloor pc \rfloor, Enter)\rangle \langle C \neq ClassMain P \rangle$ 
obtain  $Ts T mxs mxl_0 is xt$  where  $PROG P \vdash C$  sees  $M:Ts \rightarrow T = (mxs, mxl_0,$ 
 $is, xt)$  in  $C$ 
  and  $instrs-of (PROG P) C M = is$ 
  by  $-(drule\ method-of-reachable-node-exists, auto)$ 
with  $CFG-Goto$  show  $?case$ 
  by  $(fastforce\ dest!: wt-jvm-prog-impl-wt-instr [OF\ wf-jvmprog-is-wf-typr])$ 
next
case  $(CFG-CmpEq C P C0 Main M pc ek)$ 
from  $\langle(P, C0, Main) \vdash \Rightarrow(C, M, \lfloor pc \rfloor, Enter)\rangle \langle C \neq ClassMain P \rangle$ 
obtain  $Ts T mxs mxl_0 is xt$  where  $PROG P \vdash C$  sees  $M:Ts \rightarrow T = (mxs, mxl_0,$ 
 $is, xt)$  in  $C$ 
  and  $instrs-of (PROG P) C M = is$ 
  by  $-(drule\ method-of-reachable-node-exists, auto)$ 
with  $CFG-CmpEq$  show  $?case$ 
  by  $(fastforce\ dest!: wt-jvm-prog-impl-wt-instr [OF\ wf-jvmprog-is-wf-typr])$ 
next
case  $(CFG-IfFalse-False C P C0 Main M pc i ek)$ 
from  $\langle(P, C0, Main) \vdash \Rightarrow(C, M, \lfloor pc \rfloor, Enter)\rangle \langle C \neq ClassMain P \rangle$ 
obtain  $Ts T mxs mxl_0 is xt$  where  $PROG P \vdash C$  sees  $M:Ts \rightarrow T = (mxs, mxl_0,$ 
 $is, xt)$  in  $C$ 
  and  $instrs-of (PROG P) C M = is$ 
  by  $-(drule\ method-of-reachable-node-exists, auto)$ 
with  $CFG-IfFalse-False$  show  $?case$ 
  by  $(fastforce\ dest!: wt-jvm-prog-impl-wt-instr [OF\ wf-jvmprog-is-wf-typr])$ 
next
case  $(CFG-IfFalse-True C P C0 Main M pc i ek)$ 
from  $\langle(P, C0, Main) \vdash \Rightarrow(C, M, \lfloor pc \rfloor, Enter)\rangle \langle C \neq ClassMain P \rangle$ 
obtain  $Ts T mxs mxl_0 is xt$  where  $PROG P \vdash C$  sees  $M:Ts \rightarrow T = (mxs, mxl_0,$ 
 $is, xt)$  in  $C$ 
  and  $instrs-of (PROG P) C M = is$ 

```

```

    by  $-(drule\ method-of-reachable-node-exists, auto)$ 
  with CFG-IfFalse-True show ?case
    using  $[[simproc\ del:\ list-to-set-comprehension]]$  by  $(fastforce\ dest!:\ wt-jvm-prog-impl-wt-instr$ 
 $[OF\ wf-jvmprog-is-wf-typr])$ 
next
  case  $(CFG-New-Update\ C\ P\ C0\ Main\ M\ pc\ Cl\ ek)$ 
  from  $\langle(P, C0, Main) \vdash \Rightarrow(C, M, [pc], Normal)\rangle \langle C \neq ClassMain\ P\rangle$ 
  obtain  $Ts\ T\ mxs\ mxl_0\ is\ xt$  where  $PROG\ P \vdash C\ sees\ M:Ts \rightarrow T = (mxs, mxl_0,$ 
 $is, xt)$  in  $C$ 
    and  $instrs-of\ (PROG\ P)\ C\ M = is$ 
    by  $-(drule\ method-of-reachable-node-exists, auto)$ 
  with CFG-New-Update show ?case
    by  $(fastforce\ dest!:\ wt-jvm-prog-impl-wt-instr\ [OF\ wf-jvmprog-is-wf-typr])$ 
next
  case  $(CFG-New-Exceptional-handle\ C\ P\ C0\ Main\ M\ pc\ pc'\ Cl\ ek)$ 
  hence  $TYPING\ P\ C\ M ! pc \neq None$  and  $pc < length\ (instrs-of\ (PROG\ P)\ C$ 
 $M)$ 
    by simp-all
  moreover from  $\langle(P, C0, Main) \vdash \Rightarrow(C, M, [pc], Exceptional\ [pc']\ Enter)\rangle \langle C$ 
 $\neq ClassMain\ P\rangle$ 
  obtain  $Ts\ T\ mxs\ mxl_0$  where
     $PROG\ P \vdash C\ sees\ M:Ts \rightarrow T = (mxs, mxl_0, instrs-of\ (PROG\ P)\ C\ M,$ 
 $ex-table-of\ (PROG\ P)\ C\ M)$  in  $C$ 
    by  $(fastforce\ dest:\ method-of-reachable-node-exists)$ 
  with  $\langle pc < length\ (instrs-of\ (PROG\ P)\ C\ M)\rangle \langle instrs-of\ (PROG\ P)\ C\ M ! pc$ 
 $= New\ Cl\rangle$ 
  have  $PROG\ P, T, mxs, length\ (instrs-of\ (PROG\ P)\ C\ M), ex-table-of\ (PROG\ P)$ 
 $C\ M$ 
     $\vdash New\ Cl, pc :: TYPING\ P\ C\ M$ 
    by  $(fastforce\ dest!:\ wt-jvm-prog-impl-wt-instr\ [OF\ wf-jvmprog-is-wf-typr])$ 
  moreover from  $\langle(P, C0, Main) \vdash \Rightarrow(C, M, [pc], Exceptional\ [pc']\ Enter)\rangle \langle C$ 
 $\neq ClassMain\ P\rangle$ 
     $\langle instrs-of\ (PROG\ P)\ C\ M ! pc = New\ Cl\rangle$  obtain  $d'$ 
  where  $match-ex-table\ (PROG\ P)\ OutOfMemory\ pc\ (ex-table-of\ (PROG\ P)\ C$ 
 $M) = \lfloor(pc', d')\rfloor$ 
    by cases  $(fastforce\ elim:\ JVMCFG.cases)$ 
  hence  $\exists(f, t, D, h, d) \in set\ (ex-table-of\ (PROG\ P)\ C\ M).$ 
 $matches-ex-entry\ (PROG\ P)\ OutOfMemory\ pc\ (f, t, D, h, d) \wedge h = pc' \wedge d$ 
 $= d'$ 
    by  $-(drule\ match-ex-table-SomeD)$ 
  ultimately show ?case using  $\langle instrs-of\ (PROG\ P)\ C\ M ! pc = New\ Cl\rangle$ 
  by  $(fastforce\ simp:\ relevant-entries-def\ is-relevant-entry-def\ matches-ex-entry-def)$ 
next
  case  $(CFG-Getfield-Update\ C\ P\ C0\ Main\ M\ pc\ F\ Cl\ ek)$ 
  from  $\langle(P, C0, Main) \vdash \Rightarrow(C, M, [pc], Normal)\rangle \langle C \neq ClassMain\ P\rangle$ 
  obtain  $Ts\ T\ mxs\ mxl_0\ is\ xt$  where  $PROG\ P \vdash C\ sees\ M:Ts \rightarrow T = (mxs, mxl_0,$ 
 $is, xt)$  in  $C$ 
    and  $instrs-of\ (PROG\ P)\ C\ M = is$ 
    by  $-(drule\ method-of-reachable-node-exists, auto)$ 

```

with *CFG-Getfield-Update* **show** *?case*
by (*fastforce dest!:* *wt-jvm-prog-impl-wt-instr* [*OF wf-jvmprog-is-wf-ty*])
next
case (*CFG-Getfield-Exceptional-handle* *C P C0 Main M pc pc' F Cl ek*)
hence *TYPING P C M ! pc ≠ None* **and** *pc < length (instrs-of (PROG P) C M)*
by *simp-all*
moreover from $\langle (P, C0, Main) \vdash \Rightarrow (C, M, \lfloor pc \rfloor, \text{Exceptional } \lfloor pc' \rfloor \text{ Enter}) \rangle \langle C \neq \text{ClassMain } P \rangle$
obtain *Ts T mxs mxl₀* **where**
PROG P $\vdash C$ *sees* $M:Ts \rightarrow T = (mxs, mxl_0, \text{instrs-of } (PROG P) C M, \text{ex-table-of } (PROG P) C M)$ *in* *C*
by (*fastforce dest:* *method-of-reachable-node-exists*)
with $\langle pc < \text{length } (\text{instrs-of } (PROG P) C M) \rangle \langle \text{instrs-of } (PROG P) C M ! pc = \text{Getfield } F Cl \rangle$
have *PROG P, T, mxs, length (instrs-of (PROG P) C M), ex-table-of (PROG P) C M*
 $\vdash \text{Getfield } F Cl, pc :: \text{TYPING } P C M$
by (*fastforce dest!:* *wt-jvm-prog-impl-wt-instr* [*OF wf-jvmprog-is-wf-ty*])
moreover from $\langle (P, C0, Main) \vdash \Rightarrow (C, M, \lfloor pc \rfloor, \text{Exceptional } \lfloor pc' \rfloor \text{ Enter}) \rangle \langle C \neq \text{ClassMain } P \rangle$
 $\langle \text{instrs-of } (PROG P) C M ! pc = \text{Getfield } F Cl \rangle$ **obtain** *d'*
where *match-ex-table (PROG P) NullPointer pc (ex-table-of (PROG P) C M)*
 $= \lfloor (pc', d') \rfloor$
by *cases (fastforce elim: JVMCFG.cases)*
hence $\exists (f, t, D, h, d) \in \text{set } (\text{ex-table-of } (PROG P) C M)$.
matches-ex-entry (PROG P) NullPointer pc (f, t, D, h, d) $\wedge h = pc' \wedge d = d'$
by $\neg(\text{drule match-ex-table-SomeD})$
ultimately show *?case using* $\langle \text{instrs-of } (PROG P) C M ! pc = \text{Getfield } F Cl \rangle$
by (*fastforce simp:* *relevant-entries-def is-relevant-entry-def matches-ex-entry-def*)
next
case (*CFG-Putfield-Update* *C P C0 Main M pc F Cl ek*)
from $\langle (P, C0, Main) \vdash \Rightarrow (C, M, \lfloor pc \rfloor, \text{Normal}) \rangle \langle C \neq \text{ClassMain } P \rangle$
obtain *Ts T mxs mxl₀ is xt* **where** *PROG P* $\vdash C$ *sees* $M:Ts \rightarrow T = (mxs, mxl_0, \text{instrs-of } (PROG P) C M, \text{ex-table-of } (PROG P) C M)$ *in* *C*
and *instrs-of (PROG P) C M = is*
by $\neg(\text{drule method-of-reachable-node-exists, auto})$
with *CFG-Putfield-Update* **show** *?case*
by (*fastforce dest!:* *wt-jvm-prog-impl-wt-instr* [*OF wf-jvmprog-is-wf-ty*])
next
case (*CFG-Putfield-Exceptional-handle* *C P C0 Main M pc pc' F Cl ek*)
hence *TYPING P C M ! pc ≠ None* **and** *pc < length (instrs-of (PROG P) C M)*
by *simp-all*
moreover from $\langle (P, C0, Main) \vdash \Rightarrow (C, M, \lfloor pc \rfloor, \text{Exceptional } \lfloor pc' \rfloor \text{ Enter}) \rangle \langle C \neq \text{ClassMain } P \rangle$
obtain *Ts T mxs mxl₀* **where**
PROG P $\vdash C$ *sees* $M:Ts \rightarrow T = (mxs, mxl_0, \text{instrs-of } (PROG P) C M, \text{ex-table-of } (PROG P) C M)$ *in* *C*

by (*fastforce dest: method-of-reachable-node-exists*)
with $\langle pc < \text{length} (\text{instrs-of } (PROG P) C M) \rangle \langle \text{instrs-of } (PROG P) C M ! pc = \text{Putfield } F Cl \rangle$
have $PROG P, T, mxs, \text{length} (\text{instrs-of } (PROG P) C M), \text{ex-table-of } (PROG P) C M$
 $\vdash \text{Putfield } F Cl, pc :: \text{TYPING } P C M$
by (*fastforce dest!: wt-jvm-prog-impl-wt-instr [OF wf-jvmprog-is-wf-ty]*)
moreover from $\langle (P, C0, Main) \vdash \Rightarrow (C, M, [pc], \text{Exceptional } [pc'] \text{ Enter}) \rangle \langle C \neq \text{ClassMain } P \rangle$
 $\langle \text{instrs-of } (PROG P) C M ! pc = \text{Putfield } F Cl \rangle$ **obtain** d'
where $\text{match-ex-table } (PROG P) \text{ NullPointer } pc (\text{ex-table-of } (PROG P) C M) = [(pc', d')]$
by cases (*fastforce elim: JVMCFG.cases*)
hence $\exists (f, t, D, h, d) \in \text{set} (\text{ex-table-of } (PROG P) C M)$.
 $\text{matches-ex-entry } (PROG P) \text{ NullPointer } pc (f, t, D, h, d) \wedge h = pc' \wedge d = d'$
by $-(\text{drule match-ex-table-SomeD})$
ultimately show $?case$ **using** $\langle \text{instrs-of } (PROG P) C M ! pc = \text{Putfield } F Cl \rangle$
by (*fastforce simp: relevant-entries-def is-relevant-entry-def matches-ex-entry-def*)
next
case (*CFG-Checkcast-Check-Normal* $C P C0 Main M pc Cl ek$)
from $\langle (P, C0, Main) \vdash \Rightarrow (C, M, [pc], \text{Enter}) \rangle \langle C \neq \text{ClassMain } P \rangle$
obtain $Ts T mxs mxl_0$ **is** xt **where** $PROG P \vdash C \text{ sees } M:Ts \rightarrow T = (mxs, mxl_0, is, xt)$ **in** C
and $\text{instrs-of } (PROG P) C M = is$
by $-(\text{drule method-of-reachable-node-exists, auto})$
with *CFG-Checkcast-Check-Normal* **show** $?case$
by (*fastforce dest!: wt-jvm-prog-impl-wt-instr [OF wf-jvmprog-is-wf-ty]*)
next
case (*CFG-Checkcast-Exceptional-handle* $C P C0 Main M pc pc' Cl ek$)
hence $\text{TYPING } P C M ! pc \neq \text{None}$ **and** $pc < \text{length} (\text{instrs-of } (PROG P) C M)$
by *simp-all*
moreover from $\langle (P, C0, Main) \vdash \Rightarrow (C, M, [pc], \text{Exceptional } [pc'] \text{ Enter}) \rangle \langle C \neq \text{ClassMain } P \rangle$
obtain $Ts T mxs mxl_0$ **where**
 $PROG P \vdash C \text{ sees } M:Ts \rightarrow T = (mxs, mxl_0, \text{instrs-of } (PROG P) C M, \text{ex-table-of } (PROG P) C M)$ **in** C
by (*fastforce dest: method-of-reachable-node-exists*)
with $\langle pc < \text{length} (\text{instrs-of } (PROG P) C M) \rangle \langle \text{instrs-of } (PROG P) C M ! pc = \text{Checkcast } Cl \rangle$
have $PROG P, T, mxs, \text{length} (\text{instrs-of } (PROG P) C M), \text{ex-table-of } (PROG P) C M$
 $\vdash \text{Checkcast } Cl, pc :: \text{TYPING } P C M$
by (*fastforce dest!: wt-jvm-prog-impl-wt-instr [OF wf-jvmprog-is-wf-ty]*)
moreover from $\langle (P, C0, Main) \vdash \Rightarrow (C, M, [pc], \text{Exceptional } [pc'] \text{ Enter}) \rangle \langle C \neq \text{ClassMain } P \rangle$
 $\langle \text{instrs-of } (PROG P) C M ! pc = \text{Checkcast } Cl \rangle$ **obtain** d'
where $\text{match-ex-table } (PROG P) \text{ ClassCast } pc (\text{ex-table-of } (PROG P) C M) = [(pc', d')]$

by cases (fastforce elim: JVMCFG.cases)
 hence $\exists (f, t, D, h, d) \in \text{set } (\text{ex-table-of } (\text{PROG } P) C M)$.
 matches-ex-entry (PROG P) ClassCast pc (f, t, D, h, d) $\wedge h = pc' \wedge d = d'$
 by $\text{--}(d\text{rule match-ex-table-Some}D)$
 ultimately show ?case using $\langle \text{instrs-of } (\text{PROG } P) C M ! pc = \text{Checkcast } Cl \rangle$
 by (fastforce simp: relevant-entries-def is-relevant-entry-def matches-ex-entry-def)
 next
 case (CFG-Throw-handle C P C0 Main M pc pc' ek)
 hence TYPING P C M ! pc \neq None and pc < length (instrs-of (PROG P) C M)
 by simp-all
 moreover from $\langle (P, C0, Main) \vdash \Rightarrow (C, M, [pc], \text{Exceptional } [pc'] \text{ Enter}) \rangle \langle C \neq \text{ClassMain } P \rangle$
 obtain Ts T mxs mxl₀ where
 PROG P $\vdash C$ sees M:Ts \rightarrow T = (mxs, mxl₀, instrs-of (PROG P) C M, ex-table-of (PROG P) C M) in C
 by (fastforce dest: method-of-reachable-node-exists)
 with $\langle pc < \text{length } (\text{instrs-of } (\text{PROG } P) C M) \rangle \langle \text{instrs-of } (\text{PROG } P) C M ! pc = \text{Throw} \rangle$
 have PROG P, T, mxs, length (instrs-of (PROG P) C M), ex-table-of (PROG P) C M
 $\vdash \text{Throw}, pc :: \text{TYPING } P C M$
 by (fastforce dest!: wt-jvm-prog-impl-wt-instr [OF wf-jvmprog-is-wf-ty])
 moreover from $\langle (P, C0, Main) \vdash \Rightarrow (C, M, [pc], \text{Exceptional } [pc'] \text{ Enter}) \rangle \langle C \neq \text{ClassMain } P \rangle$
 $\langle \text{instrs-of } (\text{PROG } P) C M ! pc = \text{Throw} \rangle$ obtain d' Exc
 where match-ex-table (PROG P) Exc pc (ex-table-of (PROG P) C M) = $[(pc', d')]$
 by cases (fastforce elim: JVMCFG.cases)
 hence $\exists (f, t, D, h, d) \in \text{set } (\text{ex-table-of } (\text{PROG } P) C M)$.
 matches-ex-entry (PROG P) Exc pc (f, t, D, h, d) $\wedge h = pc' \wedge d = d'$
 by $\text{--}(d\text{rule match-ex-table-Some}D)$
 ultimately show ?case using $\langle \text{instrs-of } (\text{PROG } P) C M ! pc = \text{Throw} \rangle$
 by (fastforce simp: relevant-entries-def is-relevant-entry-def matches-ex-entry-def)
 next
 case (CFG-Invoke-NP-handle C P C0 Main M pc pc' M' n ek)
 hence TYPING P C M ! pc \neq None and pc < length (instrs-of (PROG P) C M)
 by simp-all
 moreover from $\langle (P, C0, Main) \vdash \Rightarrow (C, M, [pc], \text{Exceptional } [pc'] \text{ Enter}) \rangle \langle C \neq \text{ClassMain } P \rangle$
 obtain Ts T mxs mxl₀ where
 PROG P $\vdash C$ sees M:Ts \rightarrow T = (mxs, mxl₀, instrs-of (PROG P) C M, ex-table-of (PROG P) C M) in C
 by (fastforce dest: method-of-reachable-node-exists)
 with $\langle pc < \text{length } (\text{instrs-of } (\text{PROG } P) C M) \rangle \langle \text{instrs-of } (\text{PROG } P) C M ! pc = \text{Invoke } M' n \rangle$
 have PROG P, T, mxs, length (instrs-of (PROG P) C M), ex-table-of (PROG P) C M

$\vdash \text{Invoke } M' n, pc :: \text{TYPING } P C M$
by (*fastforce dest!: wt-jvm-prog-impl-wt-instr [OF wf-jvmprog-is-wf-typp]*)
moreover from $\langle (P, C0, \text{Main}) \vdash \Rightarrow (C, M, [pc], \text{Exceptional } [pc'] \text{Enter}) \rangle \langle C \neq \text{ClassMain } P \rangle$
 $\langle \text{instrs-of } (\text{PROG } P) C M ! pc = \text{Invoke } M' n \rangle$ **obtain** d'
where $\text{match-ex-table } (\text{PROG } P) \text{NullPointer } pc$ (*ex-table-of* $(\text{PROG } P) C M$)
 $= \lfloor (pc', d') \rfloor$
by cases (*fastforce elim: JVMCFG.cases*)
hence $\exists (f, t, D, h, d) \in \text{set } (\text{ex-table-of } (\text{PROG } P) C M)$.
 $\text{matches-ex-entry } (\text{PROG } P) \text{NullPointer } pc (f, t, D, h, d) \wedge h = pc' \wedge d = d'$
by $-(\text{drule match-ex-table-SomeD})$
ultimately show $?case$ **using** $\langle \text{instrs-of } (\text{PROG } P) C M ! pc = \text{Invoke } M' n \rangle$
by (*fastforce simp: relevant-entries-def is-relevant-entry-def matches-ex-entry-def*)
next
case (*CFG-Invoke-Return-Exceptional-handle* $C P C0 \text{Main } M pc pc' M' n ek$)
hence $\text{TYPING } P C M ! pc \neq \text{None}$ **and** $pc < \text{length } (\text{instrs-of } (\text{PROG } P) C M)$
by *simp-all*
moreover from $\langle (P, C0, \text{Main}) \vdash \Rightarrow (C, M, [pc], \text{Exceptional } [pc'] \text{Return}) \rangle \langle C \neq \text{ClassMain } P \rangle$
obtain $Ts T mxs mxl_0$ **where**
 $\text{PROG } P \vdash C \text{ sees } M:Ts \rightarrow T = (mxs, mxl_0, \text{instrs-of } (\text{PROG } P) C M, \text{ex-table-of } (\text{PROG } P) C M)$ *in* C
by (*fastforce dest: method-of-reachable-node-exists*)
with $\langle pc < \text{length } (\text{instrs-of } (\text{PROG } P) C M) \rangle \langle \text{instrs-of } (\text{PROG } P) C M ! pc = \text{Invoke } M' n \rangle$
have $\text{PROG } P, T, mxs, \text{length } (\text{instrs-of } (\text{PROG } P) C M), \text{ex-table-of } (\text{PROG } P) C M$
 $\vdash \text{Invoke } M' n, pc :: \text{TYPING } P C M$
by (*fastforce dest!: wt-jvm-prog-impl-wt-instr [OF wf-jvmprog-is-wf-typp]*)
moreover from $\langle (P, C0, \text{Main}) \vdash \Rightarrow (C, M, [pc], \text{Exceptional } [pc'] \text{Return}) \rangle \langle C \neq \text{ClassMain } P \rangle$
 $\langle \text{instrs-of } (\text{PROG } P) C M ! pc = \text{Invoke } M' n \rangle$ **obtain** $d' \text{Exc}$
where $\text{match-ex-table } (\text{PROG } P) \text{Exc } pc$ (*ex-table-of* $(\text{PROG } P) C M$) $= \lfloor (pc', d') \rfloor$
by cases (*fastforce elim: JVMCFG.cases*)
hence $\exists (f, t, D, h, d) \in \text{set } (\text{ex-table-of } (\text{PROG } P) C M)$.
 $\text{matches-ex-entry } (\text{PROG } P) \text{Exc } pc (f, t, D, h, d) \wedge h = pc' \wedge d = d'$
by $-(\text{drule match-ex-table-SomeD})$
ultimately show $?case$ **using** $\langle \text{instrs-of } (\text{PROG } P) C M ! pc = \text{Invoke } M' n \rangle$
by (*fastforce simp: relevant-entries-def is-relevant-entry-def matches-ex-entry-def*)
next
case (*CFG-Invoke-Return-Check-Normal* $C P C0 \text{Main } M pc M' n ST LT ek$)
from $\langle (P, C0, \text{Main}) \vdash \Rightarrow (C, M, [pc], \text{Return}) \rangle \langle C \neq \text{ClassMain } P \rangle$
obtain $Ts T mxs mxl_0$ *is* xt **where** $\text{PROG } P \vdash C \text{ sees } M:Ts \rightarrow T = (mxs, mxl_0, \text{instrs-of } (\text{PROG } P) C M = is$
and $\text{instrs-of } (\text{PROG } P) C M = is$
by $-(\text{drule method-of-reachable-node-exists, auto})$
with *CFG-Invoke-Return-Check-Normal* **show** $?case$

by (*fastforce dest!:* *wt-jvm-prog-impl-wt-instr* [*OF wf-jvmprog-is-wf-typp*])
next
case (*Method-LTrue* *P C0 Main C M*)
from $\langle (P, C0, Main) \vdash \Rightarrow (C, M, None, Enter) \rangle \langle C \neq ClassMain P \rangle$
obtain *Ts T mxs mxl₀ is xt* **where** *PROG P* \vdash *C sees* $M:Ts \rightarrow T = (mxs, mxl_0, is, xt)$ *in C*
and *instrs-of* (*PROG P*) *C M* = *is*
by $-(drule\ method-of-reachable-node-exists, auto)$
with *Method-LTrue* **show** *?case*
by (*fastforce dest!:* *wt-jvm-prog-impl-wt-start* [*OF wf-jvmprog-is-wf-typp*] *simp: wt-start-def*)
next
case (*reachable-step* *P C0 Main C M opc nt ek C' M' opc' nt'*)
thus *?case*
by (*cases C = ClassMain P*) (*fastforce elim: JVMCFG.cases, simp*)
qed *simp-all*

lemma *reachable-node-impl-wt-instr:*
assumes $(P, C0, Main) \vdash \Rightarrow (C, M, [pc], nt)$
and $C \neq ClassMain P$
shows $\exists T\ mxs\ mpc\ xt.\ PROG\ P, T, mxs, mpc, xt \vdash (instrs-of\ (PROG\ P)\ C\ M\ !\ pc), pc :: TYPING\ P\ C\ M$
proof $-$
from $\langle C \neq ClassMain P \rangle \langle (P, C0, Main) \vdash \Rightarrow (C, M, [pc], nt) \rangle$
method-of-reachable-node-exists [*of P C0 Main C M [pc] nt*]
instr-of-reachable-node-typable [*of P C0 Main C M [pc] nt*]
obtain *Ts T mxs mxl₀ is xt*
where *PROG P* \vdash *C sees* $M:Ts \rightarrow T = (mxs, mxl_0, is, xt)$ *in C*
and *TYPING P C M ! pc* $\neq None$
and $pc < length\ (instrs-of\ (PROG\ P)\ C\ M)$
by *fastforce+*
with *wf-jvmprog-is-wf-typp* [*of P*]
have *PROG P, T, mxs, length is, xt* \vdash *instrs-of* (*PROG P*) *C M ! pc, pc :: TYPING P C M*
by (*fastforce dest!:* *wt-jvm-prog-impl-wt-instr*)
thus *?thesis*
by *blast*
qed

lemma
 $\llbracket (P, C0, Main) \vdash (C, M, pc, nt) -ek \rightarrow (C', M', pc', nt'); C \neq ClassMain P \vee C' \neq ClassMain P \rrbracket$
 $\Longrightarrow \exists T\ mb\ D.\ PROG\ P \vdash C0\ sees\ Main:\square \rightarrow T = mb\ in\ D$
and *reachable-node-impl-Main-ex:*
 $\llbracket (P, C0, Main) \vdash \Rightarrow (C, M, pc, nt); C \neq ClassMain P \rrbracket$
 $\Longrightarrow \exists T\ mb\ D.\ PROG\ P \vdash C0\ sees\ Main:\square \rightarrow T = mb\ in\ D$
by (*induct rule: JVMCFG-reachable-inducts*) *fastforce+*

end

theory *JVMInterpretation* **imports** *JVMCFG ../StaticInter/CFGExit* **begin**

3.2 Instatiation of the *CFG* locale

abbreviation *sourcenode* :: *cfg-edge* \Rightarrow *cfg-node*
where *sourcenode* *e* \equiv *fst e*

abbreviation *targetnode* :: *cfg-edge* \Rightarrow *cfg-node*
where *targetnode* *e* \equiv *snd(snd e)*

abbreviation *kind* :: *cfg-edge* \Rightarrow (*var*, *val*, *cname* \times *mname* \times *pc*, *cname* \times *mname*) *edge-kind*
where *kind* *e* \equiv *fst(snd e)*

definition *valid-edge* :: *jvm-method* \Rightarrow *cfg-edge* \Rightarrow *bool*
where *valid-edge* *P e* \equiv *P* \vdash (*sourcenode e*) $\text{--}(\textit{kind e})\text{--}$ (*targetnode e*)

fun *methods* :: *cname* \Rightarrow *JVMInstructions.jvm-method mdecl list* \Rightarrow ((*cname* \times *mname*) \times *var list* \times *var list*) *list*
where *methods* *C* [] = []
| *methods* *C* ((*M*, *Ts*, *T*, *mb*) # *ms*)
= ((*C*, *M*), *Heap* # (map *Local* [0..*Suc* (*length Ts*)]), [*Heap*, *Stack* 0, *Exception*]) # (*methods* *C* *ms*)

fun *procs* :: *jvm-prog* \Rightarrow ((*cname* \times *mname*) \times *var list* \times *var list*) *list*
where *procs* [] = []
| *procs* ((*C*, *D*, *fs*, *ms*) # *P*) = (*methods* *C* *ms*) @ (*procs* *P*)

lemma *in-set-methodsI*: *map-of ms M* = [(*Ts*, *T*, *mxs*, *mxl0*, *is*, *xt*)]
 \implies ((*C'*, *M*), *Heap* # map *Local* [0..*length Ts*] @ [*Local* (*length Ts*)], [*Heap*, *Stack* 0, *Exception*])
 \in *set* (*methods* *C'* *ms*)
by (*induct rule: methods.induct*) (*auto split: split-if-asm*)

lemma *in-methods-in-msD*: ((*C*, *M*), *ins*, *outs*) \in *set* (*methods* *D* *ms*)
 \implies *M* \in *set* (map *fst* *ms*) \wedge *D* = *C*
by (*induct ms*) *auto*

lemma *in-methods-in-msD'*: ((*C*, *M*), *ins*, *outs*) \in *set* (*methods* *D* *ms*)
 \implies \exists *Ts* *T* *mb*. (*M*, *Ts*, *T*, *mb*) \in *set* *ms*
 \wedge *D* = *C*
 \wedge *ins* = *Heap* # (map *Local* [0..*Suc* (*length Ts*)])
 \wedge *outs* = [*Heap*, *Stack* 0, *Exception*]
by (*induct rule: methods.induct*) *fastforce+*

lemma *in-set-methodsE*:
assumes ((*C*, *M*), *ins*, *outs*) \in *set* (*methods* *D* *ms*)

```

obtains  $Ts\ T\ mb$ 
where  $(M, Ts, T, mb) \in set\ ms$ 
and  $D = C$ 
and  $ins = Heap \# (map\ Local\ [0..<Suc\ (length\ Ts)])$ 
and  $outs = [Heap, Stack\ 0, Exception]$ 
using  $assms$ 
by  $(induct\ ms)\ fastforce+$ 

lemma  $in-set-procsI$ :
assumes  $sees: P \vdash D\ sees\ M: Ts \rightarrow T = mb\ in\ D$ 
and  $ins-def: ins = Heap \# map\ Local\ [0..<Suc\ (length\ Ts)]$ 
and  $outs-def: outs = [Heap, Stack\ 0, Exception]$ 
shows  $((D, M), ins, outs) \in set\ (procs\ P)$ 
proof -
from  $sees$  obtain  $D'\ fs\ ms$  where  $map-of\ P\ D = [(D', fs, ms)]$  and  $map-of$ 
 $ms\ M = [(Ts, T, mb)]$ 
by  $(fastforce\ dest: visible-method-exists\ simp: class-def)$ 
hence  $(D, D', fs, ms) \in set\ P$ 
by  $-(drule\ map-of-SomeD)$ 
thus  $?thesis$ 
proof  $(induct\ P)$ 
case  $Nil$  thus  $?case$  by  $simp$ 
next
case  $(Cons\ Class\ P)$ 
with  $ins-def\ outs-def$   $\langle map-of\ ms\ M = [(Ts, T, mb)] \rangle$  show  $?case$ 
by  $(cases\ Class, cases\ mb)\ (auto\ intro: in-set-methodsI)$ 
qed
qed

lemma  $distinct-methods: distinct\ (map\ fst\ ms) \implies distinct\ (map\ fst\ (methods\ C\ ms))$ 
proof  $(induct\ ms)$ 
case  $Nil$  thus  $?case$  by  $simp$ 
next
case  $(Cons\ M\ ms)$ 
thus  $?case$ 
by  $(cases\ M)\ (auto\ dest: in-methods-in-msD)$ 
qed

lemma  $in-set-procsD$ :
 $((C, M), ins, out) \in set\ (procs\ P) \implies \exists D\ fs\ ms. (C, D, fs, ms) \in set\ P \wedge M \in set\ (map\ fst\ ms)$ 
proof  $(induct\ P)$ 
case  $Nil$  thus  $?case$  by  $simp$ 
next
case  $(Cons\ Class\ P)$ 
thus  $?case$ 
by  $(cases\ Class)\ (fastforce\ dest: in-methods-in-msD\ intro: rev-image-eqI)$ 
qed

```

lemma *in-set-procsE'*:
assumes $((C, M), ins, outs) \in set (procs P)$
obtains $D fs ms Ts T mb$
where $(C, D, fs, ms) \in set P$
and $(M, Ts, T, mb) \in set ms$
and $ins = Heap \# (map (\lambda n. Local n) [0..<Suc (length Ts)])$
and $outs = [Heap, Stack 0, Exception]$
using *assms*
by $(induct P) (fastforce elim: in-set-methodsE)+$

lemma *distinct-Local-vars [simp]*: *distinct (map Local [0..<n])*
by $(induct n) auto$

lemma *distinct-Stack-vars [simp]*: *distinct (map Stack [0..<n])*
by $(induct n) auto$

inductive-set *get-return-edges* :: *wf-jvmprog* \Rightarrow *cfg-edge* \Rightarrow *cfg-edge set*
for $P :: wf-jvmprog$
and $a :: cfg-edge$
where
kind $a = Q:(C, M, pc) \leftrightarrow_{(D, M')} paramDefs$
 $\implies ((D, M', None, Return),$
 $(\lambda(s, ret). ret = (C, M, pc) \leftrightarrow_{(D, M')} (\lambda s s'. s'(Heap := s Heap, Exception :=$
 $s Exception,$
 $Stack (stkLength (P, C, M) (Suc pc) - 1)$
 $:= s (Stack 0))),$
 $(C, M, [pc], Return)) \in (get-return-edges P a)$

lemma *get-return-edgesE [elim!]*:
assumes $a \in get-return-edges P a'$
obtains $Q C M pc D M' paramDefs$ **where**
kind $a' = Q:(C, M, pc) \leftrightarrow_{(D, M')} paramDefs$
and $a = ((D, M', None, Return),$
 $(\lambda(s, ret). ret = (C, M, pc) \leftrightarrow_{(D, M')} (\lambda s s'. s'(Heap := s Heap, Exception :=$
 $s Exception,$
 $Stack (stkLength (P, C, M) (Suc pc) - 1) := s (Stack 0))),$
 $(C, M, [pc], Return))$
using *assms*
by $-(cases a, cases a', clarsimp, erule get-return-edges.cases, fastforce)$

lemma *distinct-class-names: distinct-fst (PROG P)*
using *wf-jvmprog-is-wf-typ [of P]*
by $(clarsimp simp: wf-jvm-prog-phi-def wf-prog-def)$

lemma *distinct-method-names:*
class (PROG P) C = [(D, fs, ms)] \implies distinct-fst ms
using *wf-jvmprog-is-wf-typ [of P]*
unfolding *wf-jvm-prog-phi-def*

by (fastforce dest: class-wf simp: wf-cdecl-def)

lemma *distinct-fst-is-distinct-fst*: *distinct-fst* = *BasicDefs.distinct-fst*
 by (simp add: *distinct-fst-def BasicDefs.distinct-fst-def*)

lemma *ClassMain-not-in-set-PROG* [dest!]: $(\text{ClassMain } P, D, fs, ms) \in \text{set } (\text{PROG } P) \implies \text{False}$
 using *distinct-class-names* [of *P*] *ClassMain-is-no-class* [of *P*]
 by (fastforce intro: *map-of-SomeI simp: class-def*)

lemma *in-set-procsE*:
 assumes $((C, M), ins, outs) \in \text{set } (\text{procs } (\text{PROG } P))$
 obtains *D fs ms Ts T mb*
 where *class* $(\text{PROG } P) C = [(D, fs, ms)]$
 and $\text{PROG } P \vdash C \text{ sees } M:Ts \rightarrow T = mb \text{ in } C$
 and $ins = \text{Heap} \# (\text{map } (\lambda n. \text{Local } n) [0..<\text{Suc } (\text{length } Ts)])$
 and $outs = [\text{Heap}, \text{Stack } 0, \text{Exception}]$
proof –
from $((C, M), ins, outs) \in \text{set } (\text{procs } (\text{PROG } P))$
obtain *D fs ms Ts T mxs mxl₀ is xt*
 where $(C, D, fs, ms) \in \text{set } (\text{PROG } P)$
 and $(M, Ts, T, mxs, mxl_0, is, xt) \in \text{set } ms$
 and $ins = \text{Heap} \# (\text{map } (\lambda n. \text{Local } n) [0..<\text{Suc } (\text{length } Ts)])$
 and $outs = [\text{Heap}, \text{Stack } 0, \text{Exception}]$
 by (fastforce elim: *in-set-procsE'*)
moreover from $(C, D, fs, ms) \in \text{set } (\text{PROG } P)$ *distinct-class-names* [of *P*]
have *class* $(\text{PROG } P) C = [(D, fs, ms)]$
 by (fastforce intro: *map-of-SomeI simp: class-def*)
moreover from *wf-jvmprog-is-wf-typ* [of *P*]
 $((M, Ts, T, mxs, mxl_0, is, xt) \in \text{set } ms) ((C, D, fs, ms) \in \text{set } (\text{PROG } P))$
have $\text{PROG } P \vdash C \text{ sees } M:Ts \rightarrow T = (mxs, mxl_0, is, xt) \text{ in } C$
 by (fastforce intro: *mdecl-visible simp: wf-jvm-prog-phi-def*)
ultimately show *?thesis* using $((\bigwedge D fs ms Ts T mb. [(class (\text{PROG } P) C = [(D, fs, ms)]); \text{PROG } P \vdash C \text{ sees } M: Ts \rightarrow T = mb \text{ in } C];$
 $ins = \text{Heap} \# \text{map } \text{Local } [0..<\text{Suc } (\text{length } Ts)]; outs = [\text{Heap}, \text{Stack } 0, \text{Exception}]])$
 $\implies \text{thesis})$
 by *blast*
qed

declare *has-method-def* [simp]

interpretation *JVMCFG-Interpret*:
CFG sourcenode targetnode kind valid-edge $(P, C0, \text{Main})$
 $(\text{ClassMain } P, \text{MethodMain } P, \text{None}, \text{Enter})$
 $(\lambda(C, M, pc, type). (C, M)) \text{get-return-edges } P$
 $((\text{ClassMain } P, \text{MethodMain } P), [], []) \# \text{procs } (\text{PROG } P) (\text{ClassMain } P, \text{MethodMain } P)$

```

for  $P\ C0\ Main$ 
proof (unfold-locales)
  fix  $e$ 
  assume valid-edge ( $P, C0, Main$ )  $e$ 
    and targetnode  $e = (ClassMain\ P, MethodMain\ P, None, Enter)$ 
  thus False
    by (auto simp: valid-edge-def)(erule JVMCFG.cases, auto)+
next
  show  $(\lambda(C, M, pc, type). (C, M)) (ClassMain\ P, MethodMain\ P, None, Enter)$ 
  =
    (ClassMain  $P, MethodMain\ P$ )
    by simp
next
  fix  $a\ Q\ r\ p\ fs$ 
  assume valid-edge ( $P, C0, Main$ )  $a$ 
    and kind  $a = Q:r\hookrightarrow_pfs$ 
    and sourcenode  $a = (ClassMain\ P, MethodMain\ P, None, Enter)$ 
  thus False
    by (auto simp: valid-edge-def) (erule JVMCFG.cases, auto)
next
  fix  $a\ a'$ 
  assume valid-edge ( $P, C0, Main$ )  $a$ 
    and valid-edge ( $P, C0, Main$ )  $a'$ 
    and sourcenode  $a = sourcenode\ a'$ 
    and targetnode  $a = targetnode\ a'$ 
  thus  $a = a'$ 
    by (cases a, cases a') (fastforce simp: valid-edge-def dest: JVMCFG-edge-det)
next
  fix  $a\ Q\ r\ f$ 
  assume valid-edge ( $P, C0, Main$ )  $a$ 
    and kind  $a = Q:r\hookrightarrow(ClassMain\ P, MethodMain\ P)^f$ 
  thus False
    by (clarsimp simp: valid-edge-def) (erule JVMCFG.cases, auto)
next
  fix  $a\ Q'\ f'$ 
  assume valid-edge ( $P, C0, Main$ )  $a$  and kind  $a = Q'\hookrightarrow(ClassMain\ P, MethodMain\ P)^{f'}$ 
  thus False
    by (clarsimp simp: valid-edge-def) (erule JVMCFG.cases, auto)+
next
  fix  $a\ Q\ r\ p\ fs$ 
  assume valid-edge ( $P, C0, Main$ )  $a$ 
    and kind  $a = Q:r\hookrightarrow_pfs$ 
  then obtain  $C\ M\ pc\ nt\ C'\ M'\ pc'\ nt'$ 
    where  $(P, C0, Main) \vdash (C, M, pc, nt) -Q:r\hookrightarrow_pfs\rightarrow (C', M', pc', nt')$ 
    by (cases a) (clarsimp simp: valid-edge-def)
  thus  $\exists ins\ outs.$ 
     $(p, ins, outs) \in set\ (((ClassMain\ P, MethodMain\ P), [], []) \# procs\ (PROG\ P))$ 
proof cases

```

```

    case (Main-Call T mxs mxl0 is xt initParams)
    hence ((C', Main), [Heap, Local 0], [Heap, Stack 0, Exception]) ∈ set (procs
(PROG P))
      and p = (C', Main)
      by (auto intro: in-set-procsI dest: sees-method-idemp)
    thus ?thesis by fastforce
  next
    case (CFG-Invoke-Call - n - - - Ts)
    hence ((C', M'), Heap # map (λn. Local n) [0.. $Suc$  (length Ts)],
[Heap, Stack 0, Exception]) ∈ set (procs (PROG P))
      and p = (C', M')
      by (auto intro: in-set-procsI dest: sees-method-idemp)
    thus ?thesis by fastforce
  qed simp-all
next
  fix a
  assume valid-edge (P, C0, Main) a and intra-kind (kind a)
  thus (λ(C, M, pc, type). (C, M)) (sourcenode a) =
    (λ(C, M, pc, type). (C, M)) (targetnode a)
  by (clarsimp simp: valid-edge-def) (erule JVMCFG.cases, auto simp: intra-kind-def)
next
  fix a Q r p fs
  assume valid-edge (P, C0, Main) a and kind a = Q:r↔pfs
  thus (λ(C, M, pc, type). (C, M)) (targetnode a) = p
  by (clarsimp simp: valid-edge-def) (erule JVMCFG.cases, auto)
next
  fix a Q' p f'
  assume valid-edge (P, C0, Main) a and kind a = Q'↔pf'
  thus (λ(C, M, pc, type). (C, M)) (sourcenode a) = p
  by (clarsimp simp: valid-edge-def) (erule JVMCFG.cases, auto)
next
  fix a Q r p fs
  assume valid-edge (P, C0, Main) a and kind a = Q:r↔pfs
  thus ∀ a'. valid-edge (P, C0, Main) a' ∧ targetnode a' = targetnode a
    → (∃ Qx rx fsx. kind a' = Qx:rx↔pfsx)
  by (cases a, clarsimp simp: valid-edge-def) (erule JVMCFG.cases, auto)+
next
  fix a Q' p f'
  assume valid-edge (P, C0, Main) a and kind a = Q'↔pf'
  thus ∀ a'. valid-edge (P, C0, Main) a' ∧ sourcenode a' = sourcenode a
    → (∃ Qx fx. kind a' = Qx↔pfx)
  by (cases a, clarsimp simp: valid-edge-def) (erule JVMCFG.cases, auto)+
next
  fix a Q r p fs
  assume valid-edge (P, C0, Main) a and kind a = Q:r↔pfs
  then have ∃ a'. a' ∈ get-return-edges P a
  by (cases p, cases r) (fastforce intro: get-return-edges.intros)
  then show get-return-edges P a ≠ {}
  by (simp only: ex-in-conv) simp

```

```

next
  fix  $a a'$ 
  assume  $\text{valid-edge } (P, C0, \text{Main}) a a' \in \text{get-return-edges } P a$ 
  then obtain  $Q C M pc D M' \text{ paramDefs}$ 
    where  $(P, C0, \text{Main}) \vdash \text{sourcenode } a - Q : (C, M, pc) \leftrightarrow (D, M') \text{ paramDefs} \rightarrow$ 
     $\text{targetnode } a$ 
    and  $\text{kind } a = Q : (C, M, pc) \leftrightarrow (D, M') \text{ paramDefs}$ 
    and  $a'\text{-def} : a' = ((D, M', \text{None}, \text{nodeType.Return}),$ 
     $\lambda(s, \text{ret}).$ 
     $\text{ret} = (C, M, pc) \leftrightarrow (D, M') \lambda s s'. s'(\text{Heap} := s \text{Heap}, \text{Exception} := s \text{Exception},$ 
     $\text{Stack } (\text{stkLength } (P, C, M) (\text{Suc } pc) - 1) := s (\text{Stack } 0)),$ 
     $C, M, [pc], \text{nodeType.Return})$ 
    by  $(\text{fastforce simp: valid-edge-def})$ 
  thus  $\text{valid-edge } (P, C0, \text{Main}) a'$ 
proof cases
  case  $(\text{Main-Call } T \text{ mxs } \text{m}x\text{l}0 \text{ is } \text{xt } D')$ 
  hence  $D = D'$  and  $M' = \text{Main}$ 
    by  $\text{simp-all}$ 
  with  $\langle (P, C0, \text{Main}) \vdash \Rightarrow (\text{ClassMain } P, \text{MethodMain } P, [0], \text{Normal}) \rangle$ 
   $\langle \text{PROG } P \vdash C0 \text{ sees Main} : [] \rightarrow T = (\text{mxs}, \text{m}x\text{l}0, \text{is}, \text{xt}) \text{ in } D' \rangle$ 
  have  $(P, C0, \text{Main}) \vdash \Rightarrow (D, M', \text{None}, \text{Enter})$ 
  by  $-(\text{rule reachable-step, fastforce, fastforce intro: JVMCFG-reachable.Main-Call})$ 
  hence  $(P, C0, \text{Main}) \vdash \Rightarrow (D, M', \text{None}, \text{nodeType.Return})$ 
  by  $-(\text{rule reachable-step, fastforce, fastforce intro: JVMCFG-reachable.Method-LFalse})$ 
  with  $a'\text{-def Main-Call}$  show  $?thesis$ 
  by  $(\text{fastforce intro: CFG-Return-from-Method JVMCFG-reachable.Main-Call}$ 
   $\text{simp: valid-edge-def})$ 
  next
  case  $(\text{CFG-Invoke-Call } \dots M'' \dots D')$ 
  hence  $D = D'$  and  $M' = M''$ 
    by  $\text{simp-all}$ 
  with  $\text{CFG-Invoke-Call}$ 
  have  $(P, C0, \text{Main}) \vdash \Rightarrow (D, M', \text{None}, \text{Enter})$ 
  by  $-(\text{rule reachable-step, fastforce, fastforce intro: JVMCFG-reachable.CFG-Invoke-Call})$ 
  hence  $(P, C0, \text{Main}) \vdash \Rightarrow (D, M', \text{None}, \text{nodeType.Return})$ 
  by  $-(\text{rule reachable-step, fastforce, fastforce intro: JVMCFG-reachable.Method-LFalse})$ 
  with  $a'\text{-def CFG-Invoke-Call}$  show  $?thesis$ 
  by  $(\text{fastforce intro: CFG-Return-from-Method JVMCFG-reachable.CFG-Invoke-Call}$ 
   $\text{simp: valid-edge-def})$ 
  qed  $\text{simp-all}$ 
next
  fix  $a a'$ 
  assume  $\text{valid-edge } (P, C0, \text{Main}) a$  and  $a' \in \text{get-return-edges } P a$ 
  thus  $\exists Q r p fs. \text{kind } a = Q : r \leftrightarrow p fs$ 
  by  $\text{clarsimp}$ 
next
  fix  $a Q r p fs a'$ 
  assume  $\text{valid-edge } (P, C0, \text{Main}) a$  and  $\text{kind } a = Q : r \leftrightarrow p fs$  and  $a' \in \text{get-return-edges}$ 
   $P a$ 

```



```

thus  $\exists Q' f'. \text{kind } a' = Q' \leftrightarrow pf'$ 
  by clarsimp
next
fix  $a Q' p f'$ 
assume valid-edge  $(P, C0, \text{Main}) a$  and  $\text{kind } a = Q' \leftrightarrow pf'$ 
show  $\exists !a'. \text{valid-edge } (P, C0, \text{Main}) a' \wedge$ 
   $(\exists Q r fs. \text{kind } a' = Q:r \leftrightarrow pfs) \wedge a \in \text{get-return-edges } P a'$ 
proof (rule ex-ex1I)
  from  $\langle \text{valid-edge } (P, C0, \text{Main}) a \rangle$ 
  have  $(P, C0, \text{Main}) \vdash \text{sourcenode } a -\text{kind } a \rightarrow \text{targetnode } a$ 
  by (clarsimp simp: valid-edge-def)
  from  $\langle \text{kind } a = Q' \leftrightarrow pf' \rangle$ 
  show  $\exists a'. \text{valid-edge } (P, C0, \text{Main}) a' \wedge (\exists Q r fs. \text{kind } a' = Q:r \leftrightarrow pfs)$ 
   $\wedge a \in \text{get-return-edges } P a'$ 
  by cases (cases a, fastforce intro: get-return-edges.intros[simplified] simp:
valid-edge-def)+
next
fix  $a' a''$ 
assume valid-edge  $(P, C0, \text{Main}) a'$ 
   $\wedge (\exists Q r fs. \text{kind } a' = Q:r \leftrightarrow pfs) \wedge a \in \text{get-return-edges } P a'$ 
  and valid-edge  $(P, C0, \text{Main}) a''$ 
   $\wedge (\exists Q r fs. \text{kind } a'' = Q:r \leftrightarrow pfs) \wedge a \in \text{get-return-edges } P a''$ 
thus  $a' = a''$ 
  by (cases a', cases a'', clarsimp simp: valid-edge-def)
  (erule JVMCFG.cases, simp-all, clarsimp? )+
qed
next
fix  $a a'$ 
assume valid-edge  $(P, C0, \text{Main}) a$  and  $a' \in \text{get-return-edges } P a$ 
thus  $\exists a''. \text{valid-edge } (P, C0, \text{Main}) a'' \wedge$ 
   $\text{sourcenode } a'' = \text{targetnode } a \wedge$ 
   $\text{targetnode } a'' = \text{sourcenode } a' \wedge \text{kind } a'' = (\lambda cf. \text{False})_{\surd}$ 
  by (clarsimp simp: valid-edge-def) (erule JVMCFG.cases, auto intro: JVMCFG-reachable.intros)
next
fix  $a a'$ 
assume valid-edge  $(P, C0, \text{Main}) a$  and  $a' \in \text{get-return-edges } P a$ 
thus  $\exists a''. \text{valid-edge } (P, C0, \text{Main}) a'' \wedge$ 
   $\text{sourcenode } a'' = \text{sourcenode } a \wedge$ 
   $\text{targetnode } a'' = \text{targetnode } a' \wedge \text{kind } a'' = (\lambda cf. \text{False})_{\surd}$ 
  by (clarsimp simp: valid-edge-def) (erule JVMCFG.cases, auto intro: JVMCFG-reachable.intros)
next
fix  $a Q r p fs$ 
assume valid-edge  $(P, C0, \text{Main}) a$  and  $\text{kind } a = Q:r \leftrightarrow pfs$ 
hence call:  $(P, C0, \text{Main}) \vdash \text{sourcenode } a -Q:r \leftrightarrow pfs \rightarrow \text{targetnode } a$ 
  by (clarsimp simp: valid-edge-def)
show  $\exists !a'. \text{valid-edge } (P, C0, \text{Main}) a' \wedge$ 
   $\text{sourcenode } a' = \text{sourcenode } a \wedge \text{intra-kind } (\text{kind } a')$ 
proof (rule ex-ex1I)
  from call

```

```

show  $\exists a'. \text{valid-edge } (P, C0, \text{Main}) a' \wedge \text{sourcenode } a' = \text{sourcenode } a \wedge$ 
intra-kind (kind  $a'$ )
  by cases (fastforce intro: JVMCFG-reachable.intros simp: intra-kind-def
valid-edge-def)+
  next
    fix  $a' a''$ 
    assume  $\text{valid-edge } (P, C0, \text{Main}) a' \wedge \text{sourcenode } a' = \text{sourcenode } a \wedge$ 
intra-kind (kind  $a'$ )
    and  $\text{valid-edge } (P, C0, \text{Main}) a'' \wedge \text{sourcenode } a'' = \text{sourcenode } a \wedge \text{intra-kind}$ 
(kind  $a''$ )
    with call show  $a' = a''$ 
    by (cases a, cases a', cases a'', clarsimp simp: valid-edge-def intra-kind-def)
(erule JVMCFG.cases, simp-all, clarsimp?)+
  qed
next
  fix  $a Q' p f'$ 
  assume  $\text{valid-edge } (P, C0, \text{Main}) a$  and  $\text{kind } a = Q' \leftrightarrow_{pf'} f'$ 
  hence return:  $(P, C0, \text{Main}) \vdash \text{sourcenode } a - Q' \leftrightarrow_{pf'} \rightarrow \text{targetnode } a$ 
  by (clarsimp simp: valid-edge-def)
  show  $\exists! a'. \text{valid-edge } (P, C0, \text{Main}) a' \wedge$ 
 $\text{targetnode } a' = \text{targetnode } a \wedge \text{intra-kind } (\text{kind } a')$ 
  proof (rule ex-ex1I)
    from return
    show  $\exists a'. \text{valid-edge } (P, C0, \text{Main}) a' \wedge \text{targetnode } a' = \text{targetnode } a \wedge$ 
intra-kind (kind  $a'$ )
    proof cases
      case (CFG-Return-from-Method C M C' M' pc' Q'' ps Q stateUpdate)
      hence [simp]:  $Q = Q'$  and [simp]:  $p = (C, M)$  and [simp]:  $f' = \text{stateUpdate}$ 
      by simp-all
      from  $(P, C0, \text{Main}) \vdash (C', M', \lfloor pc' \rfloor, \text{Normal}) - Q'' : (C', M', pc') \hookrightarrow (C, M) ps \rightarrow$ 
( $C, M, \text{None}, \text{Enter}$ )
      have invoke-reachable:  $(P, C0, \text{Main}) \vdash \Rightarrow (C', M', \lfloor pc' \rfloor, \text{Normal})$ 
      by  $-(\text{drule } \text{sourcenode-reachable})$ 
      show ?thesis
      proof (cases C' = ClassMain P)
        case True
        with invoke-reachable CFG-Return-from-Method show ?thesis
        by  $-(\text{erule JVMCFG.cases, simp-all, fastforce intro: Main-Call-LFalse simp: valid-edge-def intra-kind-def})$ 
      next
        case False
        with invoke-reachable CFG-Return-from-Method show ?thesis
        by  $-(\text{erule JVMCFG.cases, simp-all, fastforce intro: CFG-Invoke-False simp: valid-edge-def intra-kind-def})$ 
      qed
    qed simp-all
  next
    fix  $a' a''$ 
    assume  $\text{valid-edge } (P, C0, \text{Main}) a' \wedge \text{targetnode } a' = \text{targetnode } a \wedge \text{intra-kind}$ 

```

```

(kind a')
  and valid-edge (P, C0, Main) a''  $\wedge$  targetnode a'' = targetnode a  $\wedge$  intra-kind
(kind a')
  with return show a' = a''
  by (cases, auto, cases a, cases a', cases a'', clarsimp simp: valid-edge-def
intra-kind-def)
  (erule JVMCFG.cases, simp-all, clarsimp?)+
qed
next
fix a a' Q1 r1 p fs1 Q2 r2 fs2
assume valid-edge (P, C0, Main) a and valid-edge (P, C0, Main) a'
  and kind a = Q1:r1 $\hookrightarrow$ pfs1 and kind a' = Q2:r2 $\hookrightarrow$ pfs2
thus targetnode a = targetnode a'
  by (cases a, cases a', clarsimp simp: valid-edge-def)
  (erule JVMCFG.cases, simp-all, clarsimp?)+
next
from distinct-method-names [of P] distinct-class-names [of P]
have  $\bigwedge C D fs ms. (C, D, fs, ms) \in \text{set } (PROG P) \implies \text{distinct-fst } ms$ 
  by (fastforce intro: map-of-SomeI simp: class-def)
moreover {
  fix P
  assume distinct-fst (P :: jvm-prog)
  and  $\bigwedge C D fs ms. (C, D, fs, ms) \in \text{set } P \implies \text{distinct-fst } ms$ 
  hence distinct-fst (procs P)
  by (induct P, simp)
  (fastforce intro: equalsOI rev-image-eqI dest: in-methods-in-msD in-set-procsD
  simp: distinct-methods distinct-fst-def)
}
ultimately have distinct-fst (procs (PROG P)) using distinct-class-names [of
P]
  by blast
hence BasicDefs.distinct-fst (procs (PROG P))
  by (simp add: distinct-fst-is-distinct-fst)
thus BasicDefs.distinct-fst (((ClassMain P, MethodMain P), [], []) # procs
(PROG P))
  by (fastforce elim: in-set-procsE)
next
fix C M P p ins outs
assume (p, ins, outs)  $\in \text{set } (((C, M), [], []) \# \text{procs } P)$ 
thus distinct ins
proof (induct P)
  case Nil
  thus ?case by simp
next
case (Cons Cl P)
then obtain C D fs ms where Cl = (C, D, fs, ms)
  by (cases Cl) blast
with Cons show ?case
  by auto (induct ms, auto)

```

```

qed
next
fix C M P p ins outs
assume (p, ins, outs) ∈ set (((C, M), [], []) # procs P)
thus distinct outs
proof (induct P)
  case Nil
  thus ?case by simp
next
case (Cons Cl P)
then obtain C D fs ms where Cl = (C, D, fs, ms)
  by (cases Cl) blast
with Cons show ?case
  by auto (induct ms, auto)
qed
qed

```

interpretation *JVMCFG-Exit-Interpret:*

```

CFGExit sourcenode targetnode kind valid-edge (P, C0, Main)
(ClassMain P, MethodMain P, None, Enter)
(λ(C, M, pc, type). (C, M)) get-return-edges P
((ClassMain P, MethodMain P), [], []) # procs (PROG P)
(ClassMain P, MethodMain P) (ClassMain P, MethodMain P, None, Return)
for P C0 Main
proof (unfold-locales)
  fix a
  assume valid-edge (P, C0, Main) a
  and sourcenode a = (ClassMain P, MethodMain P, None, nodeType.Return)
  thus False
  by (cases a, clarsimp simp: valid-edge-def) (erule JVMCFG.cases, simp-all,
 clarsimp)
next
  show (λ(C, M, pc, type). (C, M)) (ClassMain P, MethodMain P, None, node-
  Type.Return) =
  (ClassMain P, MethodMain P)
  by simp
next
  fix a Q p f
  assume valid-edge (P, C0, Main) a
  and kind a = Q↔pf
  and targetnode a = (ClassMain P, MethodMain P, None, nodeType.Return)
  thus False
  by (cases a, clarsimp simp: valid-edge-def) (erule JVMCFG.cases, simp-all)
next
  show ∃ a. valid-edge (P, C0, Main) a ∧
  sourcenode a = (ClassMain P, MethodMain P, None, Enter) ∧
  targetnode a = (ClassMain P, MethodMain P, None, nodeType.Return) ∧
  kind a = (λs. False)√
  by (fastforce intro: JVMCFG-reachable.intros simp: valid-edge-def)

```

qed

end

theory JVMCFG-wf **imports** JVMInterpretation ../StaticInter/CFGExit-wf **begin**

inductive-set Def :: wf-jvmprog \Rightarrow cfg-node \Rightarrow var set

for P :: wf-jvmprog

and n :: cfg-node

where

Def-Main-Heap:

n = (ClassMain P, MethodMain P, [0], Return)

\Rightarrow Heap \in Def P n

| Def-Main-Exception:

n = (ClassMain P, MethodMain P, [0], Return)

\Rightarrow Exception \in Def P n

| Def-Main-Stack-0:

n = (ClassMain P, MethodMain P, [0], Return)

\Rightarrow Stack 0 \in Def P n

| Def-Load:

\llbracket n = (C, M, [pc], Enter);

C \neq ClassMain P;

instrs-of (PROG P) C M ! pc = Load idx;

i = stkLength (P, C, M) pc \rrbracket

\Rightarrow Stack i \in Def P n

| Def-Store:

\llbracket n = (C, M, [pc], Enter);

C \neq ClassMain P;

instrs-of (PROG P) C M ! pc = Store idx \rrbracket

\Rightarrow Local idx \in Def P n

| Def-Push:

\llbracket n = (C, M, [pc], Enter);

C \neq ClassMain P;

instrs-of (PROG P) C M ! pc = Push v;

i = stkLength (P, C, M) pc \rrbracket

\Rightarrow Stack i \in Def P n

| Def-IAdd:

\llbracket n = (C, M, [pc], Enter);

C \neq ClassMain P;

instrs-of (PROG P) C M ! pc = IAdd;

i = stkLength (P, C, M) pc - 2 \rrbracket

\Rightarrow Stack i \in Def P n

| Def-CmpEq:

\llbracket n = (C, M, [pc], Enter);

C \neq ClassMain P;

instrs-of (PROG P) C M ! pc = CmpEq;

i = stkLength (P, C, M) pc - 2 \rrbracket

\Rightarrow Stack i \in Def P n

| Def-New-Heap:

$\llbracket n = (C, M, [pc], Normal);$
 $C \neq \text{ClassMain } P;$
 $\text{instrs-of } (PROG P) C M ! pc = \text{New } Cl \rrbracket$
 $\implies \text{Heap} \in \text{Def } P n$

| *Def-New-Stack:*
 $\llbracket n = (C, M, [pc], Normal);$
 $C \neq \text{ClassMain } P;$
 $\text{instrs-of } (PROG P) C M ! pc = \text{New } Cl;$
 $i = \text{stkLength } (P, C, M) pc \rrbracket$
 $\implies \text{Stack } i \in \text{Def } P n$

| *Def-Exception:*
 $\llbracket n = (C, M, [pc], \text{Exceptional } pco nt);$
 $C \neq \text{ClassMain } P \rrbracket$
 $\implies \text{Exception} \in \text{Def } P n$

| *Def-Exception-handle:*
 $\llbracket n = (C, M, [pc], \text{Exceptional } [pc'] \text{ Enter});$
 $C \neq \text{ClassMain } P;$
 $i = \text{stkLength } (P, C, M) pc' - 1 \rrbracket$
 $\implies \text{Stack } i \in \text{Def } P n$

| *Def-Exception-handle-return:*
 $\llbracket n = (C, M, [pc], \text{Exceptional } [pc'] \text{ Return});$
 $C \neq \text{ClassMain } P;$
 $i = \text{stkLength } (P, C, M) pc' - 1 \rrbracket$
 $\implies \text{Stack } i \in \text{Def } P n$

| *Def-Getfield:*
 $\llbracket n = (C, M, [pc], Normal);$
 $C \neq \text{ClassMain } P;$
 $\text{instrs-of } (PROG P) C M ! pc = \text{Getfield } Cl Fd;$
 $i = \text{stkLength } (P, C, M) pc - 1 \rrbracket$
 $\implies \text{Stack } i \in \text{Def } P n$

| *Def-Putfield:*
 $\llbracket n = (C, M, [pc], Normal);$
 $C \neq \text{ClassMain } P;$
 $\text{instrs-of } (PROG P) C M ! pc = \text{Putfield } Cl Fd \rrbracket$
 $\implies \text{Heap} \in \text{Def } P n$

| *Def-Invoke-Return-Heap:*
 $\llbracket n = (C, M, [pc], \text{Return});$
 $C \neq \text{ClassMain } P;$
 $\text{instrs-of } (PROG P) C M ! pc = \text{Invoke } M' n' \rrbracket$
 $\implies \text{Heap} \in \text{Def } P n$

| *Def-Invoke-Return-Exception:*
 $\llbracket n = (C, M, [pc], \text{Return});$
 $C \neq \text{ClassMain } P;$
 $\text{instrs-of } (PROG P) C M ! pc = \text{Invoke } M' n' \rrbracket$
 $\implies \text{Exception} \in \text{Def } P n$

| *Def-Invoke-Return-Stack:*
 $\llbracket n = (C, M, [pc], \text{Return});$
 $C \neq \text{ClassMain } P;$
 $\text{instrs-of } (PROG P) C M ! pc = \text{Invoke } M' n';$

$i = \text{stkLength } (P, C, M) (\text{Suc } pc) - 1 \]$
 $\implies \text{Stack } i \in \text{Def } P \ n$
| *Def-Invoke-Call-Heap*:
 $\llbracket n = (C, M, \text{None}, \text{Enter});$
 $C \neq \text{ClassMain } P \]$
 $\implies \text{Heap} \in \text{Def } P \ n$
| *Def-Invoke-Call-Local*:
 $\llbracket n = (C, M, \text{None}, \text{Enter});$
 $C \neq \text{ClassMain } P;$
 $i < \text{locLength } (P, C, M) \ 0 \]$
 $\implies \text{Local } i \in \text{Def } P \ n$
| *Def-Return*:
 $\llbracket n = (C, M, \lfloor pc \rfloor, \text{Enter});$
 $C \neq \text{ClassMain } P;$
 $\text{instrs-of } (\text{PROG } P) \ C \ M \ ! \ pc = \text{instr.Return} \]$
 $\implies \text{Stack } 0 \in \text{Def } P \ n$

inductive-set $\text{Use} :: \text{wf-jvmprog} \Rightarrow \text{cfg-node} \Rightarrow \text{var set}$
for $P :: \text{wf-jvmprog}$
and $n :: \text{cfg-node}$

where

Use-Main-Heap:
 $n = (\text{ClassMain } P, \text{MethodMain } P, \lfloor 0 \rfloor, \text{Normal})$
 $\implies \text{Heap} \in \text{Use } P \ n$
| *Use-Load*:
 $\llbracket n = (C, M, \lfloor pc \rfloor, \text{Enter});$
 $C \neq \text{ClassMain } P;$
 $\text{instrs-of } (\text{PROG } P) \ C \ M \ ! \ pc = \text{Load } \text{idx} \]$
 $\implies \text{Local } \text{idx} \in \text{Use } P \ n$
| *Use-Enter-Stack*:
 $\llbracket n = (C, M, \lfloor pc \rfloor, \text{Enter});$
 $C \neq \text{ClassMain } P;$
 $\text{case } (\text{instrs-of } (\text{PROG } P) \ C \ M \ ! \ pc)$
 $\text{of Store } n' \Rightarrow d = 1$
| $\text{Getfield } F \ Cl \Rightarrow d = 1$
| $\text{Putfield } F \ Cl \Rightarrow d = 2$
| $\text{Checkcast } Cl \Rightarrow d = 1$
| $\text{Invoke } M' \ n' \Rightarrow d = \text{Suc } n'$
| $\text{IAdd} \Rightarrow d \in \{1, 2\}$
| $\text{IfFalse } i \Rightarrow d = 1$
| $\text{CmpEq} \Rightarrow d \in \{1, 2\}$
| $\text{Throw} \Rightarrow d = 1$
| $\text{instr.Return} \Rightarrow d = 1$
| $- \Rightarrow \text{False};$
 $i = \text{stkLength } (P, C, M) \ pc - d \]$
 $\implies \text{Stack } i \in \text{Use } P \ n$
| *Use-Enter-Local*:
 $\llbracket n = (C, M, \lfloor pc \rfloor, \text{Enter});$
 $C \neq \text{ClassMain } P;$

$\text{instrs-of } (\text{PROG } P) C M ! pc = \text{Load } n' \llbracket$
 $\implies \text{Local } n' \in \text{Use } P n$
| *Use-Enter-Heap*:
 $\llbracket n = (C, M, [pc], \text{Enter});$
 $C \neq \text{ClassMain } P;$
 $\text{case } (\text{instrs-of } (\text{PROG } P) C M ! pc)$
 $\text{of New } Cl \Rightarrow \text{True}$
 $\quad | \text{Checkcast } Cl \Rightarrow \text{True}$
 $\quad | \text{Throw} \Rightarrow \text{True}$
 $\quad | - \Rightarrow \text{False} \rrbracket$
 $\implies \text{Heap} \in \text{Use } P n$
| *Use-Normal-Heap*:
 $\llbracket n = (C, M, [pc], \text{Normal});$
 $C \neq \text{ClassMain } P;$
 $\text{case } (\text{instrs-of } (\text{PROG } P) C M ! pc)$
 $\text{of New } Cl \Rightarrow \text{True}$
 $\quad | \text{Getfield } F Cl \Rightarrow \text{True}$
 $\quad | \text{Putfield } F Cl \Rightarrow \text{True}$
 $\quad | \text{Invoke } M' n' \Rightarrow \text{True}$
 $\quad | - \Rightarrow \text{False} \rrbracket$
 $\implies \text{Heap} \in \text{Use } P n$
| *Use-Normal-Stack*:
 $\llbracket n = (C, M, [pc], \text{Normal});$
 $C \neq \text{ClassMain } P;$
 $\text{case } (\text{instrs-of } (\text{PROG } P) C M ! pc)$
 $\text{of Getfield } F Cl \Rightarrow d = 1$
 $\quad | \text{Putfield } F Cl \Rightarrow d \in \{1, 2\}$
 $\quad | \text{Invoke } M' n' \Rightarrow d > 0 \wedge d \leq \text{Suc } n'$
 $\quad | - \Rightarrow \text{False};$
 $i = \text{stkLength } (P, C, M) pc - d \rrbracket$
 $\implies \text{Stack } i \in \text{Use } P n$
| *Use-Return-Heap*:
 $\llbracket n = (C, M, [pc], \text{Return});$
 $\text{instrs-of } (\text{PROG } P) C M ! pc = \text{Invoke } M' n' \vee C = \text{ClassMain } P \rrbracket$
 $\implies \text{Heap} \in \text{Use } P n$
| *Use-Return-Stack*:
 $\llbracket n = (C, M, [pc], \text{Return});$
 $(\text{instrs-of } (\text{PROG } P) C M ! pc = \text{Invoke } M' n' \wedge i = \text{stkLength } (P, C, M) (\text{Suc}$
 $pc) - 1) \vee$
 $(C = \text{ClassMain } P \wedge i = 0) \rrbracket$
 $\implies \text{Stack } i \in \text{Use } P n$
| *Use-Return-Exception*:
 $\llbracket n = (C, M, [pc], \text{Return});$
 $\text{instrs-of } (\text{PROG } P) C M ! pc = \text{Invoke } M' n' \vee C = \text{ClassMain } P \rrbracket$
 $\implies \text{Exception} \in \text{Use } P n$
| *Use-Exceptional-Stack*:
 $\llbracket n = (C, M, [pc], \text{Exceptional } \text{opc}' nt);$
 $\text{case } (\text{instrs-of } (\text{PROG } P) C M ! pc)$
 $\text{of Throw} \Rightarrow \text{True}$


```

    | - ⇒ False;
    i = stkLength (P, C, M) pc - 1 ]
    ⇒ Stack i ∈ Use P n
| Use-Exceptional-Exception:
  [ n = (C, M, [pc], Exceptional [pc'] Return);
  instrs-of (PROG P) C M ! pc = Invoke M' n' ]
  ⇒ Exception ∈ Use P n
| Use-Method-Leave-Exception:
  [ n = (C, M, None, Return);
  C ≠ ClassMain P ]
  ⇒ Exception ∈ Use P n
| Use-Method-Leave-Heap:
  [ n = (C, M, None, Return);
  C ≠ ClassMain P ]
  ⇒ Heap ∈ Use P n
| Use-Method-Leave-Stack:
  [ n = (C, M, None, Return);
  C ≠ ClassMain P ]
  ⇒ Stack 0 ∈ Use P n
| Use-Method-Entry-Heap:
  [ n = (C, M, None, Enter);
  C ≠ ClassMain P ]
  ⇒ Heap ∈ Use P n
| Use-Method-Entry-Local:
  [ n = (C, M, None, Enter);
  C ≠ ClassMain P;
  i < locLength (P, C, M) 0 ]
  ⇒ Local i ∈ Use P n

```

fun ParamDefs :: wf-jvmprog ⇒ cfg-node ⇒ var list

where

```

  ParamDefs P (C, M, [pc], Return) = [Heap, Stack (stkLength (P, C, M) (Suc
pc) - 1), Exception]
  | ParamDefs P (C, M, opc, nt) = []

```

function ParamUses :: wf-jvmprog ⇒ cfg-node ⇒ var set list

where

```

  ParamUses P (ClassMain P, MethodMain P, [0], Normal) = [{Heap},{}]
  |
  M ≠ MethodMain P ∨ opc ≠ [0] ∨ nt ≠ Normal
  ⇒ ParamUses P (ClassMain P, M, opc, nt) = []
  |
  C ≠ ClassMain P
  ⇒ ParamUses P (C, M, opc, nt) = (case opc of None ⇒ []
  | [pc] ⇒ (case nt of Normal ⇒ (case (instrs-of (PROG P) C M ! pc) of
  Invoke M' n ⇒ (
    {Heap} # rev (map (λn. {Stack (stkLength (P, C, M) pc - (Suc n))}))
  [0..<n + 1])
  )

```

```

      | - ⇒ []
      | - ⇒ []
    )
  )
)
by atomize-elim auto
termination by lexicographic-order

```

lemma *in-set-ParamDefsE*:

```

[[ V ∈ set (ParamDefs P n);
  ∧ C M pc. [[ n = (C, M, [pc], Return);
              V ∈ {Heap, Stack (stkLength (P, C, M) (Suc pc) - 1), Exception} ] ] ⇒
thesis ]
⇒ thesis
by (cases (P, n) rule: ParamDefs.cases) auto

```

lemma *in-set-ParamUsesE*:

```

assumes V-in-ParamUses: V ∈ ⋃ set (ParamUses P n)
obtains n = (ClassMain P, MethodMain P, [0], Normal) and V = Heap
| C M pc M' n' i where n = (C, M, [pc], Normal) and instrs-of (PROG P)
C M ! pc = Invoke M' n'
and V = Heap ∨ V = Stack (stkLength (P, C, M) pc - Suc i) and i < Suc
n' and C ≠ ClassMain P
proof (cases (P, n) rule: ParamUses.cases)
case 1 with V-in-ParamUses that show ?thesis by clarsimp
next
case 2 with V-in-ParamUses that show ?thesis by clarsimp
next
case (3 C P M pc nt)
with V-in-ParamUses that show ?thesis
using [[simproc del: list-to-set-comprehension]] by (cases nt, auto) (case-tac
instrs-of (PROG P) C M ! a, simp-all, fastforce)
qed

```

lemma *sees-method-fun-wf*:

```

assumes PROG P ⊢ D sees M': Ts→T = (mxs, mxl0, is, xt) in D
and (D, D', fs, ms) ∈ set (PROG P)
and (M', Ts', T', mxs', mxl0', is', xt') ∈ set ms
shows Ts = Ts' ∧ T = T' ∧ mxs = mxs' ∧ mxl0 = mxl0' ∧ is = is' ∧ xt = xt'
proof -
from distinct-class-names [of P] ⟨(D, D', fs, ms) ∈ set (PROG P)⟩
have class (PROG P) D = [(D', fs, ms)]
by (fastforce intro: map-of-SomeI simp: class-def)
moreover with distinct-method-names have distinct-fst ms
by fastforce
ultimately show ?thesis using
⟨PROG P ⊢ D sees M': Ts→T = (mxs, mxl0, is, xt) in D⟩
⟨(M', Ts', T', mxs', mxl0', is', xt') ∈ set ms⟩
by (fastforce dest: visible-method-exists map-of-SomeD distinct-fst-isin-same-fst
simp: distinct-fst-is-distinct-fst)

```

qed

interpretation *JVMCFG-wf*:

CFG-wf sourcenode targetnode kind valid-edge (P, C0, Main)
(ClassMain P, MethodMain P, None, Enter)
(λ(C, M, pc, type). (C, M)) get-return-edges P
((ClassMain P, MethodMain P), [], []) # procs (PROG P)
(ClassMain P, MethodMain P)
Def P Use P ParamDefs P ParamUses P
for *P C0 Main*
proof *(unfold-locales)*
show *Def P (ClassMain P, MethodMain P, None, Enter) = {}* \wedge
Use P (ClassMain P, MethodMain P, None, Enter) = {}
by *(fastforce elim: Def.cases Use.cases)*
next
fix *a Q r p fs ins outs*
assume *valid-edge (P, C0, Main) a*
and *kind a = Q:r↦pfs*
and *params: (p, ins, outs) ∈ set (((ClassMain P, MethodMain P), [], []) #*
procs (PROG P))
hence *(P, C0, Main) ⊢ sourcenode a -Q:r↦pfs→ targetnode a*
by *(simp add: valid-edge-def)*
from *this params show length (ParamUses P (sourcenode a)) = length ins*
proof *cases*
case *Main-Call*
with *params show ?thesis*
by *auto (erule in-set-procsE, auto dest: sees-method-idemp sees-method-fun)*
next
case *(CFG-Invoke-Call C M pc M' n ST LT D' Ts T mxs mxl0 is xt D Q'*
paramDefs)
hence *[simp]: Q' = Q and [simp]: r = (C, M, pc) and [simp]: p = (D, M')*
and *[simp]: fs = paramDefs*
by *simp-all*
from *CFG-Invoke-Call obtain T' mxs' mpc' xt' where*
PROG P, T', mxs', mpc', xt' ⊢ instrs-of (PROG P) C M ! pc, pc :: TYPING P
C M
by *(blast dest: reachable-node-impl-wt-instr)*
moreover from *(PROG P ⊢ D' sees M': Ts→T = (mxs, mxl0, is, xt) in D)*
have *PROG P ⊢ D sees M': Ts→T = (mxs, mxl0, is, xt) in D*
by *-(drule sees-method-idemp)*
with *params have PROG P ⊢ D sees M': Ts→T=(mxs, mxl0, is, xt) in D*
and *ins = Heap # map Local [0..*Suc (length Ts)*]*
by *(fastforce elim: in-set-procsE dest: sees-method-fun)+*
ultimately show *?thesis using CFG-Invoke-Call*
by *(fastforce dest: sees-method-fun list-all2-lengthD simp: min-def)*
qed *simp-all*
next
fix *a*
assume *valid-edge (P, C0, Main) a*

```

thus distinct (ParamDefs P (targetnode a))
  by (clarsimp simp: valid-edge-def) (erule JVMCFG.cases, auto)
next
  fix a Q' p f' ins outs
  assume valid-edge (P, C0, Main) a
  and kind a = Q'  $\leftrightarrow$  p f'
  and params: (p, ins, outs)  $\in$  set (((ClassMain P, MethodMain P), [], []) #
procs (PROG P))
  hence (P, C0, Main)  $\vdash$  sourcenode a  $-$  Q'  $\leftrightarrow$  p f'  $\rightarrow$  targetnode a
  by (simp add: valid-edge-def)
  from this params
  show length (ParamDefs P (targetnode a)) = length outs
  by cases (auto elim: in-set-procsE)
next
  fix n V
  assume params: V  $\in$  set (ParamDefs P n)
  and vn: CFG.valid-node sourcenode targetnode (valid-edge (P, C0, Main)) n
  then obtain ek n'
  where ve: valid-edge (P, C0, Main) (n, ek, n')  $\vee$  valid-edge (P, C0, Main) (n',
ek, n)
  by (fastforce simp: JVMCFG-Interpret.valid-node-def)
  from params obtain C M pc where [simp]: n = (C, M, [pc], Return)
  and V: V  $\in$  {Heap, Stack (stkLength (P, C, M) (Suc pc) - 1), Exception}
  by (blast elim: in-set-ParamDefsE)
  from ve show V  $\in$  Def P n
proof
  assume valid-edge (P, C0, Main) (n, ek, n')
  thus ?thesis unfolding valid-edge-def
  proof cases
    case Main-Return-to-Exit with V show ?thesis
    by (auto intro: Def-Main-Heap Def-Main-Stack-0 Def-Main-Exception simp:
stkLength-def)
  next
    case CFG-Invoke-Return-Check-Normal with V show ?thesis
    by (fastforce intro: Def-Invoke-Return-Heap
      Def-Invoke-Return-Stack Def-Invoke-Return-Exception)
  next
    case CFG-Invoke-Return-Check-Exceptional with V show ?thesis
    by (fastforce intro: Def-Invoke-Return-Heap
      Def-Invoke-Return-Stack Def-Invoke-Return-Exception)
  next
    case CFG-Invoke-Return-Exceptional-prop with V show ?thesis
    by (fastforce intro: Def-Invoke-Return-Heap
      Def-Invoke-Return-Stack Def-Invoke-Return-Exception)
  qed simp-all
next
  assume valid-edge (P, C0, Main) (n', ek, n)
  thus ?thesis unfolding valid-edge-def
  proof cases

```

```

    case Main-Call-LFalse with V show ?thesis
      by (auto intro: Def-Main-Heap Def-Main-Stack-0 Def-Main-Exception simp:
stkLength-def)
    next
    case CFG-Invoke-False with V show ?thesis
      by (fastforce intro: Def-Invoke-Return-Heap
          Def-Invoke-Return-Stack Def-Invoke-Return-Exception)
    next
    case CFG-Return-from-Method with V show ?thesis
      by (fastforce elim!: JVMCFG.cases intro!: Def-Main-Stack-0
          intro: Def-Main-Heap Def-Main-Exception Def-Invoke-Return-Heap
          Def-Invoke-Return-Exception Def-Invoke-Return-Stack simp: stkLength-def)
    qed simp-all
  qed
next
fix a Q r p fs ins outs V
assume ve: valid-edge (P, C0, Main) a
and kind: kind a = Q:r↪pfs
and params: (p, ins, outs) ∈ set (((ClassMain P, MethodMain P), [], []) #
procs (PROG P))
and V: V ∈ set ins
from params V obtain D fs ms Ts T mb where class (PROG P) (fst p) = [(D,
fs, ms)]
and method: PROG P ⊢ (fst p) sees (snd p): Ts→T = mb in (fst p)
and ins: ins = Heap # map Local [0..

```

```

    dest: sees-method-idemp wt-jvm-prog-impl-wt-start[OF wf-jvmprog-is-wf-ty]
list-all2-lengthD
    simp: locLength-def min-def wt-start-def)
qed simp-all
next
fix a Q r p fs
assume valid-edge (P, C0, Main) a and kind a = Q:r↦pfs
thus Def P (sourcnode a) = {} unfolding valid-edge-def
  by cases (auto elim: Def.cases)
next
fix n V
assume CFG.valid-node sourcnode targetnode (valid-edge (P, C0, Main)) n
  and V: V ∈ ⋃ set (ParamUses P n)
then obtain ek n'
  where ve:valid-edge (P, C0, Main) (n, ek, n') ∨ valid-edge (P, C0, Main) (n',
ek, n)
  by (fastforce simp: JVMCFG-Interpret.valid-node-def)
from V obtain C M pc M' n'' i where
  V: n = (ClassMain P, MethodMain P, [0], Normal) ∧ V = Heap ∨
  n = (C, M, [pc], Normal) ∧ instrs-of (PROG P) C M ! pc = Invoke M' n''
∧
  (V = Heap ∨ V = Stack (stkLength (P, C, M) pc - Suc i) ∧ i < Suc n''
∧ C ≠ ClassMain P
  by -(erule in-set-ParamUsesE, fastforce+)
from ve show V ∈ Use P n
proof
  assume valid-edge (P, C0, Main) (n, ek, n')
  from this V show ?thesis unfolding valid-edge-def
  proof cases
    case Main-Call-LFalse with V show ?thesis by (fastforce intro: Use-Main-Heap)
  next
    case Main-Call with V show ?thesis by (fastforce intro: Use-Main-Heap)
  next
    case CFG-Invoke-Call with V show ?thesis
    by (fastforce intro: Use-Normal-Heap Use-Normal-Stack [where d=Suc i])
  next
    case CFG-Invoke-False with V show ?thesis
    by (fastforce intro: Use-Normal-Heap Use-Normal-Stack [where d=Suc i])
  qed simp-all
next
assume valid-edge (P, C0, Main) (n', ek, n)
from this V show ?thesis unfolding valid-edge-def
proof cases
  case Main-to-Call with V show ?thesis by (fastforce intro: Use-Main-Heap)
  next
    case CFG-Invoke-Check-NP-Normal with V show ?thesis
    by (fastforce intro: Use-Normal-Heap Use-Normal-Stack [where d=Suc i])
  qed simp-all
qed

```

```

next
  fix a Q p f ins outs V
  assume valid-edge (P, C0, Main) a
    and kind a = Q $\leftrightarrow$ pf
    and (p, ins, outs)  $\in$  set (((ClassMain P, MethodMain P), [], []) # procs (PROG P))
    and V  $\in$  set outs
  thus V  $\in$  Use P (sourcenode a) unfolding valid-edge-def
    by (cases, simp-all)
    (fastforce elim: in-set-procsE
     intro: Use-Method-Leave-Heap Use-Method-Leave-Stack Use-Method-Leave-Exception)
next
  fix a V s
  assume ve: valid-edge (P, C0, Main) a
    and V-notin-Def: V  $\notin$  Def P (sourcenode a)
    and ik: intra-kind (kind a)
    and pred: JVMCFG-Interpret.pred (kind a) s
  show JVMCFG-Interpret.state-val
    (CFG.transfer (((ClassMain P, MethodMain P), [], []) # procs (PROG P))
     (kind a) s) V
    = JVMCFG-Interpret.state-val s V
  proof (cases s)
    case Nil
      thus ?thesis by simp
    next
      case Cons [simp]
        with ve V-notin-Def ik pred show ?thesis unfolding valid-edge-def
        proof cases
          case CFG-Load with V-notin-Def show ?thesis by (fastforce intro: Def-Load)
          next case CFG-Store with V-notin-Def show ?thesis by (fastforce intro:
            Def-Store)
          next case CFG-Push with V-notin-Def show ?thesis by (fastforce intro:
            Def-Push)
          next case CFG-IAdd with V-notin-Def show ?thesis by (fastforce intro:
            Def-IAdd)
          next case CFG-CmpEq with V-notin-Def show ?thesis by (fastforce intro:
            Def-CmpEq)
          next case CFG-New-Update with V-notin-Def show ?thesis
            by (fastforce intro: Def-New-Heap Def-New-Stack)
          next case CFG-New-Exceptional-prop with V-notin-Def show ?thesis
            by (fastforce intro: Def-Exception)
          next case CFG-New-Exceptional-handle with V-notin-Def show ?thesis
            by (fastforce intro: Def-Exception Def-Exception-handle)
          next case CFG-Getfield-Update with V-notin-Def show ?thesis
            by (fastforce intro: Def-Getfield split: split-split)
          next case CFG-Getfield-Exceptional-prop with V-notin-Def show ?thesis
            by (fastforce intro: Def-Exception)
          next case CFG-Getfield-Exceptional-handle with V-notin-Def show ?thesis
            by (fastforce intro: Def-Exception Def-Exception-handle)

```

```

next case CFG-Putfield-Update with V-notin-Def show ?thesis
  by (fastforce intro: Def-Putfield split: split-split)
next case CFG-Putfield-Exceptional-prop with V-notin-Def show ?thesis
  by (fastforce intro: Def-Exception)
next case CFG-Putfield-Exceptional-handle with V-notin-Def show ?thesis
  by (fastforce intro: Def-Exception Def-Exception-handle)
next case CFG-Checkcast-Exceptional-prop with V-notin-Def show ?thesis
  by (fastforce intro: Def-Exception)
next case CFG-Checkcast-Exceptional-handle with V-notin-Def show ?thesis
  by (fastforce intro: Def-Exception Def-Exception-handle)
  next case CFG-Throw-prop with V-notin-Def show ?thesis by (fastforce
intro: Def-Exception)
  next case CFG-Throw-handle with V-notin-Def show ?thesis
  by (fastforce intro: Def-Exception Def-Exception-handle)
  next case CFG-Invoke-NP-prop with V-notin-Def show ?thesis by (fastforce
intro: Def-Exception)
  next case CFG-Invoke-NP-handle with V-notin-Def show ?thesis
  by (fastforce intro: Def-Exception Def-Exception-handle)
  next case CFG-Invoke-Return-Exceptional-handle with V-notin-Def show
?thesis
  by (fastforce intro: Def-Exception-handle-return Def-Exception)
  next case CFG-Return with V-notin-Def show ?thesis by (fastforce intro:
Def-Return)
  qed (simp-all add: intra-kind-def)
qed
next
fix a s s'
assume ve: valid-edge (P, C0, Main) a
  and use-Eq:  $\forall V \in \text{Use } P \text{ (sourcenode } a). \text{ JVMCFG-Interpret.state-val } s \ V$ 
  = JVMCFG-Interpret.state-val s' V
  and ik: intra-kind (kind a)
  and pred-s: JVMCFG-Interpret.pred (kind a) s
  and pred-s': JVMCFG-Interpret.pred (kind a) s'
  then obtain cfs C M pc cs cfs' C' M' pc' cs' where [simp]:  $s = (cfs, (C, M,$ 
pc)) # cs
  and [simp]:  $s' = (cfs', (C', M', pc')) \# cs'$ 
  by (cases s, fastforce) (cases s', fastforce+)
  from ve show  $\forall V \in \text{Def } P \text{ (sourcenode } a).$ 
  JVMCFG-Interpret.state-val
  (CFG.transfer (((ClassMain P, MethodMain P), [], []) # procs (PROG
P)) (kind a) s) V =
  JVMCFG-Interpret.state-val
  (CFG.transfer (((ClassMain P, MethodMain P), [], []) # procs (PROG
P)) (kind a) s') V
  unfolding valid-edge-def
proof cases
  case Main-Call with ik show ?thesis by (simp add: intra-kind-def)
  next case Main-Return-to-Exit with use-Eq show ?thesis
  by (fastforce elim: Def.cases intro: Use-Return-Heap Use-Return-Exception

```


Use-Return-Stack)
next case *Method-LFalse* **with** *use-Eq* **show** *?thesis*
by (*fastforce elim: Def.cases intro: Use-Method-Entry-Heap Use-Method-Entry-Local*)

next case *Method-LTrue* **with** *use-Eq* **show** *?thesis*
by (*fastforce elim: Def.cases intro: Use-Method-Entry-Heap Use-Method-Entry-Local*)
next case *CFG-Load* **with** *use-Eq* **show** *?thesis*
by (*fastforce elim: Def.cases intro: Use-Enter-Local*)
next case *CFG-Store* **with** *use-Eq* **show** *?thesis*
by (*fastforce elim: Def.cases intro: Use-Enter-Stack*)
next case (*CFG-IAdd C M pc*)
hence *Stack (stkLength (P, C, M) pc - 1) ∈ Use P (sourcename a)*
and *Stack (stkLength (P, C, M) pc - 2) ∈ Use P (sourcename a)*
by (*fastforce intro: Use-Enter-Stack*)+
with *use-Eq CFG-IAdd* **show** *?thesis* **by** (*auto elim!: Def.cases*)
next case (*CFG-CmpEq C M pc*)
hence *Stack (stkLength (P, C, M) pc - 1) ∈ Use P (sourcename a)*
and *Stack (stkLength (P, C, M) pc - 2) ∈ Use P (sourcename a)*
by (*fastforce intro: Use-Enter-Stack*)+
with *use-Eq CFG-CmpEq* **show** *?thesis* **by** (*auto elim!: Def.cases*)
next case *CFG-New-Update*
hence *Heap ∈ Use P (sourcename a)* **by** (*fastforce intro: Use-Normal-Heap*)
with *use-Eq CFG-New-Update* **show** *?thesis* **by** (*fastforce elim: Def.cases*)
next case (*CFG-Getfield-Update C M pc*)
hence *Heap ∈ Use P (sourcename a)*
and *Stack (stkLength (P, C, M) pc - 1) ∈ Use P (sourcename a)*
by (*fastforce intro: Use-Normal-Heap Use-Normal-Stack*)+
with *use-Eq CFG-Getfield-Update* **show** *?thesis* **by** (*auto elim!: Def.cases split: split-split*)
next case (*CFG-Putfield-Update C M pc*)
hence *Heap ∈ Use P (sourcename a)*
and *Stack (stkLength (P, C, M) pc - 1) ∈ Use P (sourcename a)*
and *Stack (stkLength (P, C, M) pc - 2) ∈ Use P (sourcename a)*
by (*fastforce intro: Use-Normal-Heap Use-Normal-Stack*)+
with *use-Eq CFG-Putfield-Update* **show** *?thesis* **by** (*auto elim!: Def.cases split: split-split*)
next case (*CFG-Throw-prop C M pc*)
hence *Stack (stkLength (P, C, M) pc - 1) ∈ Use P (sourcename a)*
by (*fastforce intro: Use-Exceptional-Stack*)
with *use-Eq CFG-Throw-prop* **show** *?thesis* **by** (*fastforce elim: Def.cases*)
next case (*CFG-Throw-handle C M pc*)
hence *Stack (stkLength (P, C, M) pc - 1) ∈ Use P (sourcename a)*
by (*fastforce intro: Use-Exceptional-Stack*)
with *use-Eq CFG-Throw-handle* **show** *?thesis* **by** (*fastforce elim: Def.cases*)
next case *CFG-Invoke-Call* **with** *ik* **show** *?thesis* **by** (*simp add: intra-kind-def*)
next case *CFG-Invoke-Return-Check-Normal* **with** *use-Eq* **show** *?thesis*
by (*fastforce elim: Def.cases intro: Use-Return-Heap Use-Return-Exception*)
Use-Return-Stack)
next case *CFG-Invoke-Return-Check-Exceptional* **with** *use-Eq* **show** *?thesis*

```

    by (fastforce elim: Def.cases intro: Use-Return-Heap Use-Return-Exception
Use-Return-Stack)
  next case CFG-Invoke-Return-Exceptional-handle with use-Eq show ?thesis
    by (fastforce elim: Def.cases intro: Use-Exceptional-Exception)
  next case CFG-Invoke-Return-Exceptional-prop with use-Eq show ?thesis
    by (fastforce elim: Def.cases intro: Use-Return-Heap Use-Return-Exception
Use-Return-Stack)
  next case CFG-Return with use-Eq show ?thesis
    by (fastforce elim!: Def.cases intro: Use-Enter-Stack)
  next case CFG-Return-from-Method with ik show ?thesis by (simp add: intra-kind-def)
  qed (fastforce elim: Def.cases)+
next
fix a s s'
assume ve: valid-edge (P, C0, Main) a
  and pred: JVMCFG-Interpret.pred (kind a) s
  and snd (hd s) = snd (hd s')
  and use-Eq:  $\forall V \in Use\ P\ (sourcename\ a).$ 
    JVMCFG-Interpret.state-val s V = JVMCFG-Interpret.state-val s' V
  and length s = length s'
then obtain cfs C M pc cs cfs' cs' where [simp]: s = (cfs, (C, M, pc)) # cs
  and [simp]: s' = (cfs', (C, M, pc)) # cs' and length-cs: length cs = length cs'
  by (cases s, fastforce) (cases s', fastforce+)
from ve pred show JVMCFG-Interpret.pred (kind a) s'
  unfolding valid-edge-def
proof cases
  case Main-Call-LFalse with pred show ?thesis by simp
next case Main-Call with pred use-Eq show ?thesis by simp
next case Method-LTrue with pred use-Eq show ?thesis by simp
next case CFG-Goto with pred use-Eq show ?thesis by simp
next case (CFG-IfFalse-False C M pc)
  hence Stack (stkLength (P, C, M) pc - 1)  $\in Use\ P\ (sourcename\ a)$ 
  by (fastforce intro: Use-Enter-Stack)
  with use-Eq CFG-IfFalse-False pred show ?thesis by fastforce
next case (CFG-IfFalse-True C M pc)
  hence Stack (stkLength (P, C, M) pc - 1)  $\in Use\ P\ (sourcename\ a)$ 
  by (fastforce intro: Use-Enter-Stack)
  with pred use-Eq CFG-IfFalse-True show ?thesis by fastforce
next case CFG-New-Check-Normal
  hence Heap  $\in Use\ P\ (sourcename\ a)$ 
  by (fastforce intro: Use-Enter-Heap)
  with pred use-Eq CFG-New-Check-Normal show ?thesis by fastforce
next case CFG-New-Check-Exceptional
  hence Heap  $\in Use\ P\ (sourcename\ a)$ 
  by (fastforce intro: Use-Enter-Heap)
  with pred use-Eq CFG-New-Check-Exceptional show ?thesis by fastforce
next case (CFG-Getfield-Check-Normal C M pc)
  hence Stack (stkLength (P, C, M) pc - 1)  $\in Use\ P\ (sourcename\ a)$ 
  by (fastforce intro: Use-Enter-Stack)
  with pred use-Eq CFG-Getfield-Check-Normal show ?thesis by fastforce

```

next case (*CFG-Getfield-Check-Exceptional* $C \ M \ pc$)
hence $Stack \ (stkLength \ (P, \ C, \ M) \ pc - 1) \in Use \ P \ (sourcename \ a)$
by (*fastforce intro: Use-Enter-Stack*)
with *pred use-Eq CFG-Getfield-Check-Exceptional* **show** *?thesis* **by** *fastforce*
next case (*CFG-Putfield-Check-Normal* $C \ M \ pc$)
hence $Stack \ (stkLength \ (P, \ C, \ M) \ pc - 2) \in Use \ P \ (sourcename \ a)$
by (*fastforce intro: Use-Enter-Stack*)
with *pred use-Eq CFG-Putfield-Check-Normal* **show** *?thesis* **by** *fastforce*
next case (*CFG-Putfield-Check-Exceptional* $C \ M \ pc$)
hence $Stack \ (stkLength \ (P, \ C, \ M) \ pc - 2) \in Use \ P \ (sourcename \ a)$
by (*fastforce intro: Use-Enter-Stack*)
with *pred use-Eq CFG-Putfield-Check-Exceptional* **show** *?thesis* **by** *fastforce*
next case (*CFG-Checkcast-Check-Normal* $C \ M \ pc$)
hence $Stack \ (stkLength \ (P, \ C, \ M) \ pc - 1) \in Use \ P \ (sourcename \ a)$
and $Heap \in Use \ P \ (sourcename \ a)$
by (*fastforce intro: Use-Enter-Stack Use-Enter-Heap*)+
with *pred use-Eq CFG-Checkcast-Check-Normal* **show** *?thesis* **by** *fastforce*
next case (*CFG-Checkcast-Check-Exceptional* $C \ M \ pc$)
hence $Stack \ (stkLength \ (P, \ C, \ M) \ pc - 1) \in Use \ P \ (sourcename \ a)$
and $Heap \in Use \ P \ (sourcename \ a)$
by (*fastforce intro: Use-Enter-Stack Use-Enter-Heap*)+
with *pred use-Eq CFG-Checkcast-Check-Exceptional* **show** *?thesis* **by** *fastforce*
next case (*CFG-Throw-Check* $C \ M \ pc$)
hence $Stack \ (stkLength \ (P, \ C, \ M) \ pc - 1) \in Use \ P \ (sourcename \ a)$
and $Heap \in Use \ P \ (sourcename \ a)$
by (*fastforce intro: Use-Enter-Stack Use-Enter-Heap*)+
with *pred use-Eq CFG-Throw-Check* **show** *?thesis* **by** *fastforce*
next case (*CFG-Invoke-Check-NP-Normal* $C \ M \ pc \ M' \ n$)
hence $Stack \ (stkLength \ (P, \ C, \ M) \ pc - (Suc \ n)) \in Use \ P \ (sourcename \ a)$
by (*fastforce intro: Use-Enter-Stack*)
with *pred use-Eq CFG-Invoke-Check-NP-Normal* **show** *?thesis* **by** *fastforce*
next case (*CFG-Invoke-Check-NP-Exceptional* $C \ M \ pc \ M' \ n$)
hence $Stack \ (stkLength \ (P, \ C, \ M) \ pc - (Suc \ n)) \in Use \ P \ (sourcename \ a)$
by (*fastforce intro: Use-Enter-Stack*)
with *pred use-Eq CFG-Invoke-Check-NP-Exceptional* **show** *?thesis* **by** *fastforce*
next case (*CFG-Invoke-Call* $C \ M \ pc \ M' \ n$)
hence $Stack \ (stkLength \ (P, \ C, \ M) \ pc - (Suc \ n)) \in Use \ P \ (sourcename \ a)$
and $Heap \in Use \ P \ (sourcename \ a)$
by (*fastforce intro: Use-Normal-Heap Use-Normal-Stack*)+
with *pred use-Eq CFG-Invoke-Call* **show** *?thesis* **by** *fastforce*
next case *CFG-Invoke-Return-Check-Normal*
hence $Exception \in Use \ P \ (sourcename \ a)$
by (*fastforce intro: Use-Return-Exception*)
with *pred use-Eq CFG-Invoke-Return-Check-Normal* **show** *?thesis* **by** *fastforce*
next case *CFG-Invoke-Return-Check-Exceptional*
hence $Exception \in Use \ P \ (sourcename \ a)$ **and** $Heap \in Use \ P \ (sourcename \ a)$
by (*fastforce intro: Use-Return-Exception Use-Return-Heap*)+
with *pred use-Eq CFG-Invoke-Return-Check-Exceptional* **show** *?thesis* **by**
fastforce

```

next case CFG-Invoke-Return-Exceptional-prop
  hence Exception ∈ Use P (sourcenode a) and Heap ∈ Use P (sourcenode a)
  by (fastforce intro: Use-Return-Exception Use-Return-Heap)+
  with pred use-Eq CFG-Invoke-Return-Exceptional-prop show ?thesis by fastforce
next case CFG-Return-from-Method with pred length-cs show ?thesis by clarsimp
qed auto
next
  fix a Q r p fs ins outs
  assume valid-edge (P, C0, Main) a
  and kind: kind a = Q:r↔pfs
  and params: (p, ins, outs) ∈ set (((ClassMain P, MethodMain P), [], []) #
procs (PROG P))
  thus length fs = length ins unfolding valid-edge-def
  proof cases
    case (Main-Call T mxs mxl0 is xt D)
    with kind params have [simp]: p = (D, Main)
    and PROG P ⊢ D sees Main: []→T = (mxs, mxl0, is, xt) in D
    and ins = Heap # map Local [0..

```

```

    case Main-Call with ve-a' kind-a' src pred-s pred-s' show ?thesis unfolding
valid-edge-def
    by (cases a, cases a') (fastforce elim: JVMCFG.cases dest: sees-method-fun)
next
case CFG-Invoke-Call
note invoke-call1 = this
from ve-a' kind-a' show ?thesis unfolding valid-edge-def
proof cases
case Main-Call with CFG-Invoke-Call src have False by simp
thus ?thesis by simp
next
case CFG-Invoke-Call with src invoke-call1 show ?thesis
by clarsimp (cases a, cases a', fastforce dest: sees-method-fun)
qed simp-all
qed simp-all
next
fix a Q r p fs i ins outs s s'
assume ve: valid-edge (P, C0, Main) a
and kind: kind a = Q:r↦pfs
and i < length ins
and (p, ins, outs) ∈ set (((ClassMain P, MethodMain P), [], []) # procs (PROG
P))
and JVMCFG-Interpret.pred (kind a) s
and JVMCFG-Interpret.pred (kind a) s'
and use-Eq: ∀ V ∈ ParamUses P (sourcnode a) ! i.
JVMCFG-Interpret.state-val s V = JVMCFG-Interpret.state-val s' V
then obtain cfs C M pc cs cfs' C' M' pc' cs' where [simp]: s = (cfs, (C, M,
pc)) # cs
and [simp]: s' = (cfs', (C', M', pc')) # cs'
by (cases s, fastforce) (cases s', fastforce+)
from ve kind
show JVMCFG-Interpret.params fs (JVMCFG-Interpret.state-val s) ! i =
JVMCFG-Interpret.params fs (JVMCFG-Interpret.state-val s') ! i
unfolding valid-edge-def
proof cases
case Main-Call with kind use-Eq (i < length ins) show ?thesis
by (cases i) auto
next
case CFG-Invoke-Call
{ fix P C M pc n st st' i
have ∀ V ∈ rev (map (λn. {Stack (stkLength (P, C, M) pc - Suc n)}) [0..<n])
! i. st V = st' V
⇒ JVMCFG-Interpret.params
(rev (map (λi s. s (Stack (stkLength (P, C, M) pc - Suc i))) [0..<n])) st
! i =
JVMCFG-Interpret.params
(rev (map (λi s. s (Stack (stkLength (P, C, M) pc - Suc i))) [0..<n])) st'
! i
by (induct n arbitrary: i) (simp, case-tac i, auto)

```

```

}
note stack-params = this
from CFG-Invoke-Call kind use-Eq  $\langle i < \text{length } \textit{ins} \rangle$  show ?thesis
  by (cases i, auto) (case-tac nat, auto intro: stack-params)
qed simp-all
next
fix a Q' p f' ins outs vmap vmap'
assume valid-edge (P, C0, Main) a
  and kind a =  $Q' \leftrightarrow pf'$ 
  and (p, ins, outs)  $\in \text{set } (((\text{ClassMain } P, \text{MethodMain } P), [], []) \# \text{procs } (\text{PROG } P))$ 
  thus  $f' \text{ vmap } vmap' = vmap'(\text{ParamDefs } P \text{ (targetnode } a) [:=] \text{map } vmap \text{ outs})$ 
  unfolding valid-edge-def
  by (cases, simp-all) (fastforce elim: in-set-procsE simp: fun-upd-twist)
next
fix a a'
{ fix P n f n' e n''
  assume  $P \vdash n \dashv\uparrow f \rightarrow n'$  and  $P \vdash n \dashv e \rightarrow n''$ 
  hence  $e = \uparrow f \wedge n' = n''$ 
  by cases (simp-all, (fastforce elim: JVMCFG.cases)+)
}
note upd-det = this
{ fix P n Q n' Q' n'' s
  assume  $P \vdash n \dashv (Q)_{\surd} \rightarrow n'$  and  $\textit{edge}' : P \vdash n \dashv (Q')_{\surd} \rightarrow n''$  and trg:  $n' \neq n''$ 
  hence  $(Q \text{ s} \rightarrow \neg Q' \text{ s}) \wedge (Q' \text{ s} \rightarrow \neg Q \text{ s})$ 
  proof cases
    case CFG-Throw-Check with edge' trg show ?thesis by cases fastforce+
  qed (simp-all, (fastforce elim: JVMCFG.cases)+)
}
note pred-det = this
assume valid-edge (P, C0, Main) a
  and ve': valid-edge (P, C0, Main) a'
  and src: sourcenode a = sourcenode a'
  and trg: targetnode a  $\neq$  targetnode a'
  and intra-kind (kind a)
  and intra-kind (kind a')
thus  $\exists Q Q'. \textit{kind } a = (Q)_{\surd} \wedge \textit{kind } a' = (Q')_{\surd} \wedge (\forall s. (Q \text{ s} \rightarrow \neg Q' \text{ s}) \wedge (Q' \text{ s} \rightarrow \neg Q \text{ s}))$ 
  unfolding valid-edge-def intra-kind-def
  by (auto dest: upd-det pred-det)
qed

```

interpretation *JVMCFGExit-wf* :

CFGExit-wf *sourcenode targetnode kind valid-edge* (*P*, *C0*, *Main*)
(*ClassMain* *P*, *MethodMain* *P*, *None*, *Enter*)
 $\lambda(C, M, pc, type). (C, M)$ *get-return-edges* *P*
 $((\text{ClassMain } P, \text{MethodMain } P), [], []) \# \text{procs } (\text{PROG } P)$
(*ClassMain* *P*, *MethodMain* *P*)
(*ClassMain* *P*, *MethodMain* *P*, *None*, *Return*)

```

  Def P Use P ParamDefs P ParamUses P
proof
  show Def P (ClassMain P, MethodMain P, None, nodeType.Return) = {} ∧
    Use P (ClassMain P, MethodMain P, None, nodeType.Return) = {}
    by (fastforce elim: Def.cases Use.cases)
qed

end

theory JVMPostdomination imports JVMInterpretation ../StaticInter/Postdomination
begin

context CFG begin

lemma vp-snocI:
   $\llbracket n -as \rightarrow_{\sqrt{*}} n'; n' -[a] \rightarrow_* n''; \forall Q p \text{ ret fs. kind } a \neq Q \leftrightarrow_p \text{ret} \rrbracket \implies n -as @$ 
 $[a] \rightarrow_{\sqrt{*}} n''$ 
  by (cases kind a) (auto intro: path-Append valid-path-aux-Append simp: vp-def
    valid-path-def)

lemma valid-node-cases' [case-names Source Target, consumes 1]:
   $\llbracket \text{valid-node } n; \bigwedge e. \llbracket \text{valid-edge } e; \text{sourcenode } e = n \rrbracket \implies \text{thesis};$ 
 $\bigwedge e. \llbracket \text{valid-edge } e; \text{targetnode } e = n \rrbracket \implies \text{thesis} \rrbracket$ 
 $\implies \text{thesis}$ 
  by (auto simp: valid-node-def)

end

lemma disjE-strong:  $\llbracket P \vee Q; P \implies R; \llbracket Q; \neg P \rrbracket \implies R \rrbracket \implies R$ 
  by auto

lemmas path-intros [intro] = JVMCFG-Interpret.path.Cons-path JVMCFG-Interpret.path.empty-path
declare JVMCFG-Interpret.vp-snocI [intro]
declare JVMCFG-Interpret.valid-node-def [simp add]
  valid-edge-def [simp add]
  JVMCFG-Interpret.intra-path-def [simp add]

abbreviation vp-snoc :: wf-jvmprog  $\Rightarrow$  cname  $\Rightarrow$  mname  $\Rightarrow$  cfg-edge list  $\Rightarrow$  cfg-node
 $\Rightarrow$  (var, val, cname  $\times$  mname  $\times$  pc, cname  $\times$  mname) edge-kind  $\Rightarrow$  cfg-node  $\Rightarrow$ 
bool
  where vp-snoc P C0 Main as n ek n'
 $\equiv$  JVMCFG-Interpret.valid-path' P C0 Main
(ClassMain P, MethodMain P, None, Enter) (as @ [(n,ek,n')]) n'

lemma
(P, C0, Main)  $\vdash$  (C, M, pc, nt)  $-ek \rightarrow$  (C', M', pc', nt')
 $\implies$  ( $\exists$  as. CFG.valid-path' sourcenode targetnode kind (valid-edge (P, C0, Main))
(get-return-edges P) (ClassMain P, MethodMain P, None, Enter) as (C, M, pc,
nt))  $\wedge$ 

```

$(\exists \text{ as. CFG.valid-path}' \text{ sourcenode targetnode kind (valid-edge (P, C0, Main))}$
 $(\text{get-return-edges P}) (\text{ClassMain P, MethodMain P, None, Enter}) \text{ as } (C', M',$
 $pc', nt')$
and $\text{valid-Entry-path: (P, C0, Main)} \vdash \Rightarrow (C, M, pc, nt)$
 $\Rightarrow \exists \text{ as. CFG.valid-path}' \text{ sourcenode targetnode kind (valid-edge (P, C0, Main))}$
 $(\text{get-return-edges P}) (\text{ClassMain P, MethodMain P, None, Enter}) \text{ as } (C, M, pc,$
 $nt)$
proof (*induct rule: JVMCFG-reachable-inducts*)
case (*Entry-reachable P C0 Main*)
hence $\text{JVMCFG-Interpret.valid-path}' P C0 Main$
 $(\text{ClassMain P, MethodMain P, None, Enter}) \square (\text{ClassMain P, MethodMain P,$
 $\text{None, Enter})$
by (*fastforce intro: JVMCFG-Interpret.intra-path-vp Method-LTrue*
 $\text{JVMCFG-reachable.Entry-reachable}$)
thus *?case by blast*
next
case (*reachable-step P C0 Main C M pc nt ek C' M' pc' nt'*)
thus *?case by simp*
next
case (*Main-to-Call P C0 Main*)
then obtain as where $\text{JVMCFG-Interpret.valid-path}' P C0 Main$
 $(\text{ClassMain P, MethodMain P, None, Enter}) \text{ as } (\text{ClassMain P, MethodMain}$
 $P, \lfloor 0 \rfloor, \text{Enter})$
by *blast*
moreover with $\langle (P, C0, Main) \vdash \Rightarrow (\text{ClassMain P, MethodMain P, } \lfloor 0 \rfloor, \text{Enter}) \rangle$
have $\text{vp-snoc P C0 Main as } (\text{ClassMain P, MethodMain P, } \lfloor 0 \rfloor, \text{Enter}) \uparrow \text{id}$
 $(\text{ClassMain P, MethodMain P, } \lfloor 0 \rfloor, \text{Normal})$
by (*fastforce intro: JVMCFG-reachable.Main-to-Call*)
ultimately show *?case by blast*
next
case (*Main-Call-LFalse P C0 Main*)
then obtain as where $\text{JVMCFG-Interpret.valid-path}' P C0 Main$
 $(\text{ClassMain P, MethodMain P, None, Enter}) \text{ as } (\text{ClassMain P, MethodMain}$
 $P, \lfloor 0 \rfloor, \text{Normal})$
by *blast*
moreover with $\langle (P, C0, Main) \vdash \Rightarrow (\text{ClassMain P, MethodMain P, } \lfloor 0 \rfloor, \text{Nor-}$
 $\text{mal}) \rangle$
have $\text{vp-snoc P C0 Main as } (\text{ClassMain P, MethodMain P, } \lfloor 0 \rfloor, \text{Normal}) (\lambda s.$
 $\text{False}) \surd$
 $(\text{ClassMain P, MethodMain P, } \lfloor 0 \rfloor, \text{Return})$
by (*fastforce intro: JVMCFG-reachable.Main-Call-LFalse*)
ultimately show *?case by blast*
next
case (*Main-Call P C0 Main T mxs mxl₀ is xt D initParams ek*)
then obtain as where $\text{JVMCFG-Interpret.valid-path}' P C0 Main$
 $(\text{ClassMain P, MethodMain P, None, Enter}) \text{ as } (\text{ClassMain P, MethodMain}$
 $P, \lfloor 0 \rfloor, \text{Normal})$
by *blast*
moreover with $\langle (P, C0, Main) \vdash \Rightarrow (\text{ClassMain P, MethodMain P, } \lfloor 0 \rfloor, \text{Nor-}$


```

mal)
  ⟨PROG P ⊢ C0 sees Main: [] → T = (mxs, mxl0, is, xt) in D⟩
  ⟨initParams = [λs. s Heap, λs. [Value Null]]⟩
  ⟨ek = λ(s, ret). True:(ClassMain P, MethodMain P, 0) ↔(D, Main) initParams⟩
  have vp-snoc P C0 Main as (ClassMain P, MethodMain P, [0], Normal)
  ((λ(s, ret). True):(ClassMain P, MethodMain P, 0) ↔(D, Main) [(λs. s Heap), (λs.
  [Value Null])])
  (D, Main, None, Enter)
  by (fastforce intro: JVMCFG-reachable.Main-Call)
  ultimately show ?case by blast
next
  case (Main-Return-to-Exit P C0 Main)
  then obtain as where JVMCFG-Interpret.valid-path' P C0 Main
  (ClassMain P, MethodMain P, None, Enter) as (ClassMain P, MethodMain
  P, [0], nodeType.Return)
  by blast
  moreover with ⟨(P, C0, Main) ⊢ ⇒(ClassMain P, MethodMain P, [0], node-
  Type.Return)⟩
  have vp-snoc P C0 Main as (ClassMain P, MethodMain P, [0], nodeType.Return)
  ↑id
  (ClassMain P, MethodMain P, None, nodeType.Return)
  by (fastforce intro: JVMCFG-reachable.Main-Return-to-Exit)
  ultimately show ?case by blast
next
  case (Method-LFalse P C0 Main C M)
  then obtain as where JVMCFG-Interpret.valid-path' P C0 Main
  (ClassMain P, MethodMain P, None, Enter) as (C, M, None, Enter)
  by blast
  moreover with ⟨(P, C0, Main) ⊢ ⇒(C, M, None, Enter)⟩
  have vp-snoc P C0 Main as (C, M, None, Enter) (λs. False)√ (C, M, None,
  Return)
  by (fastforce intro: JVMCFG-reachable.Method-LFalse)
  ultimately show ?case by blast
next
  case (Method-LTrue P C0 Main C M)
  then obtain as where JVMCFG-Interpret.valid-path' P C0 Main
  (ClassMain P, MethodMain P, None, Enter) as (C, M, None, Enter)
  by blast
  moreover with ⟨(P, C0, Main) ⊢ ⇒(C, M, None, Enter)⟩
  have vp-snoc P C0 Main as (C, M, None, Enter) (λs. True)√ (C, M, [0],
  Enter)
  by (fastforce intro: JVMCFG-reachable.Method-LTrue)
  ultimately show ?case by blast
next
  case (CFG-Load C P C0 Main M pc n ek)
  then obtain as where JVMCFG-Interpret.valid-path' P C0 Main
  (ClassMain P, MethodMain P, None, Enter) as (C, M, [pc], Enter)
  by blast
  moreover with ⟨C ≠ ClassMain P⟩ ⟨(P, C0, Main) ⊢ ⇒(C, M, [pc], Enter)⟩

```

$\langle \text{instrs-of } (\text{PROG } P) \ C \ M \ ! \ pc = \text{Load } n \rangle$
 $\langle ek = \uparrow \lambda s. s(\text{Stack } (\text{stkLength } (P, C, M) \ pc) := s(\text{Local } n)) \rangle$
have $vp\text{-snoc } P \ C0 \ \text{Main}$ as $(C, M, [pc], \text{Enter}) \ ek \ (C, M, [Suc \ pc], \text{Enter})$
by $(\text{fastforce intro: JVMCFG-reachable.CFG-Load})$
ultimately show $?case$ **by** blast

next

case $(\text{CFG-Store } C \ P \ C0 \ \text{Main} \ M \ pc \ n \ ek)$
then obtain as **where** $\text{JVMCFG-Interpret.valid-path}' \ P \ C0 \ \text{Main}$
 $(\text{ClassMain } P, \text{MethodMain } P, \text{None}, \text{Enter})$ as $(C, M, [pc], \text{Enter})$
by blast
moreover with $\langle C \neq \text{ClassMain } P \rangle \langle (P, C0, \text{Main}) \vdash \Rightarrow (C, M, [pc], \text{Enter}) \rangle$
 $\langle \text{instrs-of } (\text{PROG } P) \ C \ M \ ! \ pc = \text{Store } n \rangle$
 $\langle ek = \uparrow \lambda s. s(\text{Local } n := s(\text{Stack } (\text{stkLength } (P, C, M) \ pc - 1))) \rangle$
have $vp\text{-snoc } P \ C0 \ \text{Main}$ as $(C, M, [pc], \text{Enter}) \ ek \ (C, M, [Suc \ pc], \text{Enter})$
by $(\text{fastforce intro: JVMCFG-reachable.CFG-Store})$
ultimately show $?case$ **by** blast

next

case $(\text{CFG-Push } C \ P \ C0 \ \text{Main} \ M \ pc \ v \ ek)$
then obtain as **where** $\text{JVMCFG-Interpret.valid-path}' \ P \ C0 \ \text{Main}$
 $(\text{ClassMain } P, \text{MethodMain } P, \text{None}, \text{Enter})$ as $(C, M, [pc], \text{Enter})$
by blast
moreover with $\langle C \neq \text{ClassMain } P \rangle \langle (P, C0, \text{Main}) \vdash \Rightarrow (C, M, [pc], \text{Enter}) \rangle$
 $\langle \text{instrs-of } (\text{PROG } P) \ C \ M \ ! \ pc = \text{Push } v \rangle$
 $\langle ek = \uparrow \lambda s. s(\text{Stack } (\text{stkLength } (P, C, M) \ pc) \mapsto \text{Value } v) \rangle$
have $vp\text{-snoc } P \ C0 \ \text{Main}$ as $(C, M, [pc], \text{Enter}) \ ek \ (C, M, [Suc \ pc], \text{Enter})$
by $(\text{fastforce intro: JVMCFG-reachable.CFG-Push})$
ultimately show $?case$ **by** blast

next

case $(\text{CFG-Pop } C \ P \ C0 \ \text{Main} \ M \ pc \ ek)$
then obtain as **where** $\text{JVMCFG-Interpret.valid-path}' \ P \ C0 \ \text{Main}$
 $(\text{ClassMain } P, \text{MethodMain } P, \text{None}, \text{Enter})$ as $(C, M, [pc], \text{Enter})$
by blast
moreover with $\langle C \neq \text{ClassMain } P \rangle \langle (P, C0, \text{Main}) \vdash \Rightarrow (C, M, [pc], \text{Enter}) \rangle$
 $\langle \text{instrs-of } (\text{PROG } P) \ C \ M \ ! \ pc = \text{Pop} \rangle \langle ek = \uparrow id \rangle$
have $vp\text{-snoc } P \ C0 \ \text{Main}$ as $(C, M, [pc], \text{Enter}) \ ek \ (C, M, [Suc \ pc], \text{Enter})$
by $(\text{fastforce intro: JVMCFG-reachable.CFG-Pop})$
ultimately show $?case$ **by** blast

next

case $(\text{CFG-IAdd } C \ P \ C0 \ \text{Main} \ M \ pc \ ek)$
then obtain as **where** $\text{JVMCFG-Interpret.valid-path}' \ P \ C0 \ \text{Main}$
 $(\text{ClassMain } P, \text{MethodMain } P, \text{None}, \text{Enter})$ as $(C, M, [pc], \text{Enter})$
by blast
moreover with $\langle C \neq \text{ClassMain } P \rangle \langle (P, C0, \text{Main}) \vdash \Rightarrow (C, M, [pc], \text{Enter}) \rangle$
 $\langle \text{instrs-of } (\text{PROG } P) \ C \ M \ ! \ pc = \text{IAdd} \rangle$
 $\langle ek = \uparrow \lambda s. \text{let } i1 = \text{the-Intg } (\text{stkAt } s \ (\text{stkLength } (P, C, M) \ pc - 1));$
 $\quad i2 = \text{the-Intg } (\text{stkAt } s \ (\text{stkLength } (P, C, M) \ pc - 2))$
 $\text{in } s(\text{Stack } (\text{stkLength } (P, C, M) \ pc - 2) \mapsto \text{Value } (\text{Intg } (i1 + i2))) \rangle$
have $vp\text{-snoc } P \ C0 \ \text{Main}$ as $(C, M, [pc], \text{Enter}) \ ek \ (C, M, [Suc \ pc], \text{Enter})$
by $(\text{fastforce intro: JVMCFG-reachable.CFG-IAdd})$

ultimately show *?case* **by blast**
next
case (*CFG-Goto C P C0 Main M pc i*)
then obtain as where *JVMCFG-Interpret.valid-path' P C0 Main*
(*ClassMain P, MethodMain P, None, Enter*) **as** (*C, M, [pc], Enter*)
by blast
moreover with $\langle C \neq \text{ClassMain } P \rangle \langle (P, C0, \text{Main}) \vdash \Rightarrow (C, M, [pc], \text{Enter}) \rangle$
 $\langle \text{instrs-of } (\text{PROG } P) \text{ } C \text{ } M \text{ } ! \text{ } pc = \text{Goto } i \rangle$
have *vp-snoc P C0 Main as* (*C, M, [pc], Enter*) $(\lambda s. \text{True})_{\surd} (C, M, [nat (int$
*pc + i)], Enter)
by (*fastforce intro: JVMCFG-reachable.CFG-Goto*)
ultimately show *?case* **by blast**
next
case (*CFG-CmpEq C P C0 Main M pc ek*)
then obtain as where *JVMCFG-Interpret.valid-path' P C0 Main*
(*ClassMain P, MethodMain P, None, Enter*) **as** (*C, M, [pc], Enter*)
by blast
moreover with $\langle C \neq \text{ClassMain } P \rangle \langle (P, C0, \text{Main}) \vdash \Rightarrow (C, M, [pc], \text{Enter}) \rangle$
 $\langle \text{instrs-of } (\text{PROG } P) \text{ } C \text{ } M \text{ } ! \text{ } pc = \text{CmpEq}$
 $\langle ek = \uparrow \lambda s. \text{let } e1 = \text{stkAt } s \text{ } (\text{stkLength } (P, C, M) \text{ } pc - 1);$
 $\text{e2} = \text{stkAt } s \text{ } (\text{stkLength } (P, C, M) \text{ } pc - 2)$
 $\text{in } s(\text{Stack } (\text{stkLength } (P, C, M) \text{ } pc - 2) \mapsto \text{Value } (\text{Bool } (e1 = e2))) \rangle$
have *vp-snoc P C0 Main as* (*C, M, [pc], Enter*) *ek* (*C, M, [Suc pc], Enter*)
by (*fastforce intro: JVMCFG-reachable.CFG-CmpEq*)
ultimately show *?case* **by blast**
next
case (*CFG-IfFalse-False C P C0 Main M pc i ek*)
then obtain as where *JVMCFG-Interpret.valid-path' P C0 Main*
(*ClassMain P, MethodMain P, None, Enter*) **as** (*C, M, [pc], Enter*)
by blast
moreover with $\langle C \neq \text{ClassMain } P \rangle \langle (P, C0, \text{Main}) \vdash \Rightarrow (C, M, [pc], \text{Enter}) \rangle$
 $\langle \text{instrs-of } (\text{PROG } P) \text{ } C \text{ } M \text{ } ! \text{ } pc = \text{IfFalse } i \rangle \langle i \neq 1 \rangle$
 $\langle ek = (\lambda s. \text{stkAt } s \text{ } (\text{stkLength } (P, C, M) \text{ } pc - 1) = \text{Bool } \text{False})_{\surd} \rangle$
have *vp-snoc P C0 Main as* (*C, M, [pc], Enter*) *ek* (*C, M, [nat (int pc + i)],*
Enter)
by (*fastforce intro: JVMCFG-reachable.CFG-IfFalse-False*)
ultimately show *?case* **by blast**
next
case (*CFG-IfFalse-True C P C0 Main M pc i ek*)
then obtain as where *JVMCFG-Interpret.valid-path' P C0 Main*
(*ClassMain P, MethodMain P, None, Enter*) **as** (*C, M, [pc], Enter*)
by blast
moreover with $\langle C \neq \text{ClassMain } P \rangle \langle (P, C0, \text{Main}) \vdash \Rightarrow (C, M, [pc], \text{Enter}) \rangle$
 $\langle \text{instrs-of } (\text{PROG } P) \text{ } C \text{ } M \text{ } ! \text{ } pc = \text{IfFalse } i \rangle$
 $\langle ek = (\lambda s. \text{stkAt } s \text{ } (\text{stkLength } (P, C, M) \text{ } pc - 1) \neq \text{Bool } \text{False} \vee i = 1)_{\surd} \rangle$
have *vp-snoc P C0 Main as* (*C, M, [pc], Enter*) *ek* (*C, M, [Suc pc], Enter*)
by (*fastforce intro: JVMCFG-reachable.CFG-IfFalse-True*)
ultimately show *?case* **by blast**
next*

case (*CFG-New-Check-Normal* $C P C0 Main M pc Cl ek$)
then obtain as where *JVMCFG-Interpret.valid-path'* $P C0 Main$
(*ClassMain* P , *MethodMain* P , *None*, *Enter*) as $(C, M, [pc], Enter)$
by blast
moreover with $\langle C \neq ClassMain P \rangle \langle (P, C0, Main) \vdash \Rightarrow (C, M, [pc], Enter) \rangle$
 $\langle instrs\text{-of } (PROG P) C M ! pc = New Cl \rangle \langle ek = (\lambda s. new\text{-Addr } (heap\text{-of } s) \neq$
None) $\checkmark \rangle$
have *vp-snoc* $P C0 Main$ as $(C, M, [pc], Enter)$ $ek (C, M, [pc], Normal)$
by (*fastforce intro: JVMCFG-reachable.CFG-New-Check-Normal*)
ultimately show *?case by blast*
next
case (*CFG-New-Check-Exceptional* $C P C0 Main M pc Cl pc' ek$)
then obtain as where *JVMCFG-Interpret.valid-path'* $P C0 Main$
(*ClassMain* P , *MethodMain* P , *None*, *Enter*) as $(C, M, [pc], Enter)$
by blast
moreover with $\langle C \neq ClassMain P \rangle \langle (P, C0, Main) \vdash \Rightarrow (C, M, [pc], Enter) \rangle$
 $\langle instrs\text{-of } (PROG P) C M ! pc = New Cl \rangle$
 $\langle pc' = (case\ match\text{-ex-table } (PROG P) OutOfMemory pc (ex\text{-table-of } (PROG$
 $P) C M) of\ None \Rightarrow None$
 $| [(pc'', d)] \Rightarrow [pc''] \rangle \langle ek = (\lambda s. new\text{-Addr } (heap\text{-of } s) = None) \checkmark \rangle$
have *vp-snoc* $P C0 Main$ as $(C, M, [pc], Enter)$ $ek (C, M, [pc], Exceptional$
 $pc' Enter)$
by (*fastforce intro: JVMCFG-reachable.CFG-New-Check-Exceptional*)
ultimately show *?case by blast*
next
case (*CFG-New-Update* $C P C0 Main M pc Cl ek$)
then obtain as where *JVMCFG-Interpret.valid-path'* $P C0 Main$
(*ClassMain* P , *MethodMain* P , *None*, *Enter*) as $(C, M, [pc], Normal)$
by blast
moreover with $\langle C \neq ClassMain P \rangle \langle (P, C0, Main) \vdash \Rightarrow (C, M, [pc], Normal) \rangle$
 $\langle instrs\text{-of } (PROG P) C M ! pc = New Cl \rangle$
 $\langle ek = \uparrow \lambda s. let\ a = the\ (new\text{-Addr } (heap\text{-of } s))\ in$
 $s(Heap \mapsto Hp (heap\text{-of } s(a \mapsto blank (PROG P) Cl)),$
 $Stack (stkLength (P, C, M) pc) \mapsto Value (Addr a)) \rangle$
have *vp-snoc* $P C0 Main$ as $(C, M, [pc], Normal)$ $ek (C, M, [Suc pc], Enter)$
by (*fastforce intro: JVMCFG-reachable.CFG-New-Update*)
ultimately show *?case by blast*
next
case (*CFG-New-Exceptional-prop* $C P C0 Main M pc Cl ek$)
then obtain as where *JVMCFG-Interpret.valid-path'* $P C0 Main$
(*ClassMain* P , *MethodMain* P , *None*, *Enter*) as $(C, M, [pc], Exceptional None$
 $Enter)$
by blast
moreover with $\langle C \neq ClassMain P \rangle \langle (P, C0, Main) \vdash \Rightarrow (C, M, [pc], Exceptional$
 $None Enter) \rangle$
 $\langle instrs\text{-of } (PROG P) C M ! pc = New Cl \rangle$
 $\langle ek = \uparrow \lambda s. s(Exception \mapsto Value (Addr (addr\text{-of-sys-xcpt } OutOfMemory))) \rangle$
have *vp-snoc* $P C0 Main$ as $(C, M, [pc], Exceptional None Enter)$ $ek (C, M,$
 $None, Return)$

by (fastforce intro: JVMCFG-reachable.CFG-New-Exceptional-prop)
 ultimately show ?case by blast

next
 case (CFG-New-Exceptional-handle $C P C0 Main M pc pc' Cl ek$)
 then obtain as where JVMCFG-Interpret.valid-path' $P C0 Main$
 (ClassMain P , MethodMain P , None, Enter) as ($C, M, [pc]$, Exceptional $[pc']$
 Enter)
 by blast
 moreover with $\langle C \neq \text{ClassMain } P \rangle \langle (P, C0, Main) \vdash \Rightarrow (C, M, [pc], \text{Exceptional } [pc'] \text{ Enter}) \rangle$
 $\langle \text{instrs-of } (PROG P) C M ! pc = \text{New } Cl \rangle$
 $\langle ek = \uparrow \lambda s. s(\text{Exception} := \text{None})(\text{Stack } (\text{stkLength } (P, C, M) pc' - 1) \mapsto$
 $\text{Value } (\text{Addr } (\text{addr-of-sys-xcpt } \text{OutOfMemory}))) \rangle$
 have $vp\text{-snoc } P C0 Main$ as ($C, M, [pc]$, Exceptional $[pc']$ Enter) $ek (C, M,$
 $[pc'], \text{Enter})$
 by (fastforce intro: JVMCFG-reachable.CFG-New-Exceptional-handle)
 ultimately show ?case by blast

next
 case (CFG-Getfield-Check-Normal $C P C0 Main M pc F Cl ek$)
 then obtain as where JVMCFG-Interpret.valid-path' $P C0 Main$
 (ClassMain P , MethodMain P , None, Enter) as ($C, M, [pc]$, Enter)
 by blast
 moreover with $\langle C \neq \text{ClassMain } P \rangle \langle (P, C0, Main) \vdash \Rightarrow (C, M, [pc], \text{Enter}) \rangle$
 $\langle \text{instrs-of } (PROG P) C M ! pc = \text{Getfield } F Cl \rangle$
 $\langle ek = (\lambda s. \text{stkAt } s (\text{stkLength } (P, C, M) pc - 1) \neq \text{Null})_{\checkmark} \rangle$
 have $vp\text{-snoc } P C0 Main$ as ($C, M, [pc]$, Enter) $ek (C, M, [pc], \text{Normal})$
 by (fastforce intro: JVMCFG-reachable.CFG-Getfield-Check-Normal)
 ultimately show ?case by blast

next
 case (CFG-Getfield-Check-Exceptional $C P C0 Main M pc F Cl pc' ek$)
 then obtain as where JVMCFG-Interpret.valid-path' $P C0 Main$
 (ClassMain P , MethodMain P , None, Enter) as ($C, M, [pc]$, Enter)
 by blast
 moreover with $\langle C \neq \text{ClassMain } P \rangle \langle (P, C0, Main) \vdash \Rightarrow (C, M, [pc], \text{Enter}) \rangle$
 $\langle \text{instrs-of } (PROG P) C M ! pc = \text{Getfield } F Cl \rangle$
 $\langle pc' = (\text{case match-ex-table } (PROG P) \text{NullPointer } pc (\text{ex-table-of } (PROG P)$
 $C M) \text{ of None } \Rightarrow \text{None}$
 $\mid [(pc'', d)] \Rightarrow [pc'']) \rangle \langle ek = (\lambda s. \text{stkAt } s (\text{stkLength } (P, C, M) pc - 1) =$
 $\text{Null})_{\checkmark} \rangle$
 have $vp\text{-snoc } P C0 Main$ as ($C, M, [pc]$, Enter) $ek (C, M, [pc], \text{Exceptional } pc' \text{ Enter})$
 by (fastforce intro: JVMCFG-reachable.CFG-Getfield-Check-Exceptional)
 ultimately show ?case by blast

next
 case (CFG-Getfield-Update $C P C0 Main M pc F Cl ek$)
 then obtain as where JVMCFG-Interpret.valid-path' $P C0 Main$
 (ClassMain P , MethodMain P , None, Enter) as ($C, M, [pc]$, Normal)
 by blast
 moreover with $\langle C \neq \text{ClassMain } P \rangle \langle (P, C0, Main) \vdash \Rightarrow (C, M, [pc], \text{Normal}) \rangle$

```

    ⟨instrs-of (PROG P) C M ! pc = Getfield F Cl⟩
    ⟨ek = ↑λs. let (D, fs) = the (heap-of s (the-Addr (stkAt s (stkLength (P, C,
M) pc - 1))))
    in s(Stack (stkLength (P, C, M) pc - 1) ↦ Value (the (fs (F, Cl))))⟩
  have vp-snoc P C0 Main as (C, M, [pc], Normal) ek (C, M, [Suc pc], Enter)
  by (fastforce intro: JVMCFG-reachable.CFG-Getfield-Update)
  ultimately show ?case by blast
next
  case (CFG-Getfield-Exceptional-prop C P C0 Main M pc F Cl ek)
  then obtain as where JVMCFG-Interpret.valid-path' P C0 Main
    (ClassMain P, MethodMain P, None, Enter) as (C, M, [pc], Exceptional None
Enter)
  by blast
  moreover with ⟨C ≠ ClassMain P⟩ ⟨(P, C0, Main) ⊢ ⇒(C, M, [pc], Excep-
tional None Enter)⟩
    ⟨instrs-of (PROG P) C M ! pc = Getfield F Cl⟩
    ⟨ek = ↑λs. s(Exception ↦ Value (Addr (addr-of-sys-xcpt NullPointer)))⟩
  have vp-snoc P C0 Main as (C, M, [pc], Exceptional None Enter) ek (C, M,
None, Return)
  by (fastforce intro: JVMCFG-reachable.CFG-Getfield-Exceptional-prop)
  ultimately show ?case by blast
next
  case (CFG-Getfield-Exceptional-handle C P C0 Main M pc pc' F Cl ek)
  then obtain as where JVMCFG-Interpret.valid-path' P C0 Main
    (ClassMain P, MethodMain P, None, Enter) as (C, M, [pc], Exceptional [pc']
Enter)
  by blast
  moreover with ⟨C ≠ ClassMain P⟩ ⟨(P, C0, Main) ⊢ ⇒(C, M, [pc], Excep-
tional [pc'] Enter)⟩
    ⟨instrs-of (PROG P) C M ! pc = Getfield F Cl⟩
    ⟨ek = ↑λs. s(Exception := None)(Stack (stkLength (P, C, M) pc' - 1) ↦
Value (Addr (addr-of-sys-xcpt NullPointer)))⟩
  have vp-snoc P C0 Main as (C, M, [pc], Exceptional [pc'] Enter) ek (C, M,
[pc'], Enter)
  by (fastforce intro: JVMCFG-reachable.CFG-Getfield-Exceptional-handle)
  ultimately show ?case by blast
next
  case (CFG-Putfield-Check-Normal C P C0 Main M pc F Cl ek)
  then obtain as where JVMCFG-Interpret.valid-path' P C0 Main
    (ClassMain P, MethodMain P, None, Enter) as (C, M, [pc], Enter)
  by blast
  moreover with ⟨C ≠ ClassMain P⟩ ⟨(P, C0, Main) ⊢ ⇒(C, M, [pc], Enter)⟩
    ⟨instrs-of (PROG P) C M ! pc = Putfield F Cl⟩
    ⟨ek = (λs. stkAt s (stkLength (P, C, M) pc - 2) ≠ Null)✓⟩
  have vp-snoc P C0 Main as (C, M, [pc], Enter) ek (C, M, [pc], Normal)
  by (fastforce intro: JVMCFG-reachable.CFG-Putfield-Check-Normal)
  ultimately show ?case by blast
next
  case (CFG-Putfield-Check-Exceptional C P C0 Main M pc F Cl pc' ek)

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then obtain as where *JVMCFG-Interpret.valid-path'* $P\ C0\ Main$
 (*ClassMain* P , *MethodMain* P , *None*, *Enter*) as $(C, M, [pc], Enter)$
by blast
moreover with $\langle C \neq \text{ClassMain } P \rangle \langle (P, C0, Main) \vdash \Rightarrow (C, M, [pc], Enter) \rangle$
 $\langle \text{instrs-of } (PROG\ P)\ C\ M\ !\ pc = \text{Putfield } F\ Cl \rangle$
 $\langle pc' = (\text{case match-ex-table } (PROG\ P)\ \text{NullPointer } pc\ (\text{ex-table-of } (PROG\ P)\ C\ M))\ \text{of } \text{None} \Rightarrow \text{None}$
 $\mid \llbracket (pc'', d) \rrbracket \Rightarrow [pc''] \rangle \langle ek = (\lambda s. \text{stkAt } s\ (\text{stkLength } (P, C, M)\ pc - 2) = \text{Null}) \checkmark \rangle$
have *vp-snoc* $P\ C0\ Main$ as $(C, M, [pc], Enter)$ $ek\ (C, M, [pc], \text{Exceptional } pc'\ Enter)$
by (*fastforce intro: JVMCFG-reachable.CFG-Putfield-Check-Exceptional*)
ultimately show *?case by blast*
next
case (*CFG-Putfield-Update* $C\ P\ C0\ Main\ M\ pc\ F\ Cl\ ek$)
then obtain as where *JVMCFG-Interpret.valid-path'* $P\ C0\ Main$
 (*ClassMain* P , *MethodMain* P , *None*, *Enter*) as $(C, M, [pc], Normal)$
by blast
moreover with $\langle C \neq \text{ClassMain } P \rangle \langle (P, C0, Main) \vdash \Rightarrow (C, M, [pc], Normal) \rangle$
 $\langle \text{instrs-of } (PROG\ P)\ C\ M\ !\ pc = \text{Putfield } F\ Cl \rangle$
 $\langle ek = \uparrow \lambda s. \text{let } v = \text{stkAt } s\ (\text{stkLength } (P, C, M)\ pc - 1);$
 $r = \text{stkAt } s\ (\text{stkLength } (P, C, M)\ pc - 2);$
 $a = \text{the-Addr } r; (D, fs) = \text{the } (\text{heap-of } s\ a); h' = \text{heap-of } s\ (a \mapsto (D, fs((F, Cl) \mapsto v)))$
 $\text{in } s(\text{Heap} \mapsto \text{Hp } h') \rangle$
have *vp-snoc* $P\ C0\ Main$ as $(C, M, [pc], Normal)$ $ek\ (C, M, [Suc\ pc], Enter)$
by (*fastforce intro: JVMCFG-reachable.CFG-Putfield-Update*)
ultimately show *?case by blast*
next
case (*CFG-Putfield-Exceptional-prop* $C\ P\ C0\ Main\ M\ pc\ F\ Cl\ ek$)
then obtain as where *JVMCFG-Interpret.valid-path'* $P\ C0\ Main$
 (*ClassMain* P , *MethodMain* P , *None*, *Enter*) as $(C, M, [pc], \text{Exceptional } None\ Enter)$
by blast
moreover with $\langle C \neq \text{ClassMain } P \rangle \langle (P, C0, Main) \vdash \Rightarrow (C, M, [pc], \text{Exceptional } None\ Enter) \rangle$
 $\langle \text{instrs-of } (PROG\ P)\ C\ M\ !\ pc = \text{Putfield } F\ Cl \rangle$
 $\langle ek = \uparrow \lambda s. s(\text{Exception} \mapsto \text{Value } (\text{Addr } (\text{addr-of-sys-xcpt } \text{NullPointer}))) \rangle$
have *vp-snoc* $P\ C0\ Main$ as $(C, M, [pc], \text{Exceptional } None\ Enter)$ $ek\ (C, M, \text{None}, \text{Return})$
by (*fastforce intro: JVMCFG-reachable.CFG-Putfield-Exceptional-prop*)
ultimately show *?case by blast*
next
case (*CFG-Putfield-Exceptional-handle* $C\ P\ C0\ Main\ M\ pc\ pc'\ F\ Cl\ ek$)
then obtain as where *JVMCFG-Interpret.valid-path'* $P\ C0\ Main$
 (*ClassMain* P , *MethodMain* P , *None*, *Enter*) as $(C, M, [pc], \text{Exceptional } [pc']\ Enter)$
by blast
moreover with $\langle C \neq \text{ClassMain } P \rangle \langle (P, C0, Main) \vdash \Rightarrow (C, M, [pc], \text{Exceptional } [pc']\ Enter) \rangle$

tional $\lfloor pc' \rfloor$ *Enter*)
 \langle *instrs-of* (*PROG P*) *C M ! pc = Putfield F Cl*
 $\langle ek = \uparrow \lambda s. s(\text{Exception} := \text{None})(\text{Stack} (\text{stkLength} (P, C, M) pc' - 1) \mapsto$
 $\text{Value} (\text{Addr} (\text{addr-of-sys-xcpt} \text{NullPointer}))) \rangle$
have *vp-snoc P C0 Main as* (*C, M, [pc], Exceptional [pc'] Enter*) *ek* (*C, M,*
 $\lfloor pc' \rfloor$, *Enter*)
by (*fastforce intro: JVMCFG-reachable.CFG-Putfield-Exceptional-handle*)
ultimately show *?case by blast*
next
case (*CFG-Checkcast-Check-Normal C P C0 Main M pc Cl ek*)
then obtain as where *JVMCFG-Interpret.valid-path' P C0 Main*
(*ClassMain P, MethodMain P, None, Enter*) *as* (*C, M, [pc], Enter*)
by *blast*
moreover with $\langle C \neq \text{ClassMain } P \rangle \langle (P, C0, \text{Main}) \vdash \Rightarrow (C, M, \lfloor pc \rfloor, \text{Enter}) \rangle$
 \langle *instrs-of* (*PROG P*) *C M ! pc = Checkcast Cl*
 $\langle ek = (\lambda s. \text{cast-ok} (PROG P) Cl (\text{heap-of } s) (\text{stkAt } s (\text{stkLength} (P, C, M) pc$
 $- 1))) \rangle$
have *vp-snoc P C0 Main as* (*C, M, [pc], Enter*) *ek* (*C, M, [Suc pc], Enter*)
by (*fastforce intro: JVMCFG-reachable.CFG-Checkcast-Check-Normal*)
ultimately show *?case by blast*
next
case (*CFG-Checkcast-Check-Exceptional C P C0 Main M pc Cl pc' ek*)
then obtain as where *JVMCFG-Interpret.valid-path' P C0 Main*
(*ClassMain P, MethodMain P, None, Enter*) *as* (*C, M, [pc], Enter*)
by *blast*
moreover with $\langle C \neq \text{ClassMain } P \rangle \langle (P, C0, \text{Main}) \vdash \Rightarrow (C, M, \lfloor pc \rfloor, \text{Enter}) \rangle$
 \langle *instrs-of* (*PROG P*) *C M ! pc = Checkcast Cl*
 $\langle pc' = (\text{case match-ex-table} (PROG P) \text{ClassCast } pc (\text{ex-table-of} (PROG P) C$
 $M) \text{ of } \text{None} \Rightarrow \text{None}$
 $| \lfloor (pc'', d) \rfloor \Rightarrow \lfloor pc'' \rfloor) \rangle$
 $\langle ek = (\lambda s. \neg \text{cast-ok} (PROG P) Cl (\text{heap-of } s) (\text{stkAt } s (\text{stkLength} (P, C, M)$
 $pc - 1))) \rangle$
have *vp-snoc P C0 Main as* (*C, M, [pc], Enter*) *ek* (*C, M, [pc], Exceptional*
 $\lfloor pc' \rfloor$ *Enter*)
by (*fastforce intro: JVMCFG-reachable.CFG-Checkcast-Check-Exceptional*)
ultimately show *?case by blast*
next
case (*CFG-Checkcast-Exceptional-prop C P C0 Main M pc Cl ek*)
then obtain as where *JVMCFG-Interpret.valid-path' P C0 Main*
(*ClassMain P, MethodMain P, None, Enter*) *as* (*C, M, [pc], Exceptional None*
Enter)
by *blast*
moreover with $\langle C \neq \text{ClassMain } P \rangle \langle (P, C0, \text{Main}) \vdash \Rightarrow (C, M, \lfloor pc \rfloor, \text{Exceptional None Enter}) \rangle$
 \langle *instrs-of* (*PROG P*) *C M ! pc = Checkcast Cl*
 $\langle ek = \uparrow \lambda s. s(\text{Exception} \mapsto \text{Value} (\text{Addr} (\text{addr-of-sys-xcpt} \text{ClassCast}))) \rangle$
have *vp-snoc P C0 Main as* (*C, M, [pc], Exceptional None Enter*) *ek* (*C, M,*
None, Return)
by (*fastforce intro: JVMCFG-reachable.CFG-Checkcast-Exceptional-prop*)

ultimately show ?case by blast

next

case (CFG-Checkcast-Exceptional-handle C P C0 Main M pc pc' Cl ek)

then obtain as where JVMCFG-Interpret.valid-path' P C0 Main
(ClassMain P, MethodMain P, None, Enter) as (C, M, [pc], Exceptional [pc']
Enter)

by blast

moreover with $\langle C \neq \text{ClassMain } P \rangle \langle (P, C0, \text{Main}) \vdash \Rightarrow (C, M, [pc], \text{Exceptional } [pc'] \text{ Enter}) \rangle$

$\langle \text{instrs-of } (\text{PROG } P) \text{ C M ! pc} = \text{Checkcast Cl} \rangle$
 $\langle \text{ek} = \uparrow \lambda s. s(\text{Exception} := \text{None})(\text{Stack } (\text{stkLength } (P, C, M) \text{ pc}' - 1) \mapsto \text{Value } (\text{Addr } (\text{addr-of-sys-xcpt } \text{ClassCast}))) \rangle$

have vp-snoc P C0 Main as (C, M, [pc], Exceptional [pc'] Enter) ek (C, M, [pc'], Enter)

by (fastforce intro: JVMCFG-reachable.CFG-Checkcast-Exceptional-handle)

ultimately show ?case by blast

next

case (CFG-Throw-Check C P C0 Main M pc pc' Exc d ek)

then obtain as where path-src: JVMCFG-Interpret.valid-path' P C0 Main
(ClassMain P, MethodMain P, None, Enter) as (C, M, [pc], Enter)

by blast

from $\langle \text{pc}' = \text{None} \vee \text{match-ex-table } (\text{PROG } P) \text{ Exc pc } (\text{ex-table-of } (\text{PROG } P) \text{ C M}) = \lfloor (\text{the } \text{pc}', d) \rfloor \rangle$

show ?case

proof (elim disjE-strong)

assume $\text{pc}' = \text{None}$

with $\langle C \neq \text{ClassMain } P \rangle \langle (P, C0, \text{Main}) \vdash \Rightarrow (C, M, [pc], \text{Enter}) \rangle$

$\langle \text{instrs-of } (\text{PROG } P) \text{ C M ! pc} = \text{Throw} \rangle$

$\langle \text{ek} = (\lambda s. \text{let } v = \text{stkAt } s \text{ (stkLength } (P, C, M) \text{ pc} - 1);$
Cl = if v = Null then NullPointer else cname-of (heap-of s) (the-Addr v)
in case pc' of None $\Rightarrow \text{match-ex-table } (\text{PROG } P) \text{ Cl pc } (\text{ex-table-of } (\text{PROG } P) \text{ C M}) = \text{None}$
| [pc''] \Rightarrow
 $\exists d. \text{match-ex-table } (\text{PROG } P) \text{ Cl pc } (\text{ex-table-of } (\text{PROG } P) \text{ C M}) = \lfloor (\text{pc}'', d) \rfloor \rangle \checkmark$

have $(P, C0, \text{Main}) \vdash (C, M, [pc], \text{Enter}) -$
 $(\lambda s. (\text{stkAt } s \text{ (stkLength } (P, C, M) \text{ pc} - \text{Suc } 0) = \text{Null} \longrightarrow$
 $\text{match-ex-table } (\text{PROG } P) \text{ NullPointer pc } (\text{ex-table-of } (\text{PROG } P) \text{ C M}) = \text{None}) \wedge$
 $(\text{stkAt } s \text{ (stkLength } (P, C, M) \text{ pc} - \text{Suc } 0) \neq \text{Null} \longrightarrow$
 $\text{match-ex-table } (\text{PROG } P) \text{ (cname-of } (\text{heap-of } s) \text{ (the-Addr } (\text{stkAt } s \text{ (stkLength } (P, C, M) \text{ pc} - \text{Suc } 0)))) \text{ pc } (\text{ex-table-of } (\text{PROG } P) \text{ C M}) = \text{None}) \checkmark \rightarrow (C, M, [pc], \text{Exceptional } \text{None } \text{Enter})$

by $-(\text{erule JVMCFG-reachable.CFG-Throw-Check, simp-all})$

with path-src $\langle \text{pc}' = \text{None} \rangle \langle \text{ek} = (\lambda s. \text{let } v = \text{stkAt } s \text{ (stkLength } (P, C, M) \text{ pc} - 1);$
Cl = if v = Null then NullPointer else cname-of (heap-of s) (the-Addr v)
in case pc' of None $\Rightarrow \text{match-ex-table } (\text{PROG } P) \text{ Cl pc } (\text{ex-table-of } (\text{PROG } P) \text{ C M}) = \text{None} \rangle$

$P) C M) = \text{None}$
 $\quad | \lfloor pc'' \rfloor \Rightarrow$
 $\quad \quad \exists d. \text{match-ex-table } (PROG P) Cl pc (\text{ex-table-of } (PROG P) C M) =$
 $\lfloor (pc'', d) \rfloor \checkmark$
have $vp\text{-snoc } P C0 \text{ Main as } (C, M, \lfloor pc \rfloor, \text{Enter}) ek (C, M, \lfloor pc \rfloor, \text{Exceptional}$
 $\text{None Enter})$
by $(\text{fastforce intro: JVMCFG-reachable.CFG-Throw-Check})$
with $\text{path-src } \langle pc' = \text{None} \rangle$ **show** $?thesis$ **by** blast
next
assume $\text{met: match-ex-table } (PROG P) Exc pc (\text{ex-table-of } (PROG P) C M)$
 $= \lfloor (the pc', d) \rfloor$
and $pc': pc' \neq \text{None}$
with $\langle C \neq \text{ClassMain } P \rangle \langle (P, C0, \text{Main}) \vdash \Rightarrow (C, M, \lfloor pc \rfloor, \text{Enter}) \rangle$
 $\langle \text{instrs-of } (PROG P) C M ! pc = \text{Throw} \rangle$
 $\langle ek = (\lambda s. \text{let } v = \text{stkAt } s (\text{stkLength } (P, C, M) pc - 1);$
 $\quad Cl = \text{if } v = \text{Null then NullPointer else cname-of } (\text{heap-of } s) (\text{the-Addr } v)$
 $\quad \text{in case } pc' \text{ of None } \Rightarrow \text{match-ex-table } (PROG P) Cl pc (\text{ex-table-of } (PROG$
 $P) C M) = \text{None}$
 $\quad | \lfloor pc'' \rfloor \Rightarrow$
 $\quad \quad \exists d. \text{match-ex-table } (PROG P) Cl pc (\text{ex-table-of } (PROG P) C M) =$
 $\lfloor (pc'', d) \rfloor \checkmark$
have $(P, C0, \text{Main}) \vdash (C, M, \lfloor pc \rfloor, \text{Enter}) -$
 $(\lambda s. (\text{stkAt } s (\text{stkLength } (P, C, M) pc - \text{Suc } 0) = \text{Null} \longrightarrow$
 $\quad (\exists d. \text{match-ex-table } (PROG P) \text{NullPointer } pc$
 $\quad (\text{ex-table-of } (PROG P) C M) =$
 $\quad \lfloor (the pc', d) \rfloor)) \wedge$
 $(\text{stkAt } s (\text{stkLength } (P, C, M) pc - \text{Suc } 0) \neq \text{Null} \longrightarrow$
 $\quad (\exists d. \text{match-ex-table } (PROG P)$
 $\quad (\text{cname-of } (\text{heap-of } s)$
 $\quad (\text{the-Addr}$
 $\quad (\text{stkAt } s (\text{stkLength } (P, C, M) pc - \text{Suc } 0))))$
 $\quad pc (\text{ex-table-of } (PROG P) C M) =$
 $\quad \lfloor (the pc', d) \rfloor)) \checkmark \rightarrow$
 $(C, M, \lfloor pc \rfloor, \text{Exceptional } \lfloor the pc' \rfloor \text{Enter})$
by $-(\text{rule JVMCFG-reachable.CFG-Throw-Check, simp-all})$
with $\text{met } pc' \text{ path-src } \langle ek = (\lambda s. \text{let } v = \text{stkAt } s (\text{stkLength } (P, C, M) pc -$
 $1);$
 $\quad Cl = \text{if } v = \text{Null then NullPointer else cname-of } (\text{heap-of } s) (\text{the-Addr } v)$
 $\quad \text{in case } pc' \text{ of None } \Rightarrow \text{match-ex-table } (PROG P) Cl pc (\text{ex-table-of } (PROG$
 $P) C M) = \text{None}$
 $\quad | \lfloor pc'' \rfloor \Rightarrow$
 $\quad \quad \exists d. \text{match-ex-table } (PROG P) Cl pc (\text{ex-table-of } (PROG P) C M) =$
 $\lfloor (pc'', d) \rfloor \checkmark$
have $vp\text{-snoc } P C0 \text{ Main as } (C, M, \lfloor pc \rfloor, \text{Enter}) ek (C, M, \lfloor pc \rfloor, \text{Exceptional}$
 $pc' \text{Enter})$
by $(\text{fastforce intro: JVMCFG-reachable.CFG-Throw-Check})$
with path-src **show** $?thesis$ **by** blast
qed
next

case (*CFG-Throw-prop* $C P C0 Main M pc ek$)
then obtain as where *JVMCFG-Interpret.valid-path'* $P C0 Main$
(*ClassMain* P , *MethodMain* P , *None*, *Enter*) as ($C, M, [pc]$, *Exceptional None*
Enter)
by blast
moreover with $\langle C \neq \text{ClassMain } P \rangle \langle (P, C0, Main) \vdash \Rightarrow (C, M, [pc], \text{Exceptional None } \text{Enter}) \rangle$
 $\langle \text{instrs-of } (PROG P) C M ! pc = \text{Throw} \rangle$
 $\langle ek = \uparrow \lambda s. s(\text{Exception} \mapsto \text{Value } (\text{stkAt } s (\text{stkLength } (P, C, M) pc - 1))) \rangle$
have *vp-snoc* $P C0 Main$ as ($C, M, [pc]$, *Exceptional None* *Enter*) $ek (C, M, \text{None}, \text{nodeType.Return})$
by (*fastforce intro: JVMCFG-reachable.CFG-Throw-prop*)
ultimately show *?case* **by blast**
next
case (*CFG-Throw-handle* $C P C0 Main M pc pc' ek$)
then obtain as where *JVMCFG-Interpret.valid-path'* $P C0 Main$
(*ClassMain* P , *MethodMain* P , *None*, *Enter*) as ($C, M, [pc]$, *Exceptional* $[pc']$
Enter)
by blast
moreover with $\langle C \neq \text{ClassMain } P \rangle \langle (P, C0, Main) \vdash \Rightarrow (C, M, [pc], \text{Exceptional } [pc'] \text{Enter}) \rangle$
 $\langle pc' \neq \text{length } (\text{instrs-of } (PROG P) C M) \rangle \langle \text{instrs-of } (PROG P) C M ! pc = \text{Throw} \rangle$
 $\langle ek = \uparrow \lambda s. s(\text{Exception} := \text{None})(\text{Stack } (\text{stkLength } (P, C, M) pc' - 1) \mapsto \text{Value } (\text{stkAt } s (\text{stkLength } (P, C, M) pc - 1))) \rangle$
have *vp-snoc* $P C0 Main$ as ($C, M, [pc]$, *Exceptional* $[pc']$ *Enter*) $ek (C, M, [pc'], \text{Enter})$
by (*fastforce intro: JVMCFG-reachable.CFG-Throw-handle*)
ultimately show *?case* **by blast**
next
case (*CFG-Invoke-Check-NP-Normal* $C P C0 Main M pc M' n ek$)
then obtain as where *JVMCFG-Interpret.valid-path'* $P C0 Main$
(*ClassMain* P , *MethodMain* P , *None*, *Enter*) as ($C, M, [pc]$, *Enter*)
by blast
moreover with $\langle C \neq \text{ClassMain } P \rangle \langle (P, C0, Main) \vdash \Rightarrow (C, M, [pc], \text{Enter}) \rangle$
 $\langle \text{instrs-of } (PROG P) C M ! pc = \text{Invoke } M' n \rangle$
 $\langle ek = (\lambda s. \text{stkAt } s (\text{stkLength } (P, C, M) pc - \text{Suc } n) \neq \text{Null}) \checkmark \rangle$
have *vp-snoc* $P C0 Main$ as ($C, M, [pc]$, *Enter*) $ek (C, M, [pc], \text{Normal})$
by (*fastforce intro: JVMCFG-reachable.CFG-Invoke-Check-NP-Normal*)
ultimately show *?case* **by blast**
next
case (*CFG-Invoke-Check-NP-Exceptional* $C P C0 Main M pc M' n pc' ek$)
then obtain as where *JVMCFG-Interpret.valid-path'* $P C0 Main$
(*ClassMain* P , *MethodMain* P , *None*, *Enter*) as ($C, M, [pc]$, *Enter*)
by blast
moreover with $\langle C \neq \text{ClassMain } P \rangle \langle (P, C0, Main) \vdash \Rightarrow (C, M, [pc], \text{Enter}) \rangle$
 $\langle \text{instrs-of } (PROG P) C M ! pc = \text{Invoke } M' n \rangle$
 $\langle pc' = (\text{case match-ex-table } (PROG P) \text{NullPointer } pc (\text{ex-table-of } (PROG P) C M) \text{ of } \text{None} \Rightarrow \text{None}) \rangle$

$\lfloor \lfloor pc'', d \rfloor \Rightarrow \lfloor pc'' \rfloor \rfloor$
 $\langle ek = (\lambda s. stkAt s (stkLength (P, C, M) pc - Suc n) = Null) \checkmark \rangle$
have $vp\text{-}snoc\ P\ C0\ Main\ as\ (C, M, \lfloor pc \rfloor, Enter)\ ek\ (C, M, \lfloor pc \rfloor, Exceptional\ pc'\ Enter)$
by (*fastforce intro: JVMCFG-reachable.CFG-Invoke-Check-NP-Exceptional*)
ultimately show $?case\ by\ blast$
next
case (*CFG-Invoke-NP-prop* $C\ P\ C0\ Main\ M\ pc\ M'\ n\ ek$)
then obtain **as** **where** *JVMCFG-Interpret.valid-path'* $P\ C0\ Main$
 $(ClassMain\ P, MethodMain\ P, None, Enter)\ as\ (C, M, \lfloor pc \rfloor, Exceptional\ None\ Enter)$
by *blast*
moreover with $\langle C \neq ClassMain\ P \rangle \langle (P, C0, Main) \vdash \Rightarrow (C, M, \lfloor pc \rfloor, Exceptional\ None\ Enter) \rangle$
 $\langle instrs\text{-}of\ (PROG\ P)\ C\ M\ !\ pc = Invoke\ M'\ n \rangle$
 $\langle ek = \uparrow \lambda s. s(Exception \mapsto Value (Addr (addr\text{-}of\text{-}sys\text{-}xcpt\ NullPointer))) \rangle$
have $vp\text{-}snoc\ P\ C0\ Main\ as\ (C, M, \lfloor pc \rfloor, Exceptional\ None\ Enter)\ ek\ (C, M, None, Return)$
by (*fastforce intro: JVMCFG-reachable.CFG-Invoke-NP-prop*)
ultimately show $?case\ by\ blast$
next
case (*CFG-Invoke-NP-handle* $C\ P\ C0\ Main\ M\ pc\ pc'\ M'\ n\ ek$)
then obtain **as** **where** *JVMCFG-Interpret.valid-path'* $P\ C0\ Main$
 $(ClassMain\ P, MethodMain\ P, None, Enter)\ as\ (C, M, \lfloor pc \rfloor, Exceptional\ \lfloor pc' \rfloor\ Enter)$
by *blast*
moreover with $\langle C \neq ClassMain\ P \rangle \langle (P, C0, Main) \vdash \Rightarrow (C, M, \lfloor pc \rfloor, Exceptional\ \lfloor pc' \rfloor\ Enter) \rangle$
 $\langle instrs\text{-}of\ (PROG\ P)\ C\ M\ !\ pc = Invoke\ M'\ n \rangle$
 $\langle ek = \uparrow \lambda s. s(Exception := None)(Stack (stkLength (P, C, M) pc' - 1) \mapsto Value (Addr (addr\text{-}of\text{-}sys\text{-}xcpt\ NullPointer))) \rangle$
have $vp\text{-}snoc\ P\ C0\ Main\ as\ (C, M, \lfloor pc \rfloor, Exceptional\ \lfloor pc' \rfloor\ Enter)\ ek\ (C, M, \lfloor pc' \rfloor, Enter)$
by (*fastforce intro: JVMCFG-reachable.CFG-Invoke-NP-handle*)
ultimately show $?case\ by\ blast$
next
case (*CFG-Invoke-Call* $C\ P\ C0\ Main\ M\ pc\ M'\ n\ ST\ LT\ D'\ Ts\ T\ mxs\ mxl_0\ is\ xt\ D\ Q\ paramDefs\ ek$)
then obtain **as** **where** *JVMCFG-Interpret.valid-path'* $P\ C0\ Main$
 $(ClassMain\ P, MethodMain\ P, None, Enter)\ as\ (C, M, \lfloor pc \rfloor, Normal)$
by *blast*
moreover with $\langle C \neq ClassMain\ P \rangle \langle (P, C0, Main) \vdash \Rightarrow (C, M, \lfloor pc \rfloor, Normal) \rangle$
 $\langle instrs\text{-}of\ (PROG\ P)\ C\ M\ !\ pc = Invoke\ M'\ n \rangle \langle TYPING\ P\ C\ M\ !\ pc = \lfloor (ST, LT) \rfloor \rangle$
 $\langle ST\ !\ n = Class\ D' \rangle \langle PROG\ P\ \vdash\ D'\ sees\ M':\ Ts \rightarrow T = (mxs, mxl_0, is, xt)\ in\ D \rangle$
 $\langle Q = (\lambda(s, ret). let\ r = stkAt\ s\ (stkLength\ (P, C, M)\ pc - Suc\ n);$
 $C' = cname\text{-}of\ (heap\text{-}of\ s)\ (the\text{-}Addr\ r)\ in\ D = fst\ (method\ (PROG\ P)\ C'\ M')) \rangle$

```

    ⟨paramDefs = (λs. s Heap) # (λs. s (Stack (stkLength (P, C, M) pc - Suc
n))) #
    rev (map (λi s. s (Stack (stkLength (P, C, M) pc - Suc i))) [0..<n])⟩
    ⟨ek = Q:(C, M, pc)↔(D, M)paramDefs⟩
have vp-snoc P C0 Main as (C, M, [pc], Normal) ek (D, M', None, Enter)
by (fastforce intro: JVMCFG-reachable.CFG-Invoke-Call)
ultimately show ?case by blast
next
case (CFG-Invoke-False C P C0 Main M pc M' n ek)
then obtain as where JVMCFG-Interpret.valid-path' P C0 Main
  (ClassMain P, MethodMain P, None, Enter) as (C, M, [pc], Normal)
by blast
moreover with ⟨C ≠ ClassMain P⟩ ⟨(P, C0, Main) ⊢ ⇒(C, M, [pc], Normal)⟩
  ⟨instrs-of (PROG P) C M ! pc = Invoke M' n⟩ ⟨ek = (λs. False)√⟩
have vp-snoc P C0 Main as (C, M, [pc], Normal) ek (C, M, [pc], Return)
by (fastforce intro: JVMCFG-reachable.CFG-Invoke-False)
ultimately show ?case by blast
next
case (CFG-Invoke-Return-Check-Normal C P C0 Main M pc M' n ST LT ek)
then obtain as where JVMCFG-Interpret.valid-path' P C0 Main
  (ClassMain P, MethodMain P, None, Enter) as (C, M, [pc], nodeType.Return)
by blast
moreover with ⟨C ≠ ClassMain P⟩ ⟨(P, C0, Main) ⊢ ⇒(C, M, [pc], node-
Type.Return)⟩
  ⟨instrs-of (PROG P) C M ! pc = Invoke M' n⟩ ⟨TYPING P C M ! pc = [(ST,
LT)]⟩
  ⟨ST ! n ≠ NT⟩ ⟨ek = (λs. s Exception = None)√⟩
have vp-snoc P C0 Main as (C, M, [pc], Return) ek (C, M, [Suc pc], Enter)
by (fastforce intro: JVMCFG-reachable.CFG-Invoke-Return-Check-Normal)
ultimately show ?case by blast
next
case (CFG-Invoke-Return-Check-Exceptional C P C0 Main M pc M' n Exc pc'
diff ek)
then obtain as where JVMCFG-Interpret.valid-path' P C0 Main
  (ClassMain P, MethodMain P, None, Enter) as (C, M, [pc], nodeType.Return)
by blast
moreover with ⟨C ≠ ClassMain P⟩ ⟨(P, C0, Main) ⊢ ⇒(C, M, [pc], node-
Type.Return)⟩
  ⟨instrs-of (PROG P) C M ! pc = Invoke M' n⟩
  ⟨match-ex-table (PROG P) Exc pc (ex-table-of (PROG P) C M) = [(pc', diff)]⟩
  ⟨pc' ≠ length (instrs-of (PROG P) C M)⟩
  ⟨ek = (λs. ∃ v d. s Exception = [v] ∧
    match-ex-table (PROG P) (cname-of (heap-of s) (the-Addr (the-Value
v))) pc
    (ex-table-of (PROG P) C M) = [(pc', d)]√⟩
have vp-snoc P C0 Main as (C, M, [pc], Return) ek (C, M, [pc], Exceptional
[pc'] Return)
by (fastforce intro: JVMCFG-reachable.CFG-Invoke-Return-Check-Exceptional)
ultimately show ?case by blast

```

next
case (*CFG-Invoke-Return-Exceptional-handle* $C P C0 Main M pc pc' M' n ek$)
then obtain as where *JVMCFG-Interpret.valid-path'* $P C0 Main$
(*ClassMain* P , *MethodMain* P , *None*, *Enter*) as $(C, M, [pc], Exceptional [pc']$
nodeType.Return)
by blast
moreover with $\langle C \neq \text{ClassMain } P \rangle \langle (P, C0, Main) \vdash \Rightarrow (C, M, [pc], \text{Exceptional } [pc'] \text{ nodeType.Return}) \rangle$
 $\langle \text{instrs-of } (PROG P) C M ! pc = \text{Invoke } M' n \rangle$
 $\langle ek = \uparrow \lambda s. s(\text{Exception} := \text{None}, \text{Stack } (\text{stkLength } (P, C, M) pc' - 1) := s \text{ Exception}) \rangle$
have *vp-snoc* $P C0 Main$ as $(C, M, [pc], Exceptional [pc'] \text{ Return}) ek (C, M,$
 $[pc'], \text{Enter})$
by (*fastforce intro: JVMCFG-reachable.CFG-Invoke-Return-Exceptional-handle*)
ultimately show *?case by blast*
next
case (*CFG-Invoke-Return-Exceptional-prop* $C P C0 Main M pc M' n ek$)
then obtain as where *JVMCFG-Interpret.valid-path'* $P C0 Main$
(*ClassMain* P , *MethodMain* P , *None*, *Enter*) as $(C, M, [pc], \text{nodeType.Return})$
by blast
moreover with $\langle C \neq \text{ClassMain } P \rangle \langle (P, C0, Main) \vdash \Rightarrow (C, M, [pc], \text{nodeType.Return}) \rangle$
 $\langle \text{instrs-of } (PROG P) C M ! pc = \text{Invoke } M' n \rangle$
 $\langle ek = (\lambda s. \exists v. s \text{ Exception} = [v] \wedge$
 $\text{match-ex-table } (PROG P) (\text{cname-of } (\text{heap-of } s) (\text{the-Addr } (\text{the-Value } v))) pc$
 $(\text{ex-table-of } (PROG P) C M) = \text{None}) \vee \rangle$
have *vp-snoc* $P C0 Main$ as $(C, M, [pc], \text{Return}) ek (C, M, \text{None}, \text{Return})$
by (*fastforce intro: JVMCFG-reachable.CFG-Invoke-Return-Exceptional-prop*)
ultimately show *?case by blast*
next
case (*CFG-Return* $C P C0 Main M pc ek$)
then obtain as where *JVMCFG-Interpret.valid-path'* $P C0 Main$
(*ClassMain* P , *MethodMain* P , *None*, *Enter*) as $(C, M, [pc], \text{Enter})$
by blast
moreover with $\langle C \neq \text{ClassMain } P \rangle \langle (P, C0, Main) \vdash \Rightarrow (C, M, [pc], \text{Enter}) \rangle$
 $\langle \text{instrs-of } (PROG P) C M ! pc = \text{instr.Return} \rangle$
 $\langle ek = \uparrow \lambda s. s(\text{Stack } 0 := s (\text{Stack } (\text{stkLength } (P, C, M) pc - 1))) \rangle$
have *vp-snoc* $P C0 Main$ as $(C, M, [pc], \text{Enter}) ek (C, M, \text{None}, \text{Return})$
by (*fastforce intro: JVMCFG-reachable.CFG-Return*)
ultimately show *?case by blast*
next
case (*CFG-Return-from-Method* $P C0 Main C M C' M' pc' Q' ps Q \text{stateUpdate}$
 ek)
from $\langle (P, C0, Main) \vdash (C', M', [pc'], \text{Normal}) - Q':(C', M', pc') \leftrightarrow (C, M) ps \rightarrow$
 $(C, M, \text{None}, \text{Enter}) \rangle$
show *?case*
proof cases
case *Main-Call*

```

with CFG-Return-from-Method obtain as where JVMCFG-Interpret.valid-path'
P C0 Main
  (ClassMain P, MethodMain P, None, Enter) as (ClassMain P, MethodMain
P, [0], Normal)
  by blast
  moreover with Main-Call have vp-snoc P C0 Main as (ClassMain P, Method-
Main P, [0], Normal)
  ( $\lambda s. \text{False}$ )✓ (ClassMain P, MethodMain P, [0], Return)
  by (fastforce intro: Main-Call-LFalse)
  ultimately show ?thesis using Main-Call CFG-Return-from-Method by blast
next
  case CFG-Invoke-Call
  with CFG-Return-from-Method obtain as where JVMCFG-Interpret.valid-path'
P C0 Main
  (ClassMain P, MethodMain P, None, Enter) as (C', M', [pc'], Normal)
  by blast
  moreover with CFG-Invoke-Call
  have vp-snoc P C0 Main as (C', M', [pc'], Normal) ( $\lambda s. \text{False}$ )✓ (C', M',
[pc'], Return)
  by (fastforce intro: CFG-Invoke-False)
  ultimately show ?thesis using CFG-Invoke-Call CFG-Return-from-Method
by blast
qed
qed

```

```

declare JVMCFG-Interpret.vp-snocI []
declare JVMCFG-Interpret.valid-node-def [simp del]
  valid-edge-def [simp del]
  JVMCFG-Interpret.intra-path-def [simp del]

```

```

definition EP :: jvm-prog
  where EP = ("C''", Object, [],
  [("M''", [], Void, 1::nat, 0::nat, [Push Unit, instr.Return], [])]) # SystemClasses

```

```

definition Phi-EP :: typ
  where Phi-EP C M = (if C = "C''" ∧ M = "M''"
  then [[([], [OK (Class "C''")]]), [(Void), [OK (Class "C''")]]] else [])

```

```

lemma distinct-classes'':
  "C''" ≠ Object
  "C''" ≠ NullPointer
  "C''" ≠ OutOfMemory
  "C''" ≠ ClassCast
  by (simp-all add: Object-def NullPointer-def OutOfMemory-def ClassCast-def)

```

```

lemmas distinct-classes =
  distinct-classes distinct-classes'' distinct-classes'' [symmetric]

```

```

declare distinct-classes [simp add]

lemma i-max-2D:  $i < \text{Suc } (\text{Suc } 0) \implies i = 0 \vee i = 1$  by auto

lemma EP-wf: wf-jvm-prog Phi-EP EP
  unfolding wf-jvm-prog-phi-def wf-prog-def
proof
  show wf-syscls EP
    by (simp add: EP-def wf-syscls-def SystemClasses-def sys-xcpts-def
      ObjectC-def NullPointerC-def OutOfMemoryC-def ClassCastC-def)
next
  have distinct-EP: distinct-fst EP
    by (auto simp: EP-def SystemClasses-def ObjectC-def NullPointerC-def OutOfMemoryC-def
      ClassCastC-def)
  moreover have classes-wf:
     $\forall c \in \text{set } EP. \text{wf-cdecl}$ 
    ( $\lambda P C (M, Ts, T_r, mxs, mxl_0, is, xt). \text{wt-method } P C Ts T_r mxs mxl_0 is xt$ 
    (Phi-EP C M)) EP c
proof
  fix C
  assume C-in-EP:  $C \in \text{set } EP$ 
  show wf-cdecl
    ( $\lambda P C (M, Ts, T_r, mxs, mxl_0, is, xt). \text{wt-method } P C Ts T_r mxs mxl_0 is xt$ 
    (Phi-EP C M)) EP C
proof (cases C \in set SystemClasses)
  case True
  thus ?thesis
    by (auto simp: wf-cdecl-def SystemClasses-def ObjectC-def NullPointerC-def
      OutOfMemoryC-def ClassCastC-def EP-def class-def)
next
  case False
  with C-in-EP have  $C = ("C", \text{the } (\text{class } EP "C"))$ 
    by (auto simp: EP-def SystemClasses-def class-def)
  thus ?thesis
    by (auto dest!: i-max-2D elim: Methods.cases
      simp: wf-cdecl-def class-def EP-def wf-mdecl-def wt-method-def Phi-EP-def
      wt-start-def check-types-def states-def JVM-SemiType.sl-def SystemClasses-def
      stk-esl-def upto-esl-def loc-sl-def SemiType.esl-def ObjectC-def
      SemiType.sup-def Err.sl-def Err.le-def err-def Listn.sl-def Method-def
      Err.esl-def Opt.esl-def Product.esl-def relevant-entries-def)
qed
qed
ultimately show ( $\forall c \in \text{set } EP. \text{wf-cdecl}$ 
  ( $\lambda P C (M, Ts, T_r, mxs, mxl_0, is, xt). \text{wt-method } P C Ts T_r mxs mxl_0 is xt$ 
  (Phi-EP C M)) EP c)  $\wedge$ 
  distinct-fst EP
  by simp
qed

```


lemma [simp]: $PROG (Abs\text{-}wf\text{-}jvmprog (EP, Phi\text{-}EP)) = EP$
proof (cases (EP, Phi-EP) ∈ wf-jvmprog)
 case True **thus** ?thesis **by** (simp add: Abs-wf-jvmprog-inverse)
next
 case False **with** EP-wf **show** ?thesis **by** (simp add: wf-jvmprog-def)
qed

lemma [simp]: $TYPING (Abs\text{-}wf\text{-}jvmprog (EP, Phi\text{-}EP)) = Phi\text{-}EP$
proof (cases (EP, Phi-EP) ∈ wf-jvmprog)
 case True **thus** ?thesis **by** (simp add: Abs-wf-jvmprog-inverse)
next
 case False **with** EP-wf **show** ?thesis **by** (simp add: wf-jvmprog-def)
qed

lemma method-in-EP-is-M:
 $EP \vdash C \text{ sees } M: Ts \rightarrow T = (mxs, mxl, is, xt) \text{ in } D$
 $\implies C = "C" \wedge M = "M" \wedge Ts = [] \wedge T = Void \wedge mxs = 1 \wedge mxl = 0 \wedge$
 $is = [Push\ Unit, instr.Return] \wedge xt = [] \wedge D = "C"$
by (fastforce elim: Methods.cases
 simp: class-def SystemClasses-def ObjectC-def NullPointerC-def OutOfMemoryC-def
 ClassCastC-def
 split-if-eq1 EP-def Method-def)

lemma [simp]:
 $\exists T Ts mxs mxl is. (\exists xt. EP \vdash "C" \text{ sees } "M": Ts \rightarrow T = (mxs, mxl, is, xt) \text{ in } "C") \wedge is \neq []$
using EP-wf
by (fastforce dest: mdecl-visible simp: wf-jvm-prog-phi-def EP-def)

lemma [simp]:
 $\exists T Ts mxs mxl is. (\exists xt. EP \vdash "C" \text{ sees } "M": Ts \rightarrow T = (mxs, mxl, is, xt) \text{ in } "C") \wedge$
 $Suc\ 0 < length\ is$
using EP-wf
by (fastforce dest: mdecl-visible simp: wf-jvm-prog-phi-def EP-def)

lemma C-sees-M-in-EP [simp]:
 $EP \vdash "C" \text{ sees } "M": [] \rightarrow Void = (Suc\ 0, 0, [Push\ Unit, instr.Return], []) \text{ in } "C"$
proof –
 have EP ⊢ "C" sees-methods ["M" ↦ (([], Void, 1, 0, [Push Unit, instr.Return], []), "C")
 by (fastforce intro: Methods.intros simp: class-def SystemClasses-def ObjectC-def EP-def)
 thus ?thesis **by** (fastforce simp: Method-def)
qed

lemma instrs-of-EP-C-M [simp]:
 $instrs\ of\ EP\ "C" "M" = [Push\ Unit, instr.Return]$

unfolding *method-def*
by (*rule theI2* [**where** $P = \lambda(D, Ts, T, m). EP \vdash "C" \text{ sees } "M": Ts \rightarrow T = m$
in D])
(*auto dest: method-in-EP-is-M*)

lemma *ClassMain-not-C* [*simp*]: $ClassMain (Abs-wf-jvmprog (EP, Phi-EP)) \neq "C"$
by (*fastforce intro: no-Call-in-ClassMain* [**where** $P = Abs-wf-jvmprog (EP, Phi-EP)$]
C-sees-M-in-EP)

lemma *method-entry* [*dest!*]: $(Abs-wf-jvmprog (EP, Phi-EP), "C", "M") \vdash \Rightarrow (C, M, None, Enter)$
 $\Rightarrow (C = ClassMain (Abs-wf-jvmprog (EP, Phi-EP)) \wedge M = MethodMain (Abs-wf-jvmprog (EP, Phi-EP)))$
 $\vee (C = "C" \wedge M = "M")$
by (*fastforce elim: reachable.cases elim!: JVMCFG.cases dest!: method-in-EP-is-M*)

lemma *valid-node-in-EP-D*:
assumes $vn: JVMCFG-Interpret.valid-node (Abs-wf-jvmprog (EP, Phi-EP)) "C"$
 $"M" n$

shows $n \in \{$
 $(ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog (EP, Phi-EP)), None, Enter),$
 $(ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog (EP, Phi-EP)), None, Return),$
 $(ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog (EP, Phi-EP)), \lfloor 0 \rfloor, Enter),$
 $(ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog (EP, Phi-EP)), \lfloor 0 \rfloor, Normal),$
 $(ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog (EP, Phi-EP)), \lfloor 0 \rfloor, Return),$
 $("C", "M", None, Enter),$
 $("C", "M", \lfloor 0 \rfloor, Enter),$
 $("C", "M", \lfloor 1 \rfloor, Enter),$
 $("C", "M", None, Return)$
 $\}$

using vn

proof (*cases rule: JVMCFG-Interpret.valid-node-cases*)

let $?prog = Abs-wf-jvmprog (EP, Phi-EP)$

case (*Source e*)

then obtain $C M pc nt ek C' M' pc' nt'$

where [*simp*]: $e = ((C, M, pc, nt), ek, (C', M', pc', nt'))$

and [*simp*]: $n = (C, M, pc, nt)$

and $edge: (?prog, "C", "M") \vdash (C, M, pc, nt) -ek \rightarrow (C', M', pc', nt')$

by (*cases e*) (*fastforce simp: valid-edge-def*)

from $edge$ **have** *src-reachable*: $(?prog, "C", "M") \vdash \Rightarrow (C, M, pc, nt)$

by $-(drule sourcenode-reachable)$

show $?thesis$

proof (*cases* $C = ClassMain ?prog$)

```

case True
with src-reachable have  $M = \text{MethodMain } ?prog$ 
  by (fastforce dest: ClassMain-imp-MethodMain)
with True edge show ?thesis
  by clarsimp (erule JVMCFG.cases, simp-all)
next
case False
with src-reachable obtain  $T Ts mb$  where  $EP \vdash C \text{ sees } M:Ts \rightarrow T = mb \text{ in } C$ 
  by (fastforce dest: method-of-reachable-node-exists)
hence [simp]:  $C = "C"$ 
  and [simp]:  $M = "M"$ 
  and [simp]:  $Ts = []$ 
  and [simp]:  $T = \text{Void}$ 
  and [simp]:  $mb = (1, 0, [\text{Push Unit}, \text{instr.Return}], [])$ 
  by (cases mb, fastforce dest: method-in-EP-is-M)+
from src-reachable False have  $pc \in \{\text{None}, [0], [1]\}$ 
  by (fastforce dest: instr-of-reachable-node-typable)
show ?thesis
proof (cases pc)
  case None
  with edge False show ?thesis
  by clarsimp (erule JVMCFG.cases, simp-all)
next
  case (Some pc')
  show ?thesis
  proof (cases pc')
    case 0
    with Some False edge show ?thesis
    by clarsimp (erule JVMCFG.cases, fastforce+)
  next
    case (Suc n)
    with ( $pc \in \{\text{None}, [0], [1]\}$ ) Some have  $pc = [1]$ 
    by simp
    with False edge show ?thesis
    by clarsimp (erule JVMCFG.cases, fastforce+)
  qed
qed
qed
next
let  $?prog = \text{Abs-wf-jvmprog } (EP, \text{Phi-EP})$ 
case (Target e)
then obtain  $C M pc nt ek C' M' pc' nt'$ 
  where [simp]:  $e = ((C, M, pc, nt), ek, (C', M', pc', nt'))$ 
  and [simp]:  $n = (C', M', pc', nt')$ 
  and edge:  $(?prog, "C", "M") \vdash (C, M, pc, nt) -ek \rightarrow (C', M', pc', nt')$ 
  by (cases e) (fastforce simp: valid-edge-def)
from edge have trg-reachable:  $(?prog, "C", "M") \vdash \Rightarrow (C', M', pc', nt')$ 
  by  $-(\text{drule targetnode-reachable})$ 
show ?thesis

```

```

proof (cases C' = ClassMain ?prog)
  case True
    with trg-reachable have M' = MethodMain ?prog
      by (fastforce dest: ClassMain-imp-MethodMain)
    with True edge show ?thesis
      by -(clarsimp, (erule JVMCFG.cases, simp-all))+
  next
    case False
      with trg-reachable obtain T Ts mb where EP ⊢ C' sees M':Ts→T = mb in
C'
        by (fastforce dest: method-of-reachable-node-exists)
      hence [simp]: C' = "C"
        and [simp]: M' = "M"
        and [simp]: Ts = []
        and [simp]: T = Void
        and [simp]: mb = (1, 0, [Push Unit, instr.Return], [])
        by (cases mb, fastforce dest: method-in-EP-is-M)+
      from trg-reachable False have pc' ∈ {None, [0], [1]}
        by (fastforce dest: instr-of-reachable-node-typable)
      show ?thesis
      proof (cases pc')
        case None
          with edge False show ?thesis
            byclarsimp (erule JVMCFG.cases, simp-all)
        next
          case (Some pc'')
            show ?thesis
            proof (cases pc'')
              case 0
                with Some False edge show ?thesis
                  by -(clarsimp, (erule JVMCFG.cases, fastforce+))+
              next
                case (Suc n)
                  with {pc' ∈ {None, [0], [1]}} Some have pc' = [1]
                    by simp
                  with False edge show ?thesis
                    by -(clarsimp, (erule JVMCFG.cases, fastforce+))+
            qed
          qed
        qed
      qed

```

lemma *Main-Entry-valid* [simp]:

JVMCFG-Interpret.valid-node (Abs-wf-jvmprog (EP, Phi-EP)) "C" "M"
 (ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog (EP,
 Phi-EP)), None, Enter)

proof –

have *valid-edge* (Abs-wf-jvmprog (EP, Phi-EP), "C", "M")
 ((ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog

```

(EP, Phi-EP), None,
  Enter),
  ( $\lambda s. \text{False}$ ) $\surd$ ,
  (ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog
(EP, Phi-EP)), None,
  Return))
  by (auto simp: valid-edge-def intro: JVMCFG-reachable.intros)
  thus ?thesis by (fastforce simp: JVMCFG-Interpret.valid-node-def)
qed

```

```

lemma main-0-Enter-reachable [simp]: (P, C0, Main)  $\vdash \Rightarrow$  (ClassMain P, Method-
Main P, [0], Enter)
  by (rule reachable-step [where n=(ClassMain P, MethodMain P, None, Enter)])
(fastforce intro: JVMCFG-reachable.intros) $+$ 

```

```

lemma main-0-Normal-reachable [simp]: (P, C0, Main)  $\vdash \Rightarrow$  (ClassMain P, Method-
Main P, [0], Normal)
  by (rule reachable-step [where n=(ClassMain P, MethodMain P, [0], Enter)],
simp)
(fastforce intro: JVMCFG-reachable.intros)

```

```

lemma main-0-Return-reachable [simp]: (P, C0, Main)  $\vdash \Rightarrow$  (ClassMain P, Method-
Main P, [0], Return)
  by (rule reachable-step [where n=(ClassMain P, MethodMain P, [0], Normal)],
simp)
(fastforce intro: JVMCFG-reachable.intros)

```

```

lemma Exit-reachable [simp]: (P, C0, Main)  $\vdash \Rightarrow$  (ClassMain P, MethodMain P,
None, Return)
  by (rule reachable-step [where n=(ClassMain P, MethodMain P, [0], Return)],
simp)
(fastforce intro: JVMCFG-reachable.intros)

```

definition

```

cfg-wf-prog =
  {(P, C0, Main). ( $\forall n. \text{JVMCFG-Interpret.valid-node } P \ C0 \ Main \ n \longrightarrow$ 
    ( $\exists as. \text{CFG.valid-path}' \ \text{sourcenode} \ \text{targetnode} \ \text{kind} \ (\text{valid-edge } (P, \ C0,
Main}))$ 
    (get-return-edges P) n as (ClassMain P, MethodMain P, None,
Return)))}

```

```

typedef cfg-wf-prog = cfg-wf-prog

```

```

  unfolding cfg-wf-prog-def

```

proof

```

  let ?prog = (Abs-wf-jvmprog (EP, Phi-EP), "C", "M")
  let ?edge-main0 = ((ClassMain (fst ?prog), MethodMain (fst ?prog), None, En-
ter),
    ( $\lambda s. \text{False}$ ) $\surd$ ,
    (ClassMain (fst ?prog), MethodMain (fst ?prog), None, Return))

```

```

let ?edge-main1 = ((ClassMain (fst ?prog), MethodMain (fst ?prog), None, Enter),
  (λs. True)✓,
  (ClassMain (fst ?prog), MethodMain (fst ?prog), [0], Enter))
let ?edge-main2 = ((ClassMain (fst ?prog), MethodMain (fst ?prog), [0], Enter),
  ↑id,
  (ClassMain (fst ?prog), MethodMain (fst ?prog), [0], Normal))
let ?edge-main3 = ((ClassMain (fst ?prog), MethodMain (fst ?prog), [0], Normal),
  (λs. False)✓,
  (ClassMain (fst ?prog), MethodMain (fst ?prog), [0], Return))
let ?edge-main4 = ((ClassMain (fst ?prog), MethodMain (fst ?prog), [0], Return),
  ↑id,
  (ClassMain (fst ?prog), MethodMain (fst ?prog), None, Return))
let ?edge-call = ((ClassMain (fst ?prog), MethodMain (fst ?prog), [0], Normal),
  ((λ(s, ret). True):(ClassMain (fst ?prog),
    MethodMain (fst ?prog), 0)↔("C", "M")[(λs. s Heap),(λs. [Value Null]])),
  ("C", "M", None, Enter))
let ?edge-C0 = (("C", "M", None, Enter),
  (λs. False)✓,
  ("C", "M", None, Return))
let ?edge-C1 = (("C", "M", None, Enter),
  (λs. True)✓,
  ("C", "M", [0], Enter))
let ?edge-C2 = (("C", "M", [0], Enter),
  ↑(λs. s(Stack 0 ↦ Value Unit)),
  ("C", "M", [1], Enter))
let ?edge-C3 = (("C", "M", [1], Enter),
  ↑(λs. s(Stack 0 := s (Stack 0))),
  ("C", "M", None, Return))
let ?edge-return = (("C", "M", None, Return),
  (λ(s, ret). ret = (ClassMain (fst ?prog),
    MethodMain (fst ?prog), 0)↔("C", "M")(λs s'. s'(Heap := s Heap,
      Exception := s Exception,
      Stack 0 := s (Stack 0))),
  (ClassMain (fst ?prog), MethodMain (fst ?prog), [0], Return))
have [simp]:
  (Abs-wf-jvmprog (EP, Phi-EP), "C", "M") ⊢ ⇒("C", "M", None, Enter)
  by (rule reachable-step [where n=(ClassMain (fst ?prog), MethodMain (fst
    ?prog), [0], Normal)]
    , simp)
  (fastforce intro: Main-Call C-sees-M-in-EP)
hence [simp]:
  (Abs-wf-jvmprog (EP, Phi-EP), "C", "M") ⊢ ⇒("C", "M", None, node-
    Type.Return)
  by (rule reachable-step [where n=("C", "M", None, Enter)])
  (fastforce intro: JVMCFG-reachable.intros)
have [simp]:

```

(Abs-wf-jvmprog (EP, Phi-EP), "C", "M") $\vdash \Rightarrow$ ("C", "M", [0], Enter)
by (rule reachable-step [where n=("C", "M", None, Enter)], simp)
 (fastforce intro: JVMCFG-reachable.intros)
hence [simp]:
 (Abs-wf-jvmprog (EP, Phi-EP), "C", "M") $\vdash \Rightarrow$ ("C", "M", [Suc 0], Enter)
by (fastforce intro: reachable-step [where n=("C", "M", [0], Enter)] CFG-Push
 simp: ClassMain-not-C [symmetric])
show ?prog \in {P, C0, Main}.
 $\forall n. \text{CFG.valid-node sourcenode targetnode (valid-edge (P, C0, Main)) } n$
 \longrightarrow
 $(\exists as. \text{CFG.valid-path' sourcenode targetnode kind (valid-edge (P, C0, Main))$
 $(\text{get-return-edges } P) n as$
 $(\text{ClassMain } P, \text{MethodMain } P, \text{None, nodeType.Return}))\}$
proof (auto dest!: valid-node-in-EP-D)
have CFG.valid-path' sourcenode targetnode kind
 (valid-edge (Abs-wf-jvmprog (EP, Phi-EP), "C", "M"))
 (get-return-edges (Abs-wf-jvmprog (EP, Phi-EP)))
 (ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog
 (EP, Phi-EP)),
 None, Enter)
 [?edge-main0]
 (ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog
 (EP, Phi-EP)),
 None, nodeType.Return)
by (fastforce intro: JVMCFG-Interpret.intra-path-vp JVMCFG-reachable.intros
 simp: JVMCFG-Interpret.intra-path-def intra-kind-def valid-edge-def)
thus $\exists as. \text{CFG.valid-path' sourcenode targetnode kind}$
 (valid-edge (Abs-wf-jvmprog (EP, Phi-EP), "C", "M"))
 (get-return-edges (Abs-wf-jvmprog (EP, Phi-EP)))
 (ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog
 (EP, Phi-EP)),
 None, Enter)
 as (ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog
 (EP, Phi-EP)),
 None, nodeType.Return)
by blast
next
have CFG.valid-path' sourcenode targetnode kind
 (valid-edge (Abs-wf-jvmprog (EP, Phi-EP), "C", "M"))
 (get-return-edges (Abs-wf-jvmprog (EP, Phi-EP)))
 (ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog
 (EP, Phi-EP)),
 None, nodeType.Return)
 \square (ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog
 (EP, Phi-EP)),
 None, nodeType.Return)
by (fastforce intro: JVMCFG-Interpret.intra-path-vp simp: JVMCFG-Interpret.intra-path-def)
thus $\exists as. \text{CFG.valid-path' sourcenode targetnode kind}$

```

      (valid-edge (Abs-wf-jvmprog (EP, Phi-EP), "C", "M"))
      (get-return-edges (Abs-wf-jvmprog (EP, Phi-EP)))
      (ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog
(EP, Phi-EP)),
      None, nodeType.Return)
      as (ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog
(EP, Phi-EP)),
      None, nodeType.Return)
    by blast
  next
  have CFG.valid-path' sourcenode targetnode kind
    (valid-edge (Abs-wf-jvmprog (EP, Phi-EP), "C", "M"))
    (get-return-edges (Abs-wf-jvmprog (EP, Phi-EP)))
    (ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog
(EP, Phi-EP)),
    [0], Enter)
    [?edge-main2, ?edge-main3, ?edge-main4]
    (ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog
(EP, Phi-EP)),
    None, nodeType.Return)
  by (fastforce intro: JVMCFG-Interpret.intra-path-vp JVMCFG-reachable.intros
simp: JVMCFG-Interpret.intra-path-def intra-kind-def valid-edge-def)
  thus  $\exists$  as. CFG.valid-path' sourcenode targetnode kind
    (valid-edge (Abs-wf-jvmprog (EP, Phi-EP), "C", "M"))
    (get-return-edges (Abs-wf-jvmprog (EP, Phi-EP)))
    (ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog
(EP, Phi-EP)),
    [0], Enter)
    as (ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog
(EP, Phi-EP)),
    None, nodeType.Return)
  by blast
  next
  have CFG.valid-path' sourcenode targetnode kind
    (valid-edge (Abs-wf-jvmprog (EP, Phi-EP), "C", "M"))
    (get-return-edges (Abs-wf-jvmprog (EP, Phi-EP)))
    (ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog
(EP, Phi-EP)),
    [0], Normal)
    [?edge-main3, ?edge-main4]
    (ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog
(EP, Phi-EP)),
    None, nodeType.Return)
  by (fastforce intro: JVMCFG-Interpret.intra-path-vp JVMCFG-reachable.intros
simp: JVMCFG-Interpret.intra-path-def intra-kind-def valid-edge-def)
  thus  $\exists$  as. CFG.valid-path' sourcenode targetnode kind
    (valid-edge (Abs-wf-jvmprog (EP, Phi-EP), "C", "M"))
    (get-return-edges (Abs-wf-jvmprog (EP, Phi-EP)))
    (ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog
(EP, Phi-EP)),

```



```

(EP, Phi-EP),
  [0], Normal)
  as (ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog
(EP, Phi-EP)),
    None, nodeType.Return)
  by blast
next
have CFG.valid-path' sourcenode targetnode kind
  (valid-edge (Abs-wf-jvmprog (EP, Phi-EP), "C", "M"))
  (get-return-edges (Abs-wf-jvmprog (EP, Phi-EP)))
  (ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog
(EP, Phi-EP)),
  [0], nodeType.Return)
  [?edge-main4]
  (ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog
(EP, Phi-EP)),
  None, nodeType.Return)
  by (fastforce intro: JVMCFG-Interpret.intra-path-vp JVMCFG-reachable.intros
simp: JVMCFG-Interpret.intra-path-def intra-kind-def valid-edge-def)
  thus  $\exists$  as. CFG.valid-path' sourcenode targetnode kind
  (valid-edge (Abs-wf-jvmprog (EP, Phi-EP), "C", "M"))
  (get-return-edges (Abs-wf-jvmprog (EP, Phi-EP)))
  (ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog
(EP, Phi-EP)),
  [0], nodeType.Return)
  as (ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog
(EP, Phi-EP)),
  None, nodeType.Return)
  by blast
next
have CFG.valid-path' sourcenode targetnode kind
  (valid-edge (Abs-wf-jvmprog (EP, Phi-EP), "C", "M"))
  (get-return-edges (Abs-wf-jvmprog (EP, Phi-EP))) ("C", "M", None,
Enter)
  [?edge-C0, ?edge-return, ?edge-main4]
  (ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog
(EP, Phi-EP)),
  None, nodeType.Return)
  by (fastforce intro: JVMCFG-reachable.intros C-sees-M-in-EP
simp: JVMCFG-Interpret.vp-def valid-edge-def stkLength-def JVMCFG-Interpret.valid-path-def)
  thus  $\exists$  as. CFG.valid-path' sourcenode targetnode kind
  (valid-edge (Abs-wf-jvmprog (EP, Phi-EP), "C", "M"))
  (get-return-edges (Abs-wf-jvmprog (EP, Phi-EP))) ("C", "M", None,
Enter) as
  (ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog
(EP, Phi-EP)),
  None, nodeType.Return)
  by blast
next

```

```

have CFG.valid-path' sourcenode targetnode kind
  (valid-edge (Abs-wf-jvmprog (EP, Phi-EP), "C", "M"))
  (get-return-edges (Abs-wf-jvmprog (EP, Phi-EP))) ("C", "M", [0], Enter)
  [?edge-C2, ?edge-C3, ?edge-return, ?edge-main4]
  (ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog
(EP, Phi-EP)),
    None, nodeType.Return)
by (fastforce intro: Main-Return-to-Exit CFG-Return-from-Method Main-Call
C-sees-M-in-EP CFG-Return CFG-Push
simp: JVMCFG-Interpret.vp-def valid-edge-def stkLength-def Phi-EP-def
ClassMain-not-C [symmetric] JVMCFG-Interpret.valid-path-def)
thus  $\exists$  as. CFG.valid-path' sourcenode targetnode kind
  (valid-edge (Abs-wf-jvmprog (EP, Phi-EP), "C", "M"))
  (get-return-edges (Abs-wf-jvmprog (EP, Phi-EP))) ("C", "M", [0], Enter)
as
  (ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog
(EP, Phi-EP)),
    None, nodeType.Return)
by blast
next
have CFG.valid-path' sourcenode targetnode kind
  (valid-edge (Abs-wf-jvmprog (EP, Phi-EP), "C", "M"))
  (get-return-edges (Abs-wf-jvmprog (EP, Phi-EP))) ("C", "M", [Suc 0],
Enter)
  [?edge-C3, ?edge-return, ?edge-main4]
  (ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog
(EP, Phi-EP)),
    None, nodeType.Return)
by (fastforce intro: JVMCFG-reachable.intros C-sees-M-in-EP
simp: JVMCFG-Interpret.vp-def valid-edge-def stkLength-def Phi-EP-def
ClassMain-not-C [symmetric] JVMCFG-Interpret.valid-path-def)
thus  $\exists$  as. CFG.valid-path' sourcenode targetnode kind
  (valid-edge (Abs-wf-jvmprog (EP, Phi-EP), "C", "M"))
  (get-return-edges (Abs-wf-jvmprog (EP, Phi-EP))) ("C", "M", [Suc 0],
Enter) as
  (ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog
(EP, Phi-EP)),
    None, nodeType.Return)
by blast
next
have CFG.valid-path' sourcenode targetnode kind
  (valid-edge (Abs-wf-jvmprog (EP, Phi-EP), "C", "M"))
  (get-return-edges (Abs-wf-jvmprog (EP, Phi-EP))) ("C", "M", None,
nodeType.Return)
  [?edge-return, ?edge-main4]
  (ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog
(EP, Phi-EP)),
    None, nodeType.Return)
by (fastforce intro: JVMCFG-reachable.intros C-sees-M-in-EP

```

*simp: JVMCFG-Interpret.vp-def valid-edge-def JVMCFG-Interpret.valid-path-def
stkLength-def*
thus \exists *as. CFG.valid-path' sourcenode targetnode kind*
(valid-edge (Abs-wf-jvmprog (EP, Phi-EP), "C", "M"))
(get-return-edges (Abs-wf-jvmprog (EP, Phi-EP))) ("C", "M", None,
nodeType.Return)
as (ClassMain (Abs-wf-jvmprog (EP, Phi-EP)), MethodMain (Abs-wf-jvmprog
(EP, Phi-EP)),
None, nodeType.Return)
by *blast*
qed
qed

abbreviation *lift-to-cfg-wf-prog :: (jvm-method \Rightarrow 'a) \Rightarrow (cfg-wf-prog \Rightarrow 'a)*
(-CFG)
where *f CFG \equiv ($\lambda P. f$ (Rep-cfg-wf-prog P))*

lemma *valid-edge-CFG-def: valid-edge CFG P = valid-edge (fst CFG P, fst (snd CFG*
P), snd (snd CFG P))
by *(cases P) (clarsimp simp: Abs-cfg-wf-prog-inverse)*

interpretation *JVMCFG-Postdomination!:*

Postdomination sourcenode targetnode kind valid-edge CFG P
(ClassMain (fst CFG P), MethodMain (fst CFG P), None, Enter)
($\lambda(C, M, pc, type). (C, M)$) get-return-edges (fst CFG P)
((ClassMain (fst CFG P), MethodMain (fst CFG P)), [], []) # procs (PROG (fst CFG
P))

(ClassMain (fst CFG P), MethodMain (fst CFG P))
(ClassMain (fst CFG P), MethodMain (fst CFG P), None, Return)
for *P*

unfolding *valid-edge-CFG-def*

proof

fix *n*

obtain *P' C0 Main where [simp]: fst CFG P = P' and [simp]: fst (snd CFG P)*
= C0

and *[simp]: snd (snd CFG P) = Main*

by *(cases P) clarsimp*

assume *CFG.valid-node sourcenode targetnode*

(valid-edge (fst CFG P, fst (snd CFG P), snd (snd CFG P))) n

thus \exists *as. CFG.valid-path' sourcenode targetnode kind*

(valid-edge (fst CFG P, fst (snd CFG P), snd (snd CFG P)))

(get-return-edges (fst CFG P))

(ClassMain (fst CFG P), MethodMain (fst CFG P), None, Enter) as n

by *(auto dest: sourcenode-reachable targetnode-reachable valid-Entry-path*

simp: JVMCFG-Interpret.valid-node-def valid-edge-def)

next

fix *n*

obtain *P' C0 Main where [simp]: fst CFG P = P' and [simp]: fst (snd CFG P)*

```

= C0
  and [simp]: snd (snd_CFG P) = Main
  and (P', C0, Main) ∈ cfg-wf-prog
  by (cases P) (clarsimp simp: Abs-cfg-wf-prog-inverse)
assume CFG.valid-node sourcenode targetnode
  (valid-edge (fst_CFG P, fst (snd_CFG P), snd (snd_CFG P))) n
with ⟨(P', C0, Main) ∈ cfg-wf-prog⟩
show ∃ as. CFG.valid-path' sourcenode targetnode kind
  (valid-edge (fst_CFG P, fst (snd_CFG P), snd (snd_CFG P)))
  (get-return-edges (fst_CFG P)) n as
  (ClassMain (fst_CFG P), MethodMain (fst_CFG P), None, nodeType.Return)
  by (cases n) (fastforce simp: cfg-wf-prog-def)
next
fix n n'
obtain P' C0 Main where [simp]: fst_CFG P = P' and [simp]: fst (snd_CFG P)
= C0
  and [simp]: snd (snd_CFG P) = Main
  by (cases P) clarsimp
assume CFGExit.method-exit sourcenode kind
  (valid-edge (fst_CFG P, fst (snd_CFG P), snd (snd_CFG P)))
  (ClassMain (fst_CFG P), MethodMain (fst_CFG P), None, nodeType.Return) n
and CFGExit.method-exit sourcenode kind
  (valid-edge (fst_CFG P, fst (snd_CFG P), snd (snd_CFG P)))
  (ClassMain (fst_CFG P), MethodMain (fst_CFG P), None, nodeType.Return) n'
and (λ(C, M, pc, type). (C, M)) n = (λ(C, M, pc, type). (C, M)) n'
thus n = n'
  by (auto simp: JVMCFG-Exit-Interpret.method-exit-def valid-edge-def)
  (fastforce elim: JVMCFG.cases)+
qed

end

theory JVMSDG imports JVMCFG-wf JVMPostdomination ../StaticInter/SDG
begin

interpretation JVMCFGExit-wf-new-type:
  CFGExit-wf sourcenode targetnode kind valid-edge_CFG P
  (ClassMain (fst_CFG P), MethodMain (fst_CFG P), None, Enter)
  (λ(C, M, pc, type). (C, M)) get-return-edges (fst_CFG P)
  ((ClassMain (fst_CFG P), MethodMain (fst_CFG P)), [], []) # procs (PROG (fst_CFG
P))
  (ClassMain (fst_CFG P), MethodMain (fst_CFG P))
  (ClassMain (fst_CFG P), MethodMain (fst_CFG P), None, Return)
  Def (fst_CFG P) Use (fst_CFG P) ParamDefs (fst_CFG P) ParamUses (fst_CFG
P)
  for P
  unfolding valid-edge-CFG-def
  ..

```

```

interpretation JVM-SDG :
  SDG sourcenode targetnode kind valid-edge CFG P
  (ClassMain (fst CFG P), MethodMain (fst CFG P), None, Enter)
  ( $\lambda(C, M, pc, type). (C, M)$ ) get-return-edges (fst CFG P)
  ((ClassMain (fst CFG P), MethodMain (fst CFG P)), [], []) # procs (PROG (fst CFG P))
  (ClassMain (fst CFG P), MethodMain (fst CFG P))
  (ClassMain (fst CFG P), MethodMain (fst CFG P), None, Return)
  Def (fst CFG P) Use (fst CFG P) ParamDefs (fst CFG P) ParamUses (fst CFG P)
for P
  ..
end

theory HRBSlicing imports
  StaticInter/CFGExit-wf
  StaticInter/SemanticsCFG
  StaticInter/FundamentalProperty
  Proc/ProcSDG
  JinjaVM-Inter/JVMSDG
begin

end

```

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